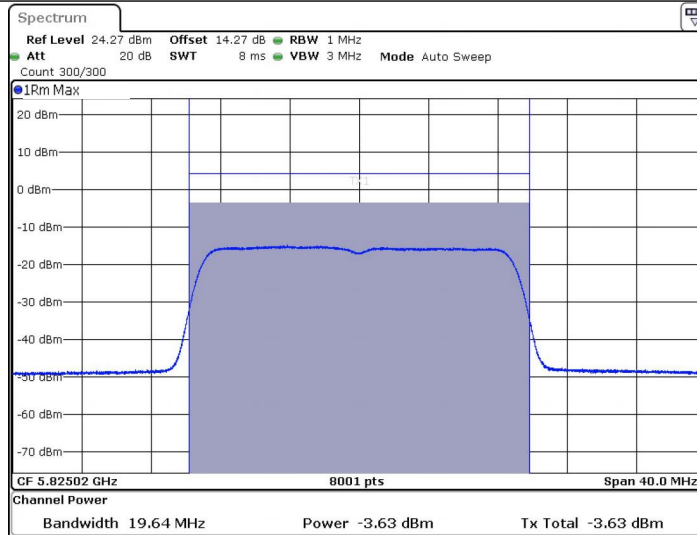
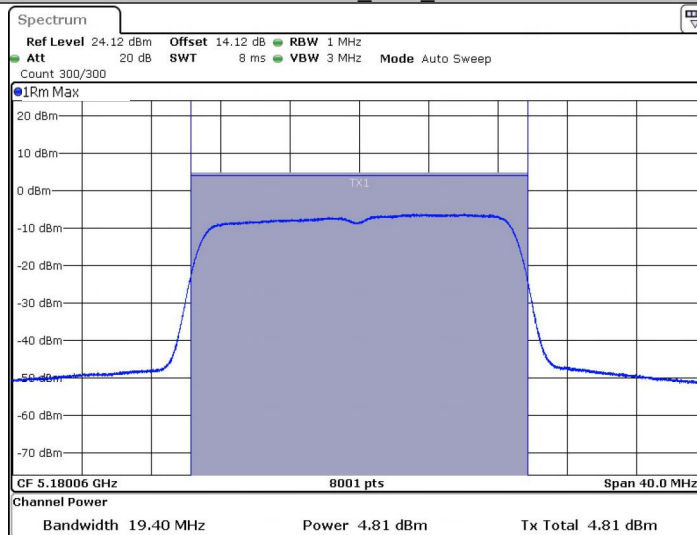


11A_Ant1_5825



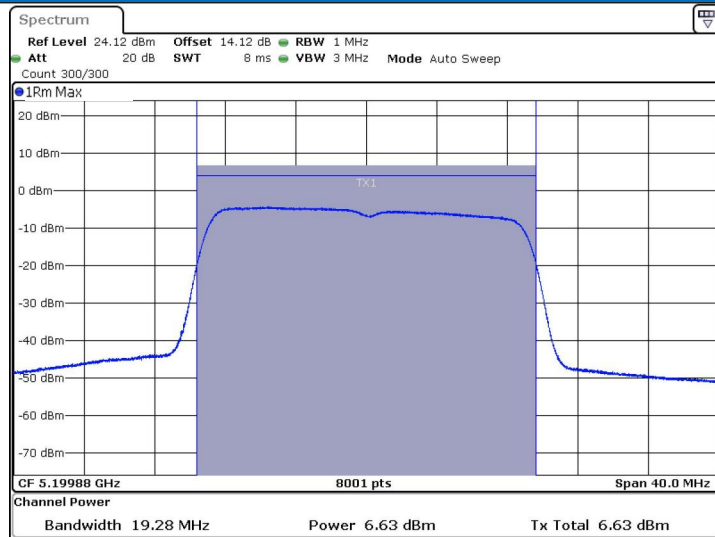
Date: 13 DEC 2024 18:35:18

11N20SISO_Ant1_5180



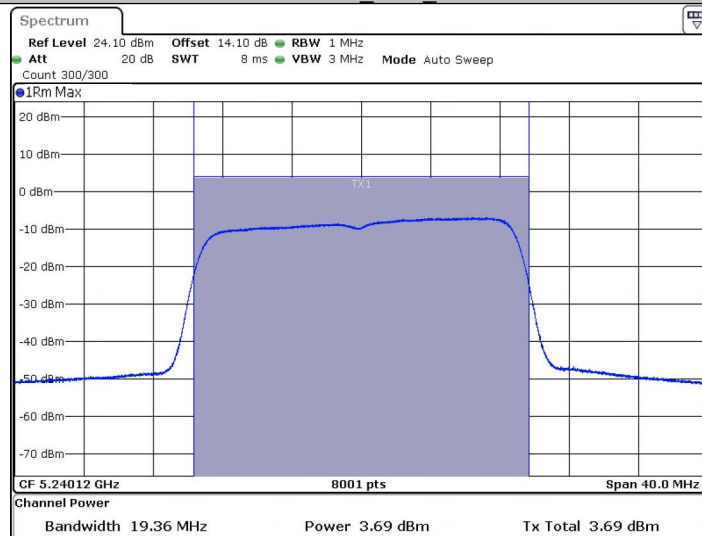
Date: 13 DEC 2024 18:39:08

11N20SISO_Ant1_5200



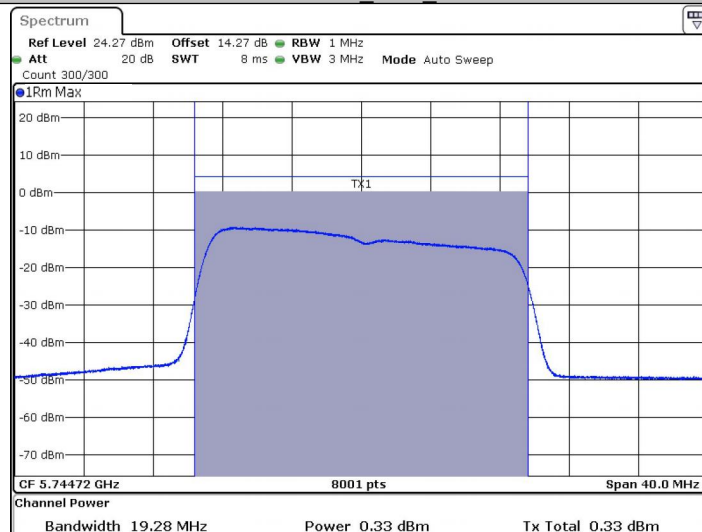
Date: 17 DEC.2024 09:08:23

11N20SISO_Ant1_5240



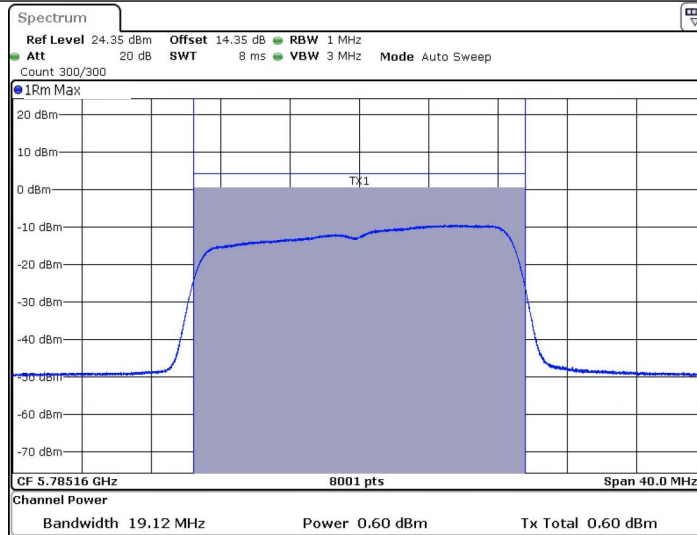
Date: 13 DEC.2024 18:45:55

11N20SISO_Ant1_5745



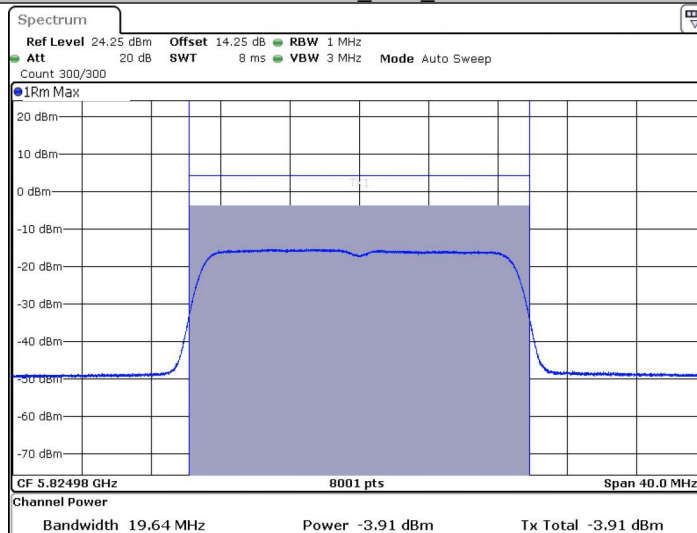
Date: 13 DEC.2024 18:48:28

11N20SISO_Ant1_5785



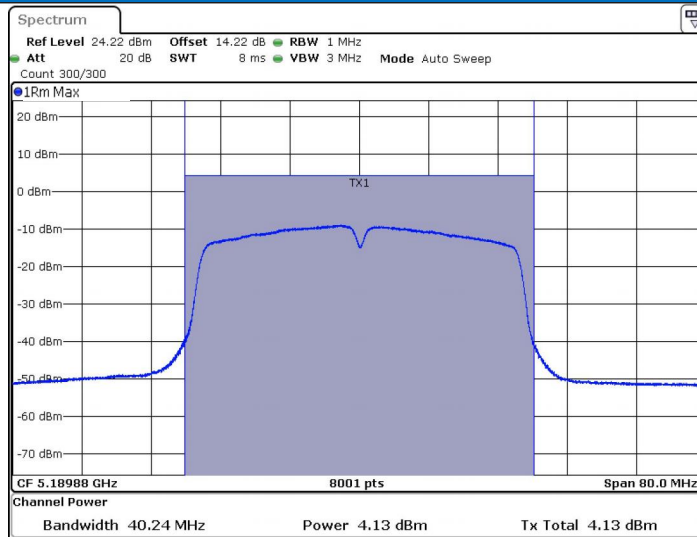
Date: 13 DEC 2024 18:51:37

11N20SISO_Ant1_5825



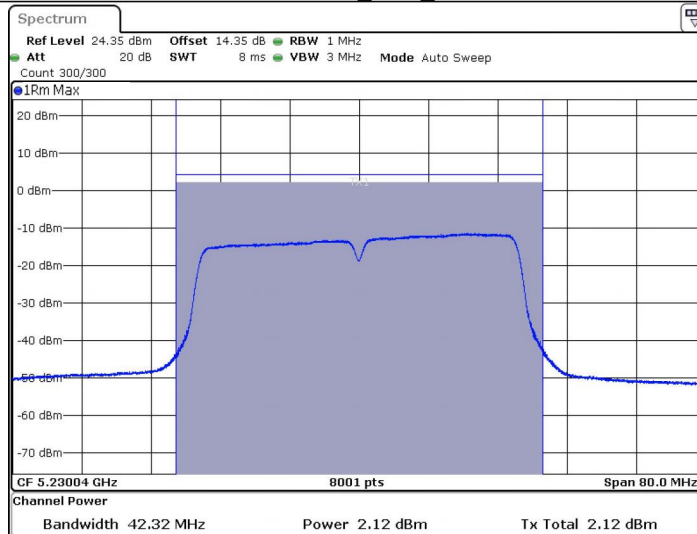
Date: 13 DEC 2024 18:53:42

11N40SISO_Ant1_5190



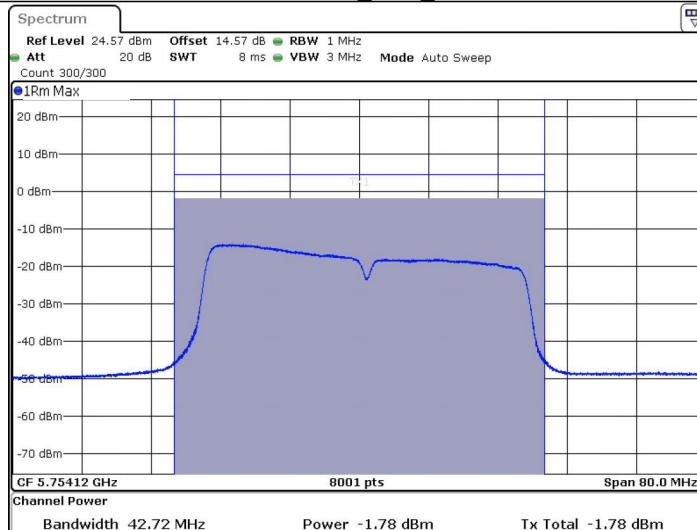
Date: 13 DEC 2024 18:56:57

11N40SISO_Ant1_5230



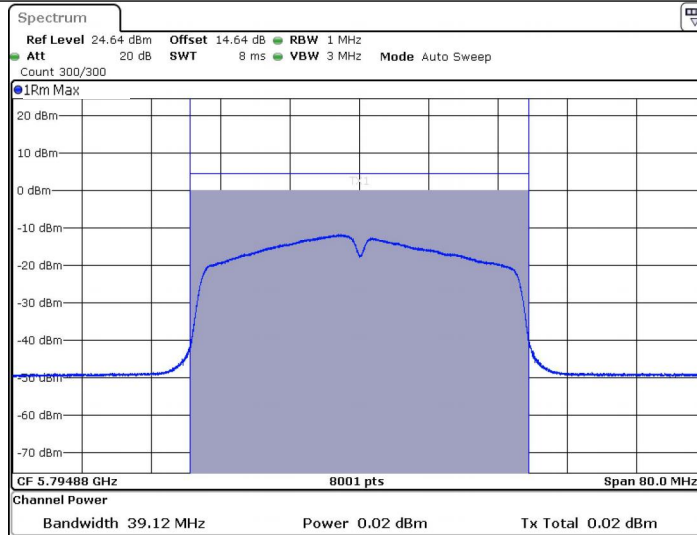
Date: 13 DEC 2024 19:00:19

11N40SISO_Ant1_5755



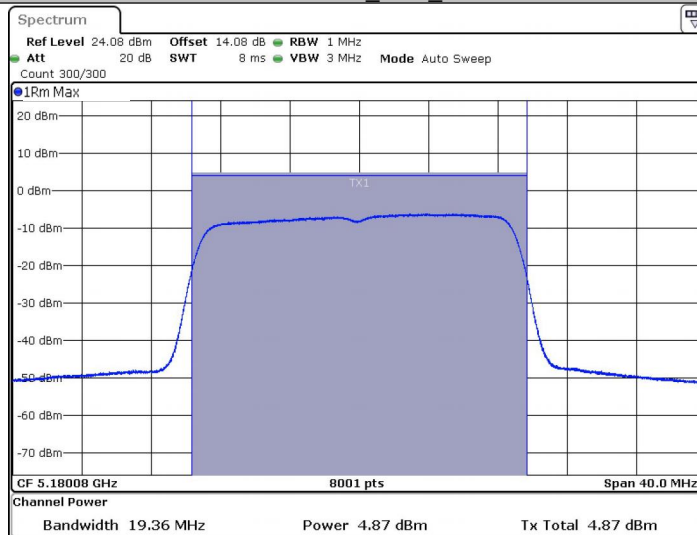
Date: 13 DEC 2024 19:03:11

11N40SISO_Ant1_5795



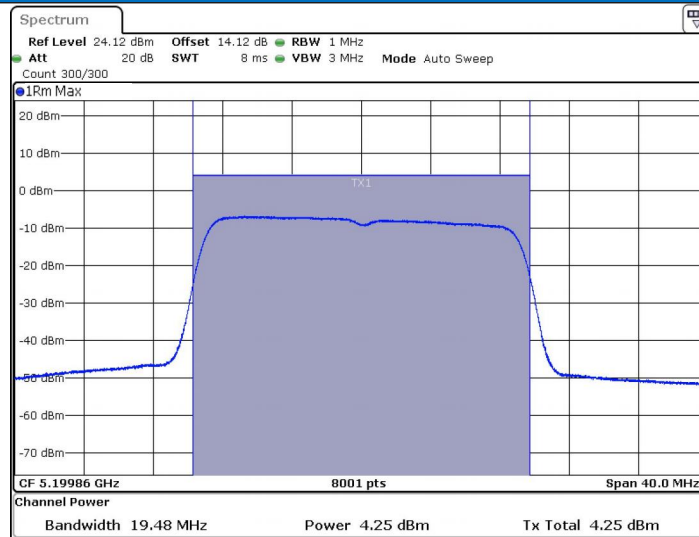
Date: 13 DEC 2024 19:06:34

11AC20SISO_Ant1_5180



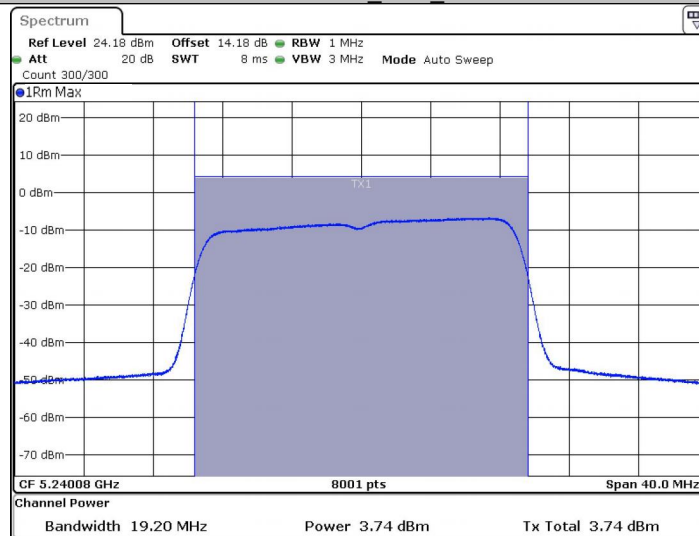
Date: 13 DEC 2024 19:09:50

11AC20SISO_Ant1_5200

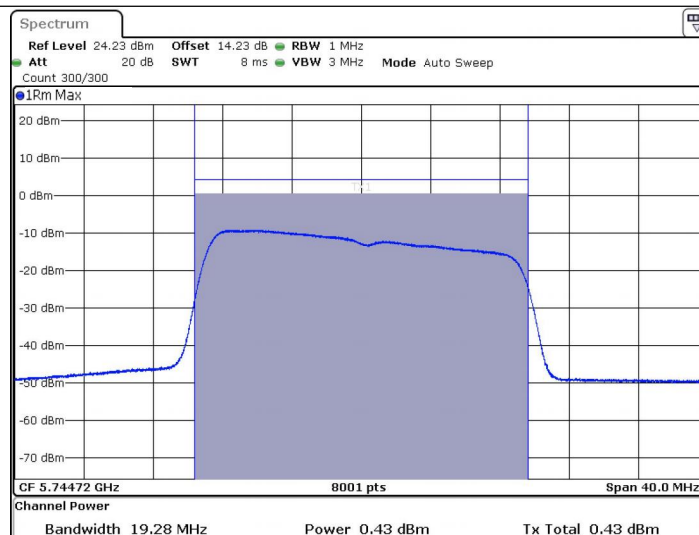


Date: 13.DEC.2024 19:15:01

11AC20SISO_Ant1_5240

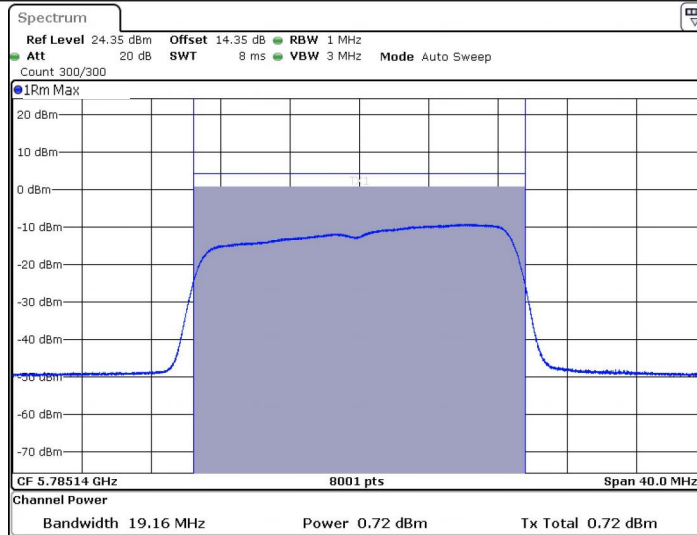


Date: 13.DEC.2024 19:16:43



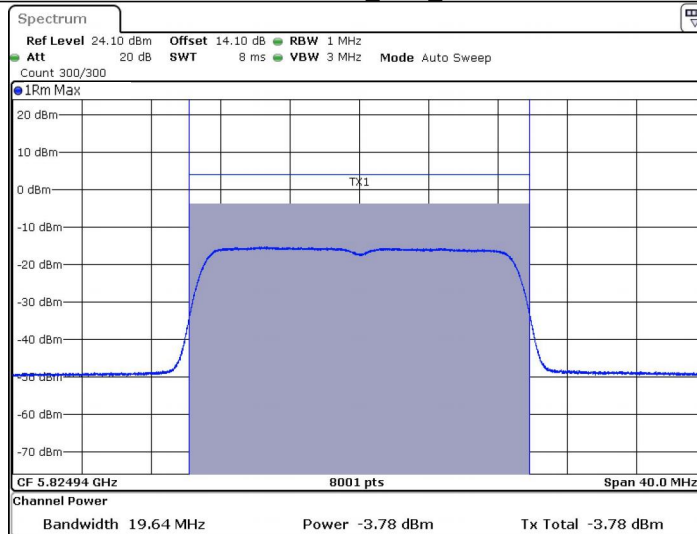
Date: 13.DEC.2024 19:26:02

11AC20SISO_Ant1_5785



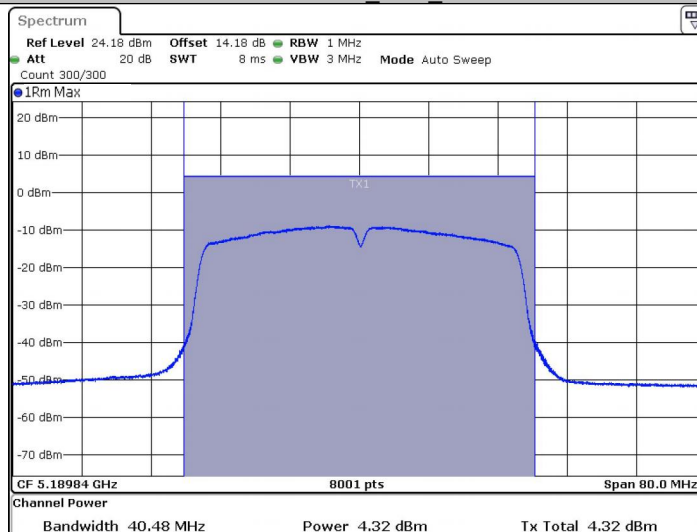
Date: 13 DEC.2024 19:29:29

11AC20SISO_Ant1_5825



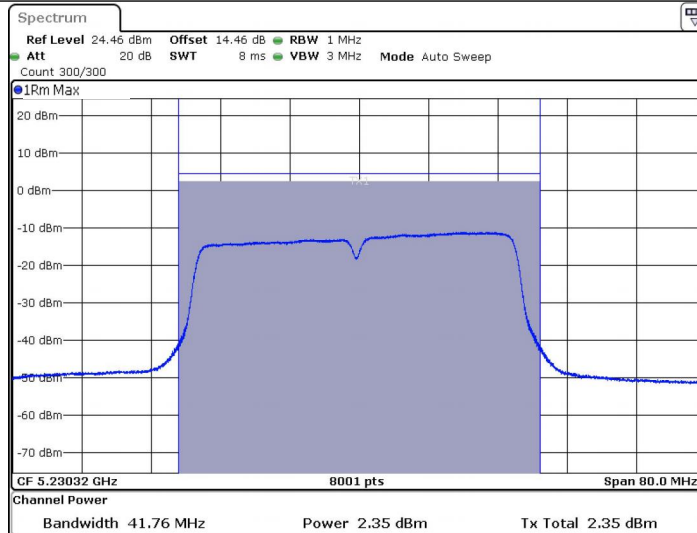
Date: 13 DEC.2024 19:31:21

11AC40SISO_Ant1_5190



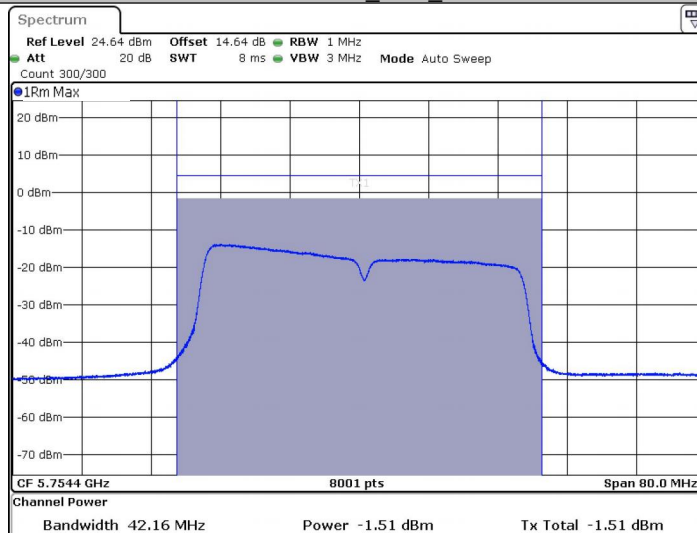
Date: 13 DEC.2024 19:34:21

11AC40SISO_Ant1_5230



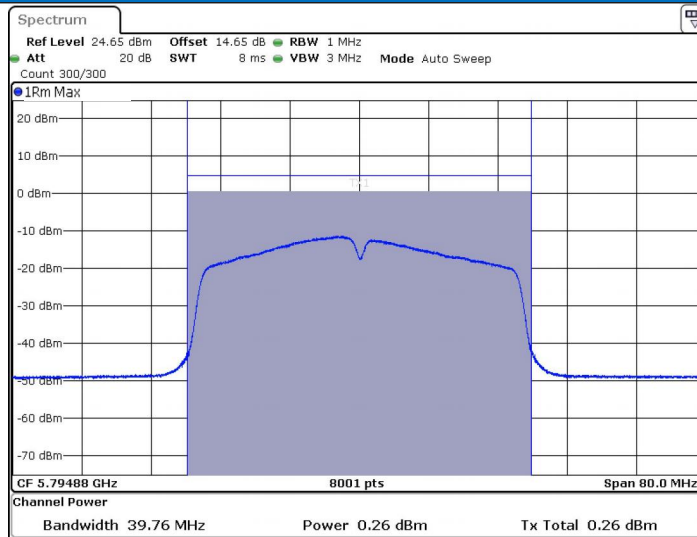
Date: 13 DEC 2024 19:38:18

11AC40SISO_Ant1_5755



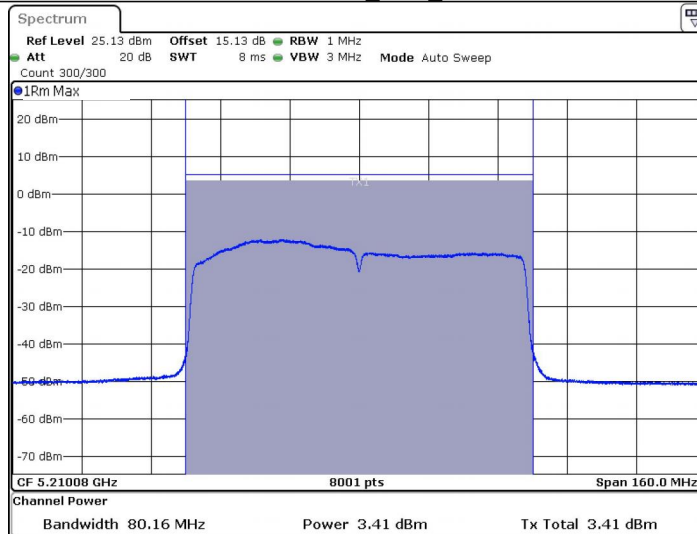
Date: 13 DEC 2024 19:40:44

11AC40SISO_Ant1_5795



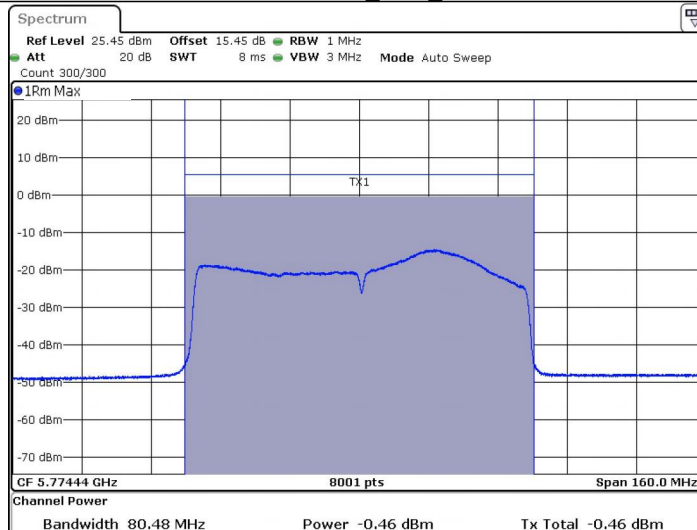
Date: 13 DEC 2024 19:43:46

11AC80SISO_Ant1_5210



Date: 13 DEC 2024 19:46:46

11AC80SISO_Ant1_5775



Date: 13 DEC 2024 19:57:59

Appendix C): Maximum Power Spectral Density

Test Requirement 47 CFR Part 15, Subpart C 15.407 (a)

Test Method: KDB 789033 D02 II F

Test Procedure:

For 5150-5725MHz:

1. Connect EUT RF output port to the Spectrum Analyzer through an RF attenuator
2. Set the EUT Work on operation frequency individually.
3. Set RBW = 1MHz.
4. Set the VBW $\geq 3 \times$ RBW. Detector = Peak. Trace mode = max hold.

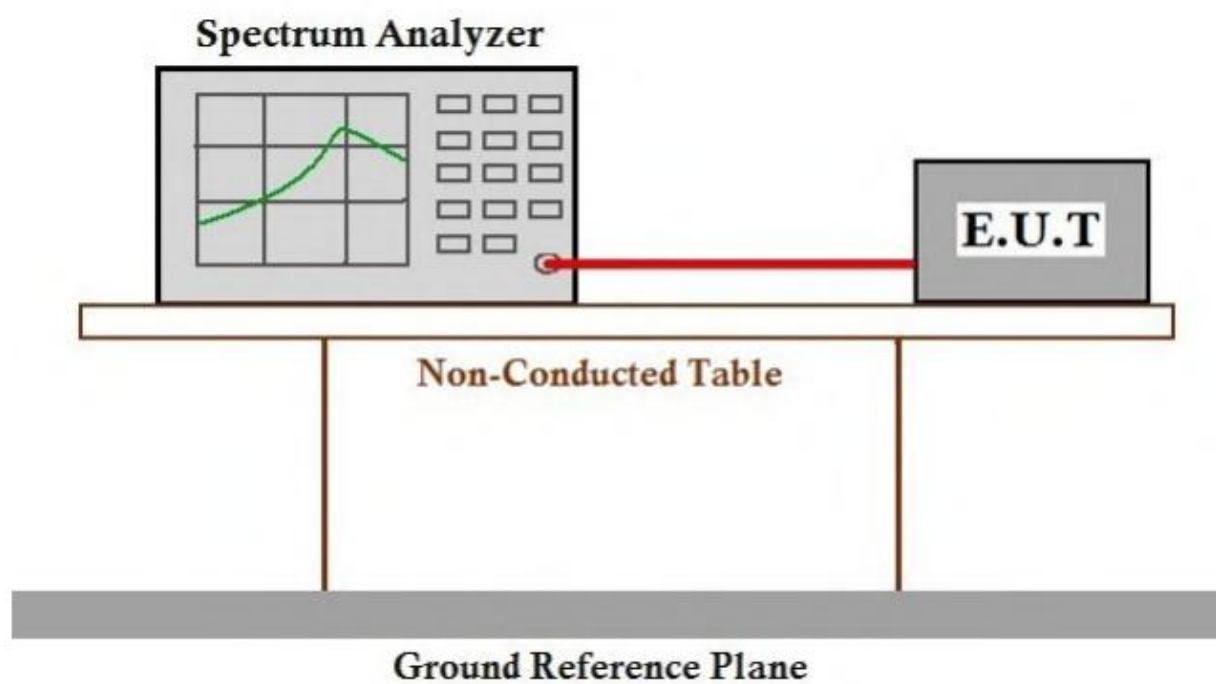
For 5725-5850MHz:

1. Connect EUT RF output port to the Spectrum Analyzer through an RF attenuator
2. Set the EUT Work on operation frequency individually.
3. Set RBW = 500KHz.
4. Set the VBW $\geq 3 \times$ RBW. Detector = Peak. Trace mode = max hold.

Limit:

Frequency band(MHz)	Limit
5150-5250	$\leq 17\text{dBm}$ in 1MHz for master device
	$\leq 11\text{dBm}$ in 1MHz for client device
5250-5350	$\leq 11\text{dBm}$ in 1MHz for client device
5470-5725	$\leq 11\text{dBm}$ in 1MHz for client device
5725-5850	$\leq 30\text{dBm}$ in 500 kHz
Remark:	The maximum power spectral density is measured as a conducted emission by direct connection of a calibrated test instrument to the equipment under test.

Test Setup Diagram



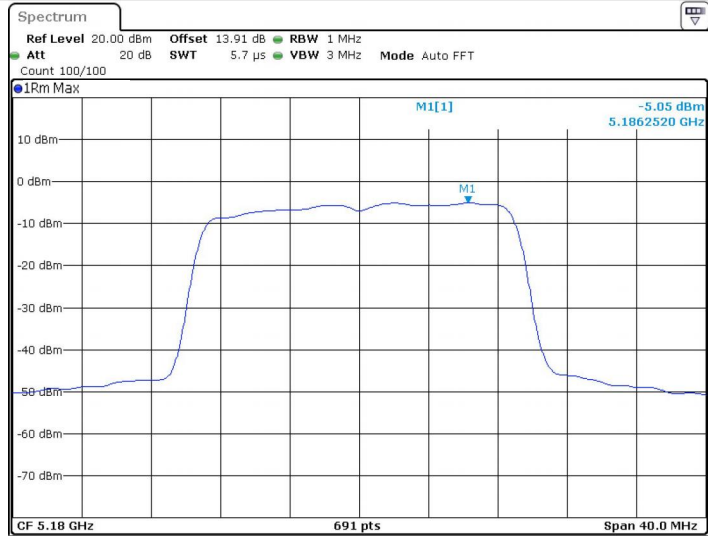
Result Table

TestMode	Freq(MHz)	Result [dBm/MHz]	Limit[dBm/MHz]	Verdict
11A	5180	-5.05	≤11.00	PASS
	5200	-6.65	≤11.00	PASS
	5240	-6.41	≤11.00	PASS
	5745	-12.64	≤30.00	PASS
	5785	-12.46	≤30.00	PASS
	5825	-18.02	≤30.00	PASS
11N20SISO	5180	-6.02	≤11.00	PASS
	5200	-4.61	≤11.00	PASS
	5240	-7.09	≤11.00	PASS
	5745	-12.57	≤30.00	PASS
	5785	-12.35	≤30.00	PASS
	5825	-18.36	≤30.00	PASS
11N40SISO	5190	-8.79	≤11.00	PASS
	5230	-11.31	≤11.00	PASS
	5755	-16.99	≤30.00	PASS
	5795	-14.87	≤30.00	PASS
11AC20SISO	5180	-5.96	≤11.00	PASS
	5200	-6.88	≤11.00	PASS
	5240	-7.20	≤11.00	PASS
	5745	-12.38	≤30.00	PASS
	5785	-12.16	≤30.00	PASS
	5825	-18.26	≤30.00	PASS
11AC40SISO	5190	-9.09	≤11.00	PASS
	5230	-11.52	≤11.00	PASS
	5755	-17.22	≤30.00	PASS
	5795	-14.74	≤30.00	PASS
11AC80SISO	5210	-12.69	≤11.00	PASS
	5775	-17.80	≤30.00	PASS

Note: 1.The Result and Limit Unit is dBm/500 kHz in the band 5.725–5.85 GHz.

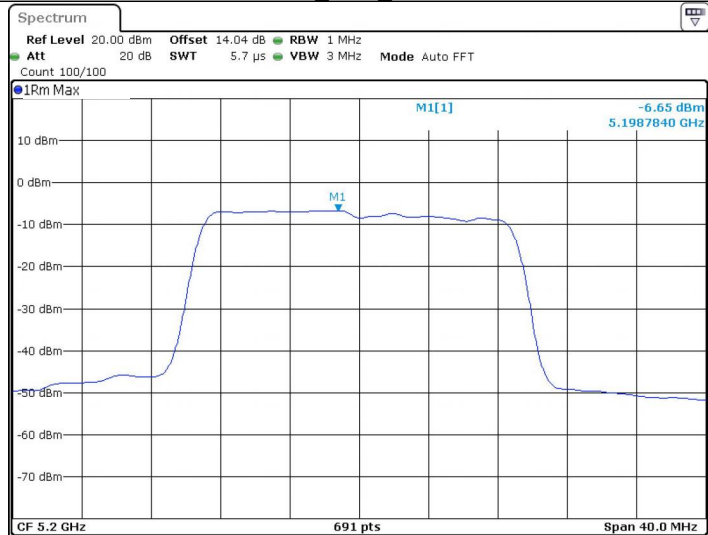
2.The Duty Cycle Factor and RBW Factor is compensated in the graph.

11A_Ant1_5180



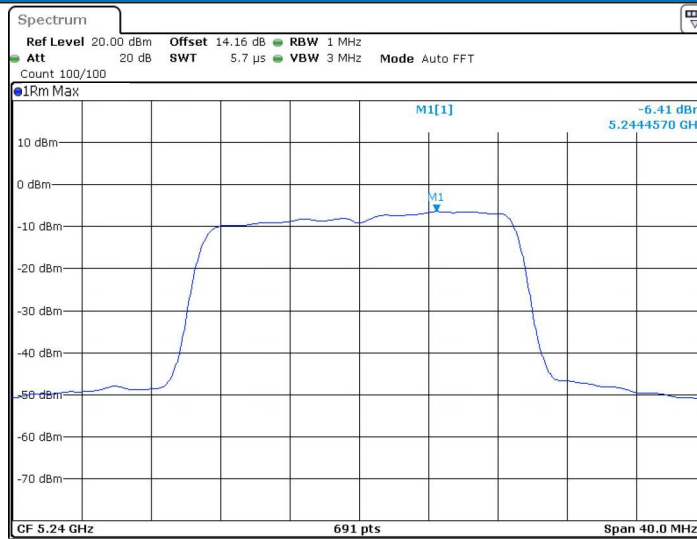
Date: 13 DEC 2024 18:23:03

11A_Ant1_5200



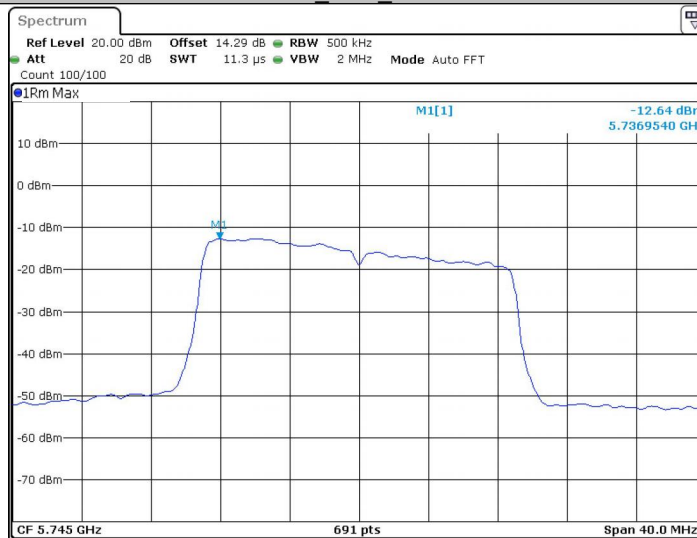
Date: 13 DEC 2024 18:26:08

11A_Ant1_5240



Date: 13 DEC 2024 18:27:48

11A Ant1_5745



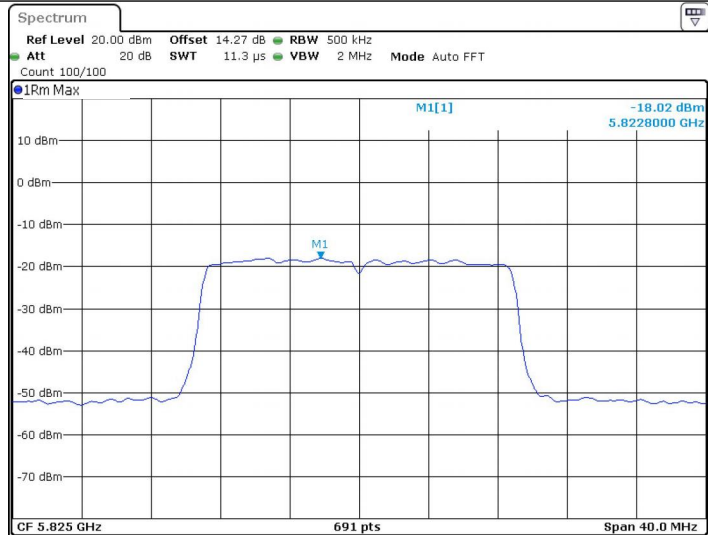
Date: 13 DEC 2024 18:30:34

11A Ant1_5785



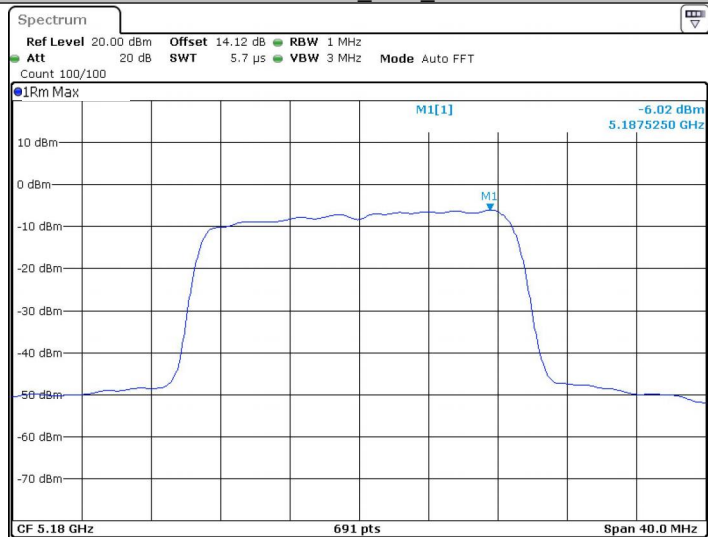
Date: 13 DEC 2024 18:33:26

11A_Ant1_5825



Date: 13 DEC 2024 18:35:27

11N20SISO_Ant1_5180



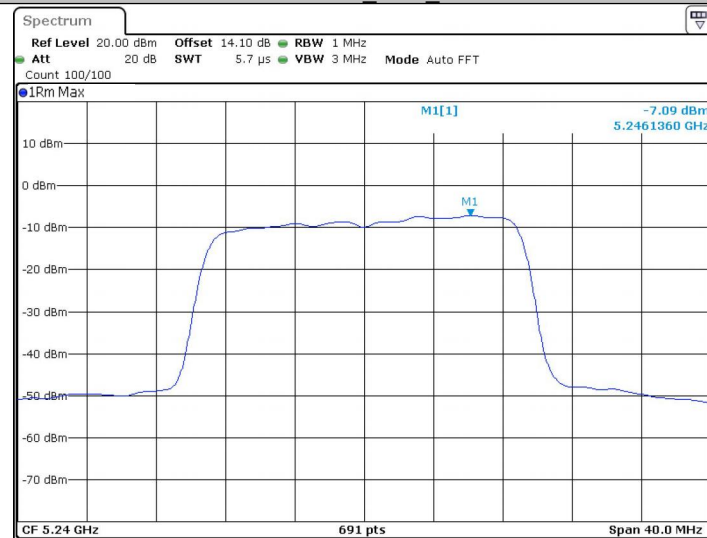
Date: 13 DEC 2024 18:39:23

11N20SISO_Ant1_5200



Date: 17.DEC.2024 09:08:41

11N20SISO_Ant1_5240



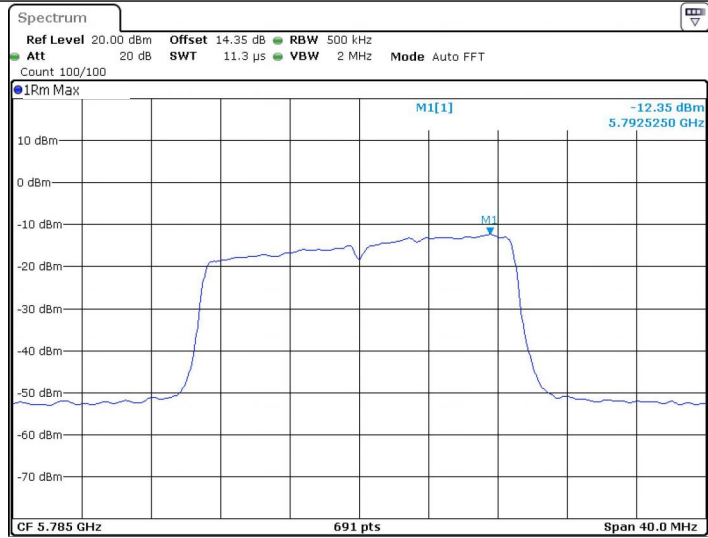
Date: 13.DEC.2024 18:46:03

11N20SISO_Ant1_5745



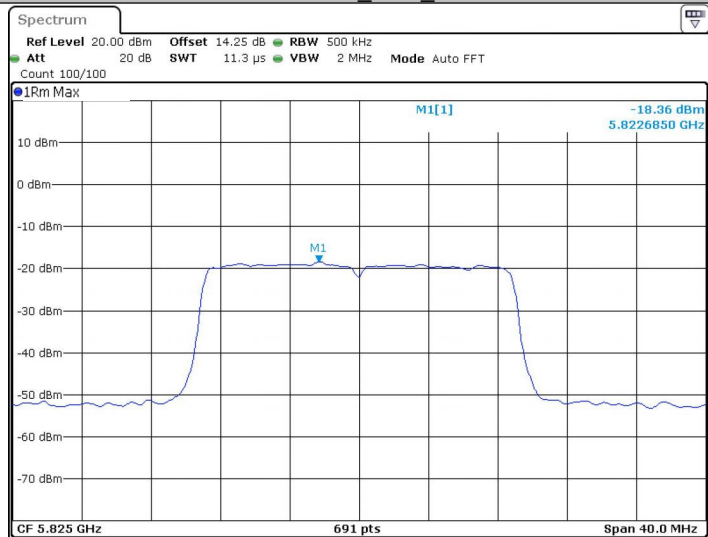
Date: 13.DEC.2024 18:48:37

11N20SISO_Ant1_5785



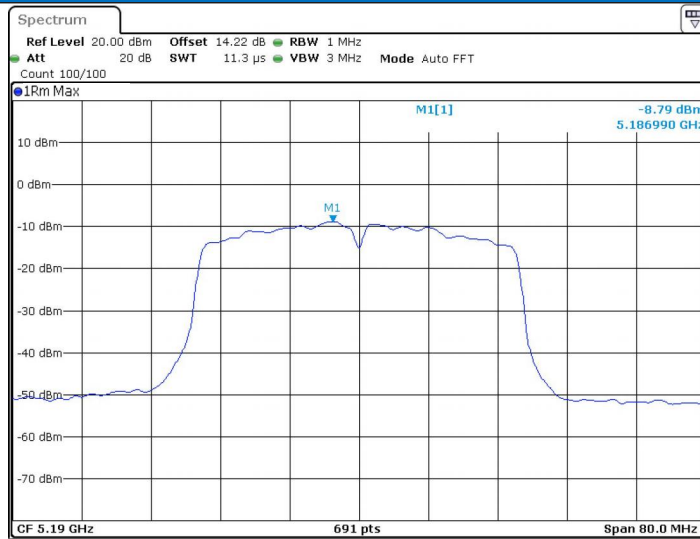
Date: 13 DEC 2024 18:51:46

11N20SISO_Ant1_5825



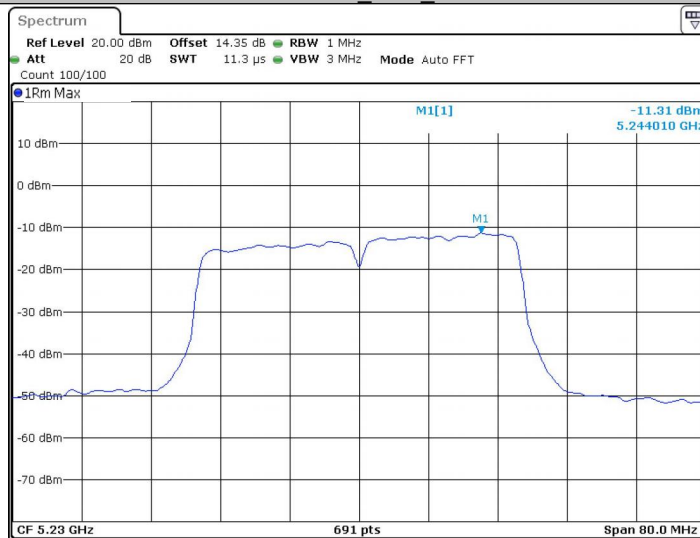
Date: 13 DEC 2024 18:53:51

11N40SISO_Ant1_5190



Date: 13 DEC.2024 18:57:06

11N40SISO_Ant1_5230



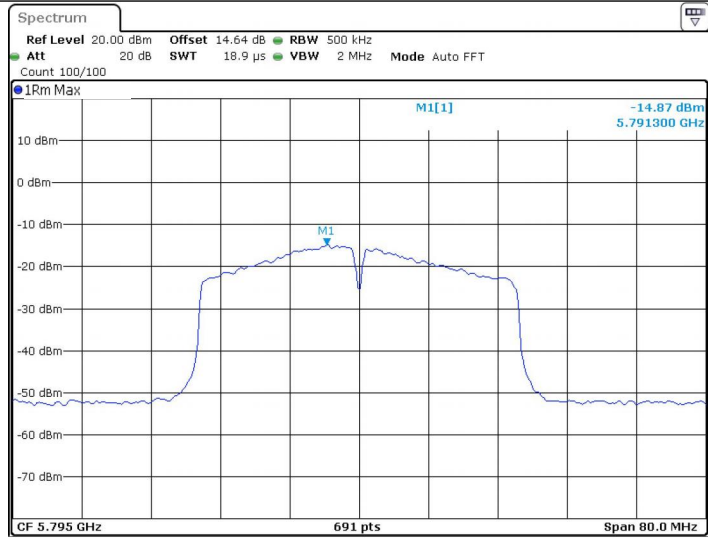
Date: 13 DEC.2024 19:00:28

11N40SISO_Ant1_5755



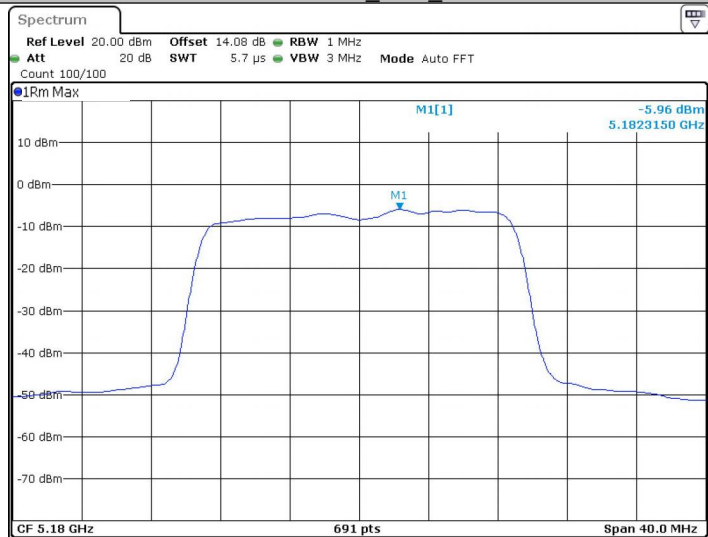
Date: 13 DEC.2024 19:03:20

11N40SISO_Ant1_5795



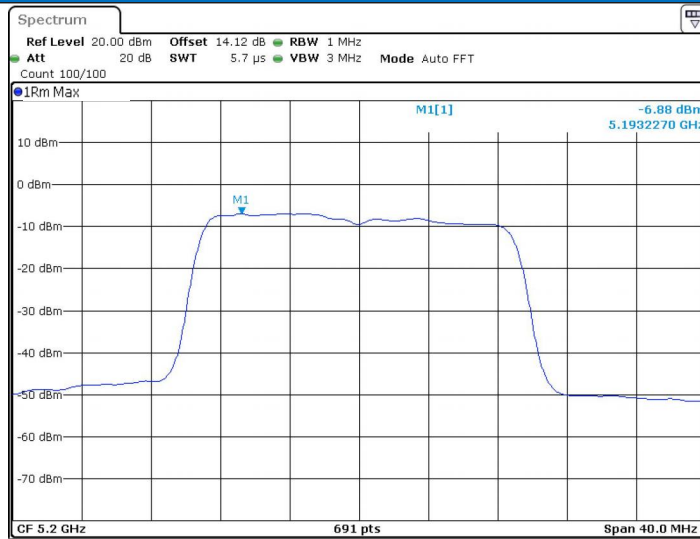
Date: 13 DEC 2024 19:06:43

11AC20SISO_Ant1_5180



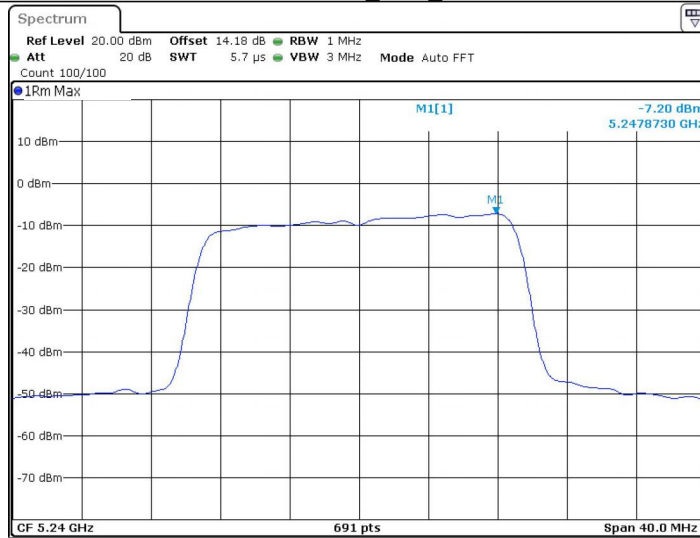
Date: 13 DEC 2024 19:10:05

11AC20SISO_Ant1_5200



Date: 13 DEC.2024 19:15:10

11AC20SISO_Ant1_5240



Date: 13 DEC.2024 19:16:58

11AC20SISO_Ant1_5745