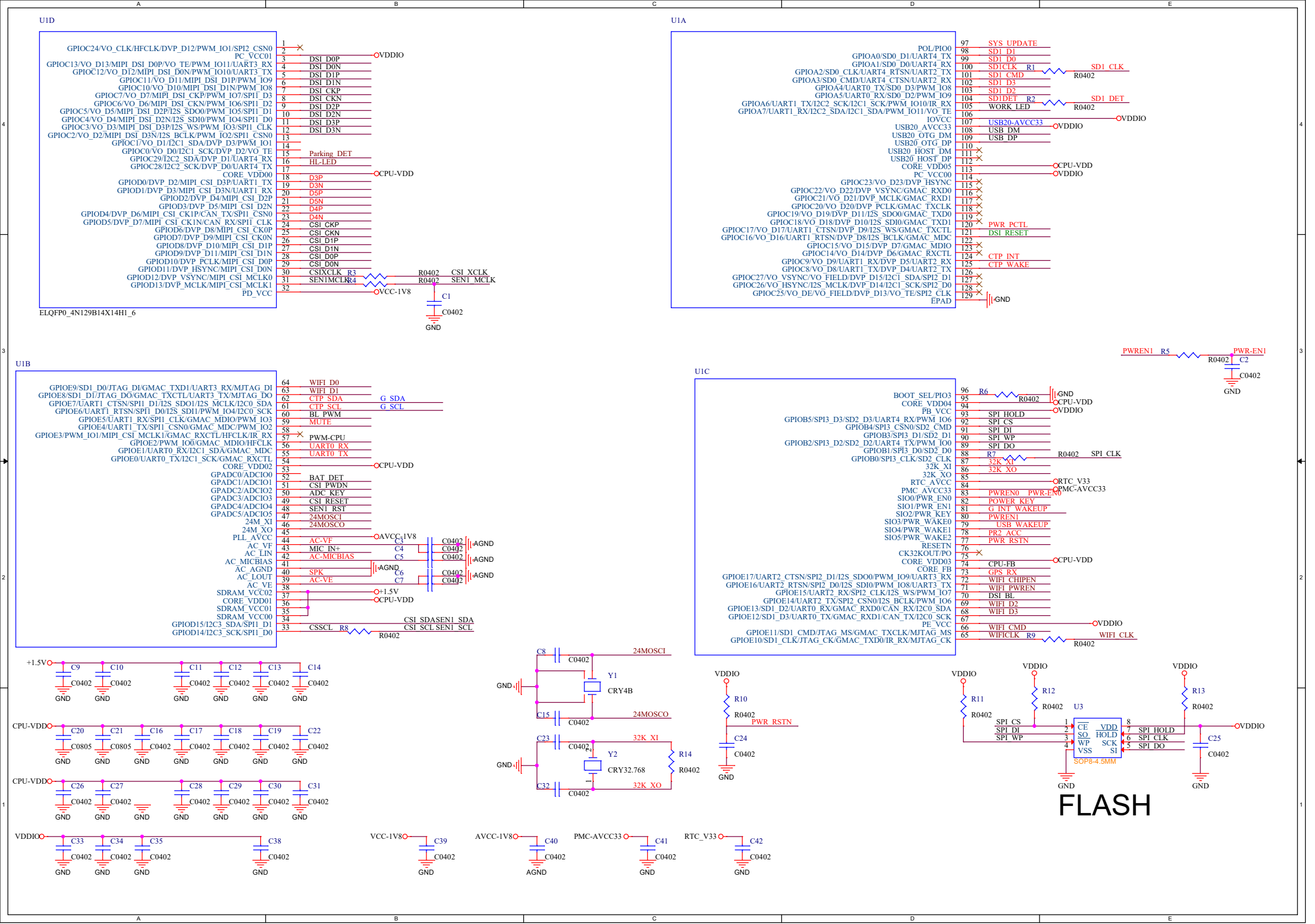
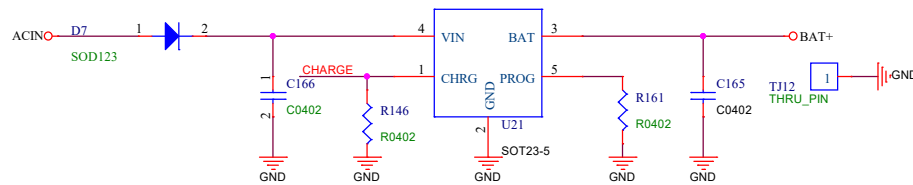
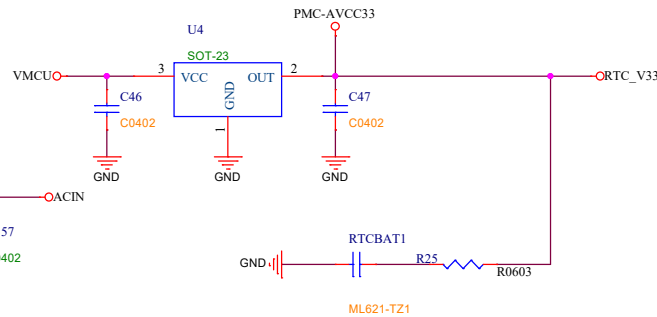
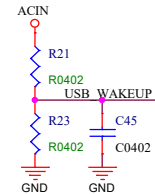
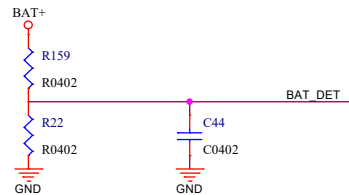
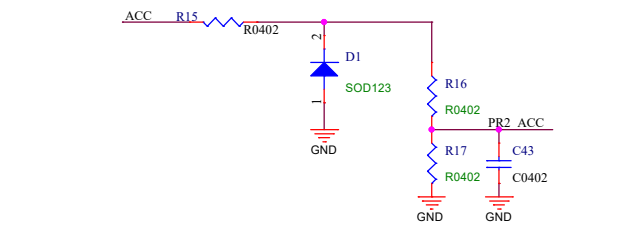
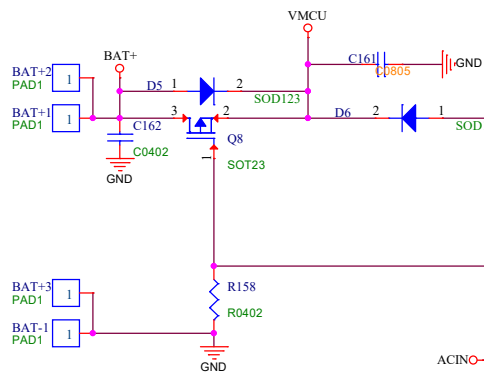
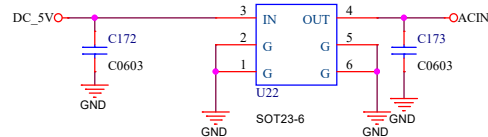
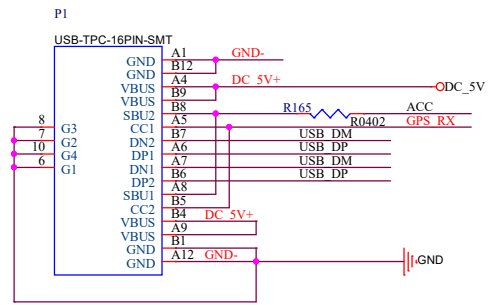






USB_WAKEUP << USB_WAKEUP 6

USB_DM << USB_DM 3

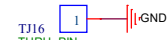
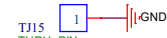
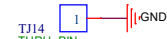
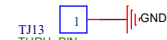
USB_DP << USB_DP 3

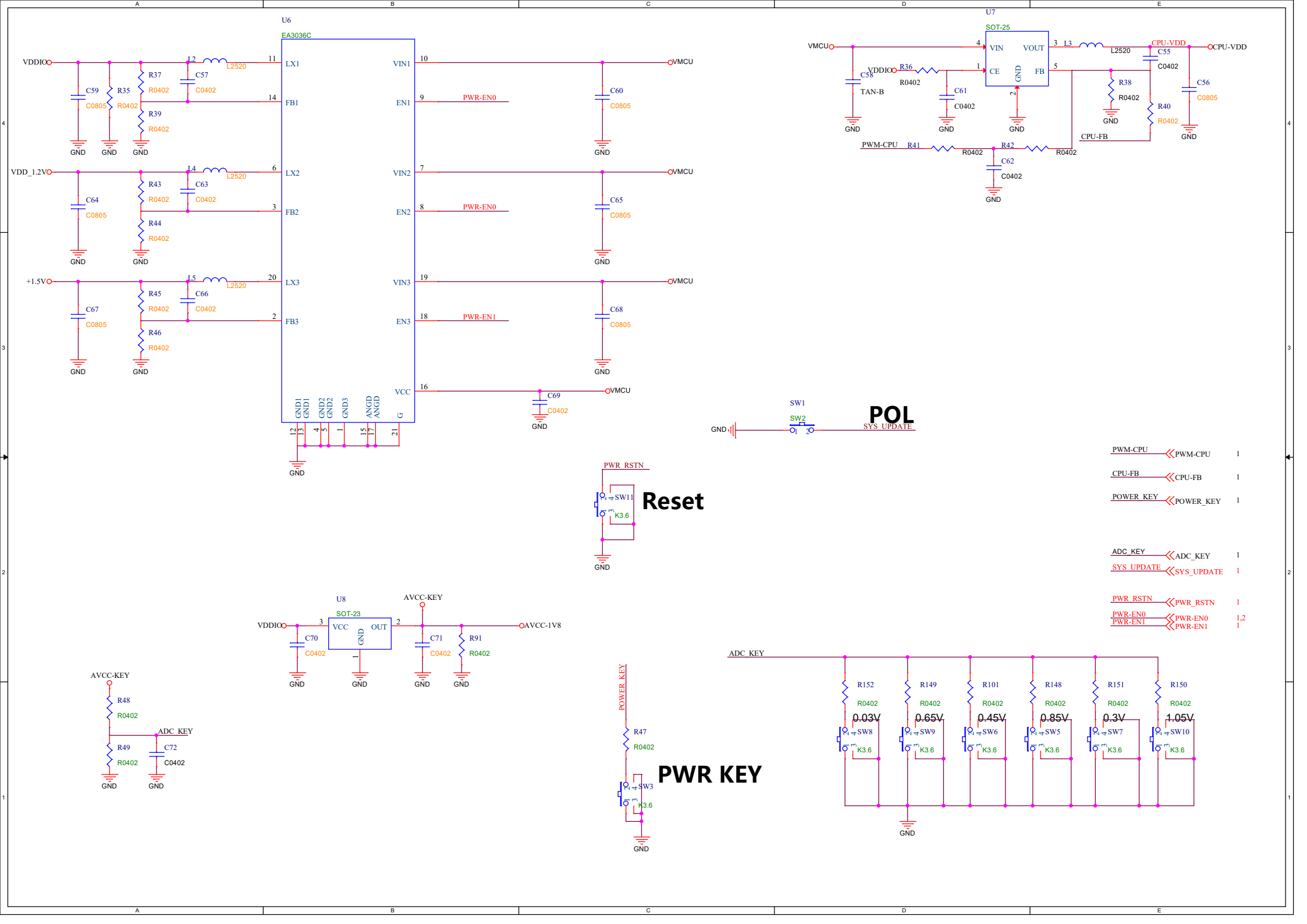
PR2_ACC << PR2_ACC 6

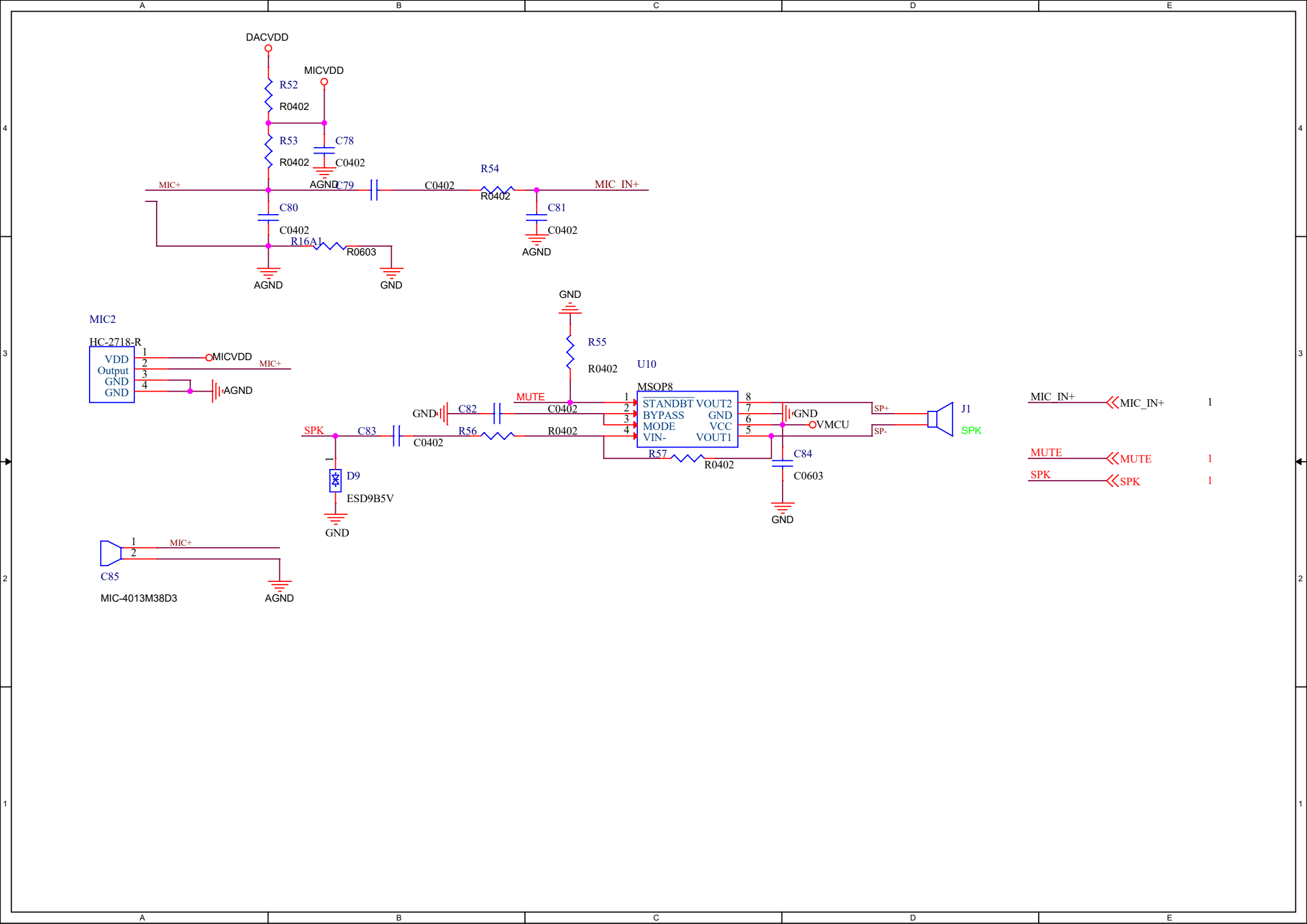
BAT_DET << BAT_DET 3

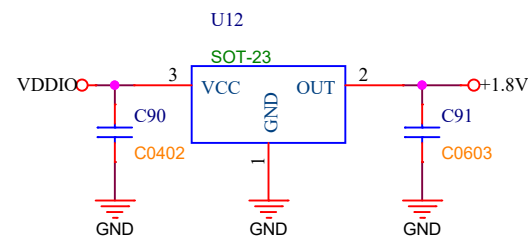
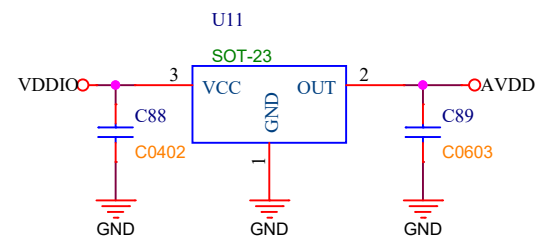
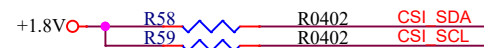
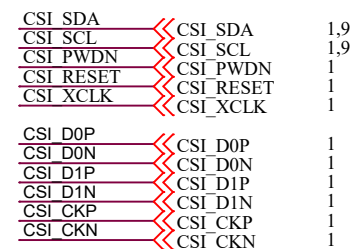
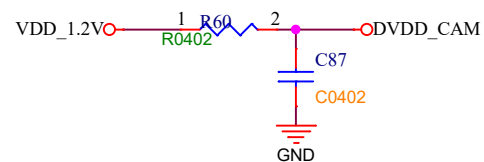
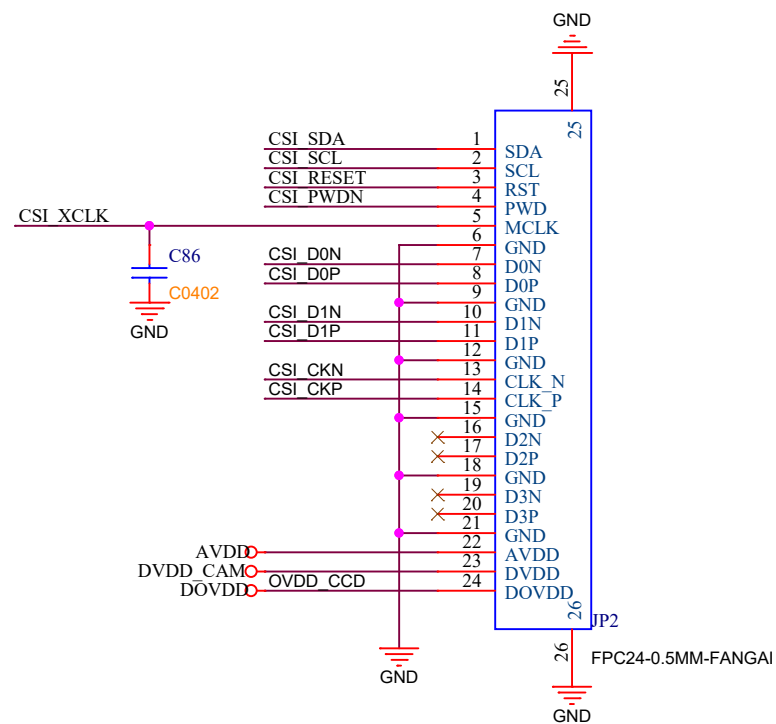
WORK_LED << WORK_LED 6

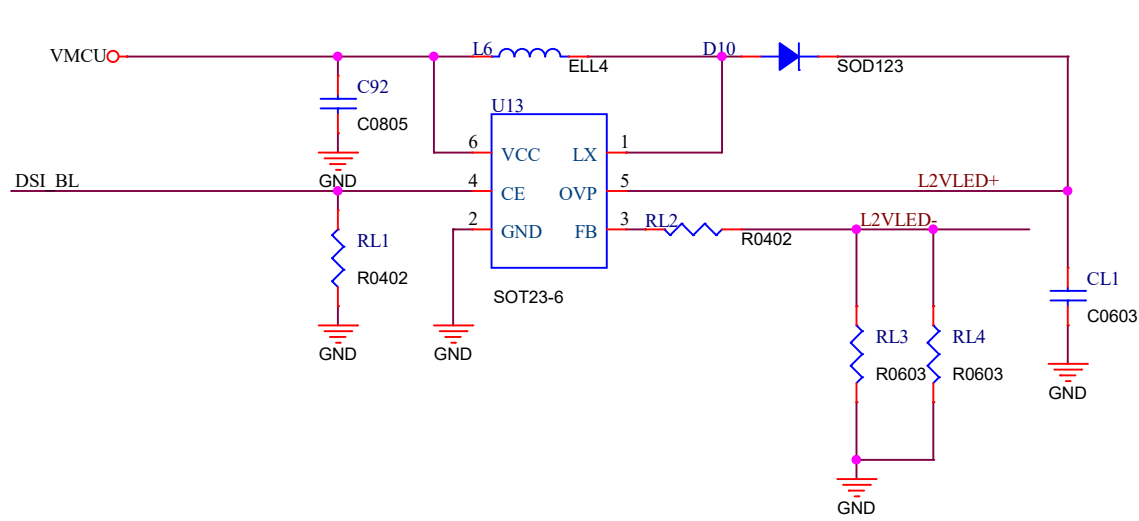
GPS_RX << GPS_RX 13



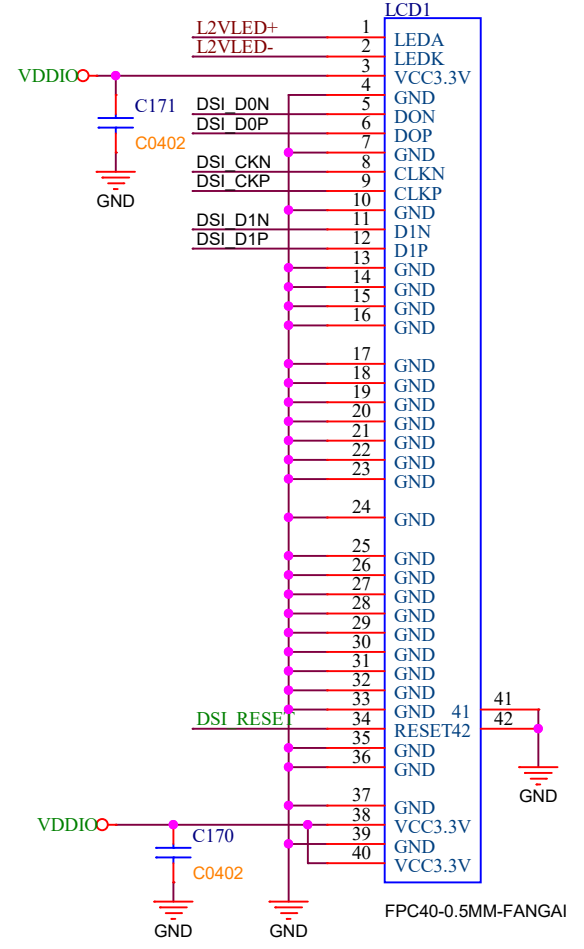


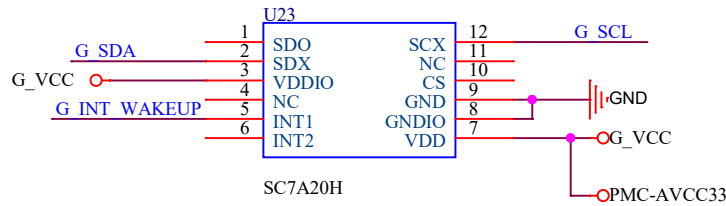
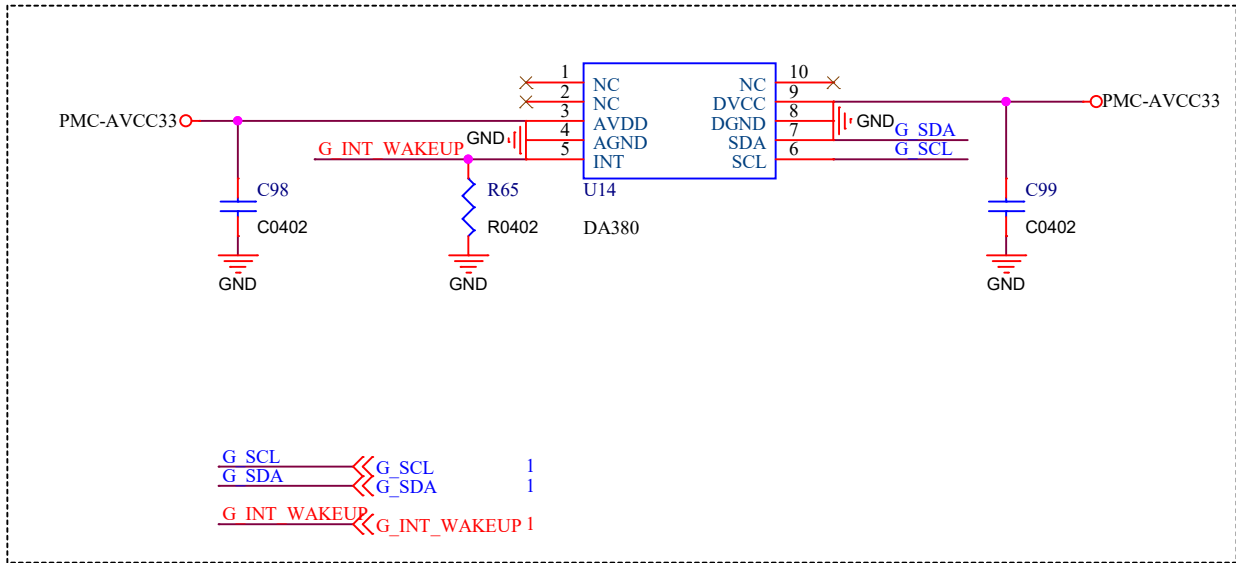
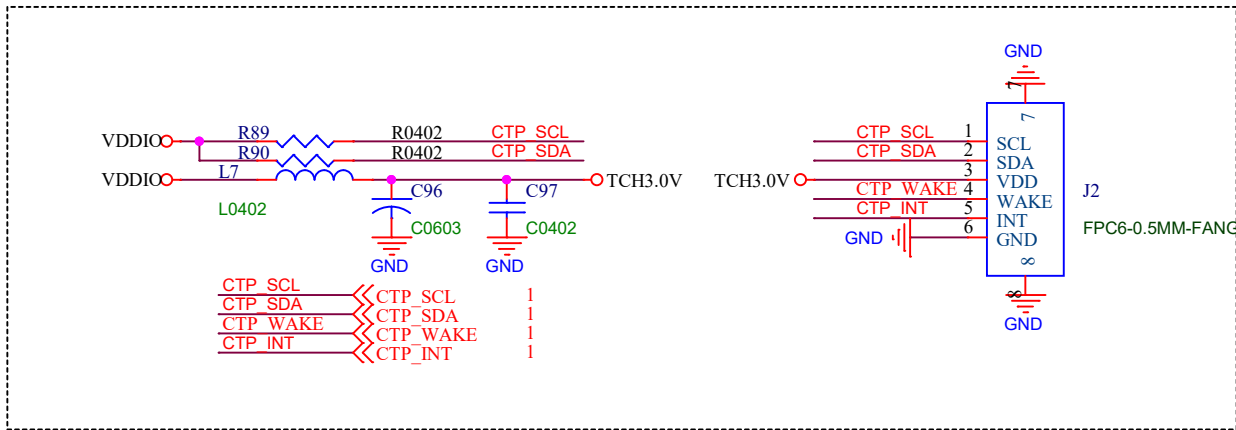


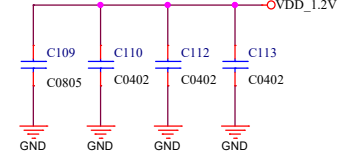


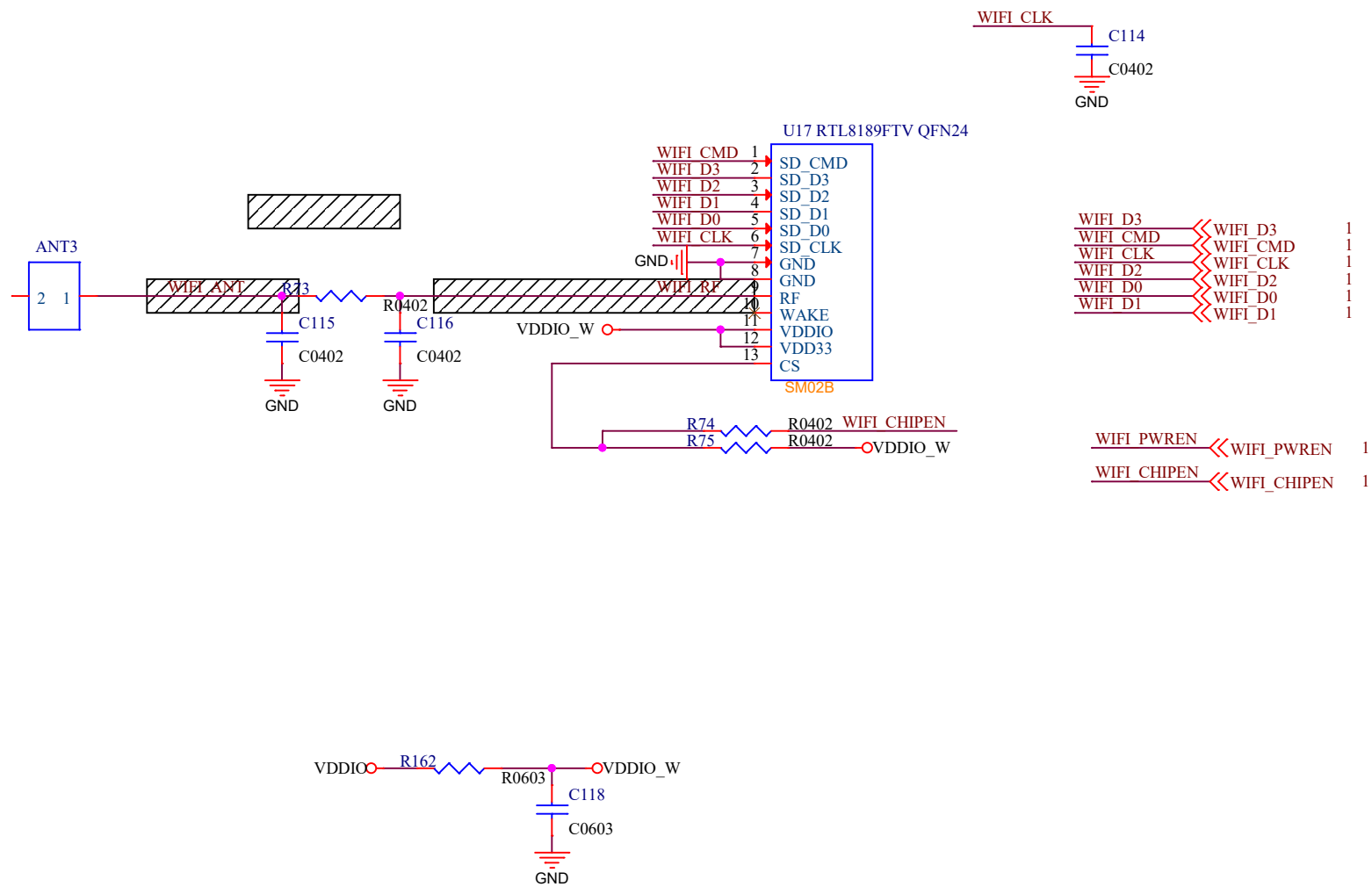


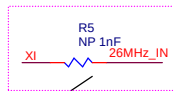
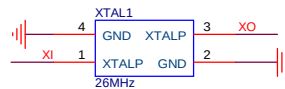
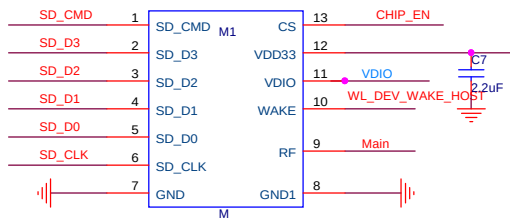
DSI D3P	DSI D3P	1
DSI D3N	DSI D3N	1
DSI D2P	DSI D2P	1
DSI D2N	DSI D2N	1
DSI D1P	DSI D1P	1
DSI D1N	DSI D1N	1
DSI D0P	DSI D0P	1
DSI D0N	DSI D0N	1
DSI CKP	DSI CKP	1
DSI CKN	DSI CKN	1
DSI RESET	DSI_RESET	1
DSI BL	DSI_BL	1
BL PWM	BL_PWM	1











Description 1
Optional 26MHz Ref. Clock for platform.
Removed XTAL1, C14.
Adjust the swing by the capacitances of R5
and C15 to meet the 89FTV requirement.

