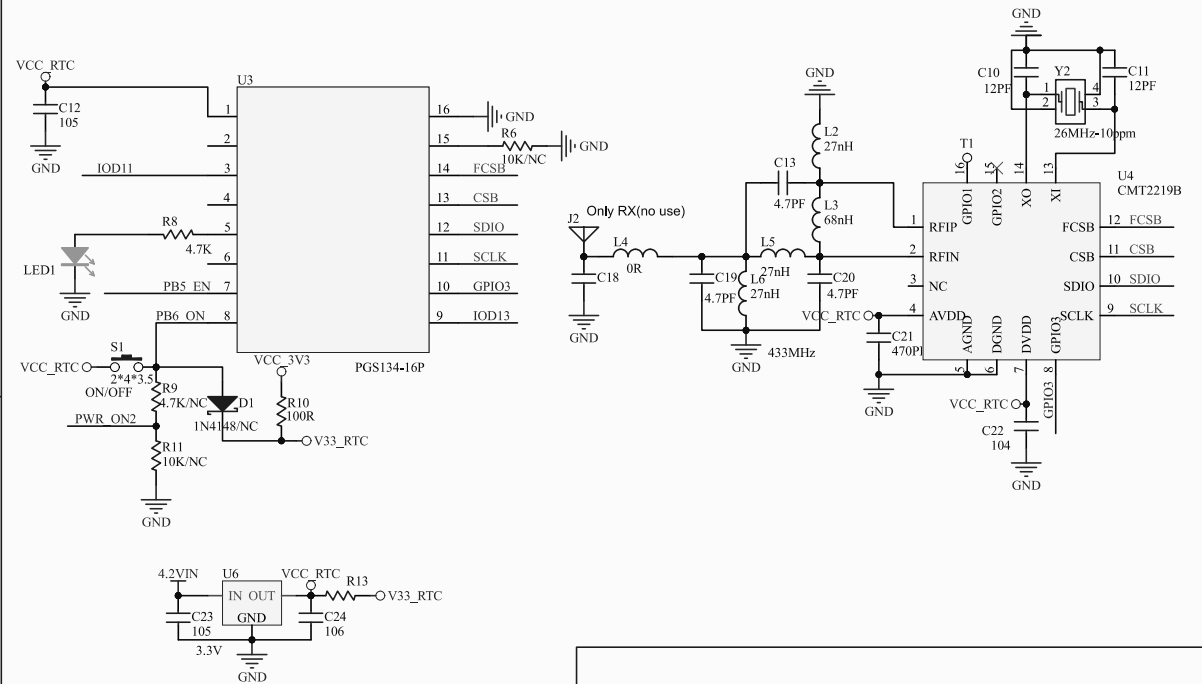
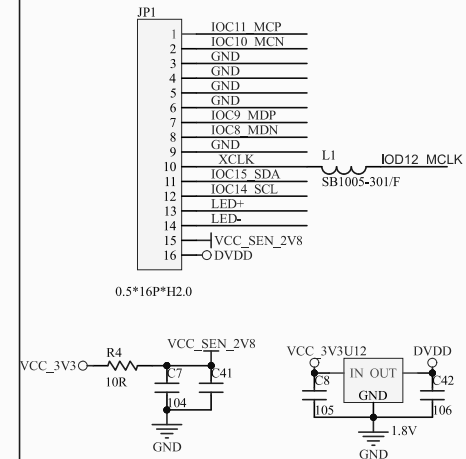
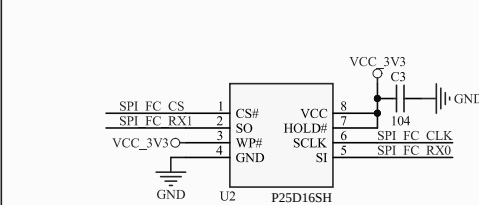
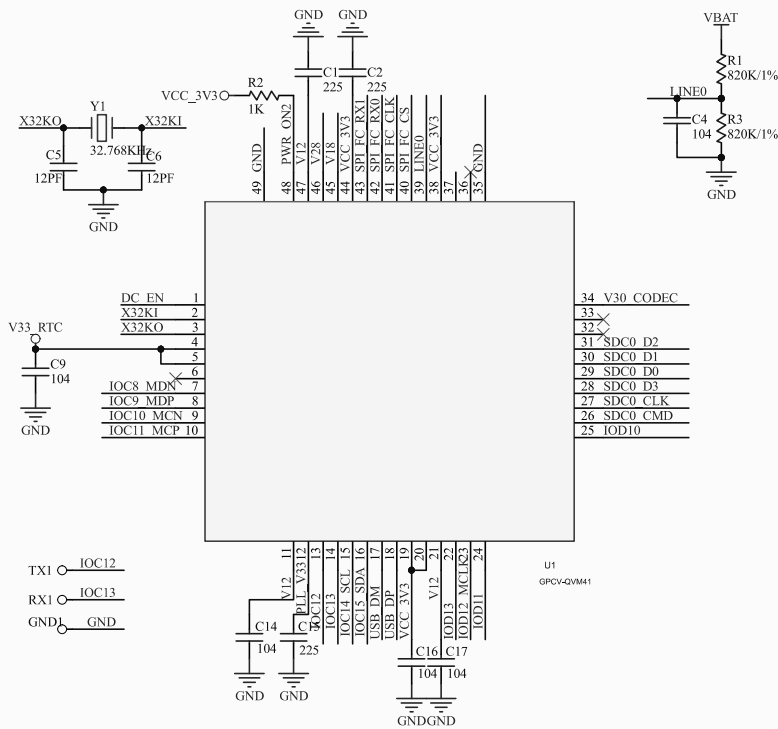
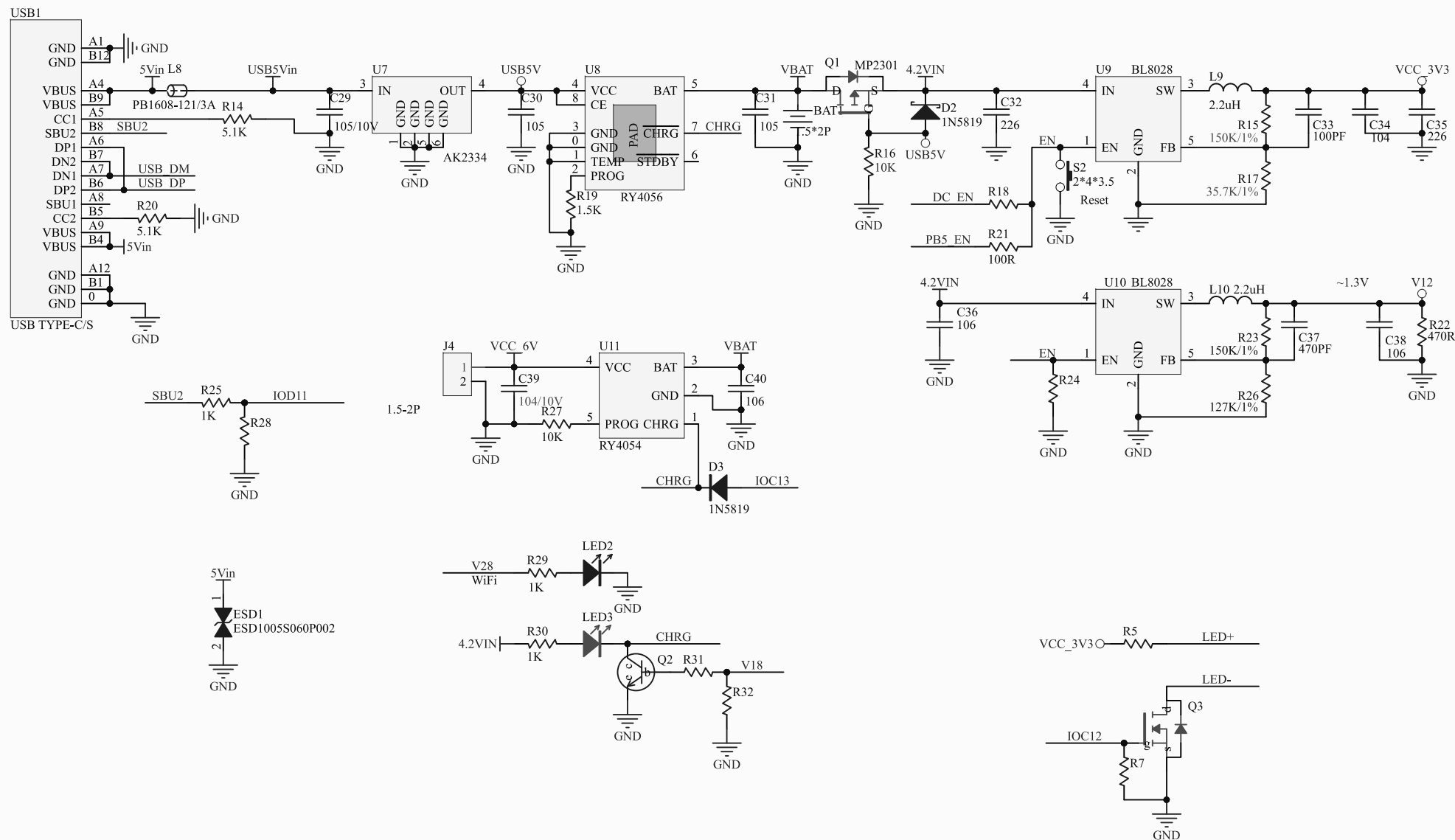
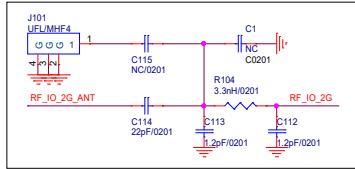


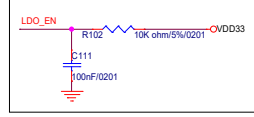




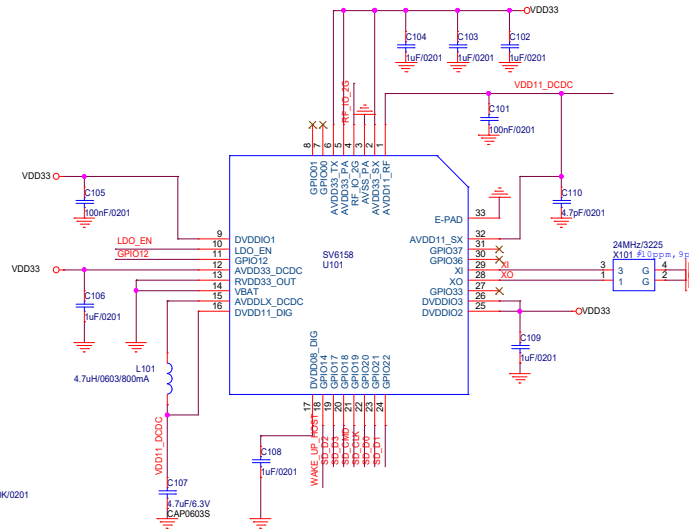
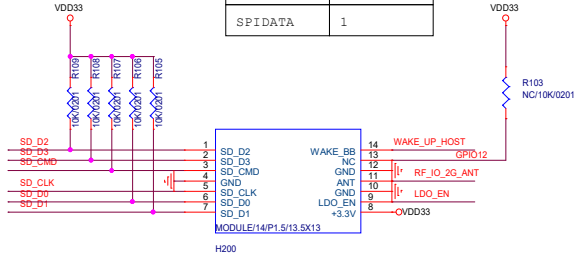
RF Matching



LDO_EN



Boot strap	GPIO12
SDIO	0 (default)
SPIDATA	1



BOM select	L101
DCDC Mode	4.7uH/600mA
LDO Mode	0R,