

November 4, 2016

Federal Communications Commission  
445 12<sup>th</sup> Street SW  
Washington, DC 20554

Subject: Request for Confidentiality  
White Stagg, LLC dba Halo Smart Labs  
FCC ID: 2AGBQLS001A

To Whom It May Concern:

White Stagg, LLC, dba Halo Smart Labs (the "Company"), is submitting an Application to the FCC Commission for FCC Certification for Halo and Halo+ (the "Devices"). In conjunction with that submission and pursuant to the provisions of Sections 0.457 and 0.459 of the Commission's rules (47 CFR §§ 0.457, 0.459), the Company hereby requests the Commission to withhold the following attachments to that submission as confidential documents from public disclosure indefinitely (Long-Term Confidentiality):

- Schematic Diagrams
- Block Diagrams
- Theory of Operation

The above-mentioned documents contain detailed system and equipment descriptions that the Company considers proprietary information with respect to operation of the Devices and this information is not publicly available. The public disclosure of the above documents might be harmful to the Company, and might give the Company's competitors an unfair advantage in the market.

In addition to above-mentioned documents, pursuant to Public Notice DA 04-1705 of the Commission's policy, in order to comply with the marketing regulations in 47 CFR §2.803 and the importation rules in 47 CFR §2.1204, while ensuring that business-sensitive information remains confidential until the actual marketing of newly authorized devices, the Company requests the Commission to grant short-term confidentiality with respect to the following attachments until February 1, 2017:

- External Photos
- Internal Photos
- Test Setup Photos
- User Manual

It is our understanding that all measurement test reports, FCC ID label format and correspondence during certification review process cannot be granted confidentiality, and that those documents will be available for public review once the grant of equipment authorization is issued.

Sincerely,



Stephen Sheppard  
Hardware Design Engineer