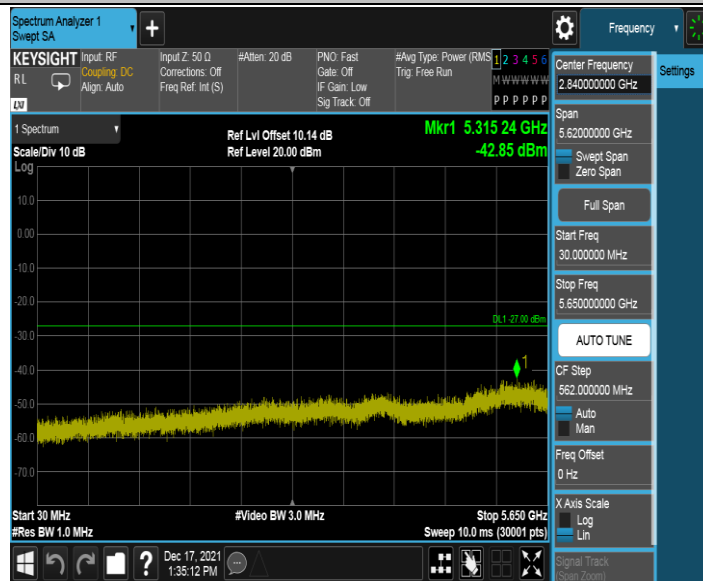
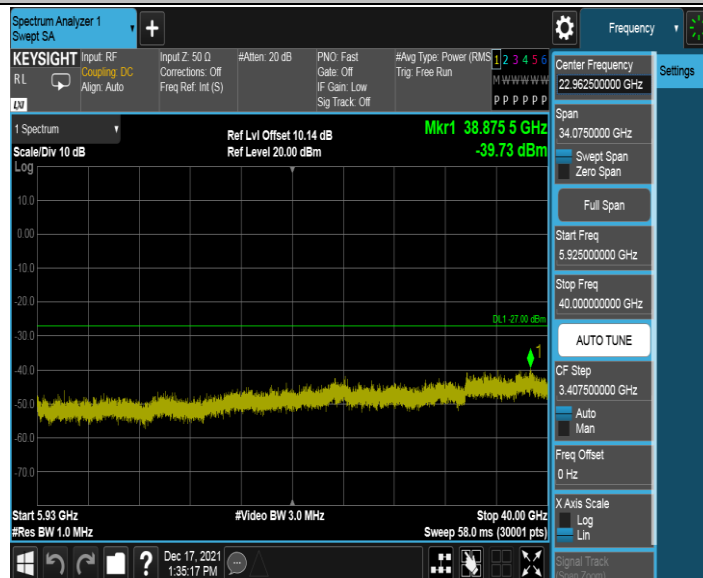


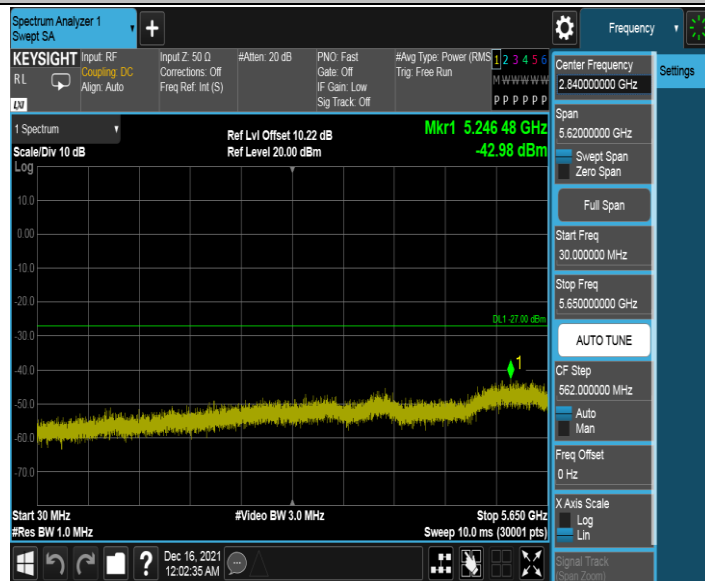
# 11N40SISO\_Ant1\_5795\_30~5650



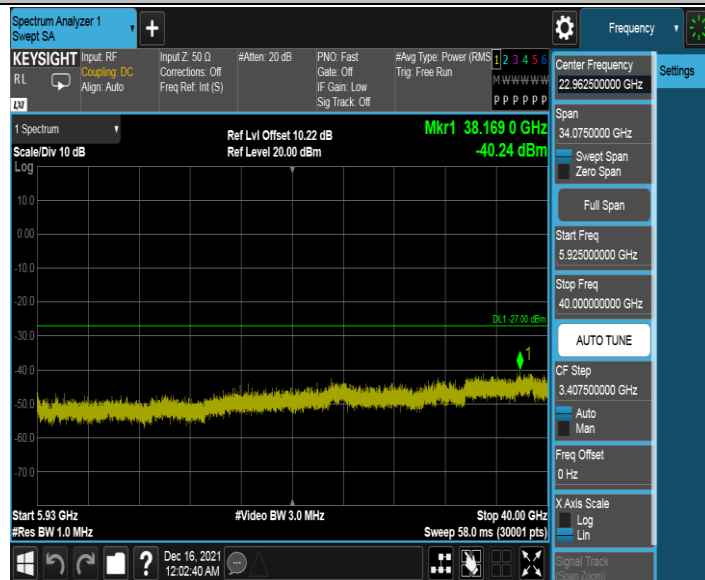
# 11N40SISO\_Ant1\_5795\_5925~40000



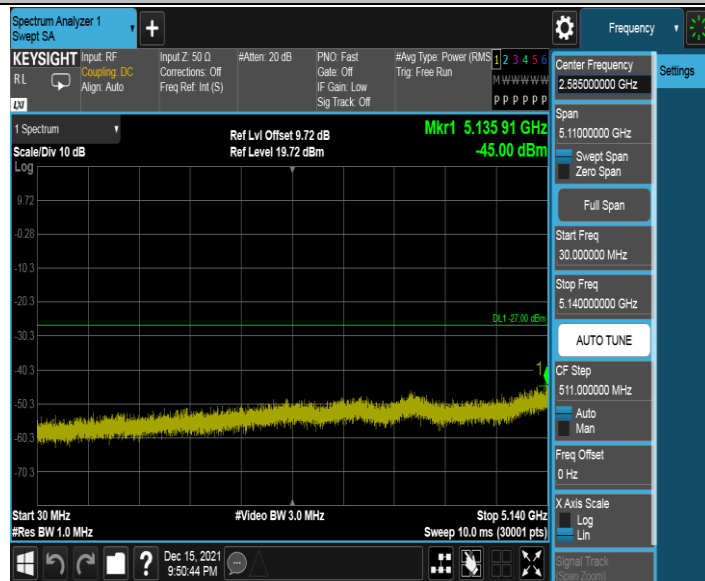
# 11N40SISO\_Ant2\_5795\_30~5650



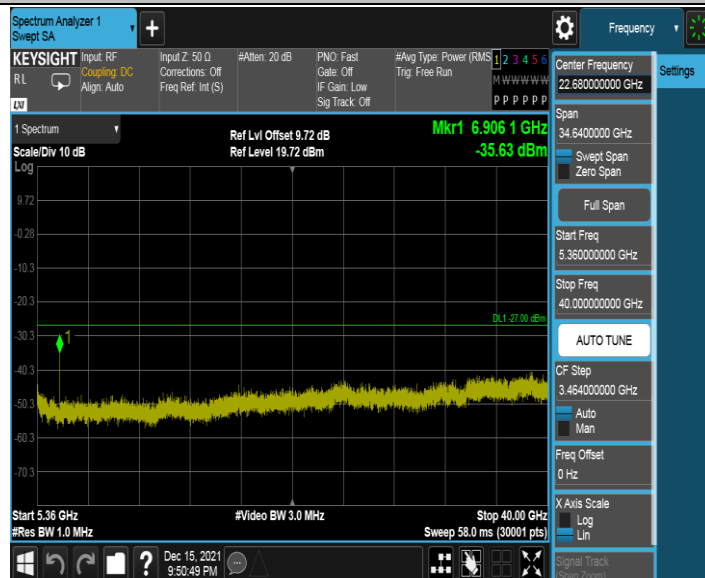
# 11N40SISO\_Ant2\_5795\_5925~40000



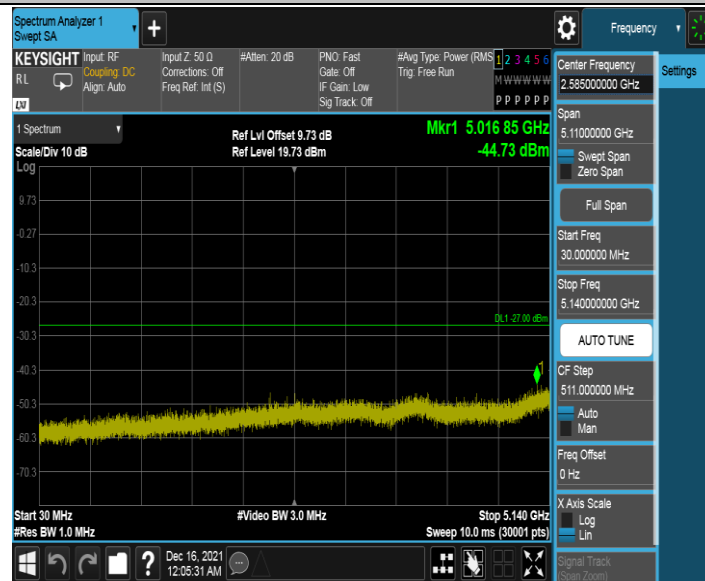
# 11AC20SISO\_Ant1\_5180\_30~5140



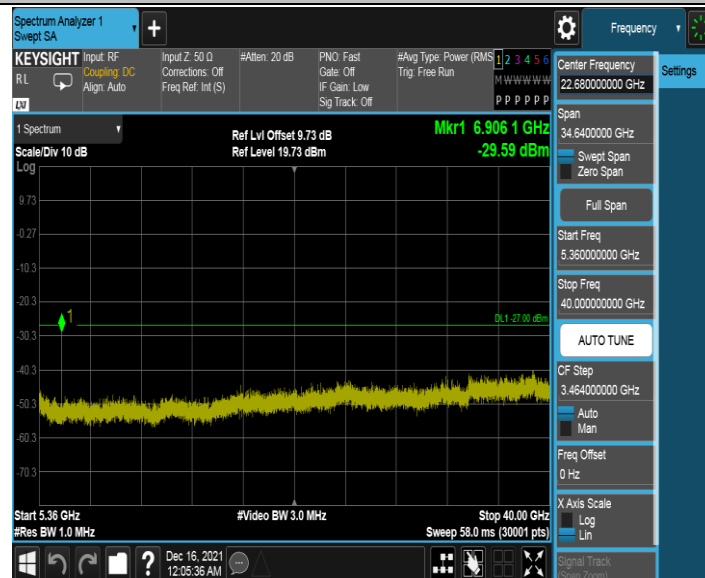
# 11AC20SISO\_Ant1\_5180\_5360~40000



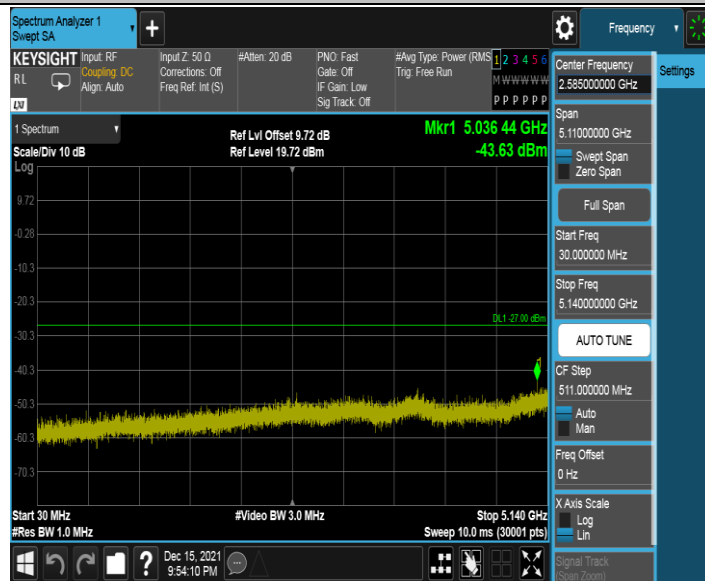
# 11AC20SISO\_Ant2\_5180\_30~5140



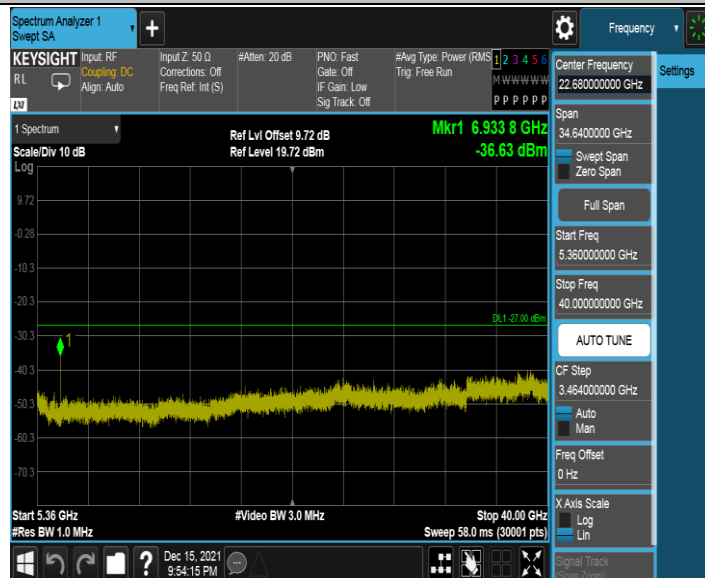
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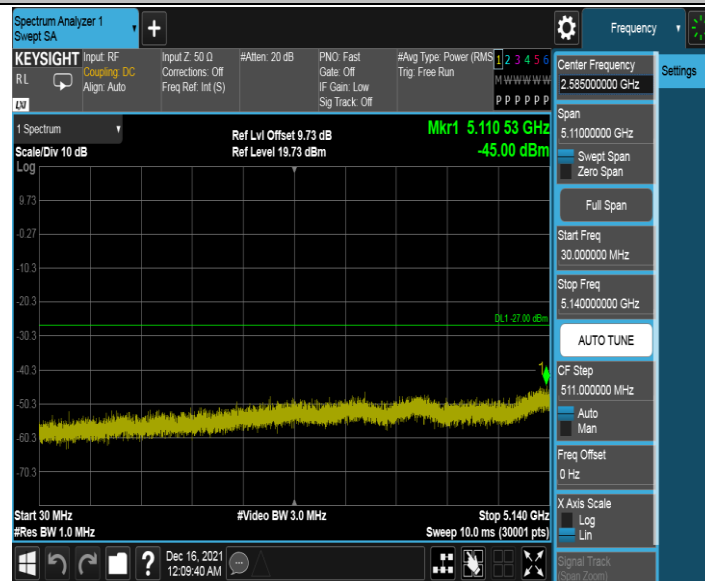
# 11AC20SISO\_Ant1\_5200\_30~5140



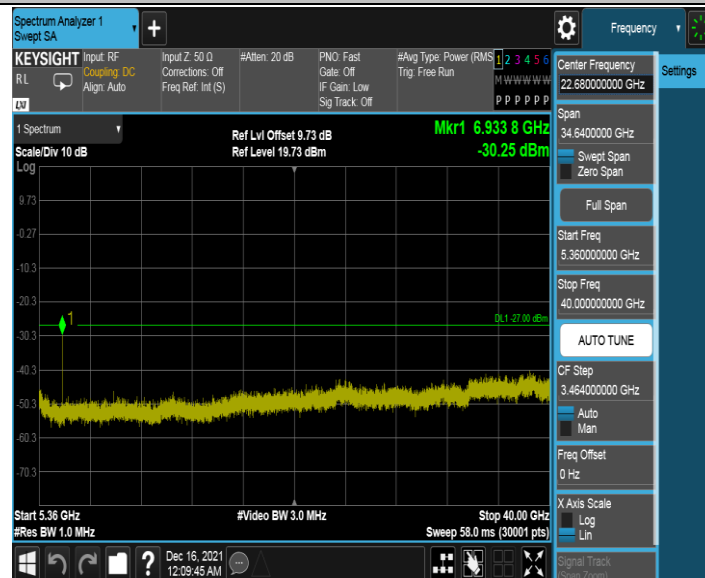
# 11AC20SISO\_Ant1\_5200\_5360~40000



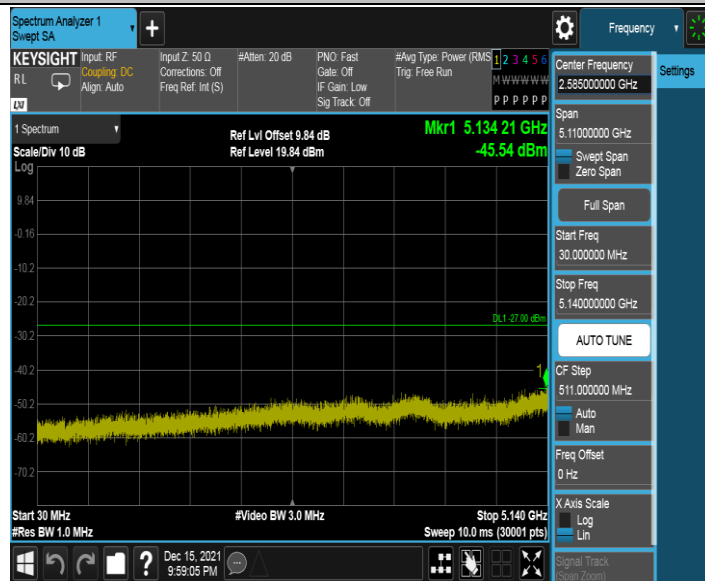
# 11AC20SISO\_Ant2\_5200\_30~5140



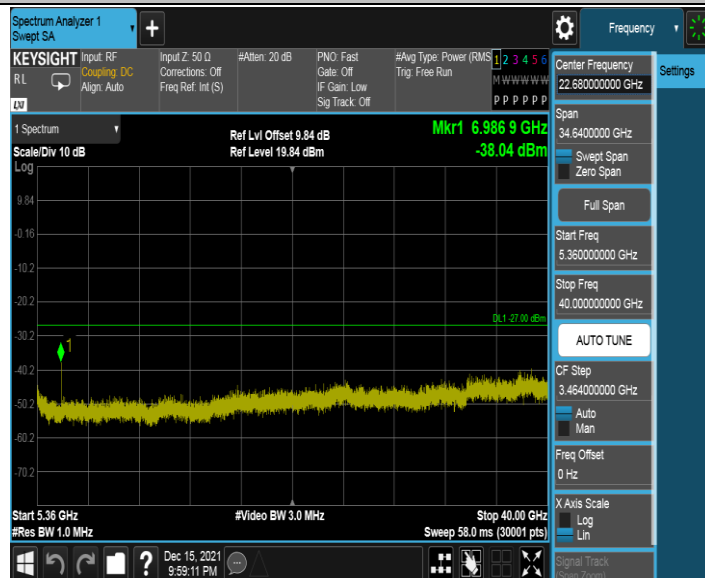
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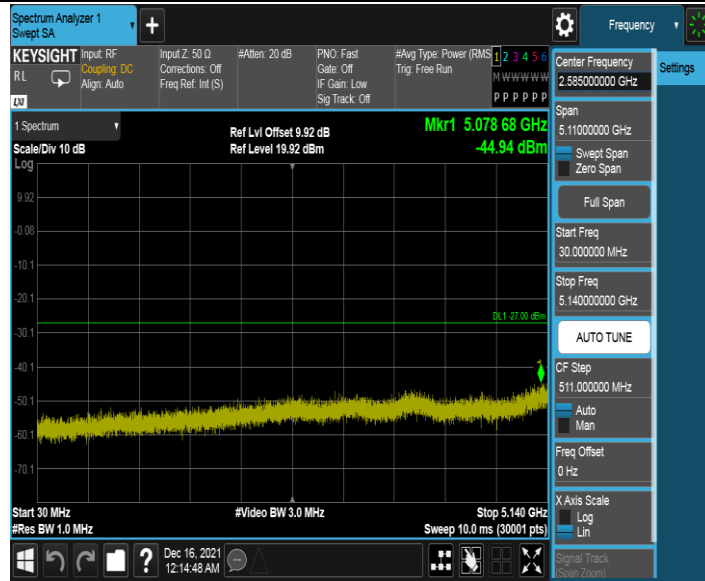
# 11AC20SISO\_Ant1\_5240\_30~5140



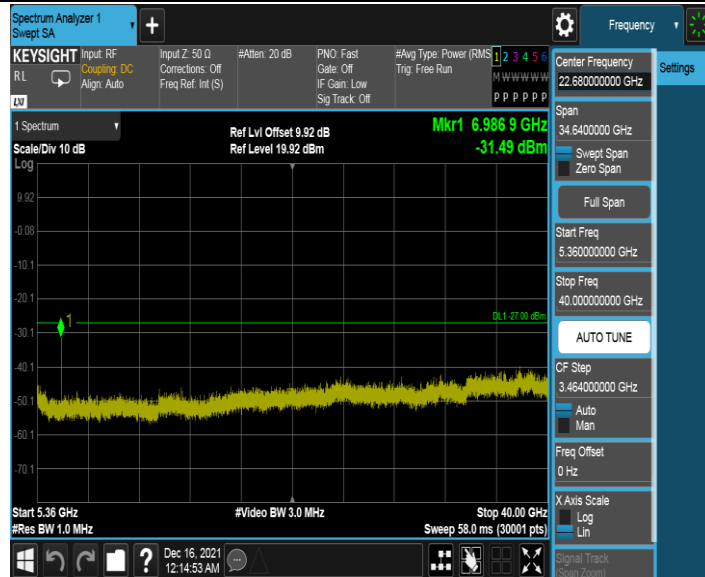
# 11AC20SISO\_Ant1\_5240\_5360~40000



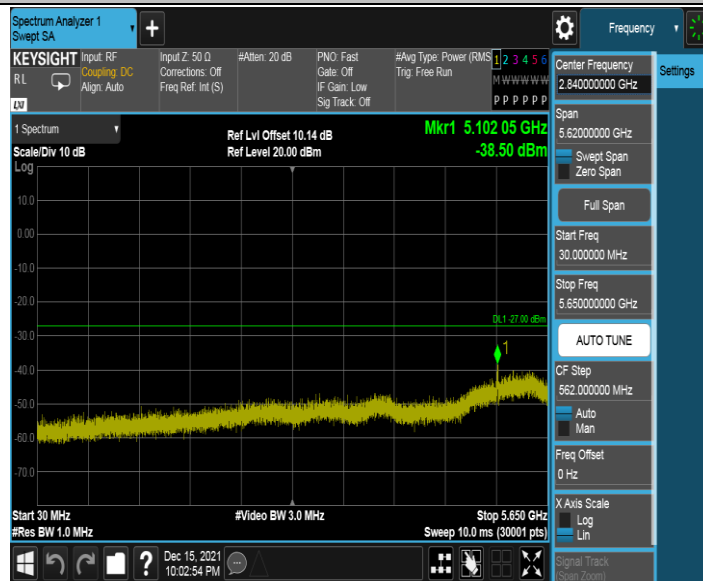
# 11AC20SISO\_Ant2\_5240\_30~5140



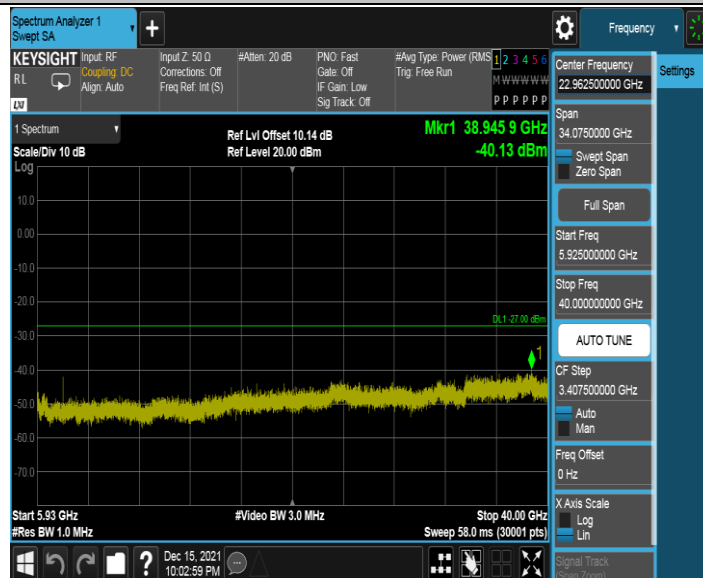
# 11AC20SISO\_Ant2\_5240\_5360~40000



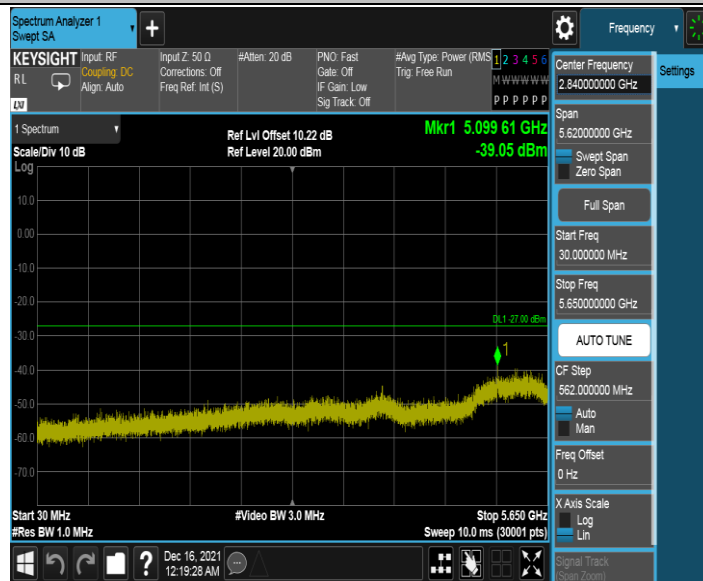
# 11AC20SISO\_Ant1\_5745\_30~5650



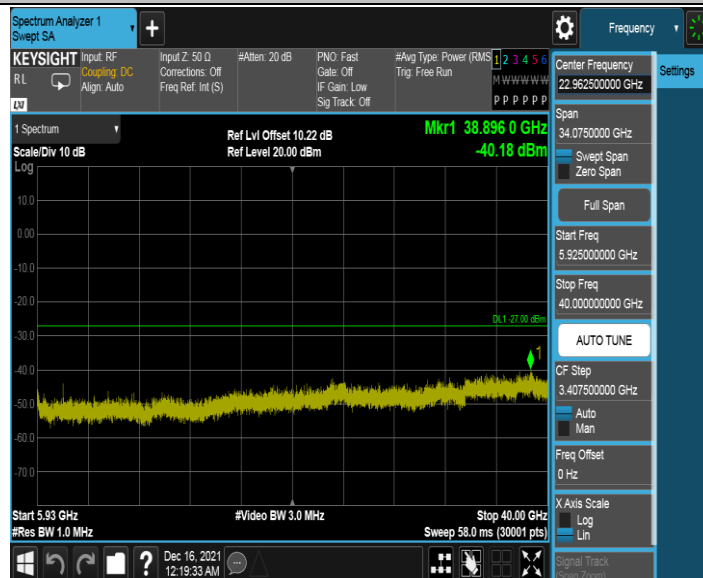
# 11AC20SISO\_Ant1\_5745\_5925~40000



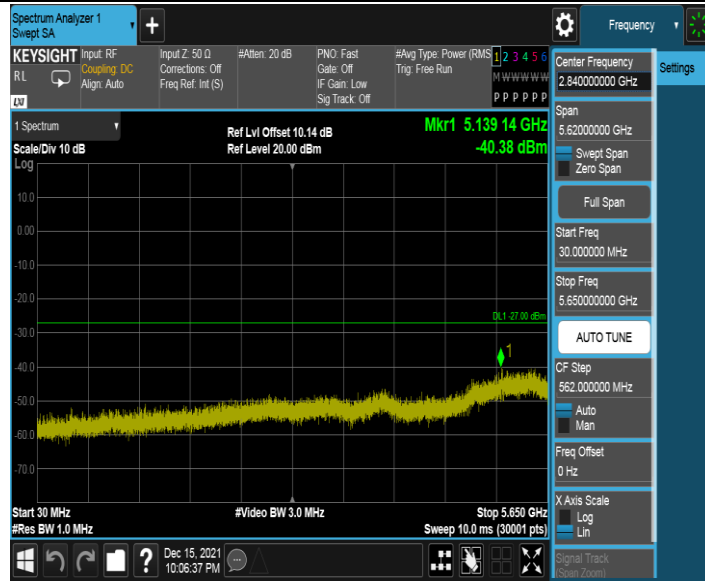
# 11AC20SISO\_Ant2\_5745\_30~5650



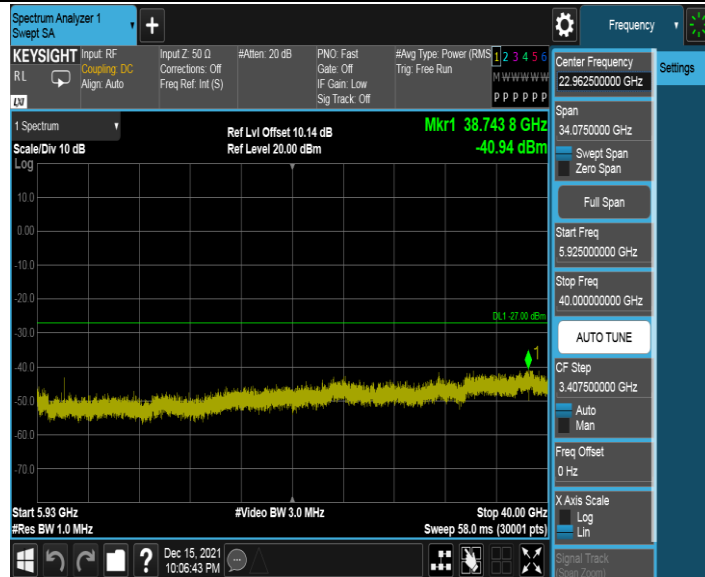
# 11AC20SISO\_Ant2\_5745\_5925~40000



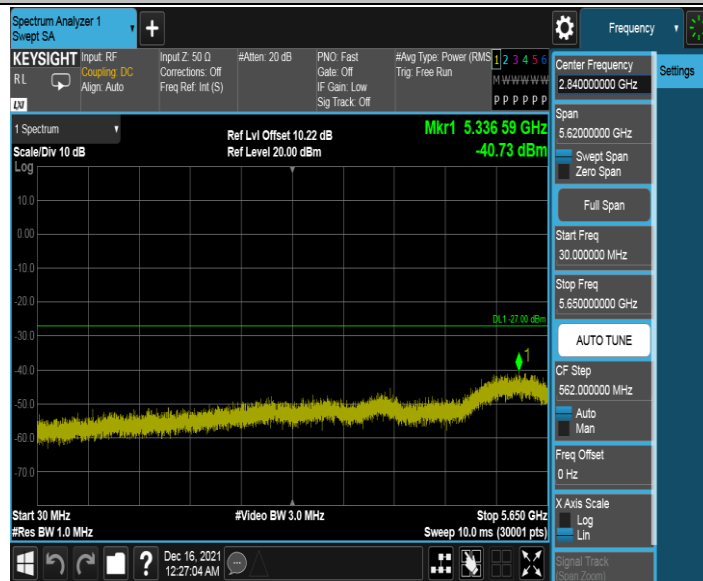
# 11AC20SISO\_Ant1\_5785\_30~5650



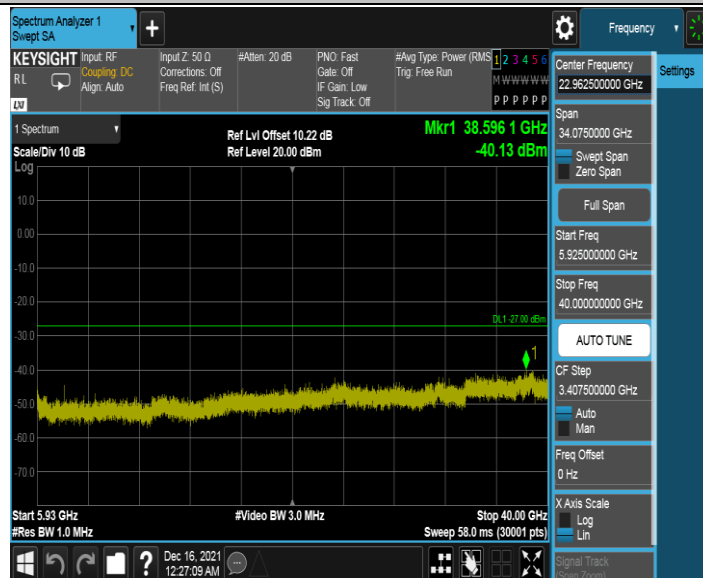
# 11AC20SISO\_Ant1\_5785\_5925~40000



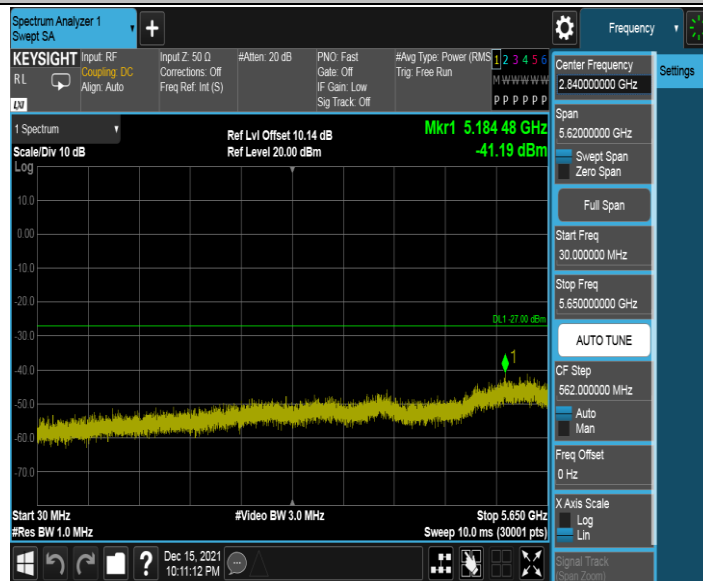
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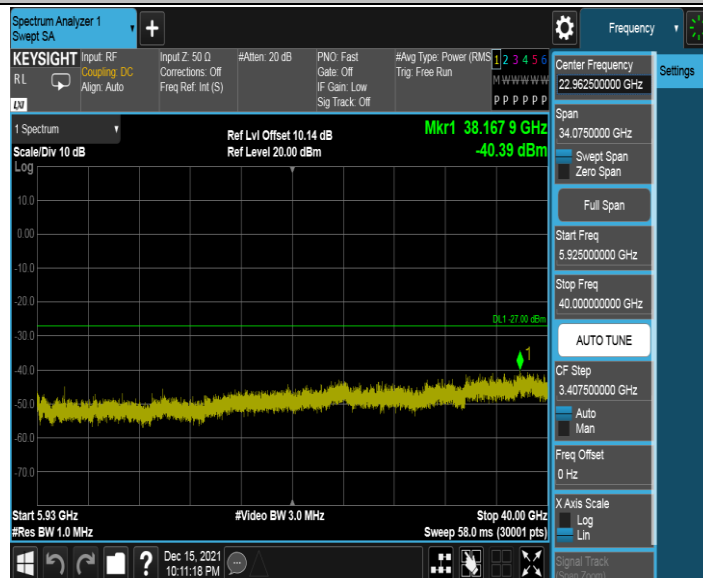
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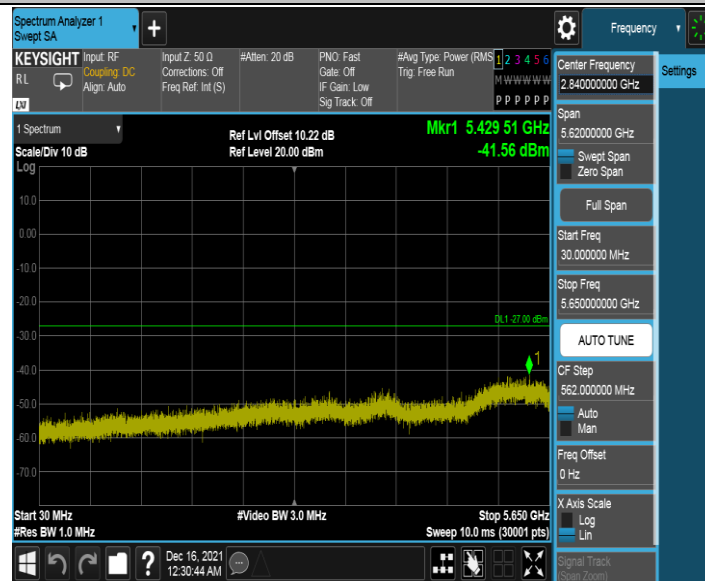
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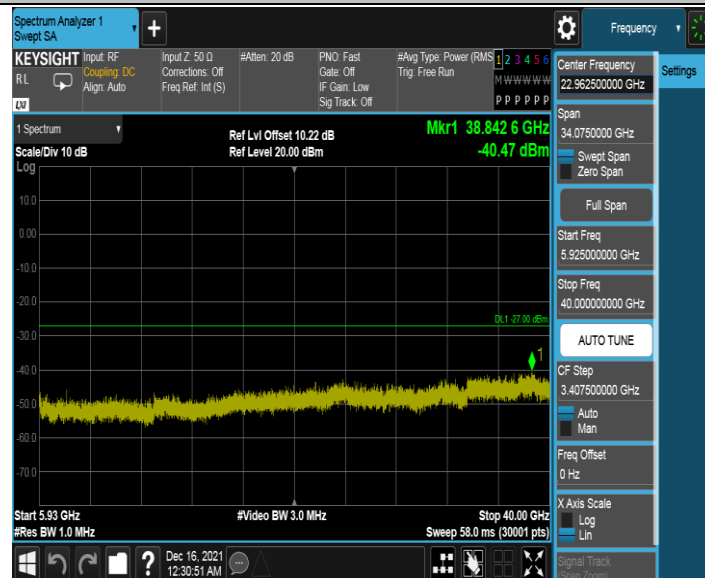
# 11AC20SISO\_Ant1\_5825\_5925~40000



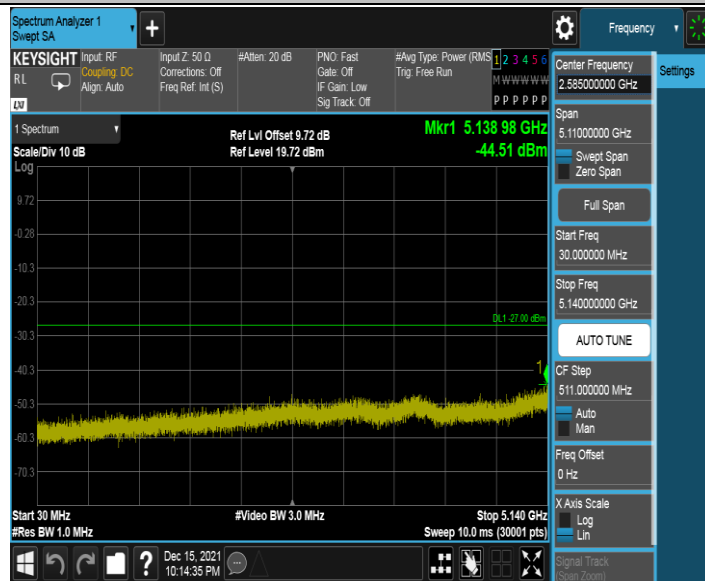
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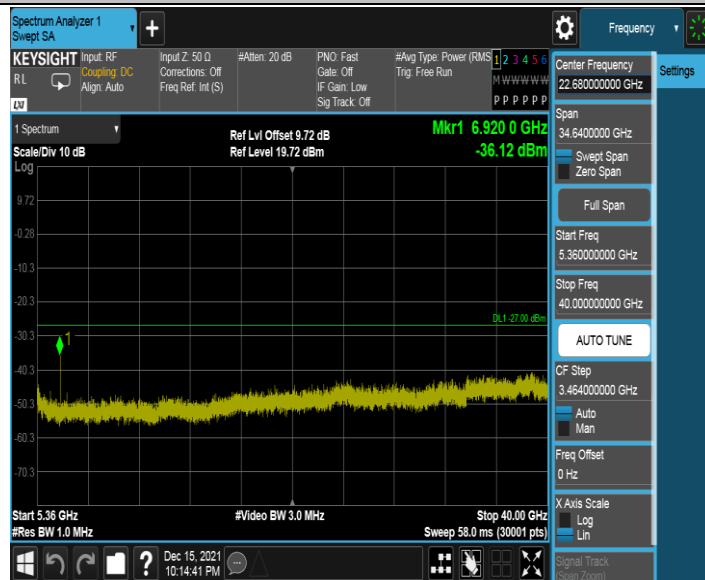
# 11AC20SISO\_Ant2\_5825\_5925~40000



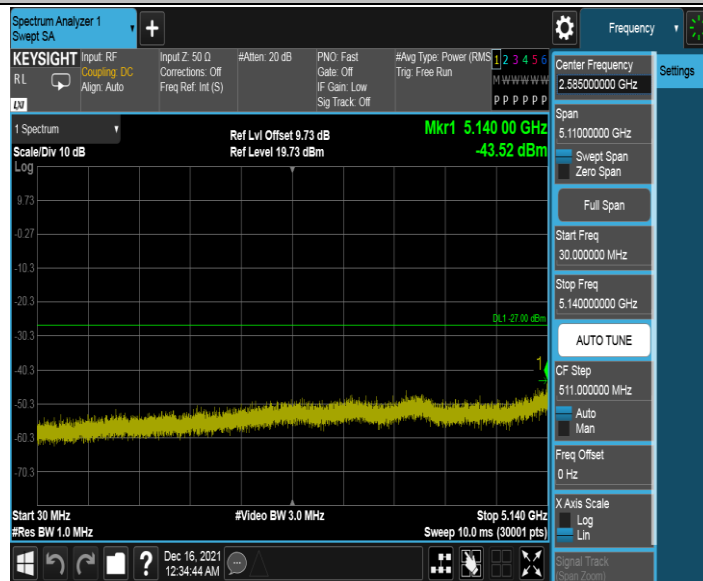
# 11AC40SISO\_Ant1\_5190\_30~5140



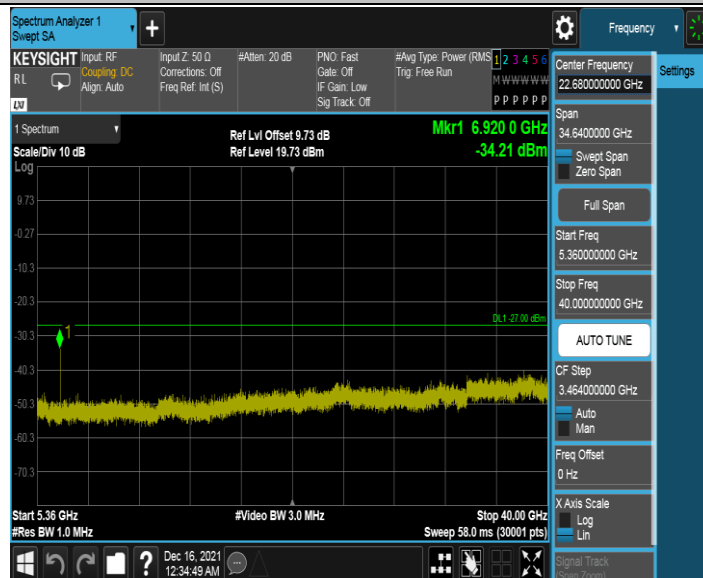
# 11AC40SISO\_Ant1\_5190\_5360~40000



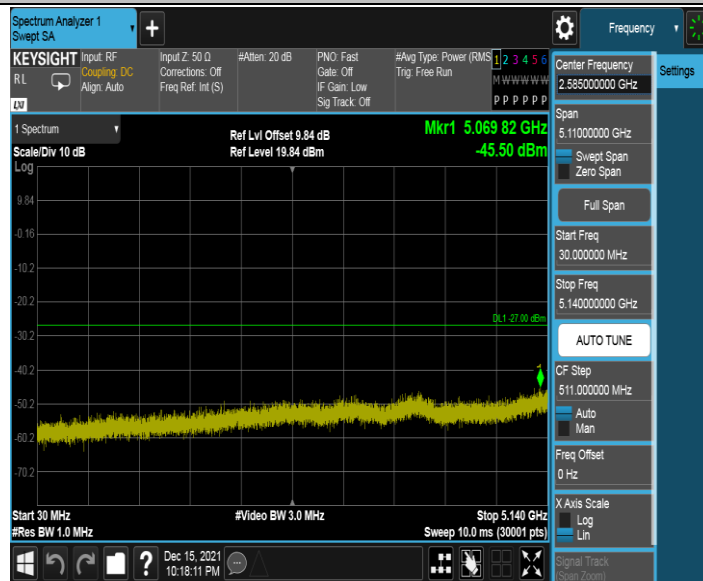
# 11AC40SISO\_Ant2\_5190\_30~5140



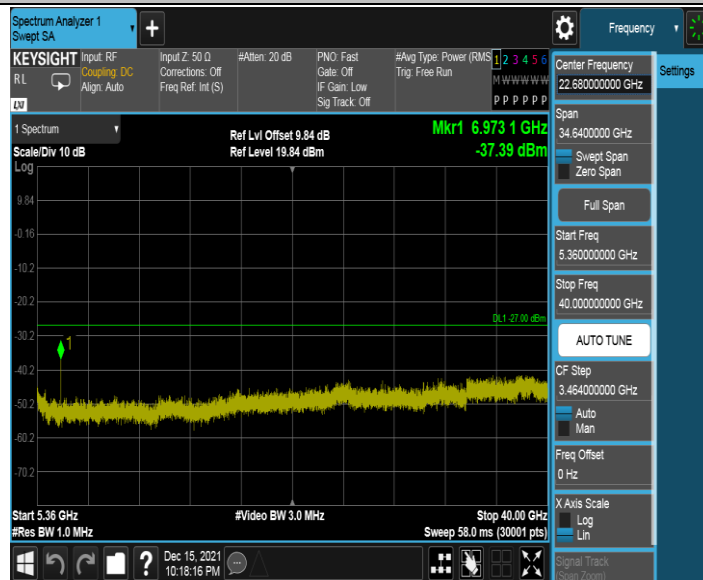
# 11AC40SISO\_Ant2\_5190\_5360~40000



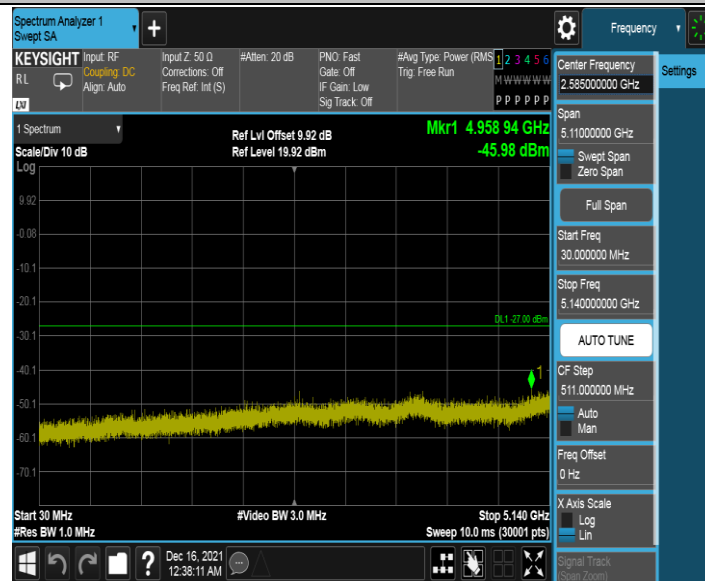
# 11AC40SISO\_Ant1\_5230\_30~5140



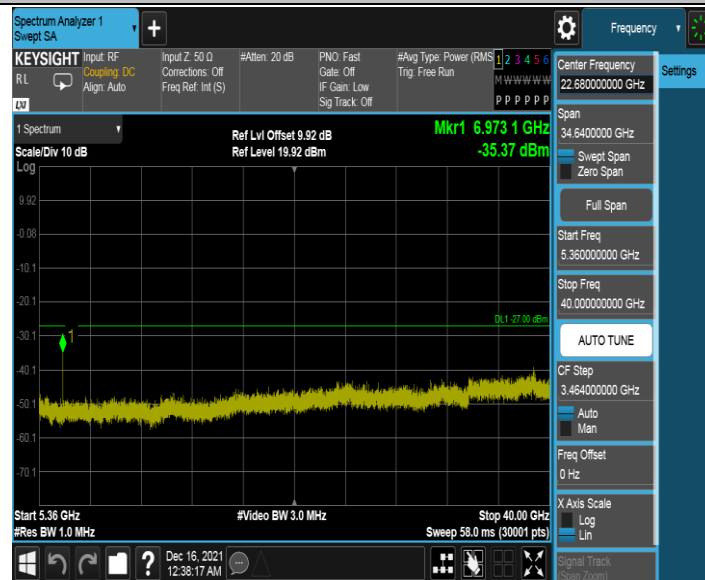
# 11AC40SISO\_Ant1\_5230\_5360~40000



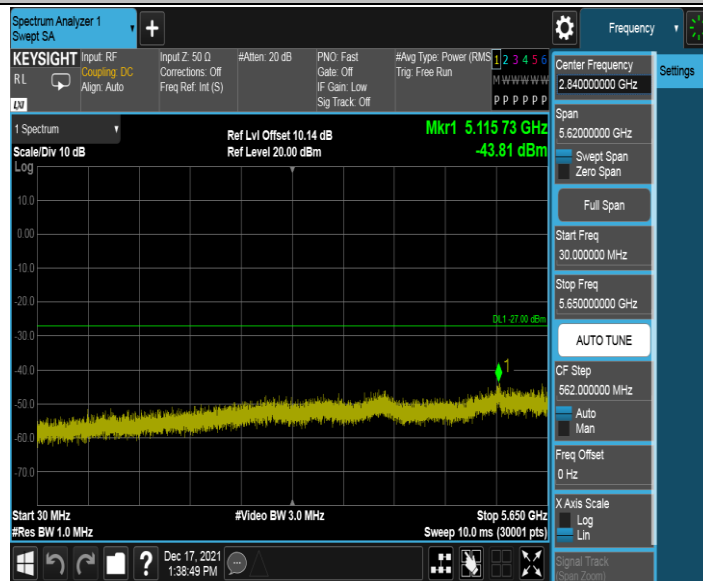
# 11AC40SISO\_Ant2\_5230\_30~5140



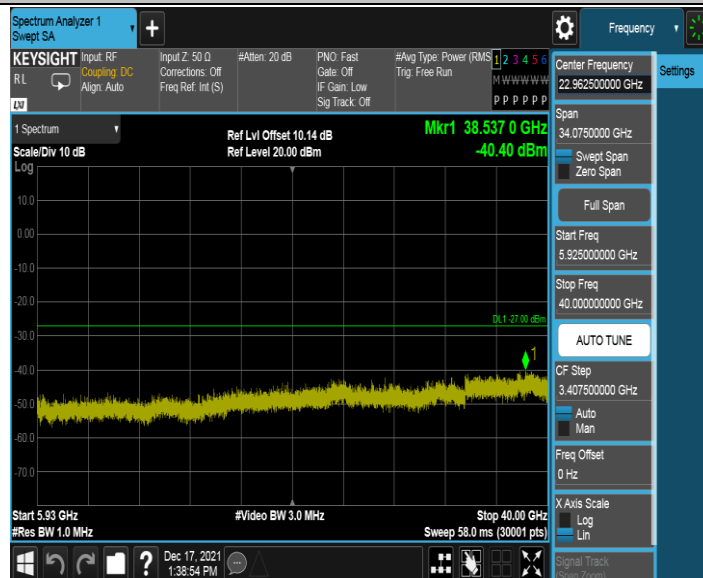
# 11AC40SISO\_Ant2\_5230\_5360~40000



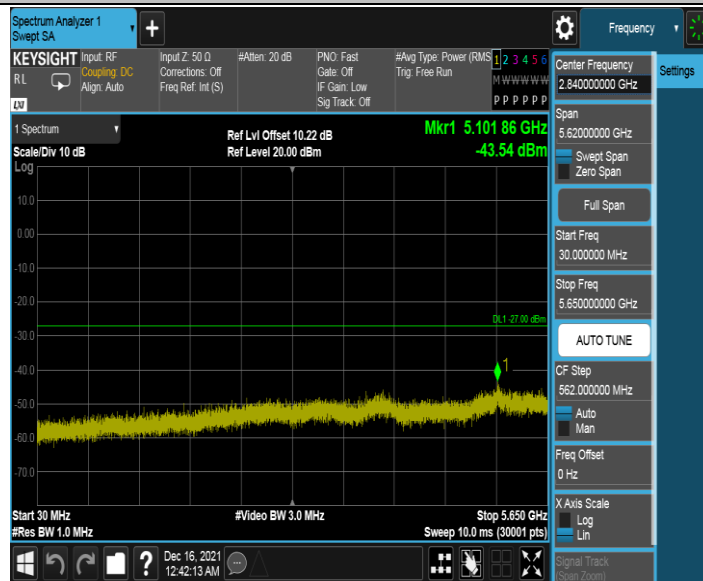
# 11AC40SISO\_Ant1\_5755\_30~5650



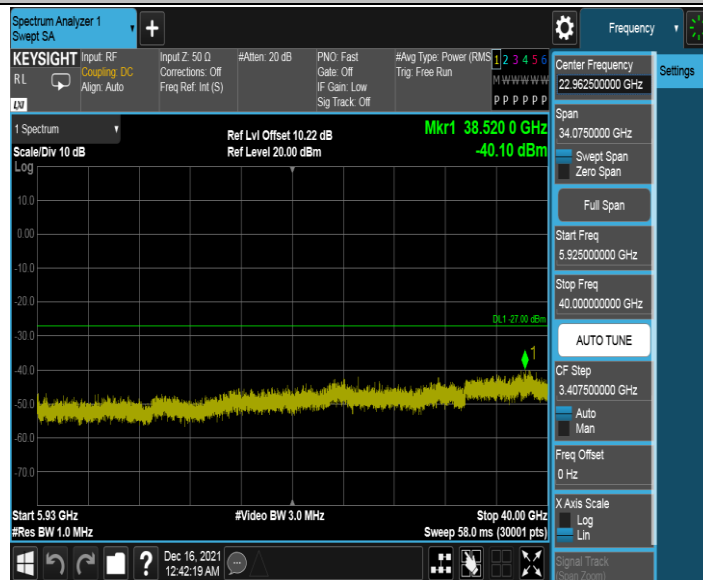
# 11AC40SISO\_Ant1\_5755\_5925~40000



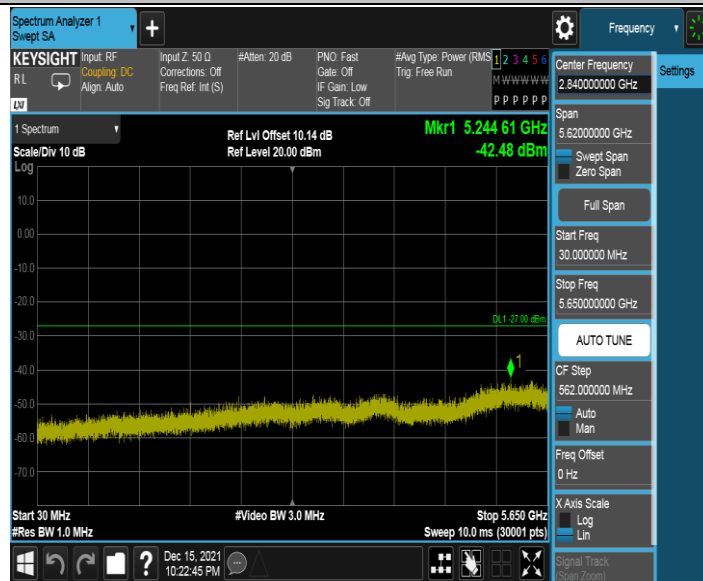
# 11AC40SISO\_Ant2\_5755\_30~5650



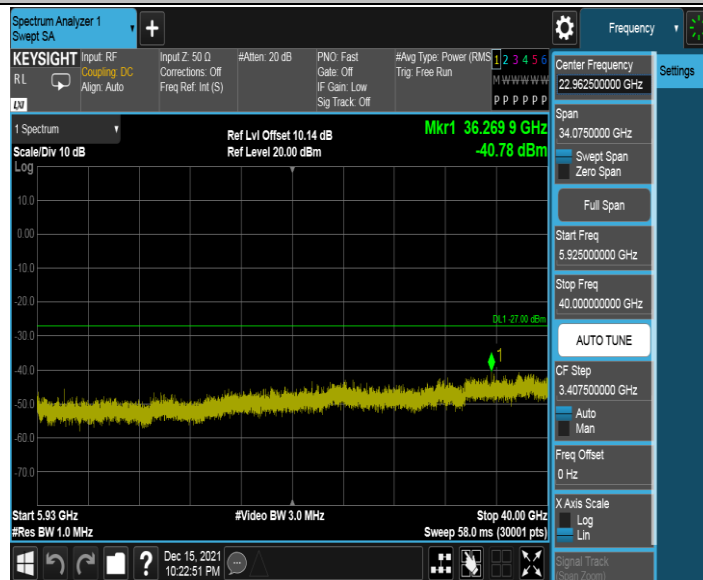
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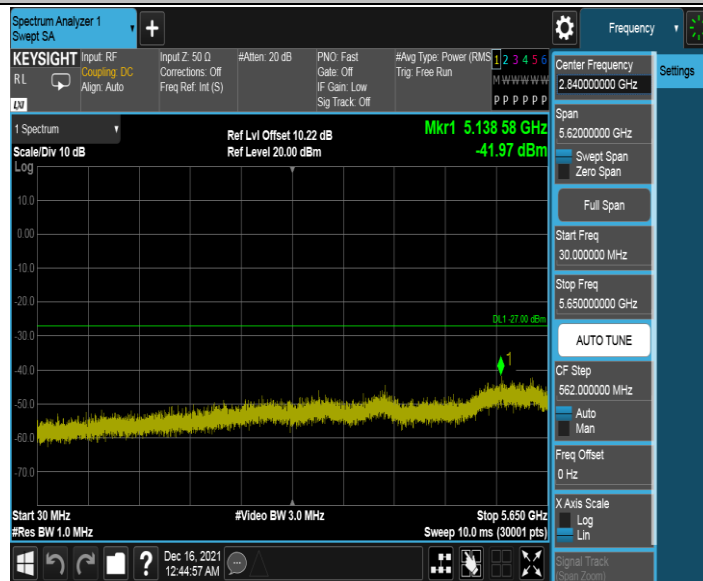
# 11AC40SISO\_Ant1\_5795\_30~5650



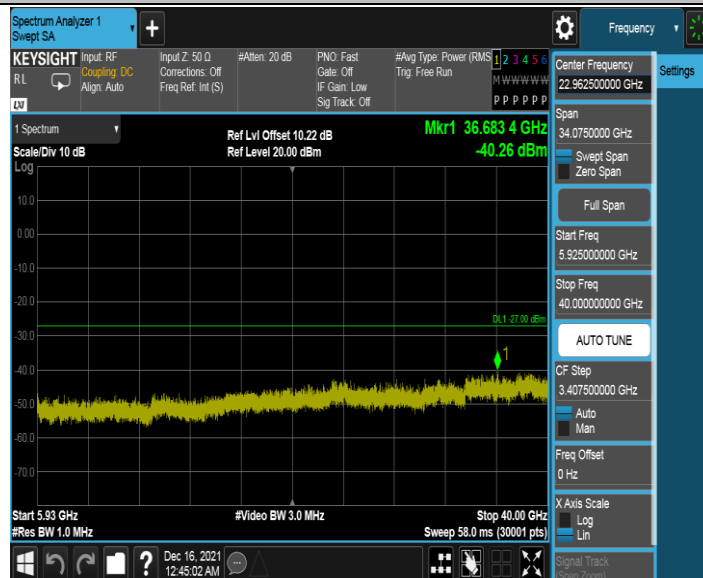
# 11AC40SISO\_Ant1\_5795\_5925~40000



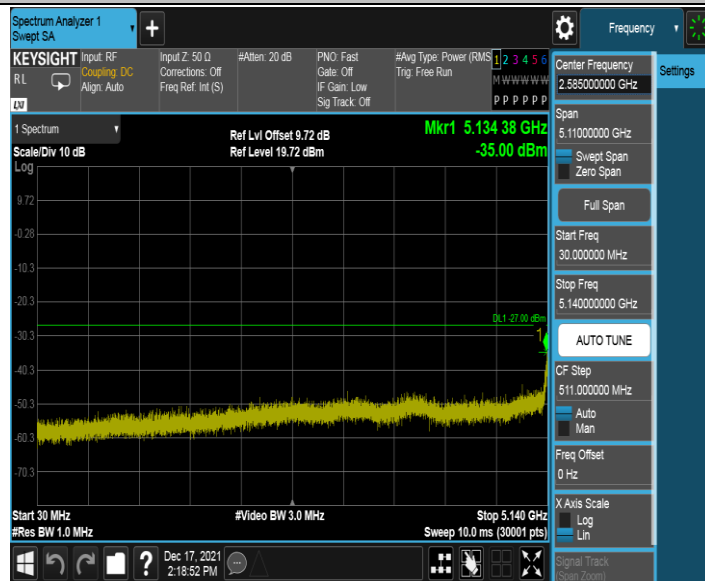
# 11AC40SISO\_Ant2\_5795\_30~5650



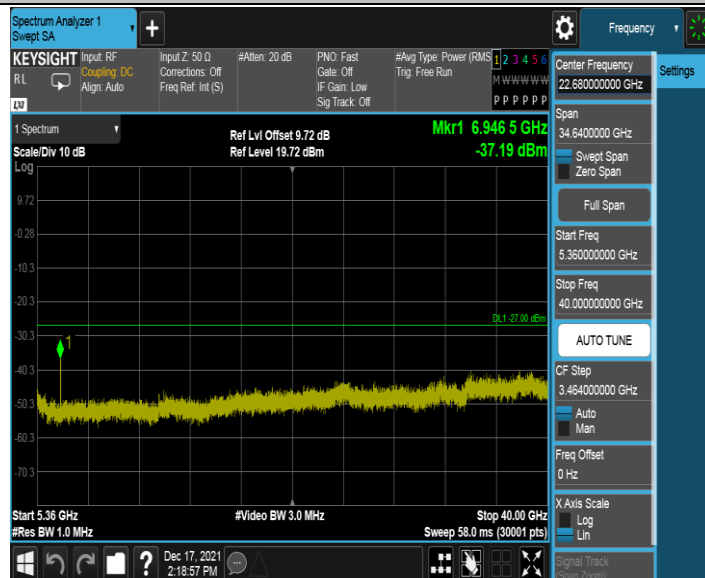
# 11AC40SISO\_Ant2\_5795\_5925~40000



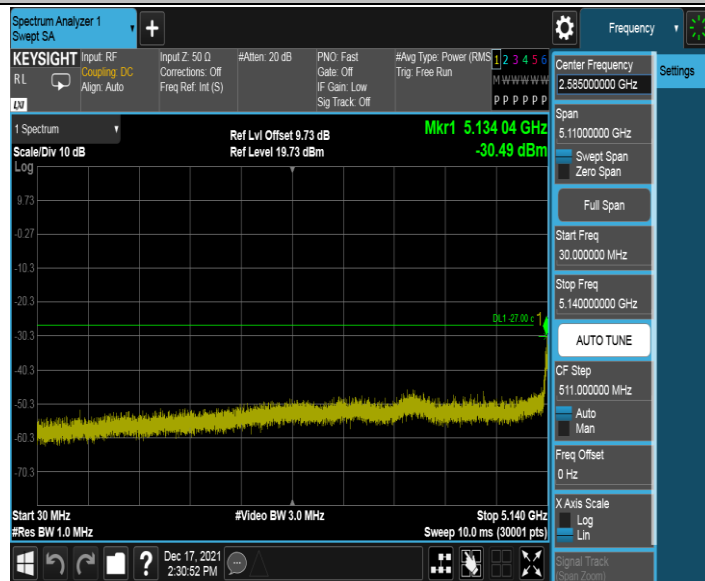
# 11AC80SISO\_Ant1\_5210\_30~5140



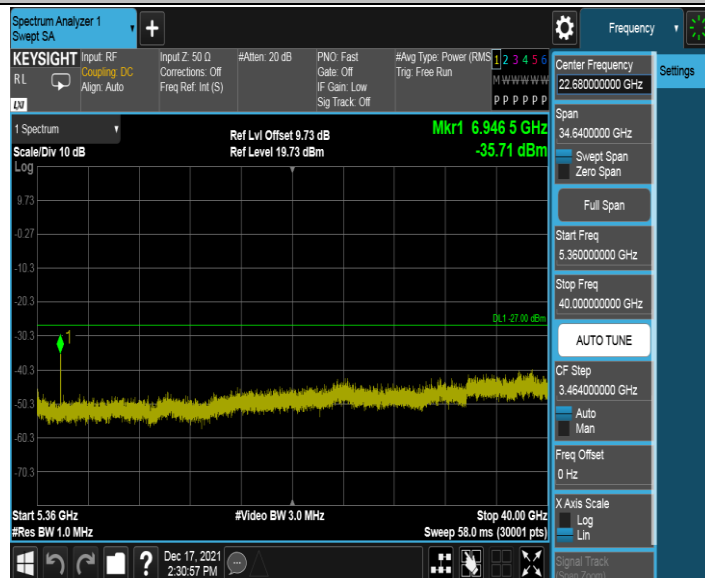
# 11AC80SISO\_Ant1\_5210\_5360~40000



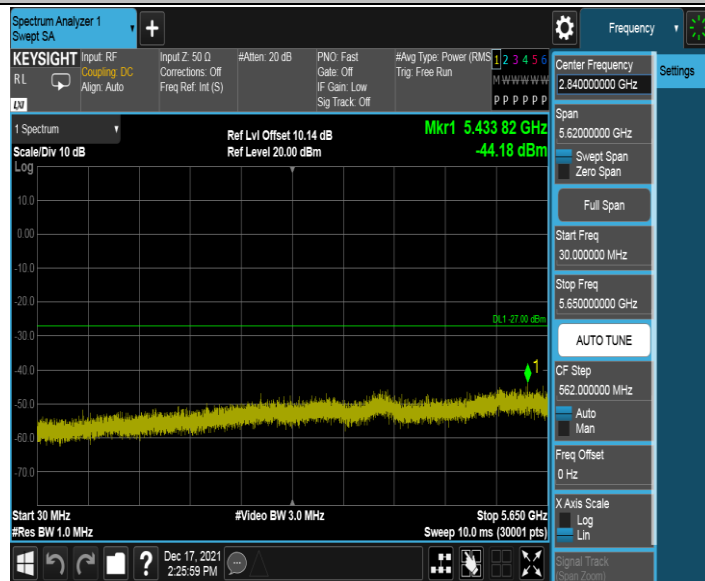
# 11AC80SISO\_Ant2\_5210\_30~5140



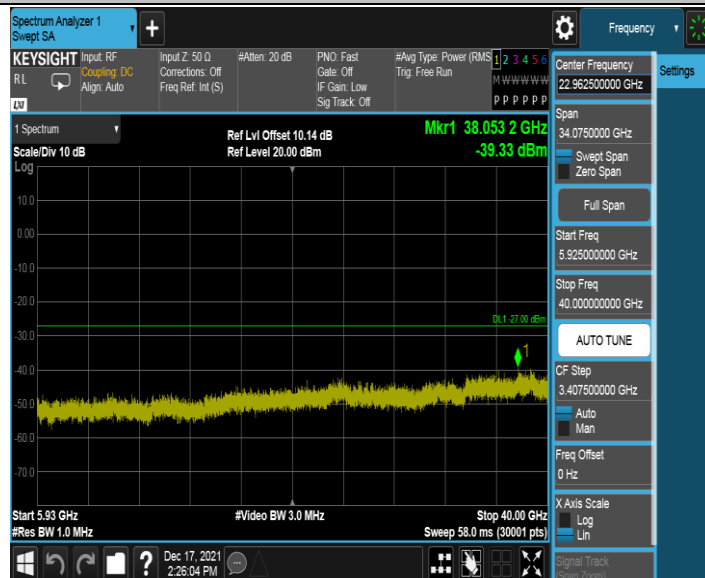
# 11AC80SISO\_Ant2\_5210\_5360~40000



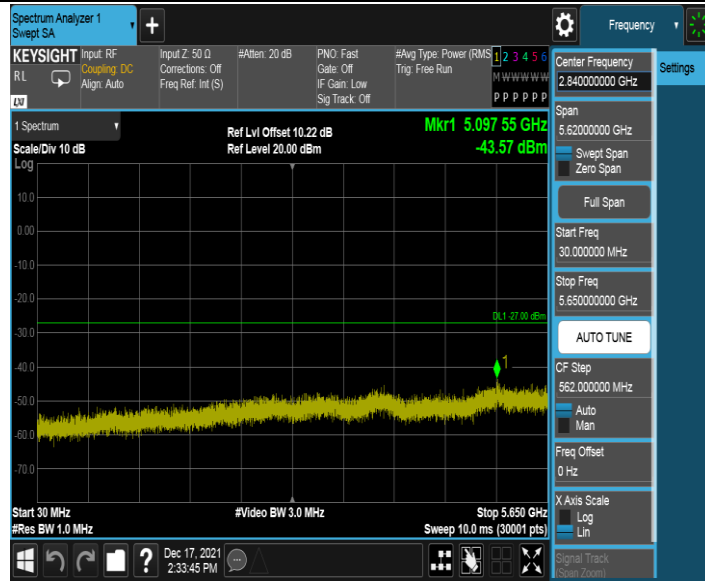
# 11AC80SISO\_Ant1\_5775\_30~5650



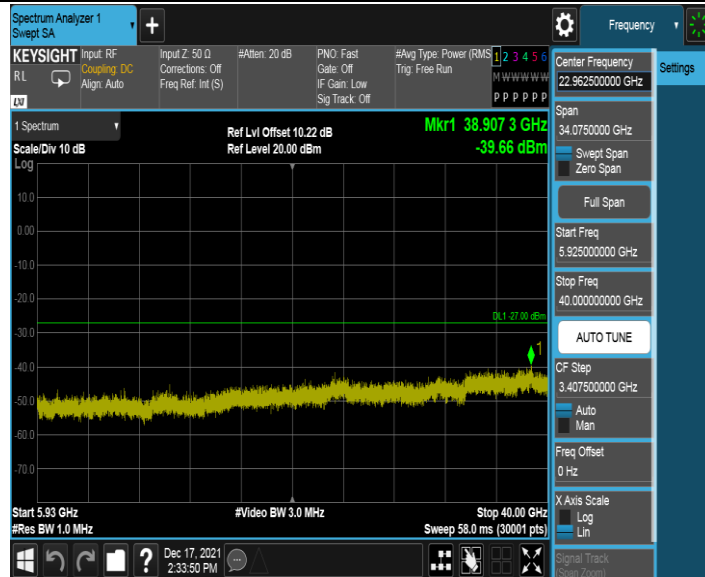
# 11AC80SISO\_Ant1\_5775\_5925~40000



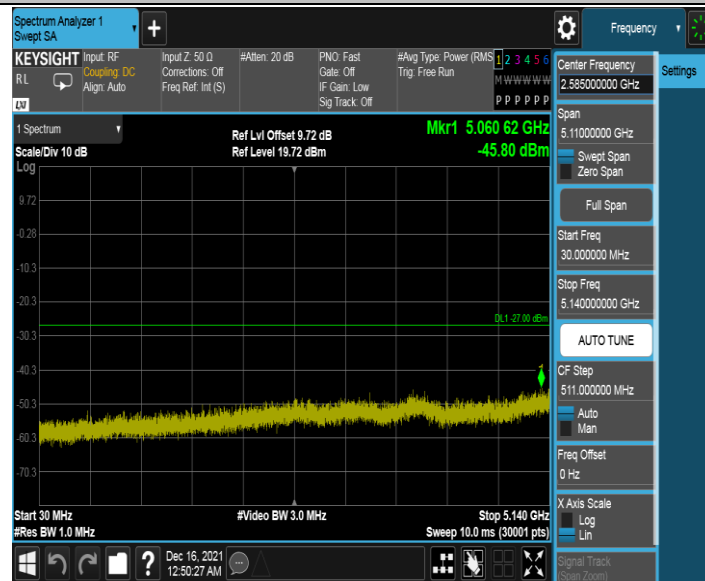
# 11AC80SISO\_Ant2\_5775\_30~5650



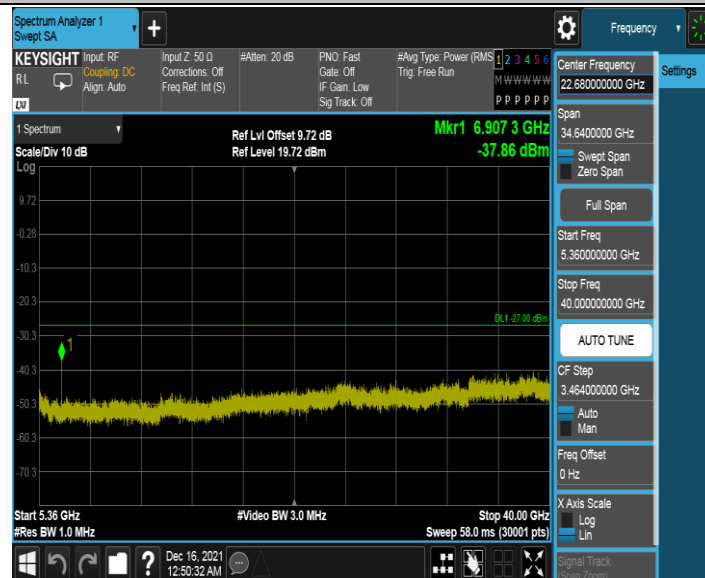
# 11AC80SISO\_Ant2\_5775\_5925~40000



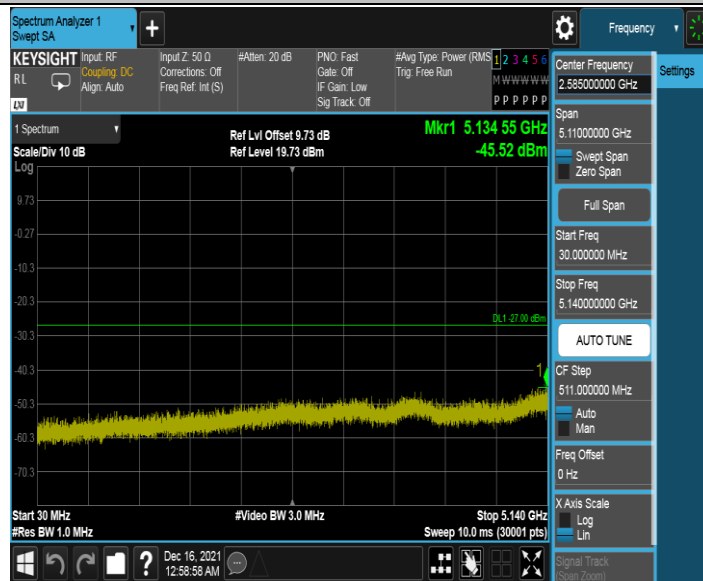
# 11A-CDD\_Ant1\_5180\_30~5140



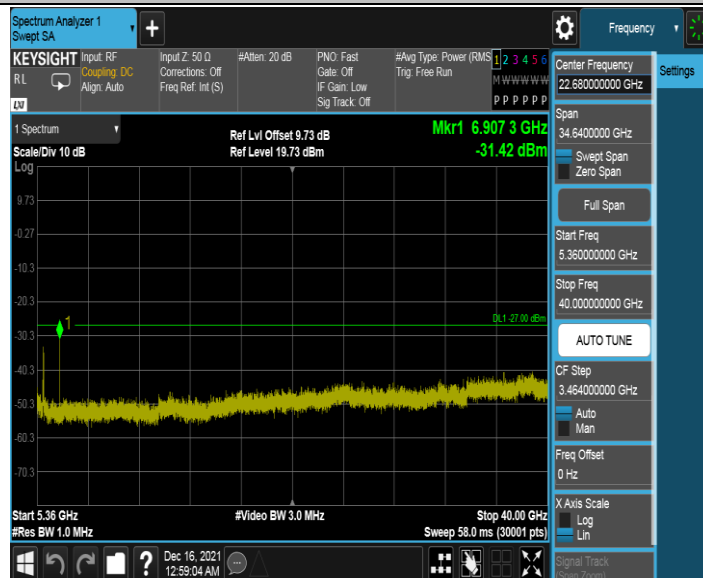
# 11A-CDD\_Ant1\_5180\_5360~40000



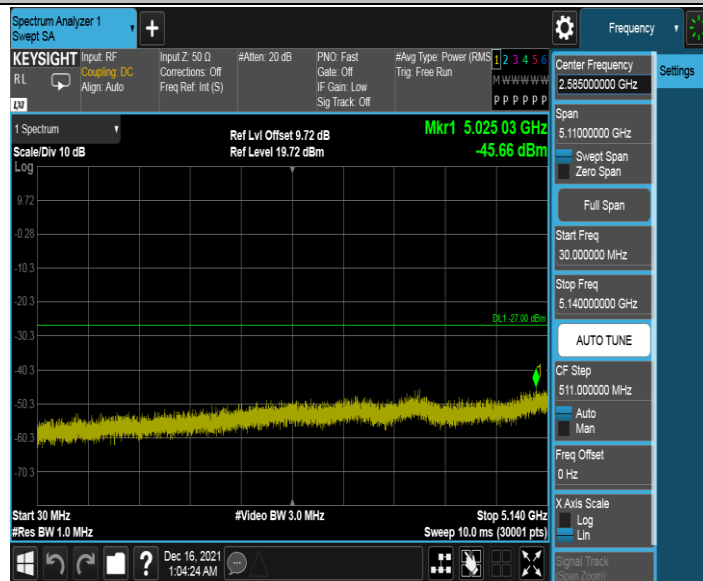
# 11A-CDD\_Ant2\_5180\_30~5140



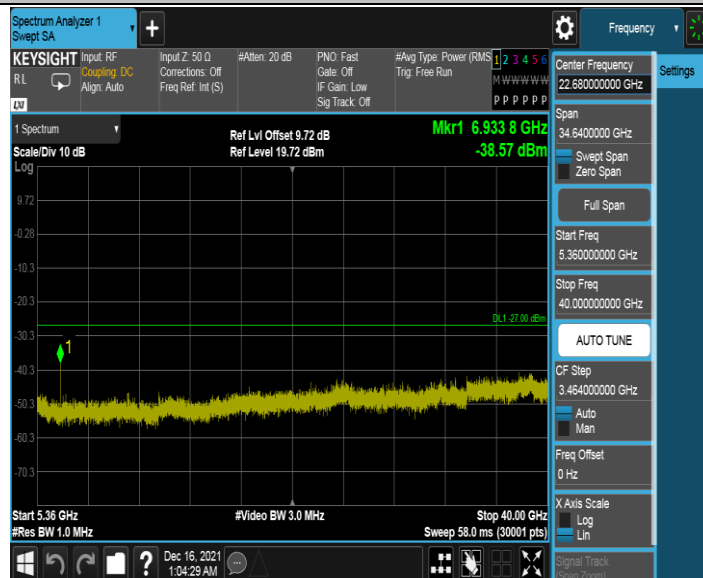
# 11A-CDD\_Ant2\_5180\_5360~40000



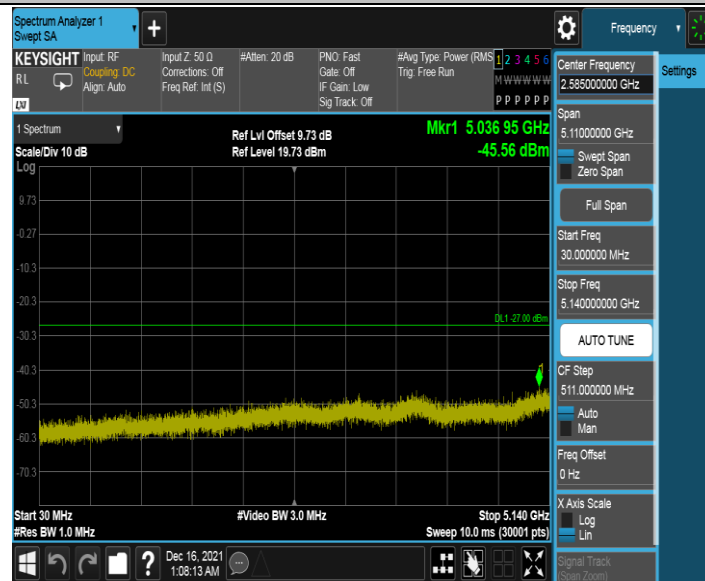
# 11A-CDD\_Ant1\_5200\_30~5140



# 11A-CDD\_Ant1\_5200\_5360~40000



# 11A-CDD\_Ant2\_5200\_30~5140



# 11A-CDD\_Ant2\_5200\_5360~40000

