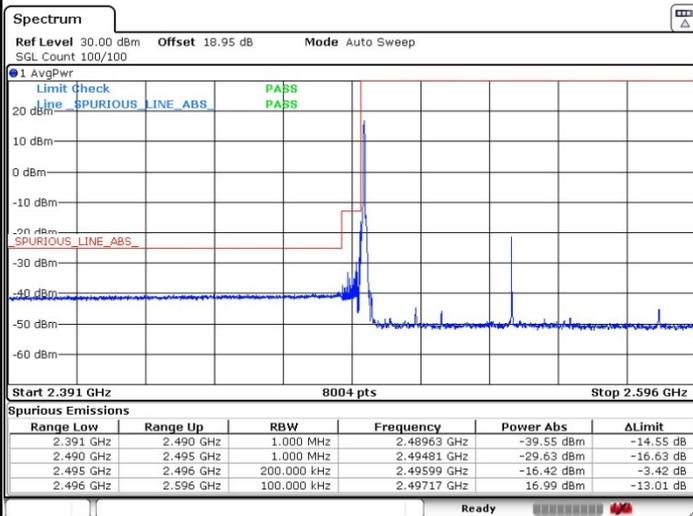




FR1 n41_UL MIMO / 90MHz / CP OFDM

90M-L-1RB0-Q

90M-L-1RBMAX-Q

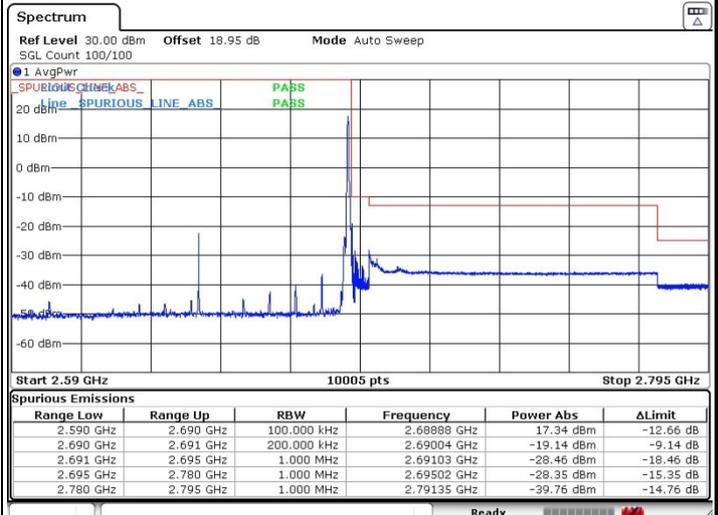
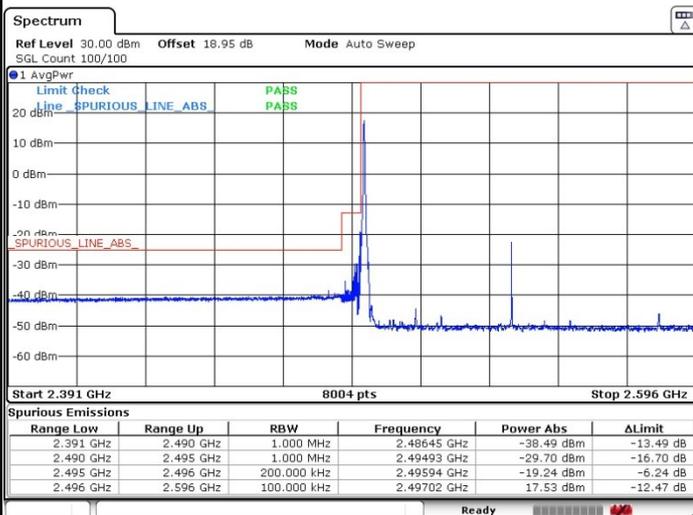


Date: 19 JAN 2021 02:59:53

Date: 19 JAN 2021 03:03:57

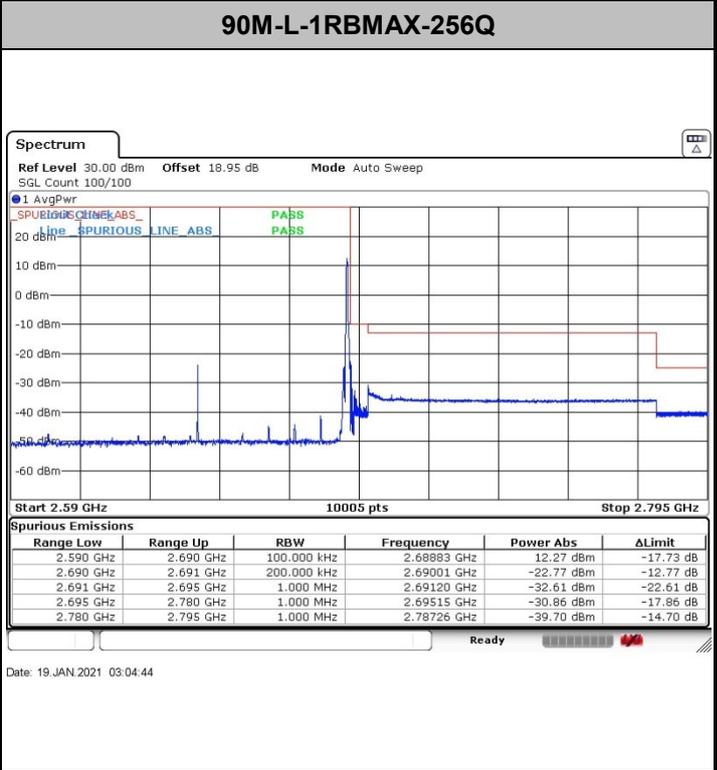
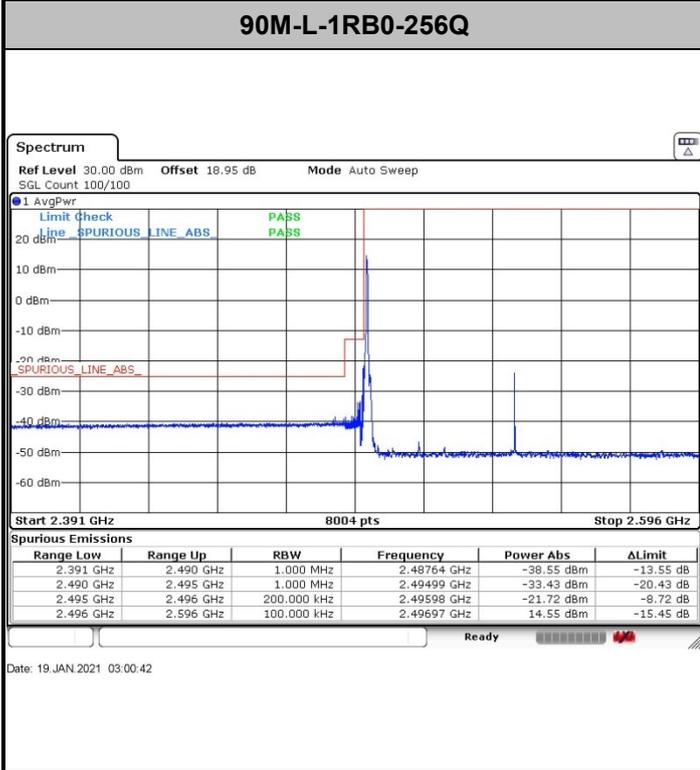
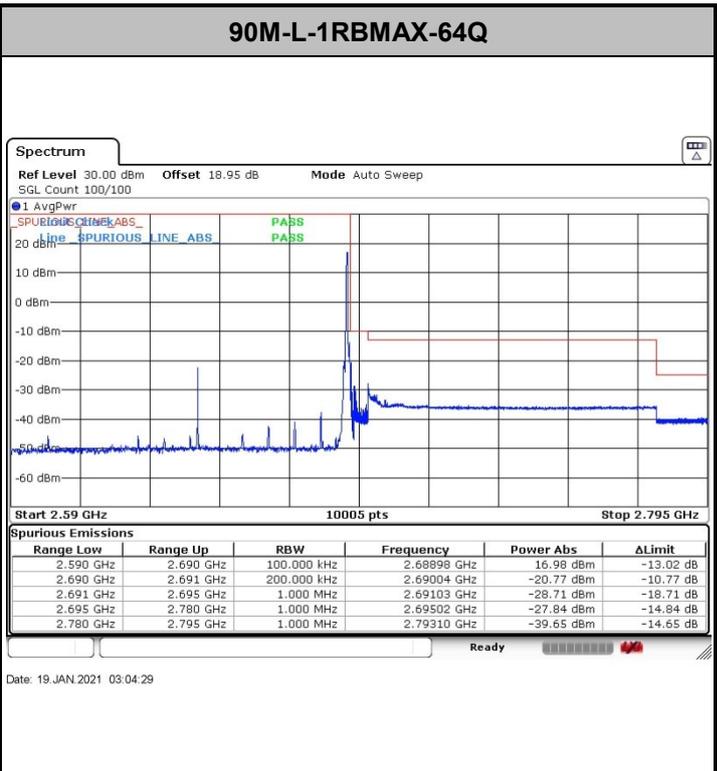
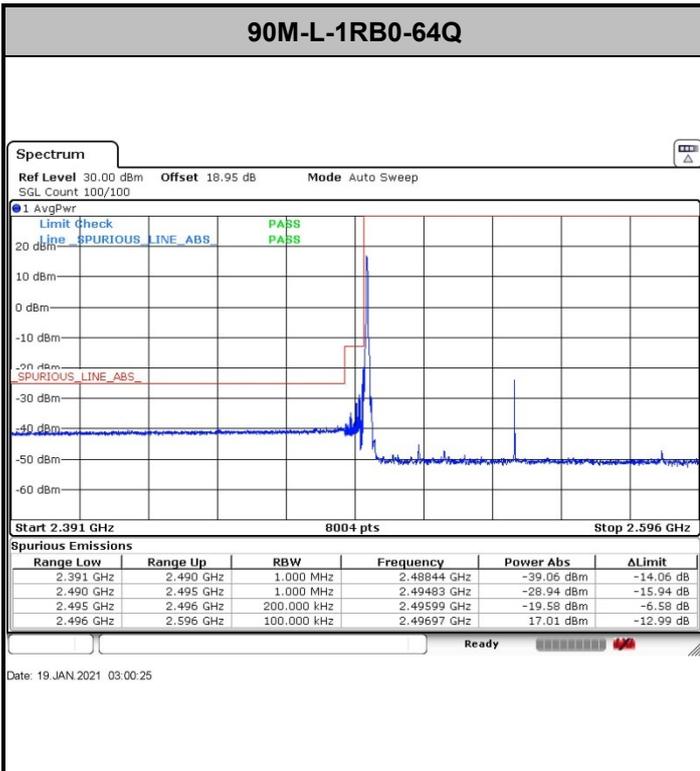
90M-L-1RB0-16Q

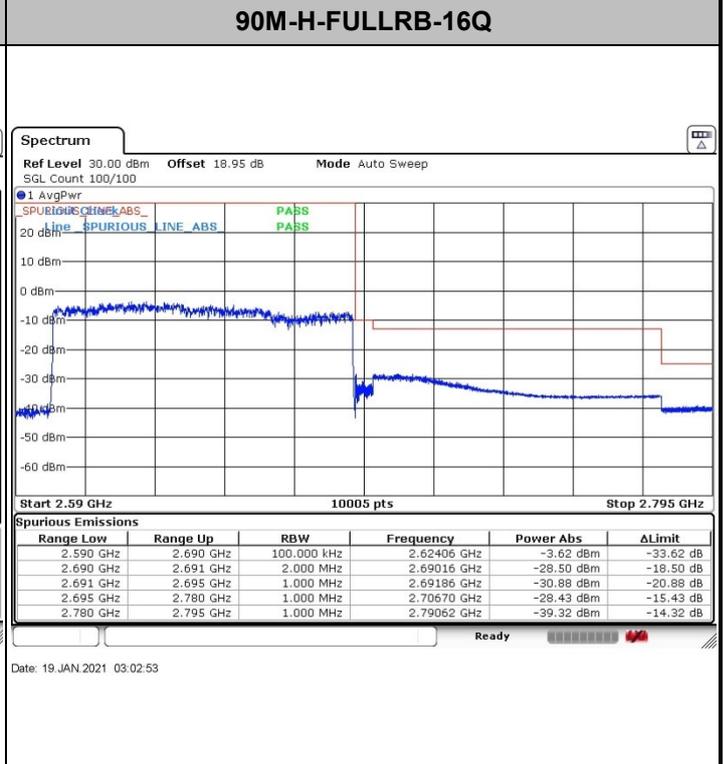
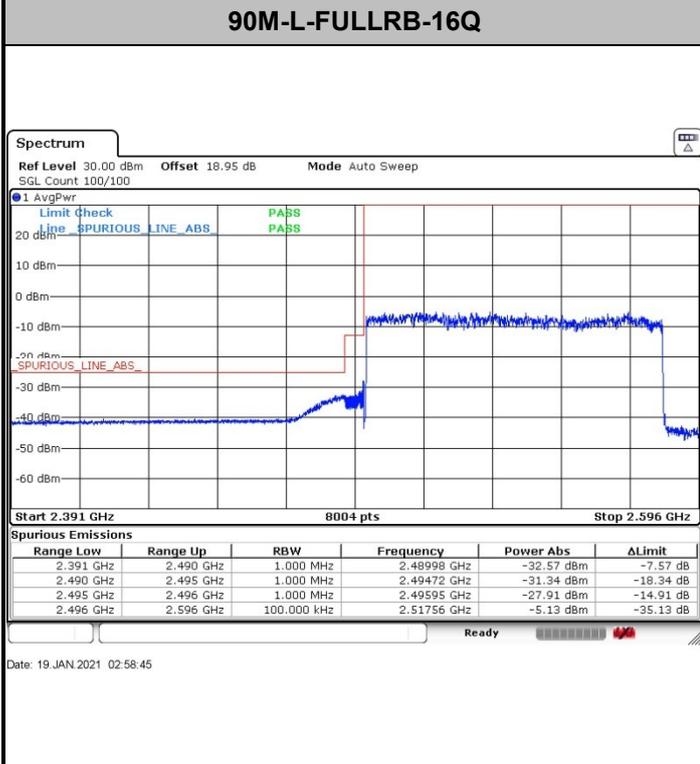
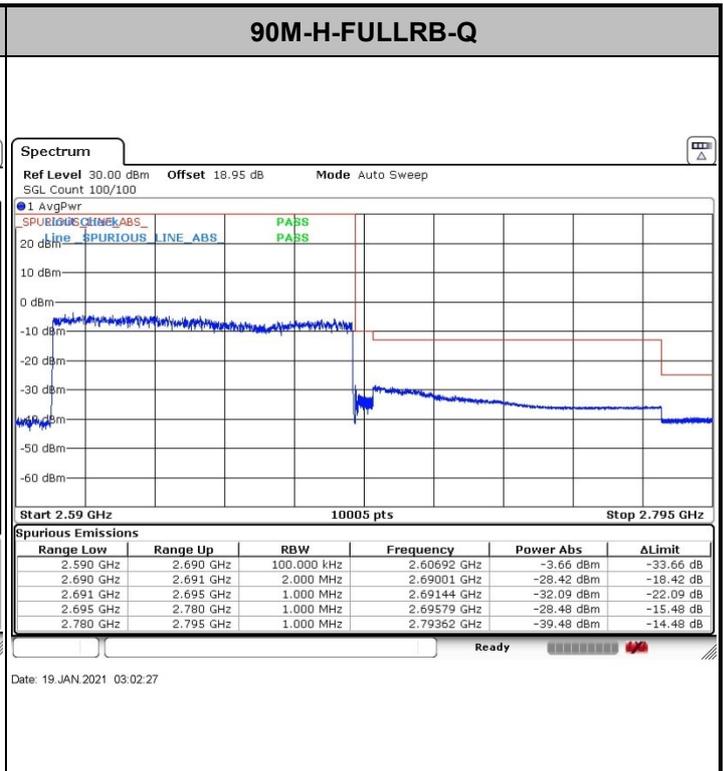
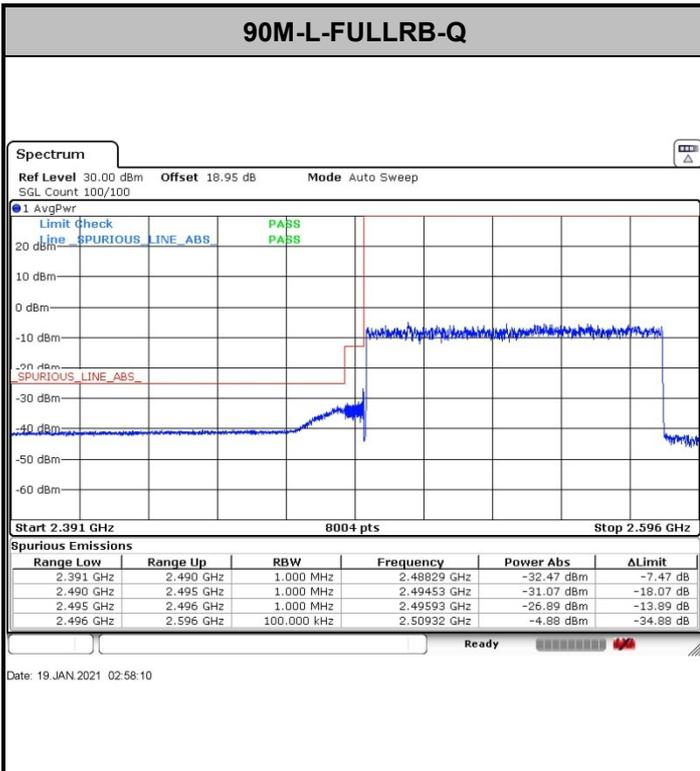
90M-L-1RBMAX-16Q

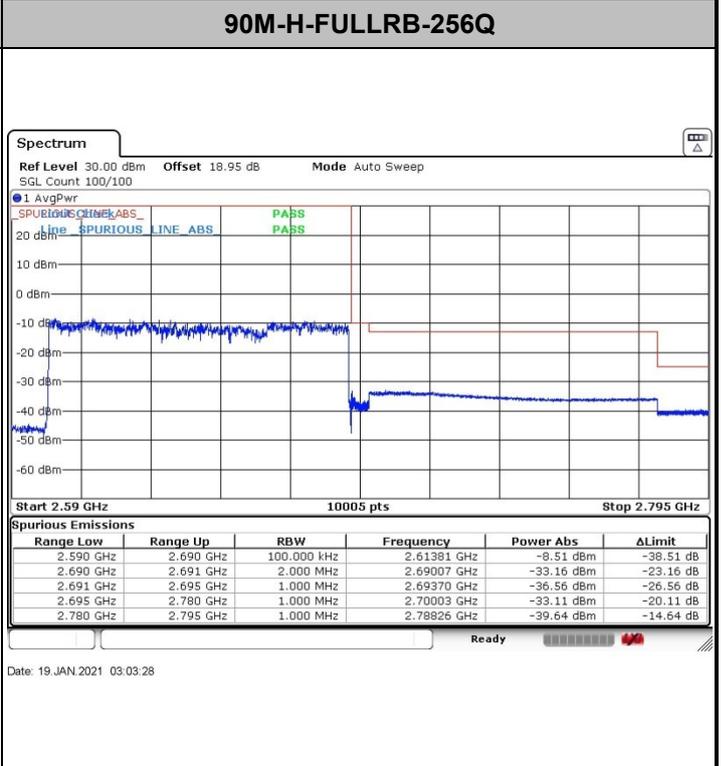
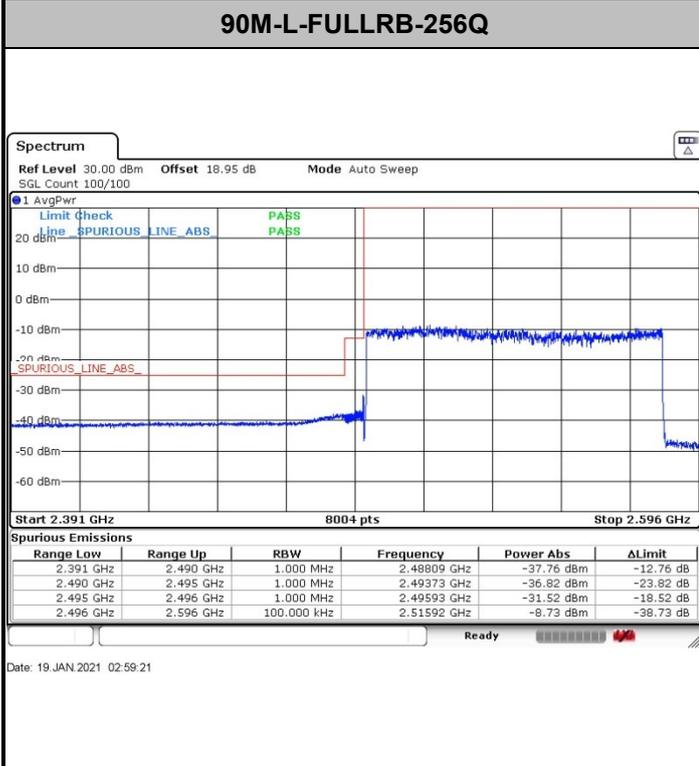
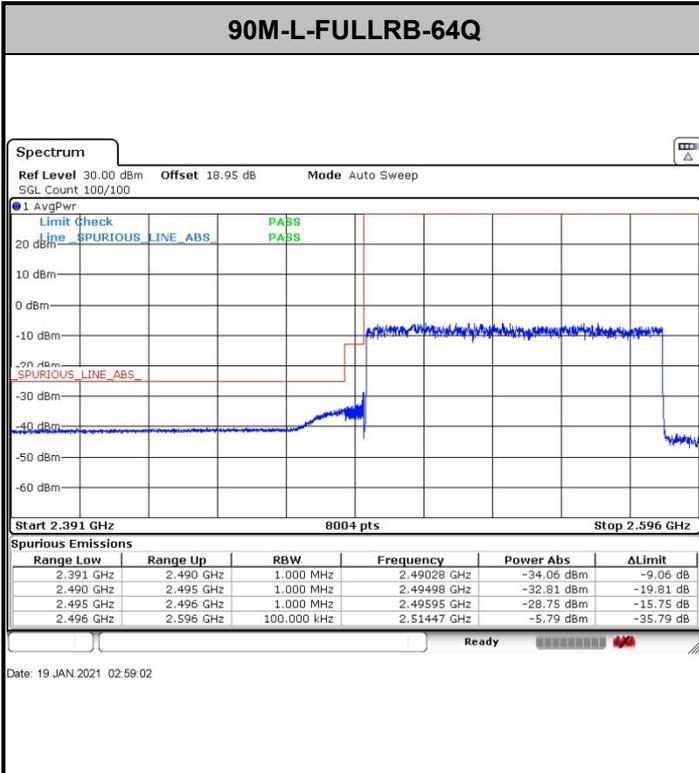


Date: 19 JAN 2021 03:00:12

Date: 19 JAN 2021 03:04:16





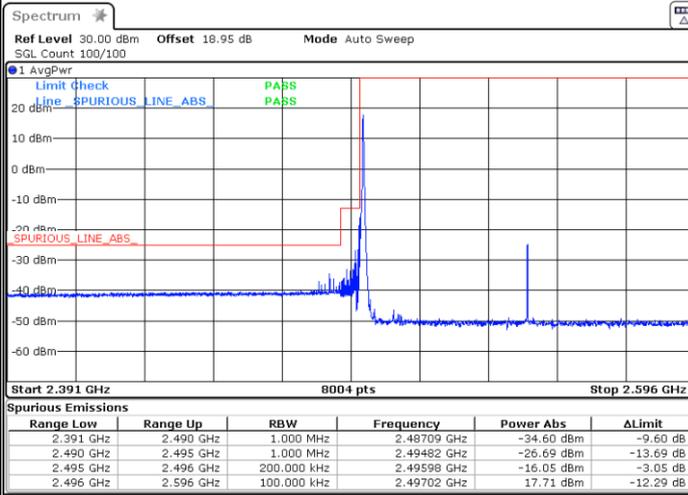




FR1 n41_UL MIMO / 100MHz / CP OFDM

100M-L-1RB0-Q

100M-H-1RBMAX-Q

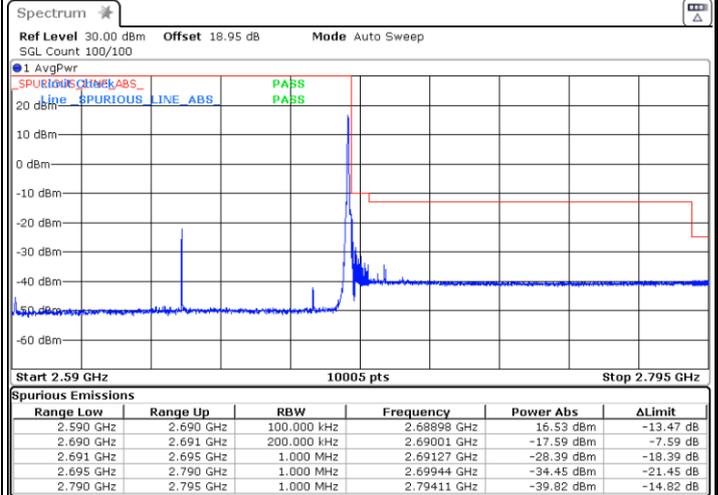
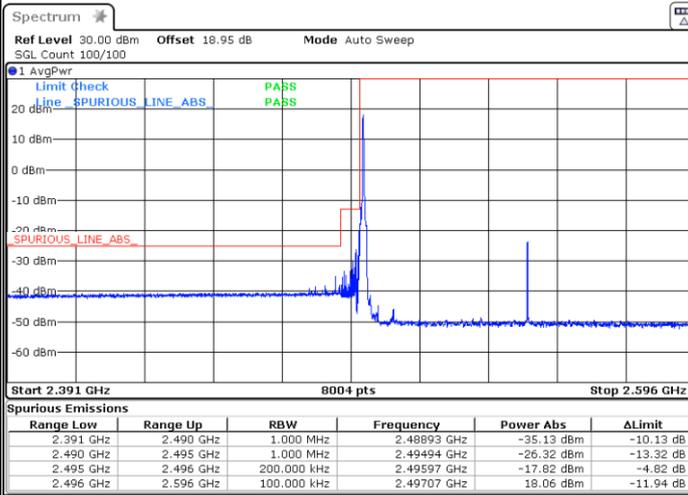


Date: 19 JAN 2021 03:16:55

Date: 19 JAN 2021 03:19:13

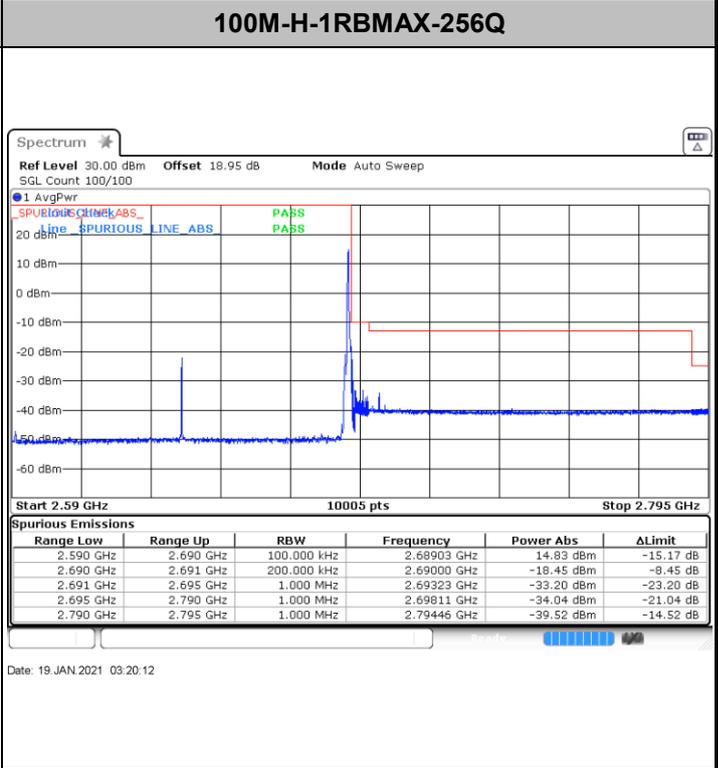
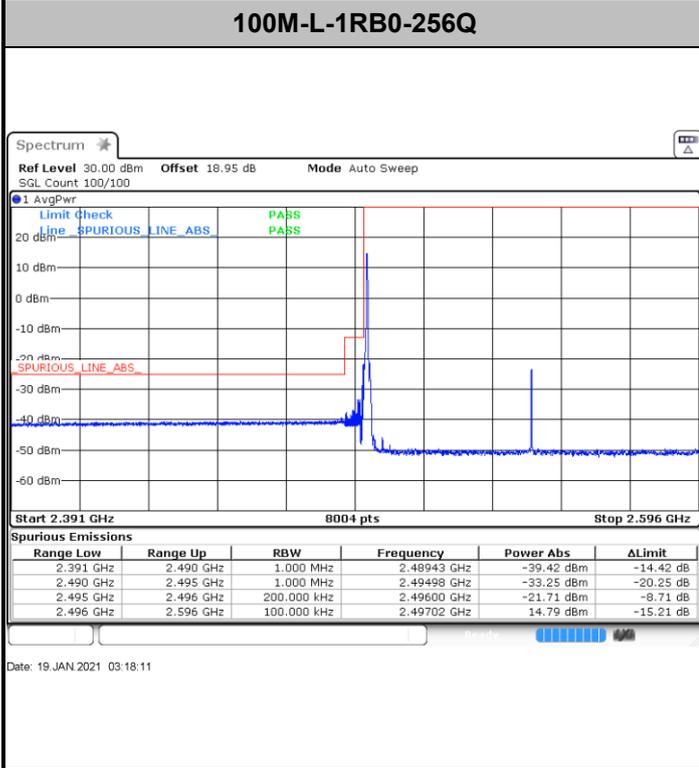
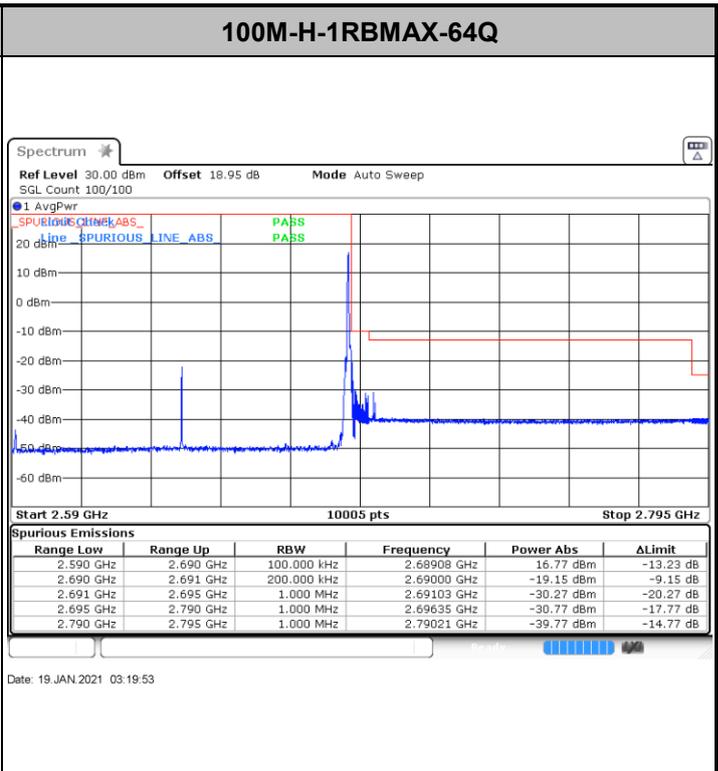
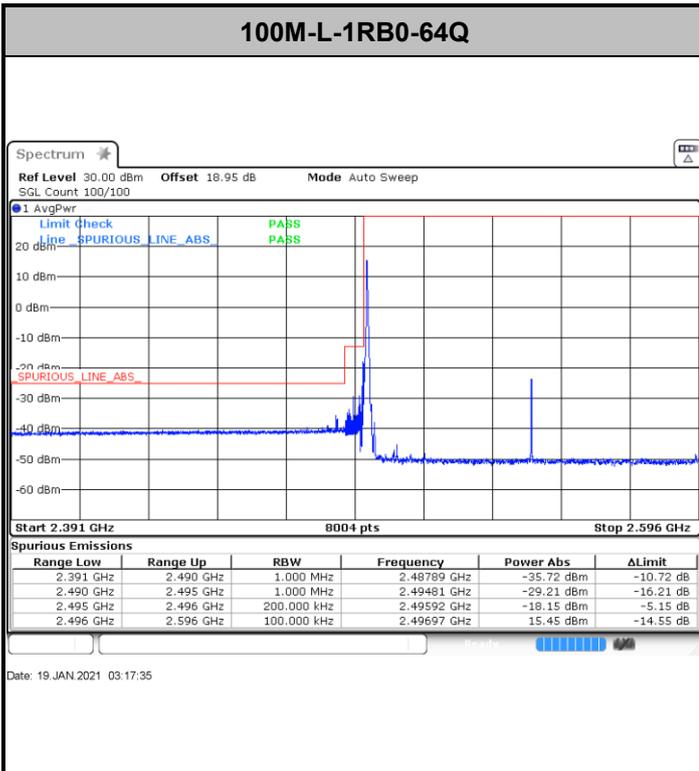
100M-L-1RB0-16Q

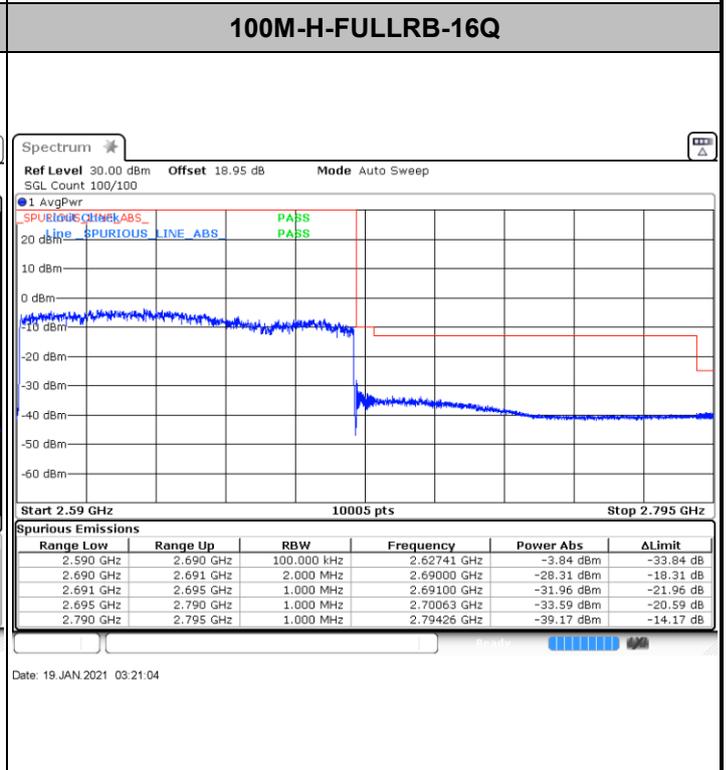
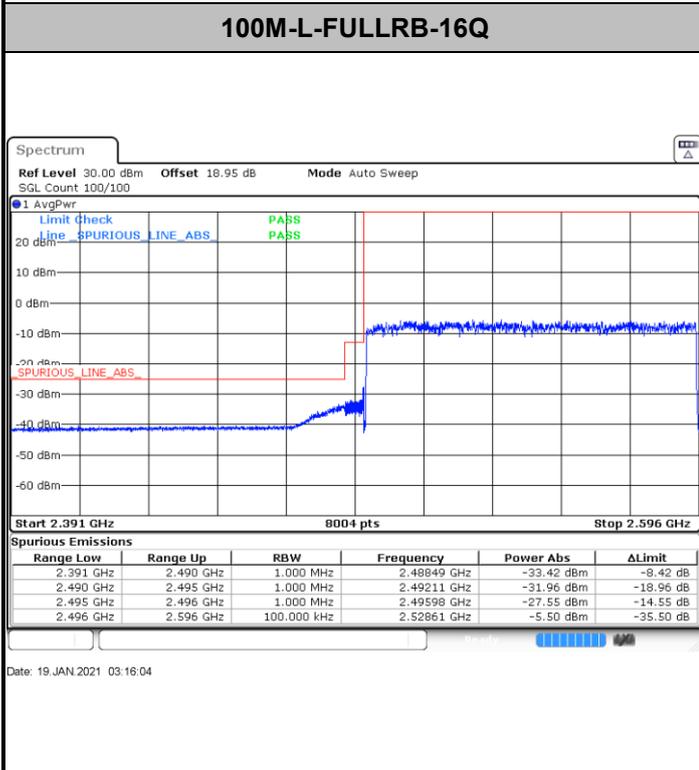
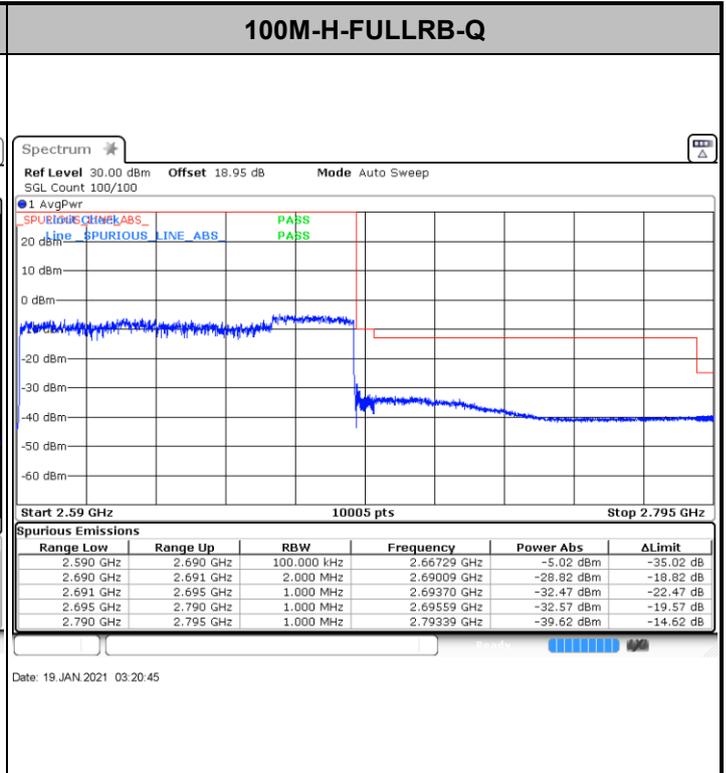
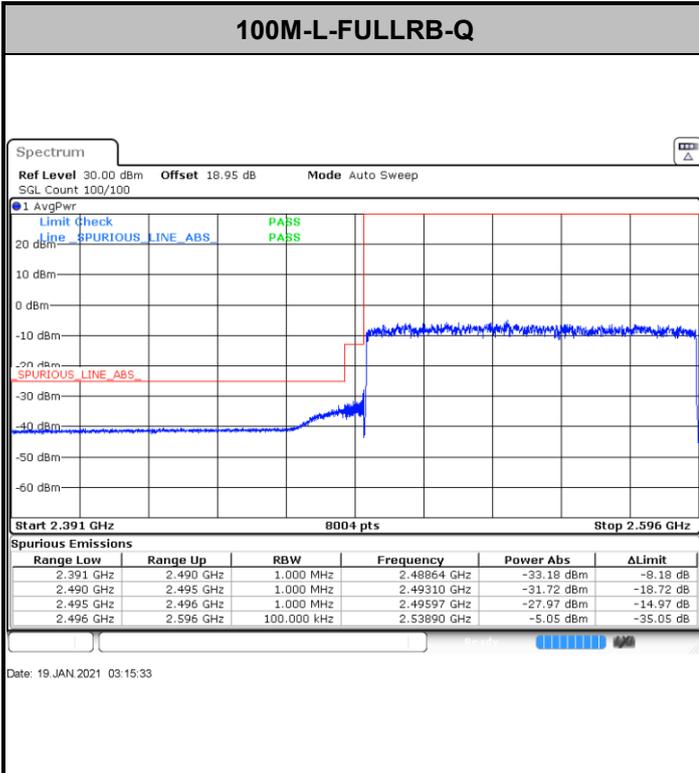
100M-H-1RBMAX-16Q

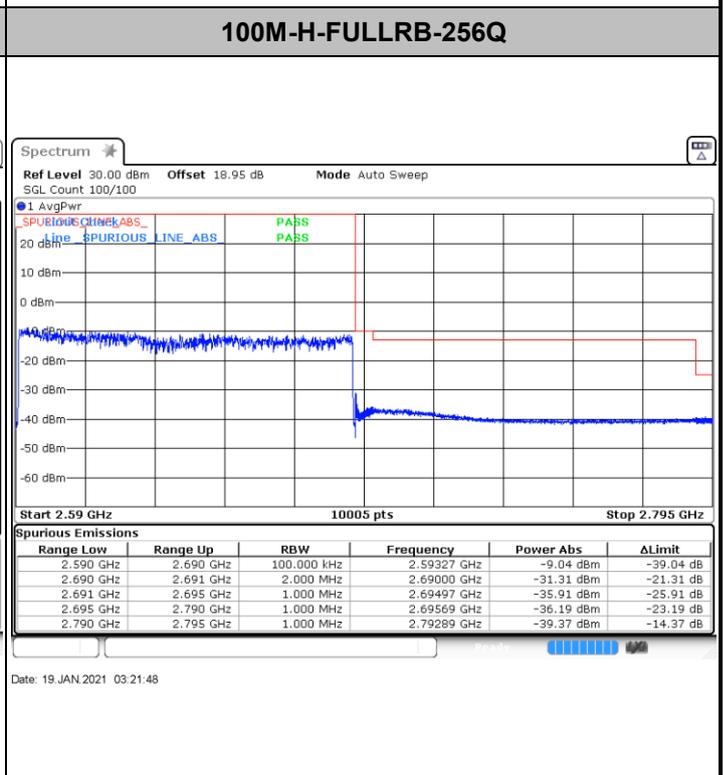
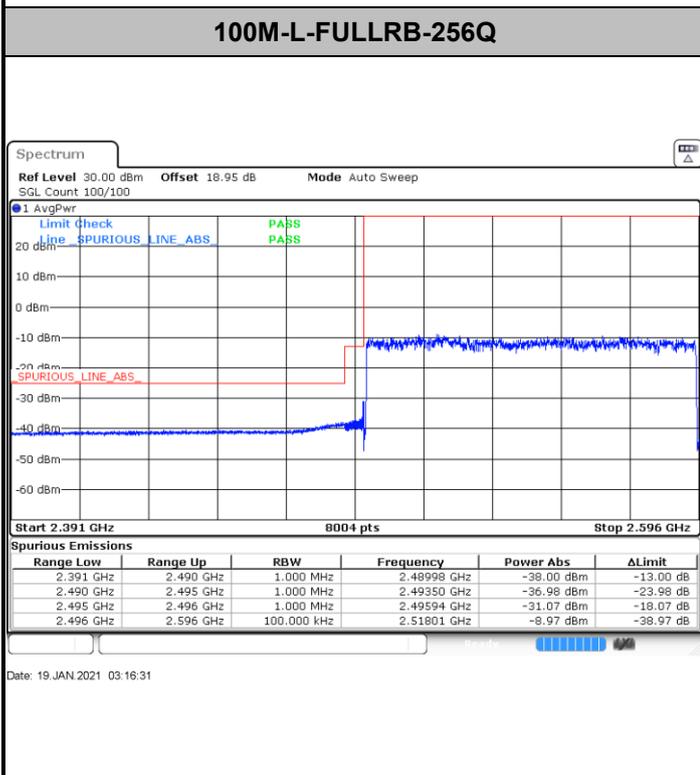
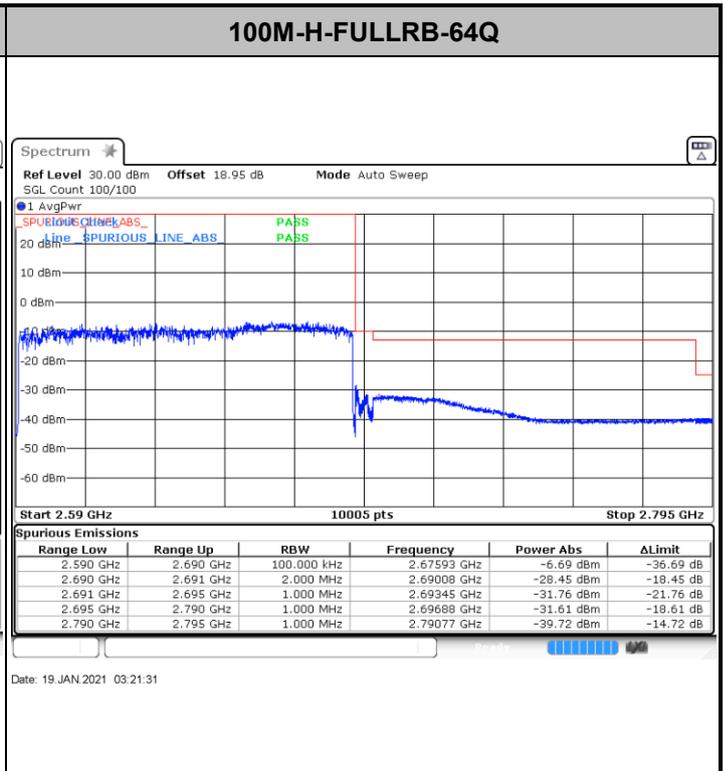
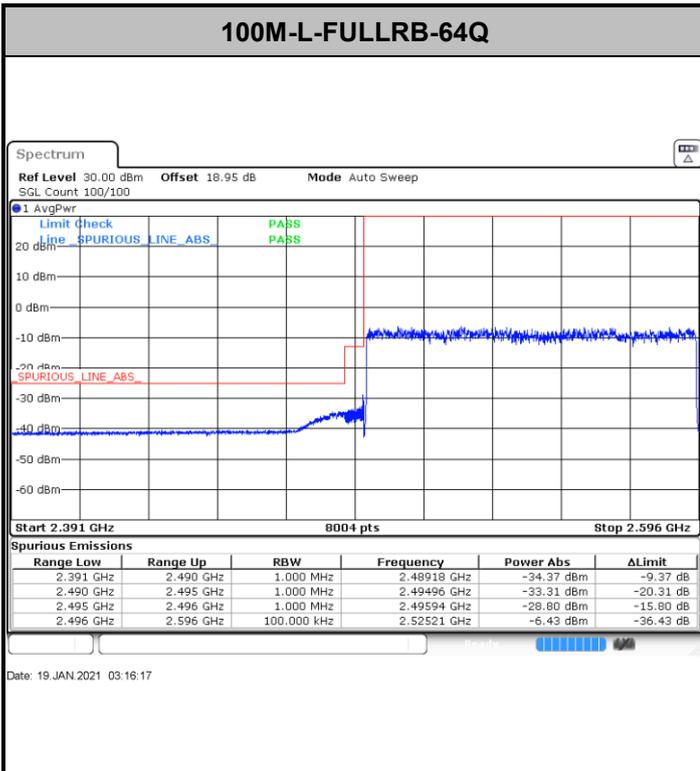


Date: 19 JAN 2021 03:17:16

Date: 19 JAN 2021 03:19:33





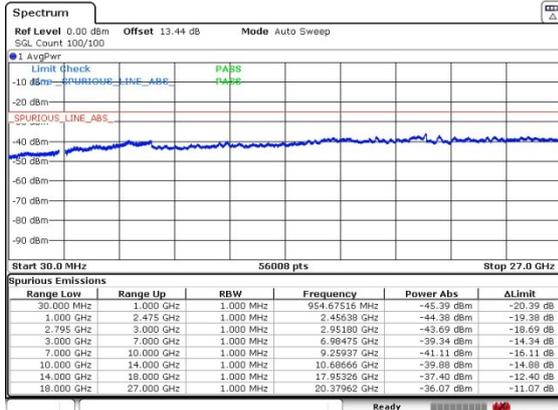




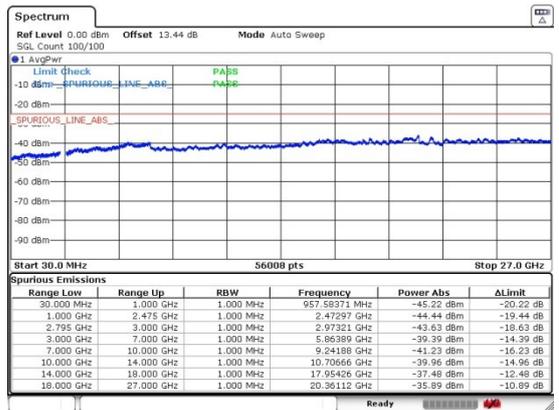
Conducted Spurious Emission

FR1 n41_UL MIMO / 20MHz / CP OFDM

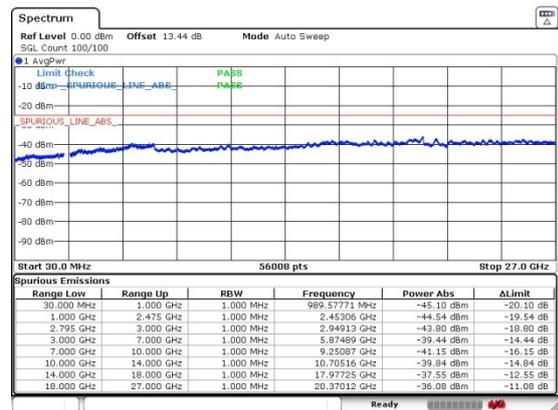
Lowest Channel / QPSK



Middle Channel / QPSK



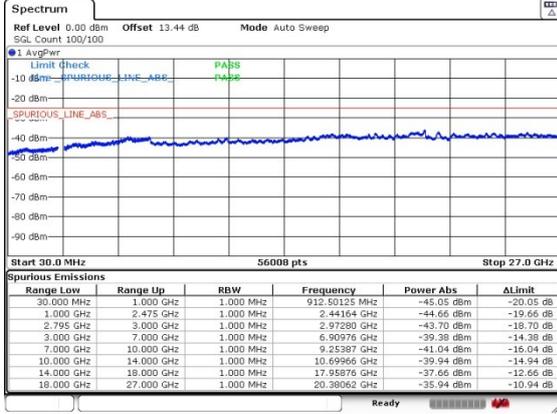
Highest Channel / QPSK



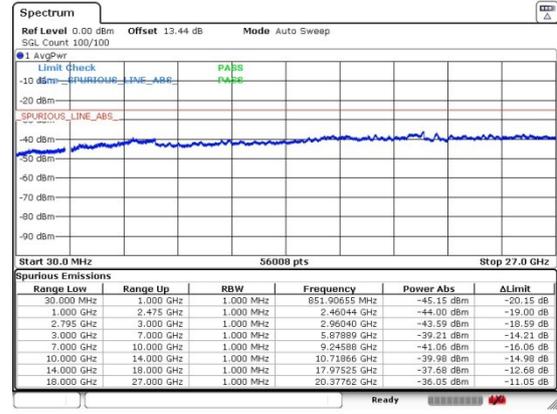


FR1 n41_UL MIMO / 30MHz / CP OFDM

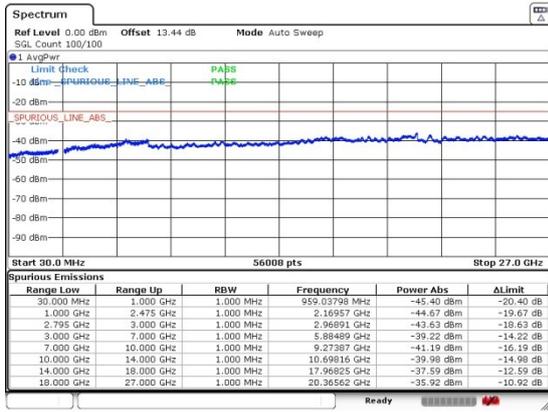
Lowest Channel / QPSK



Middle Channel / QPSK



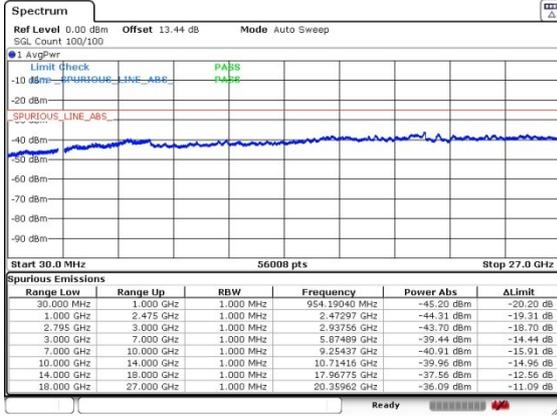
Highest Channel / QPSK



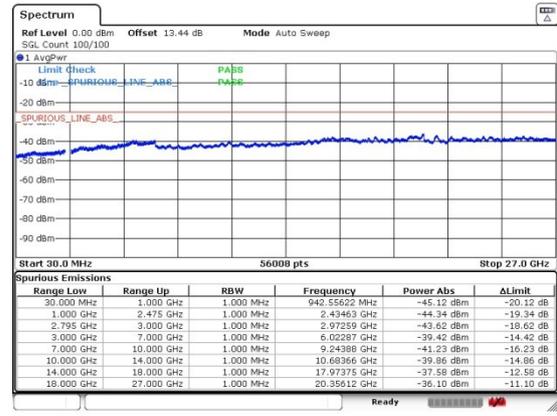


FR1 n41_UL MIMO / 40MHz / CP OFDM

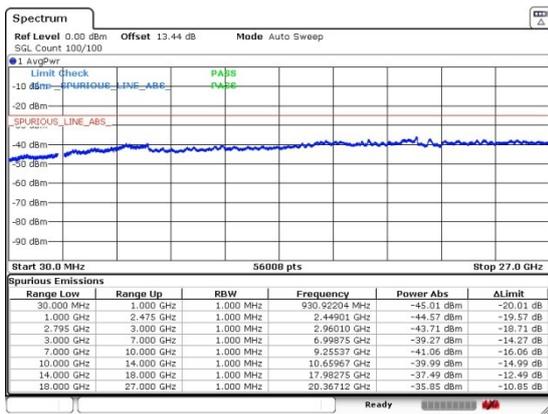
Lowest Channel / QPSK



Middle Channel / QPSK



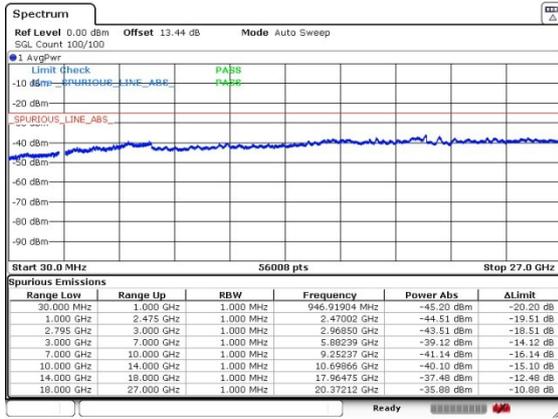
Highest Channel / QPSK



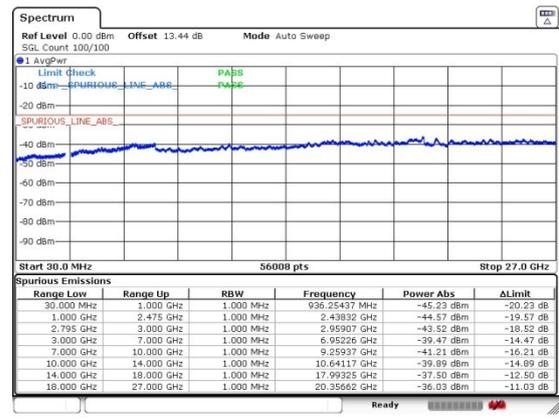


FR1 n41_UL MIMO / 50MHz / CP OFDM

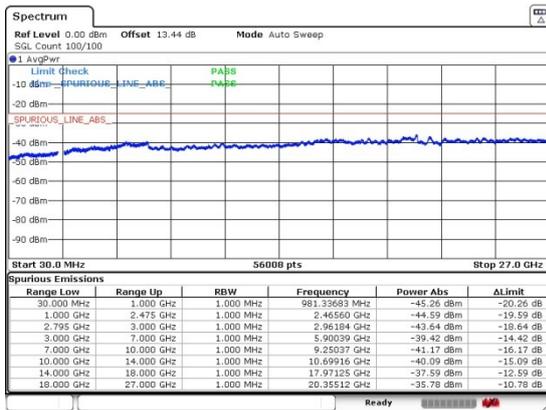
Lowest Channel / QPSK



Middle Channel / QPSK



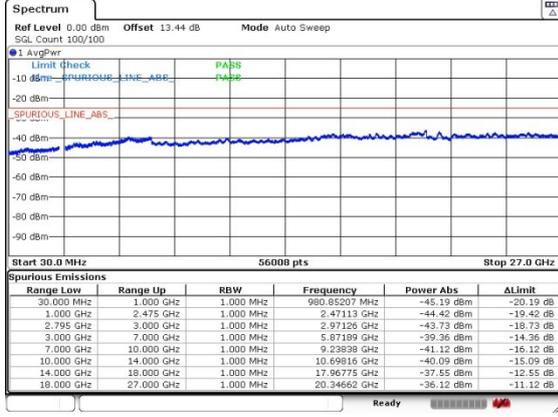
Highest Channel / QPSK



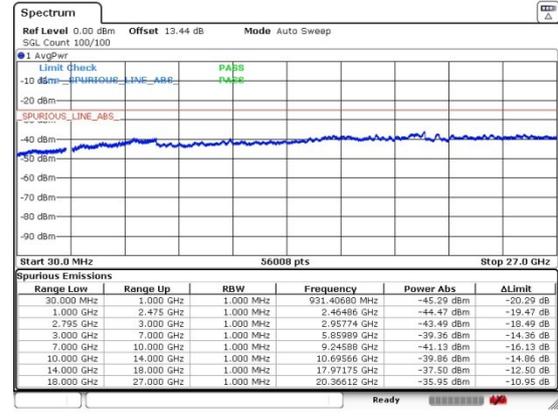


FR1 n41_UL MIMO / 60MHz / CP OFDM

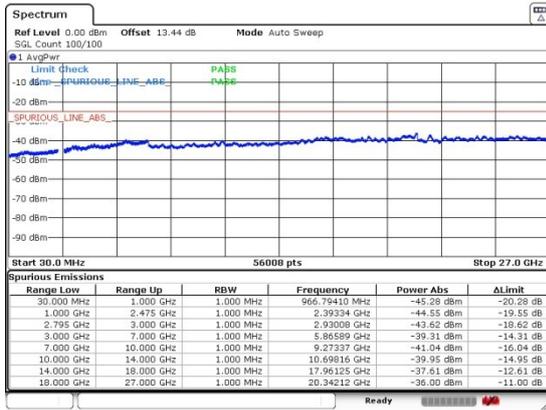
Lowest Channel / QPSK



Middle Channel / QPSK



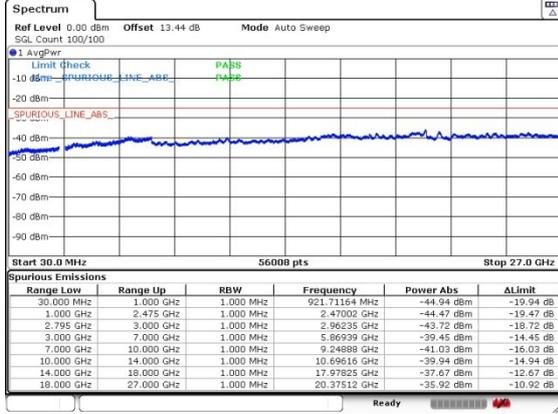
Highest Channel / QPSK



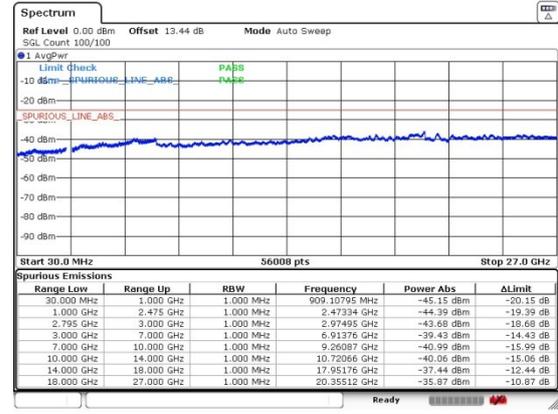


FR1 n41_UL MIMO / 80MHz / CP OFDM

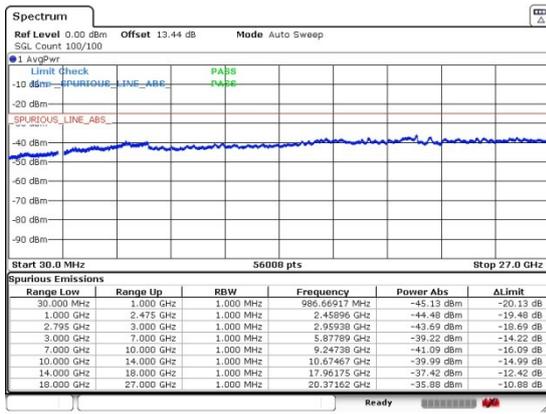
Lowest Channel / QPSK



Middle Channel / QPSK



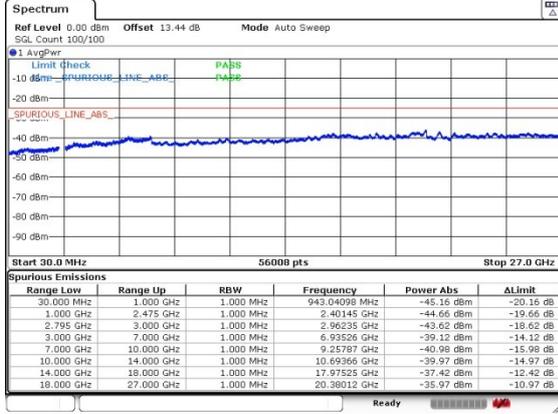
Highest Channel / QPSK



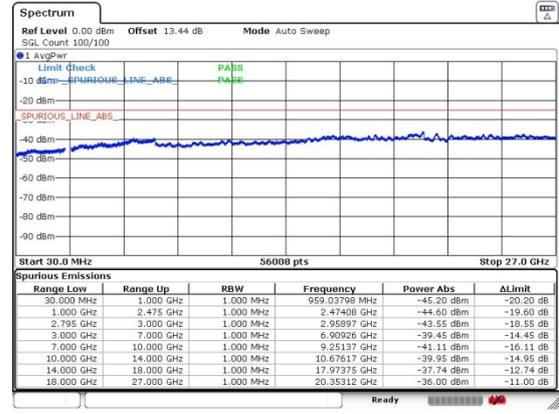


FR1 n41_UL MIMO / 90MHz / CP OFDM

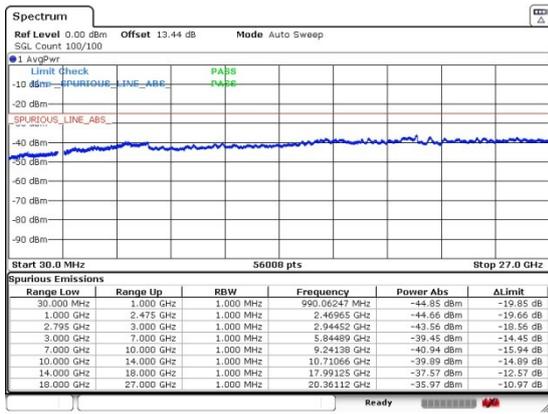
Lowest Channel / QPSK



Middle Channel / QPSK



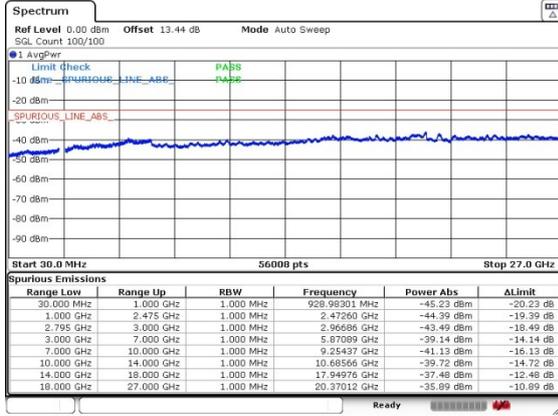
Highest Channel / QPSK



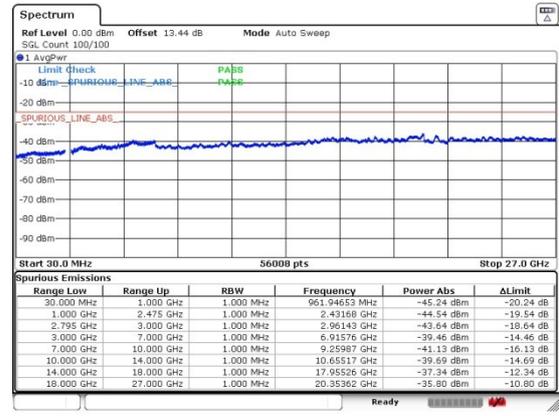


FR1 n41_UL MIMO / 100MHz / CP OFDM

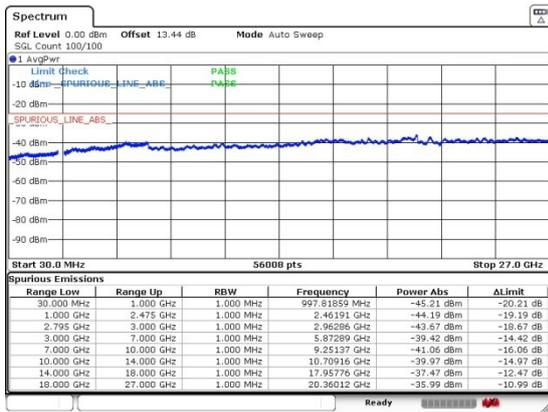
Lowest Channel / QPSK



Middle Channel / QPSK



Highest Channel / QPSK





Frequency Stability

Test Conditions		FR1 n41_UL MIMO (QPSK) / Middle Channel	Limit
Temperature (°C)	Voltage (Volt)	BW 100MHz	Note 2.
		Deviation (ppm)	Result
50	Normal Voltage	0.0016	PASS
40	Normal Voltage	0.0038	
30	Normal Voltage	0.0001	
20(Ref.)	Normal Voltage	0.0000	
10	Normal Voltage	0.0026	
0	Normal Voltage	0.0006	
-10	Normal Voltage	0.0008	
-20	Normal Voltage	0.0006	
-30	Normal Voltage	0.0035	
20	Maximum Voltage	0.0001	
20	Normal Voltage	0.0000	
20	Battery End Point	0.0019	

Note:

1. Normal Voltage =3.87 V. ; Battery End Point (BEP) =3.6 V. ; Maximum Voltage =4.4 V.
2. Note: The frequency fundamental emissions stay within the authorized frequency block.

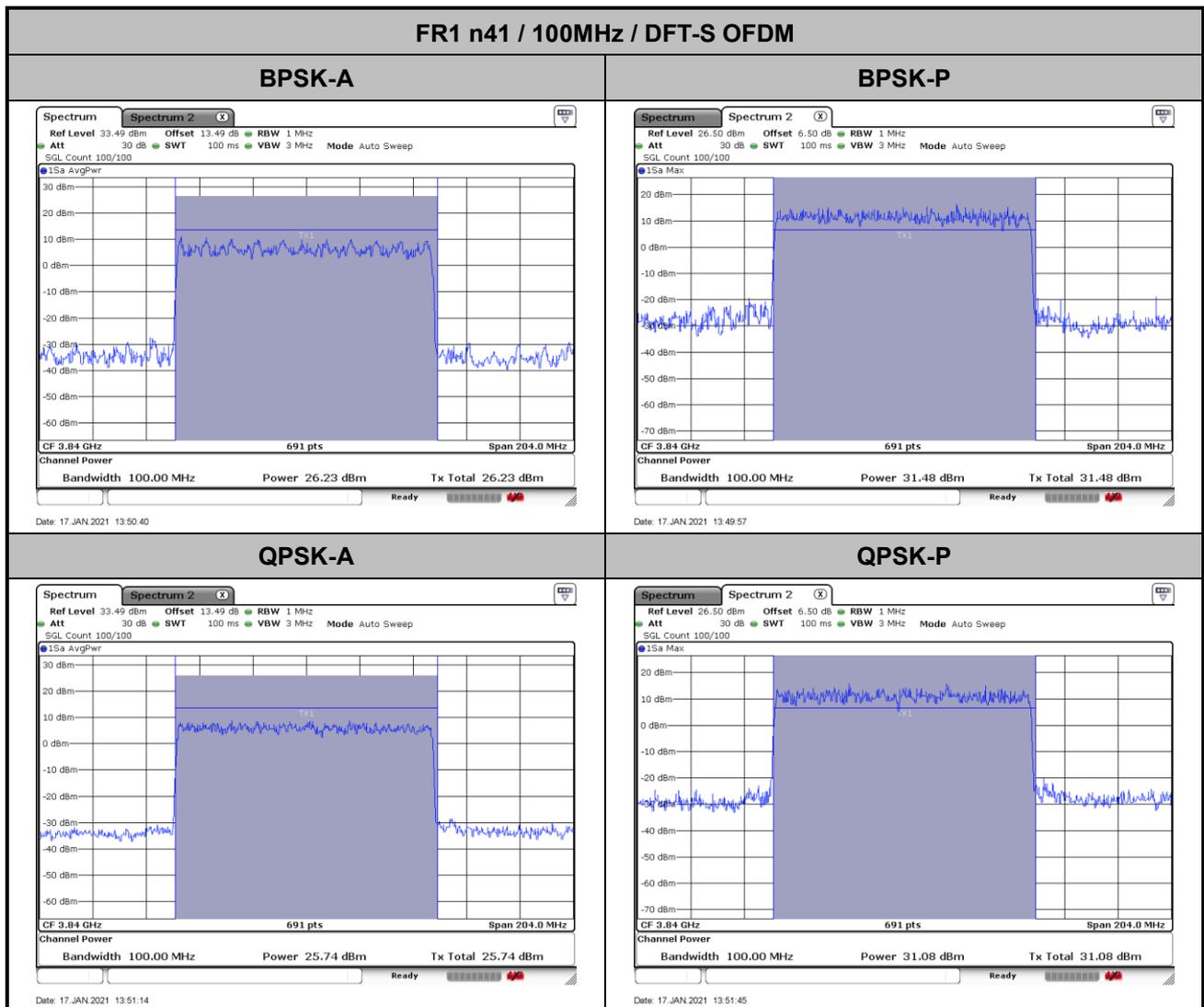


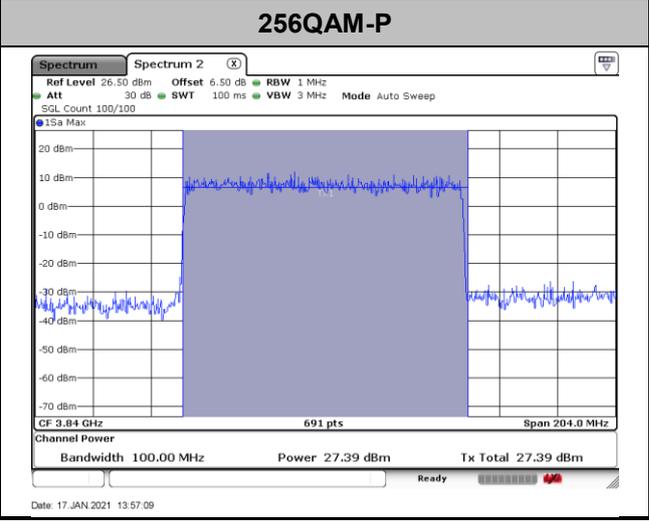
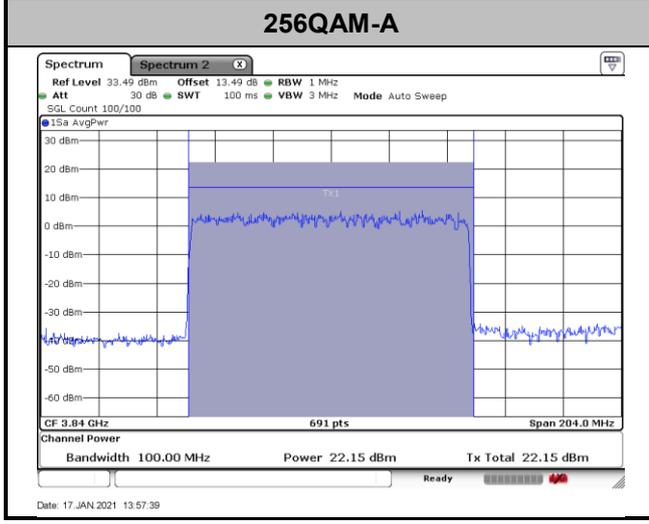
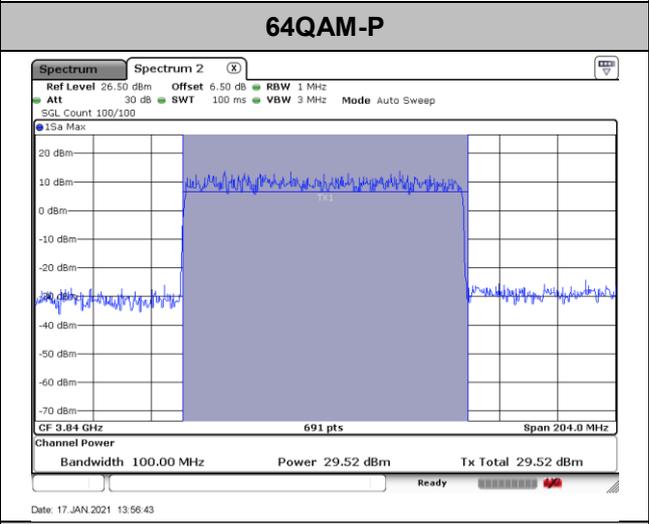
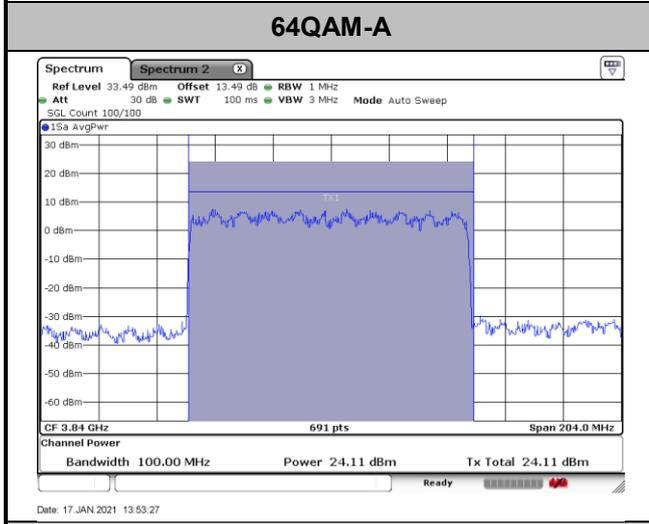
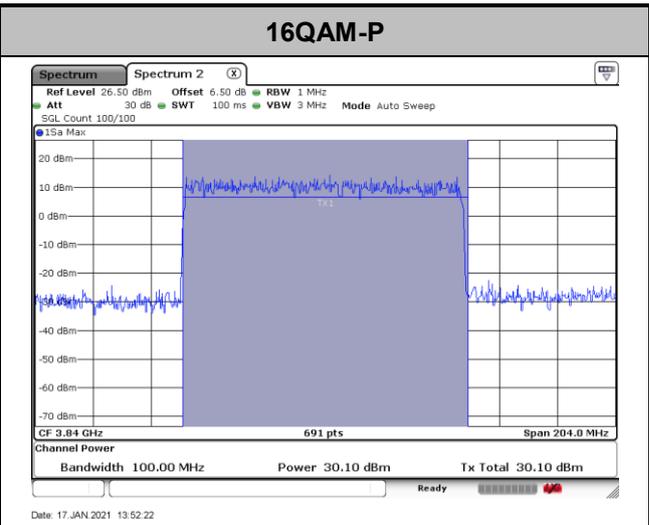
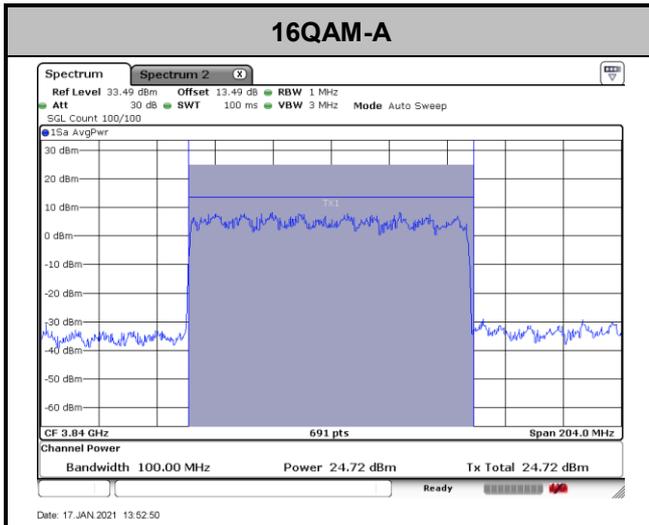
5G NR n77 Ant.4 for PA

Peak-to-Average Ratio

Mode	FR1 n77 / 100MHz / DFT-S OFDM				
Mod.	PI/2 BPSK	QPSK	16QAM	64QAM	Limit: 13dB
RB Size	Full RB	Full RB	Full RB	Full RB	Result
Middle CH	5.25	5.34	5.38	5.41	PASS
Mod.	256QAM				Limit: 13dB
RB Size	Full RB				Result
Middle CH	5.24				PASS

Note: PAR=Peak-Average







26dB Bandwidth

Mode	FR1 n77 : 26dB BW(MHz) / CP OFDM	
BW	100MHz	
Mod.	QPSK	16QAM
Middle CH	102.5	102.5

