

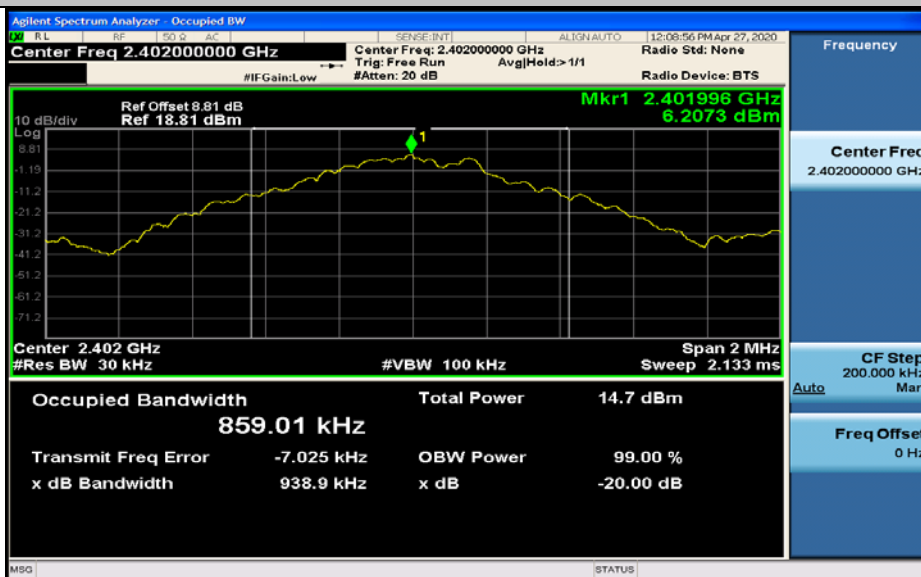


Appendix A for SHEM200300171401

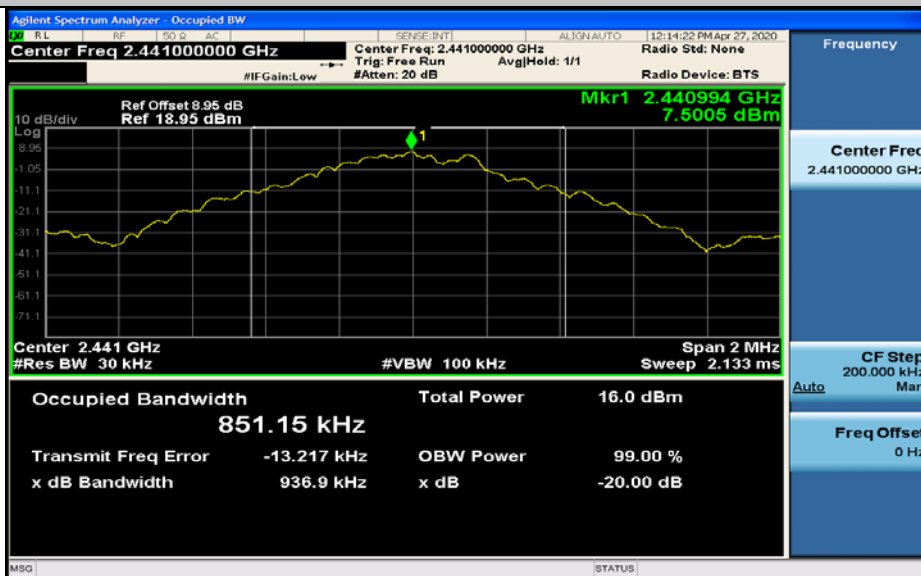
1.20 dB Bandwidth

Test Mode	Test Channel	EBW[MHz]	Limit[MHz]	Verdict
DH5	2402	0.94	---	PASS
DH5	2441	0.94	---	PASS
DH5	2480	0.94	---	PASS
2DH5	2402	1.23	---	PASS
2DH5	2441	1.23	---	PASS
2DH5	2480	1.23	---	PASS
3DH5	2402	1.27	---	PASS
3DH5	2441	1.26	---	PASS
3DH5	2480	1.26	---	PASS

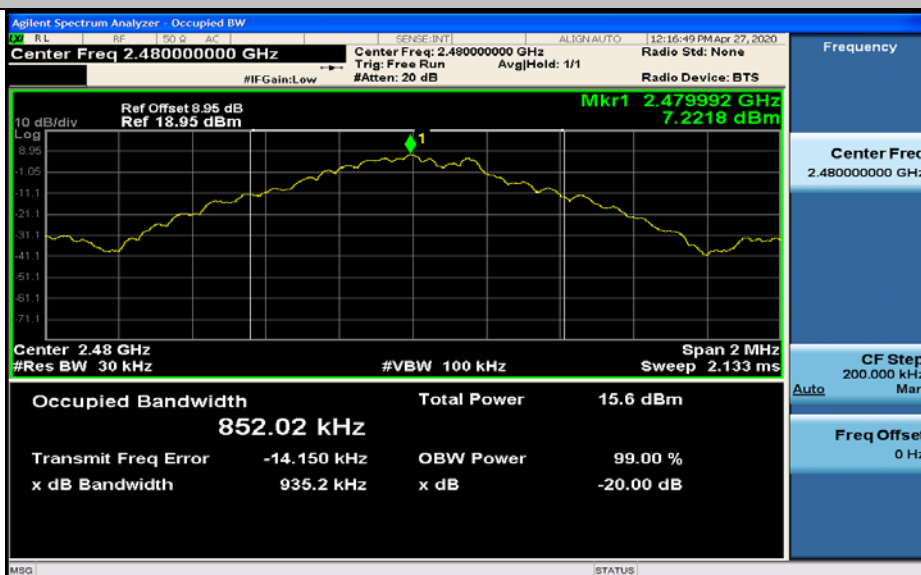
20 dB Bandwidth_DH5_2402



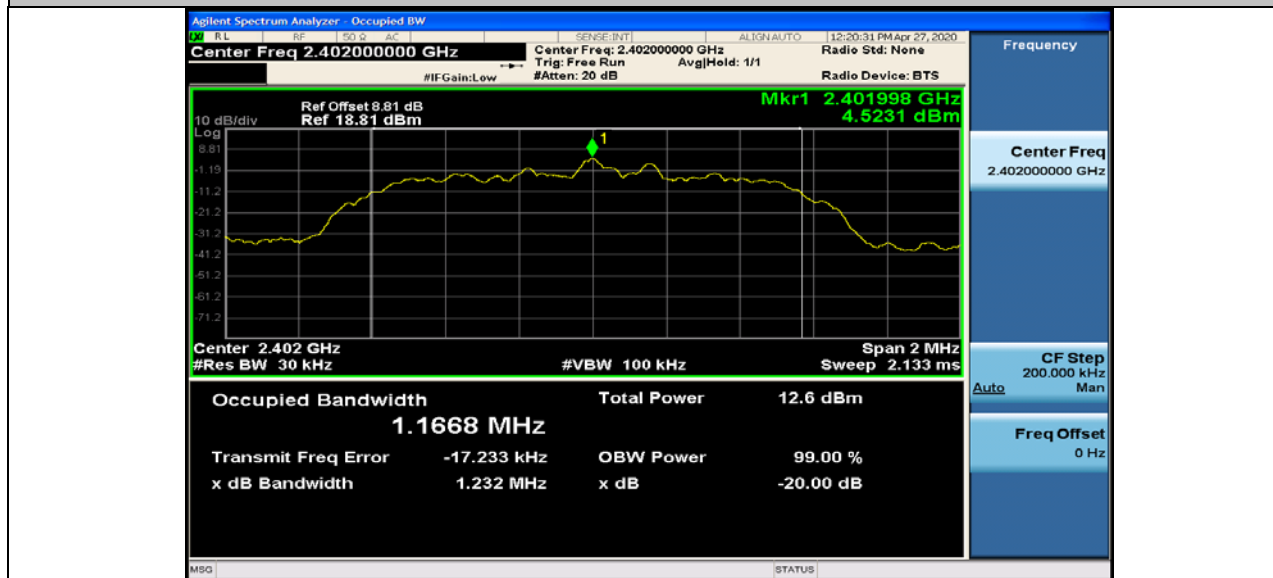
20 dB Bandwidth_DH5_2441



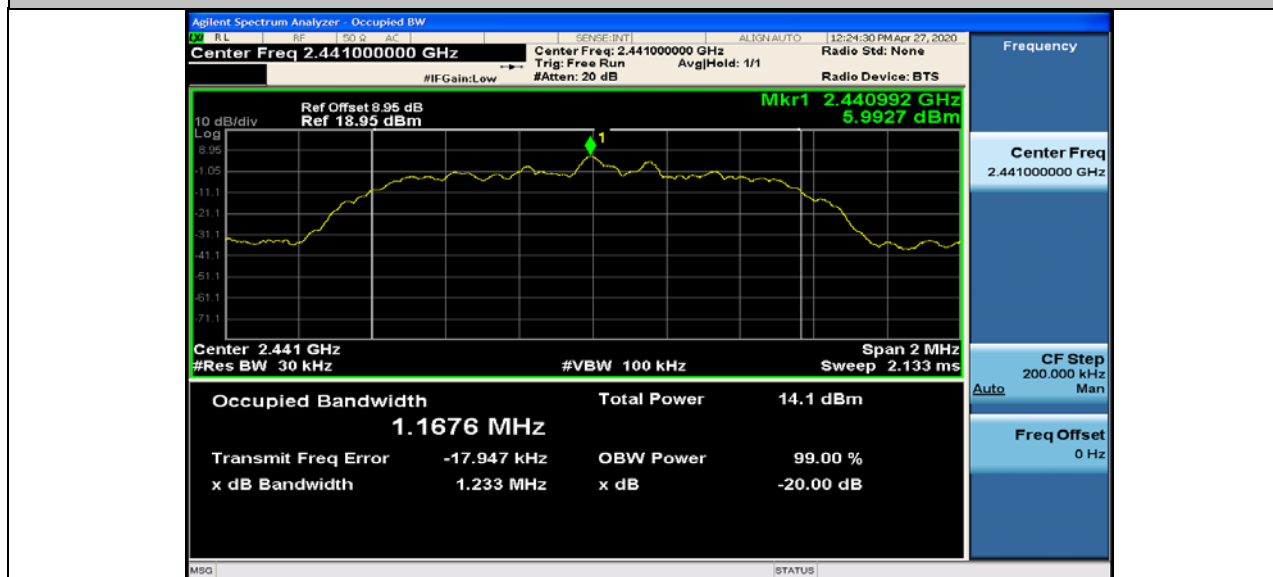
20 dB Bandwidth_DH5_2480



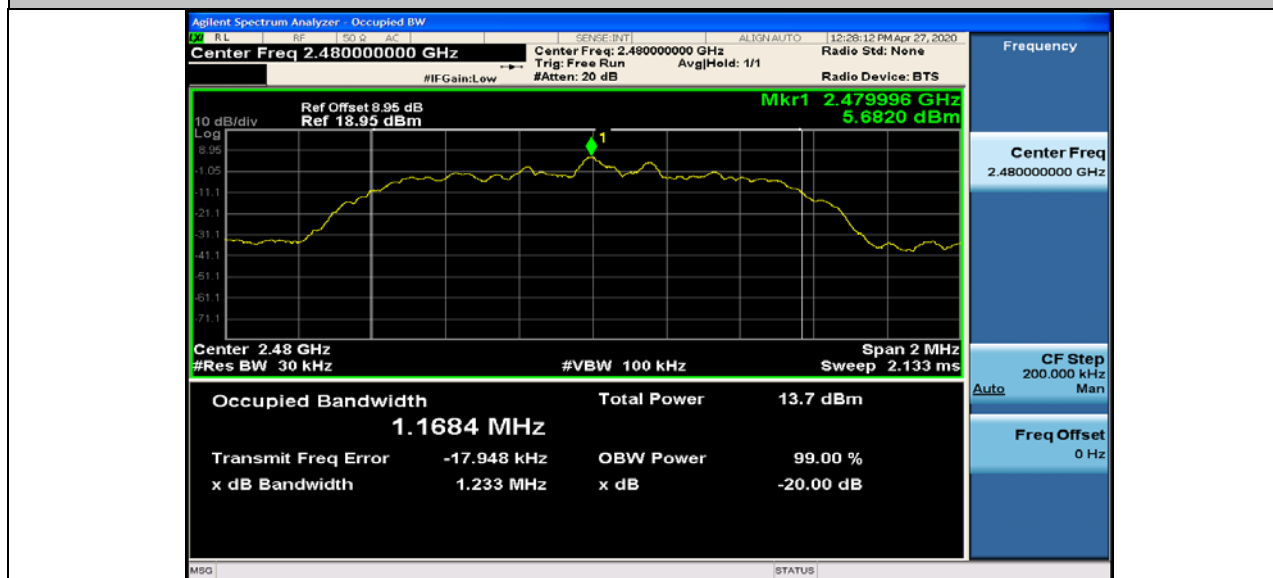
20 dB Bandwidth_2DH5_2402



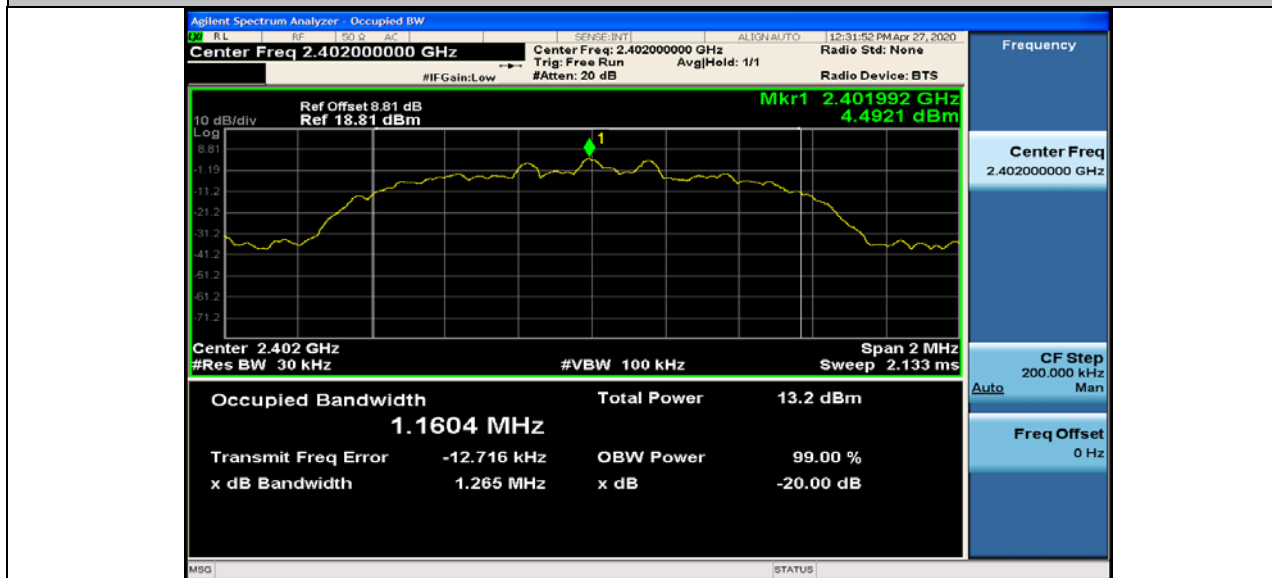
20 dB Bandwidth_2DH5_2441



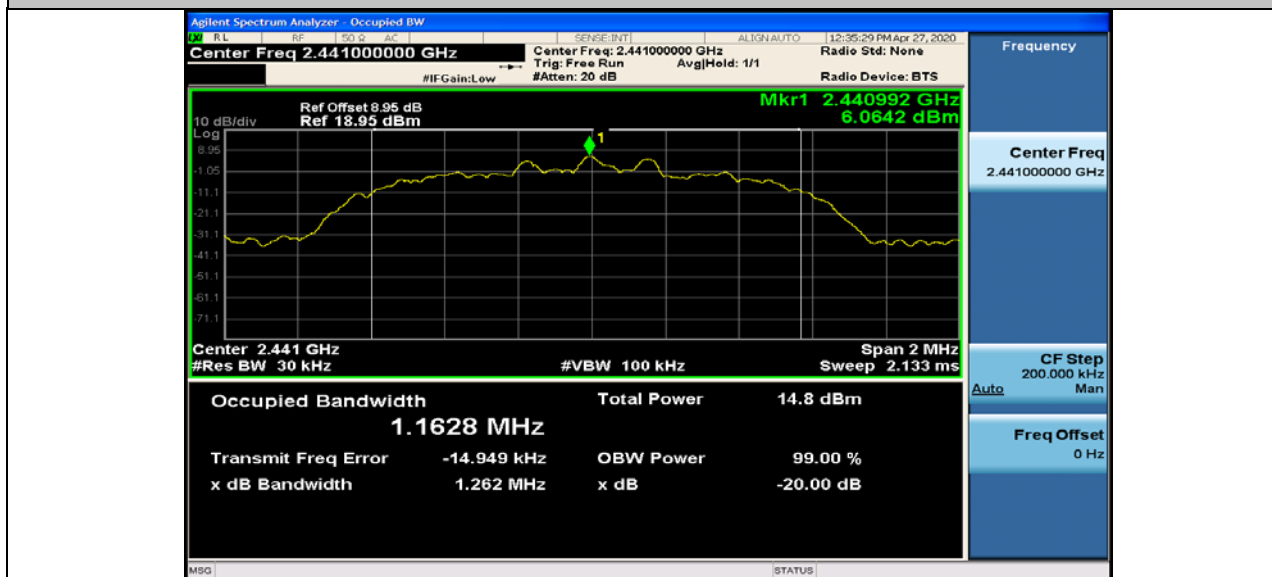
20 dB Bandwidth_2DH5_2480



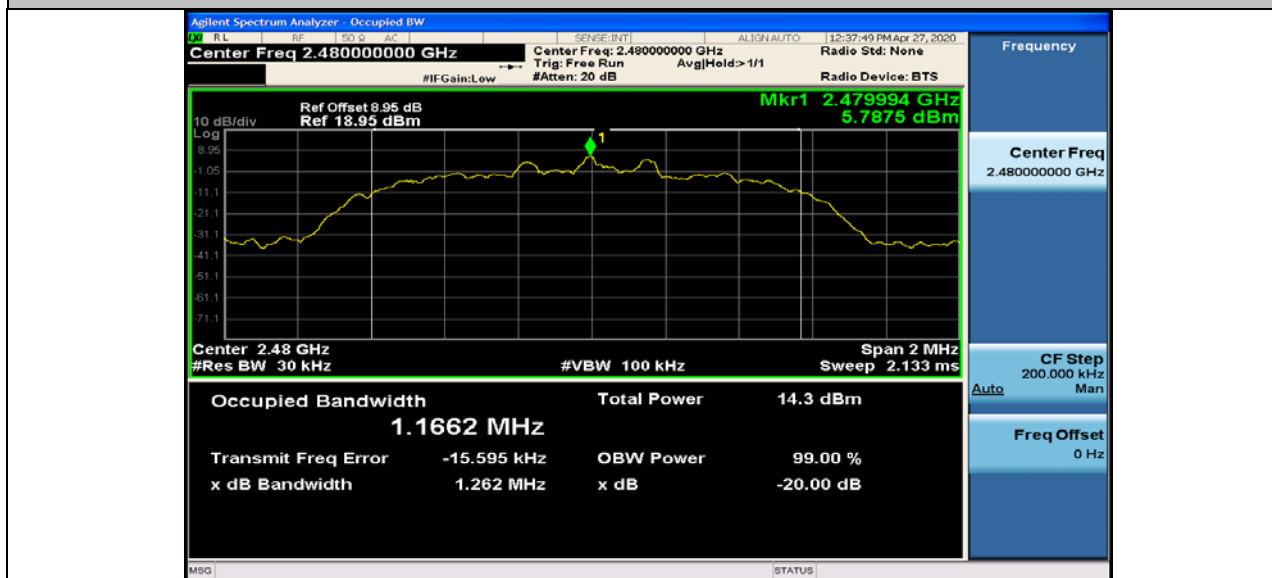
20 dB Bandwidth_3DH5_2402



20 dB Bandwidth_3DH5_2441



20 dB Bandwidth_3DH5_2480





2.Occupied Bandwidth

Test Mode	Test Channel	OBW[MHz]	Limit[MHz]	Verdict
DH5	2402	0.87	---	PASS
DH5	2441	0.85	---	PASS
DH5	2480	0.86	---	PASS
2DH5	2402	1.17	---	PASS
2DH5	2441	1.17	---	PASS
2DH5	2480	1.17	---	PASS
3DH5	2402	1.16	---	PASS
3DH5	2441	1.16	---	PASS
3DH5	2480	1.16	---	PASS

Occupied Bandwidth_DH5_2402



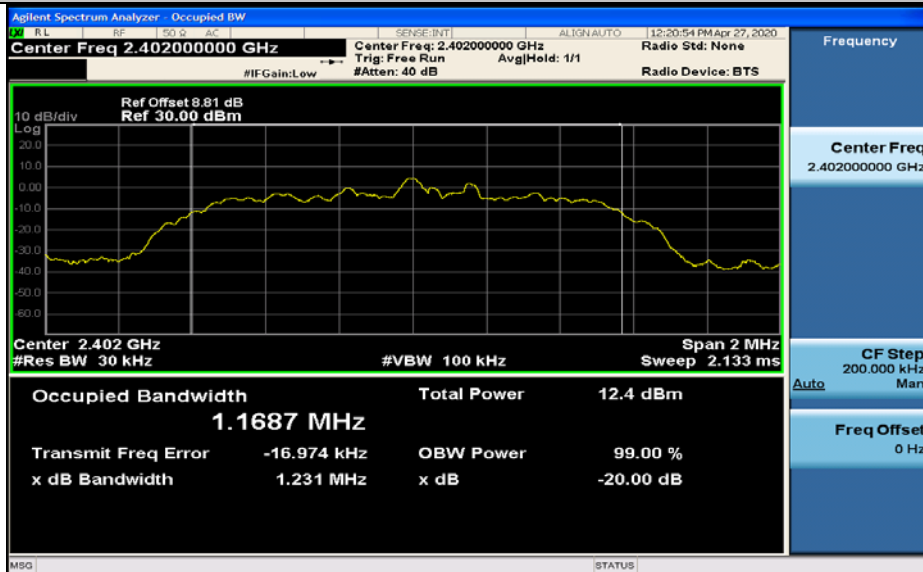
Occupied Bandwidth_DH5_2441



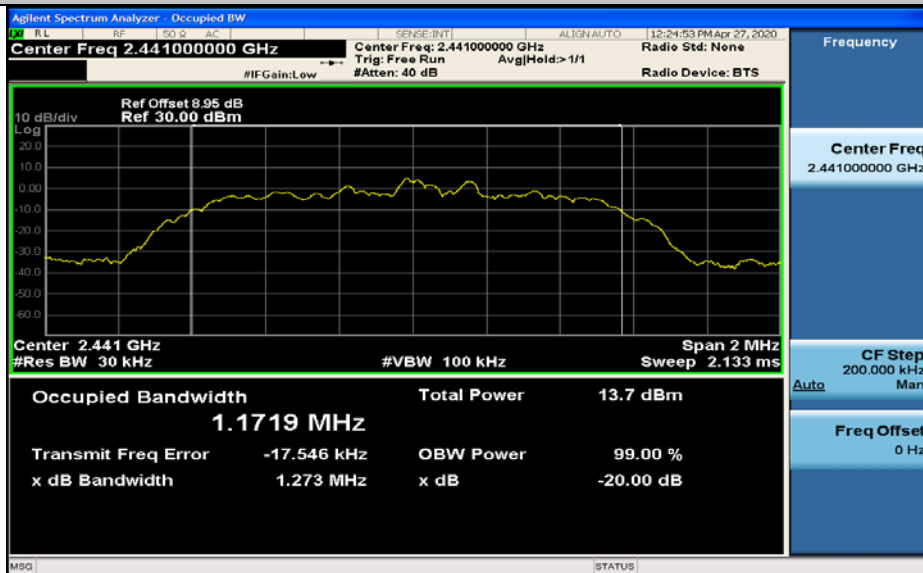
Occupied Bandwidth_DH5_2480



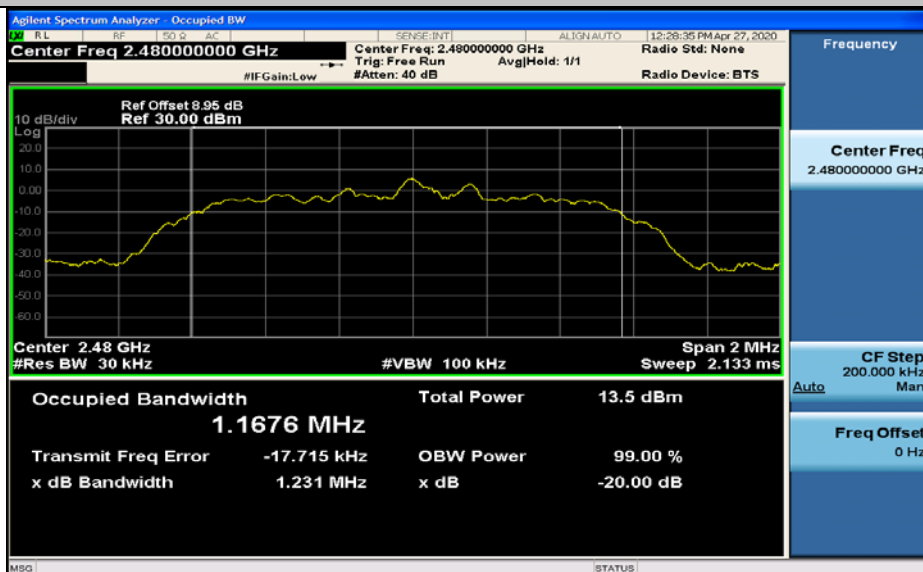
Occupied Bandwidth_2DH5_2402



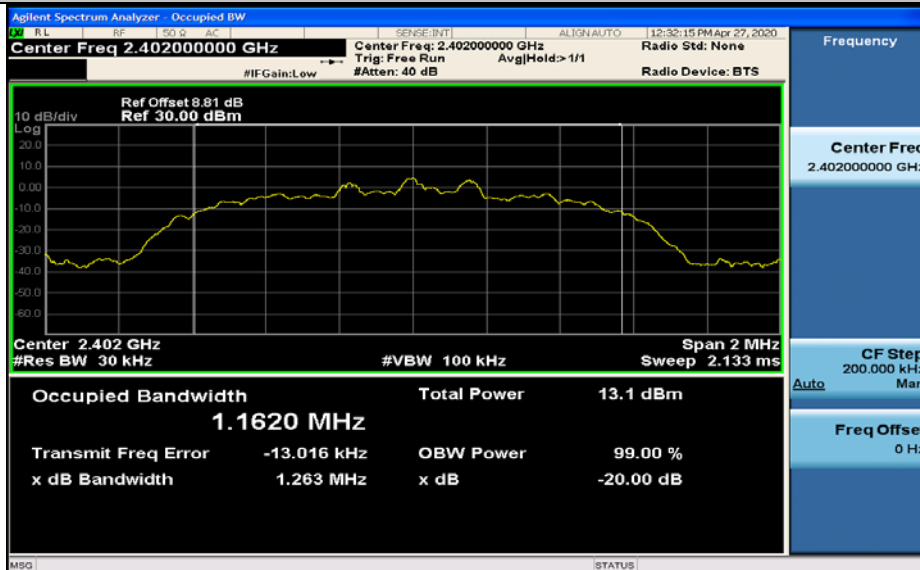
Occupied Bandwidth_2DH5_2441



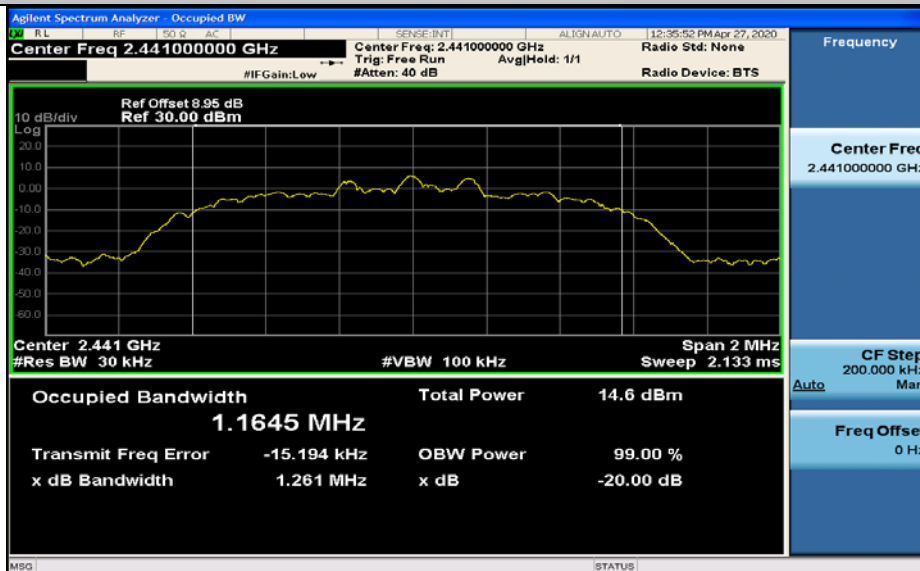
Occupied Bandwidth_2DH5_2480



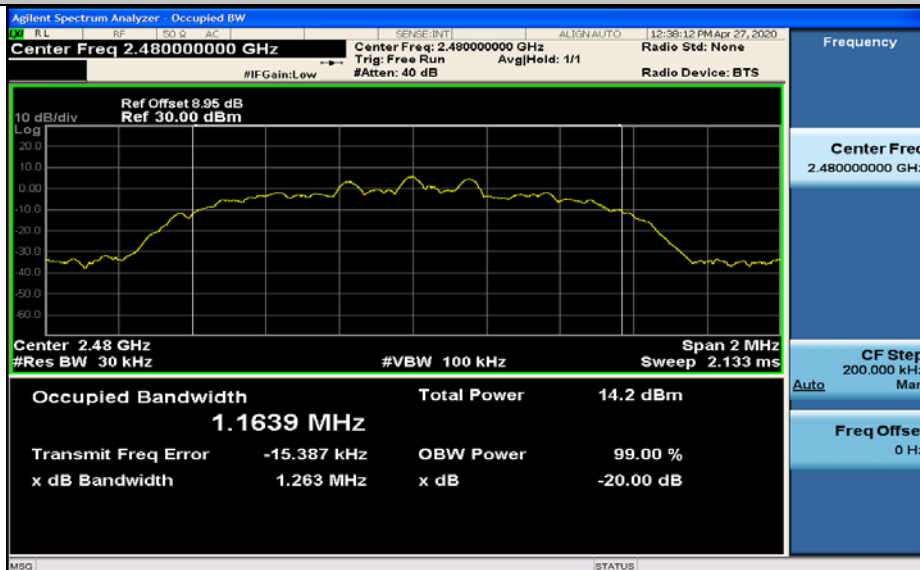
Occupied Bandwidth_3DH5_2402



Occupied Bandwidth_3DH5_2441



Occupied Bandwidth_3DH5_2480

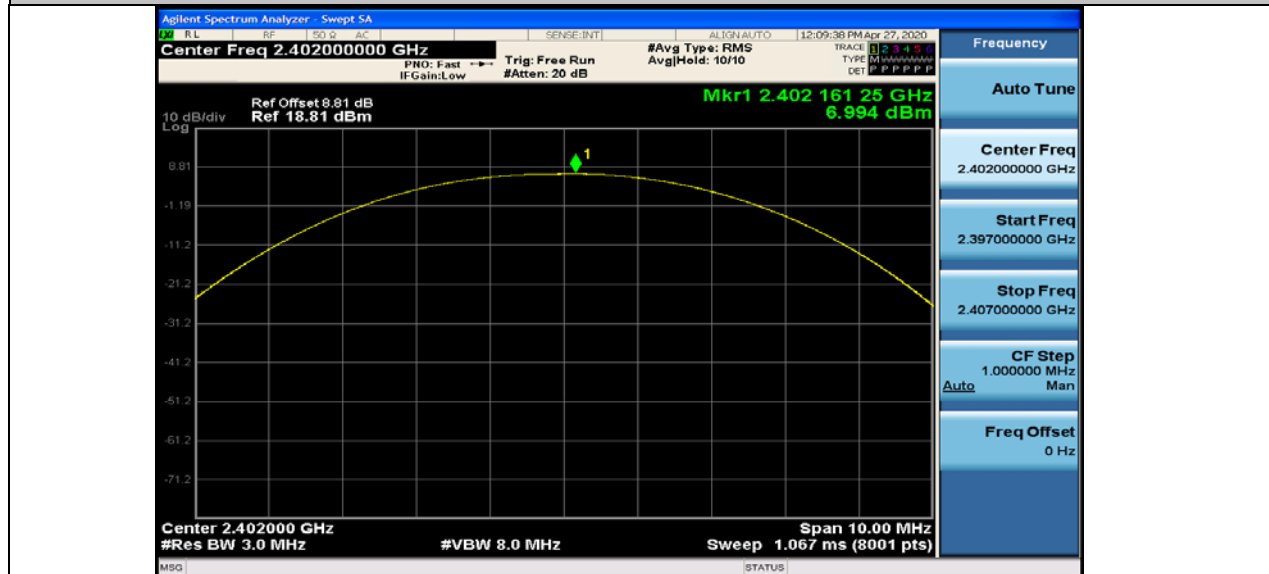




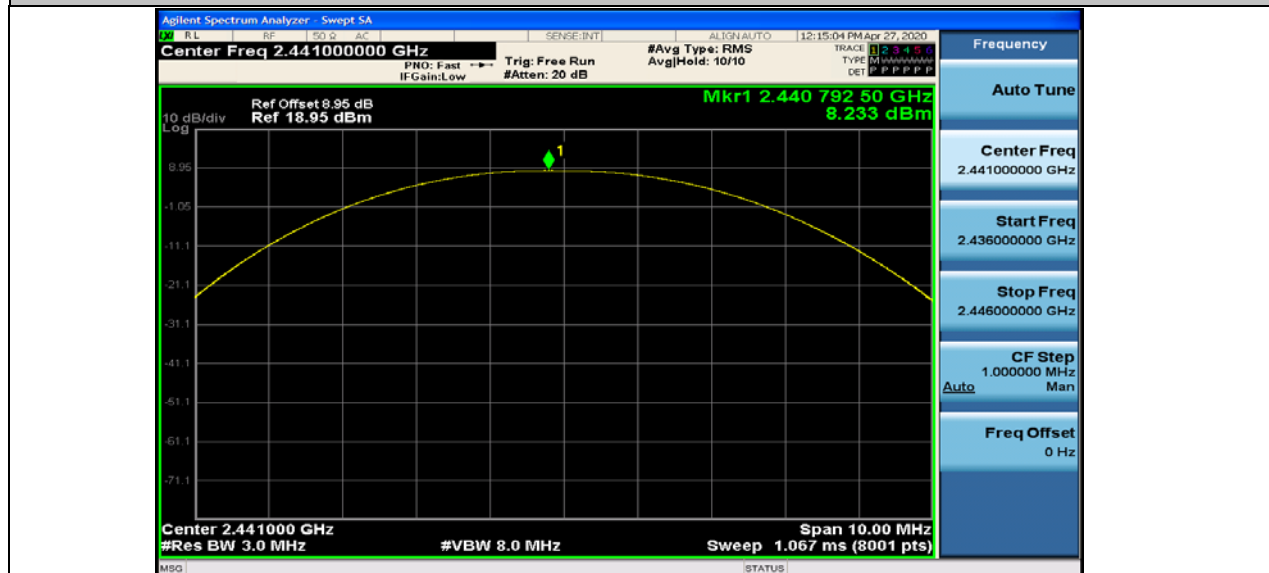
3.Conducted Peak Output Power

Test Mode	Test Channel	Power[dBm]	Limit[dBm]	Verdict
DH5	2402	6.99	21	PASS
DH5	2441	8.23	21	PASS
DH5	2480	7.9	21	PASS
2DH5	2402	6.09	21	PASS
2DH5	2441	7.45	21	PASS
2DH5	2480	7.16	21	PASS
3DH5	2402	6.32	21	PASS
3DH5	2441	7.69	21	PASS
3DH5	2480	7.32	21	PASS

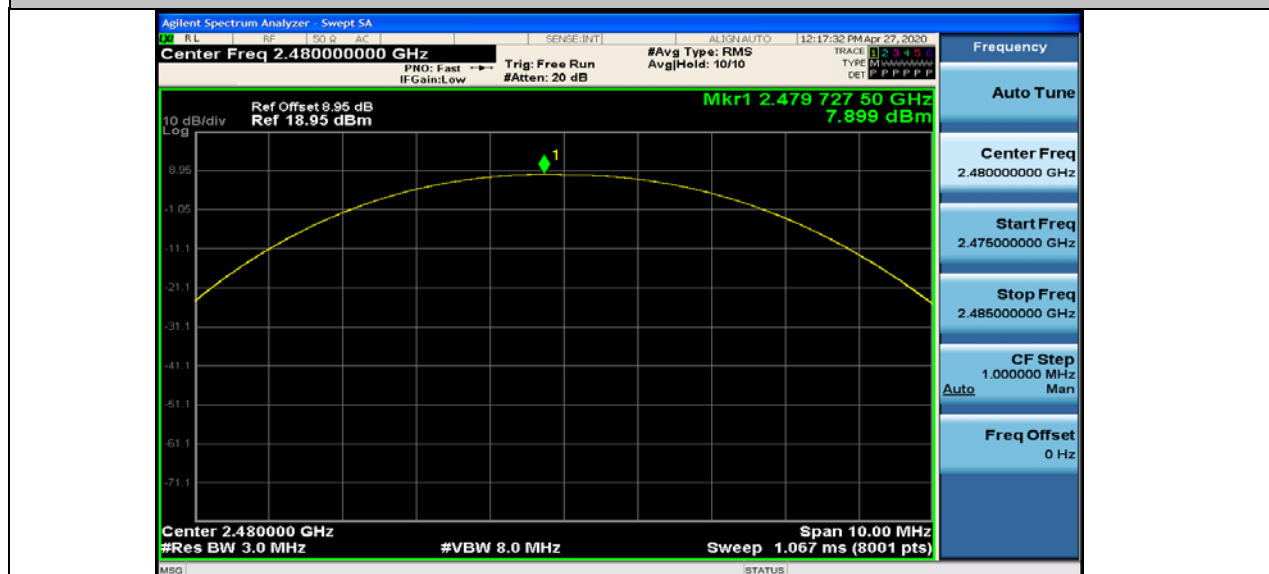
Conducted Peak Output Power_DH5_2402



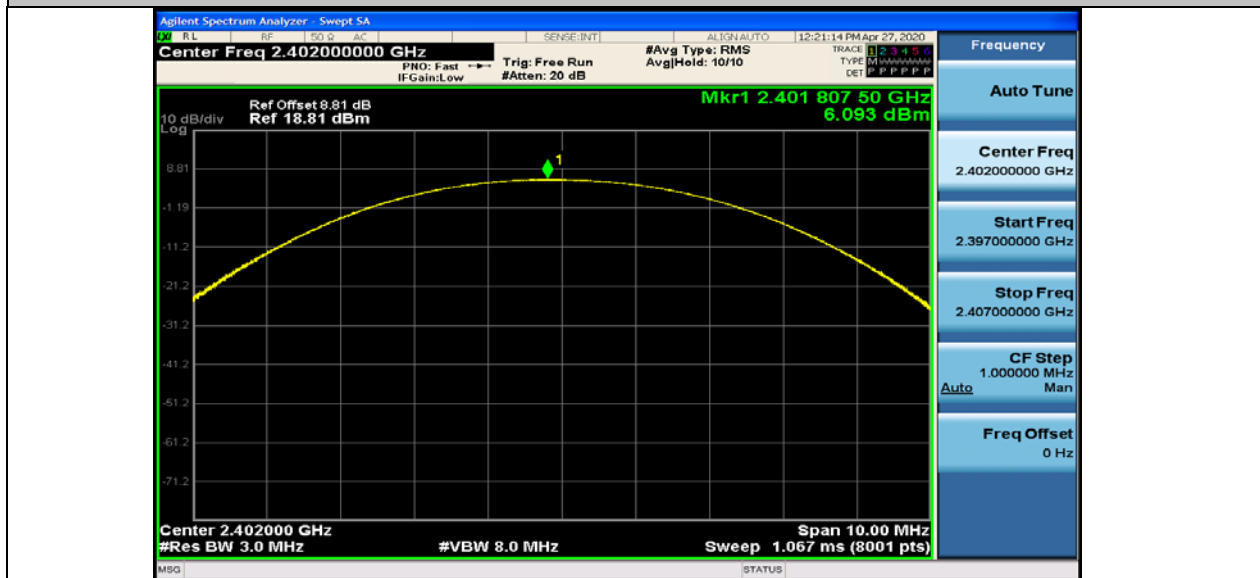
Conducted Peak Output Power_DH5_2441



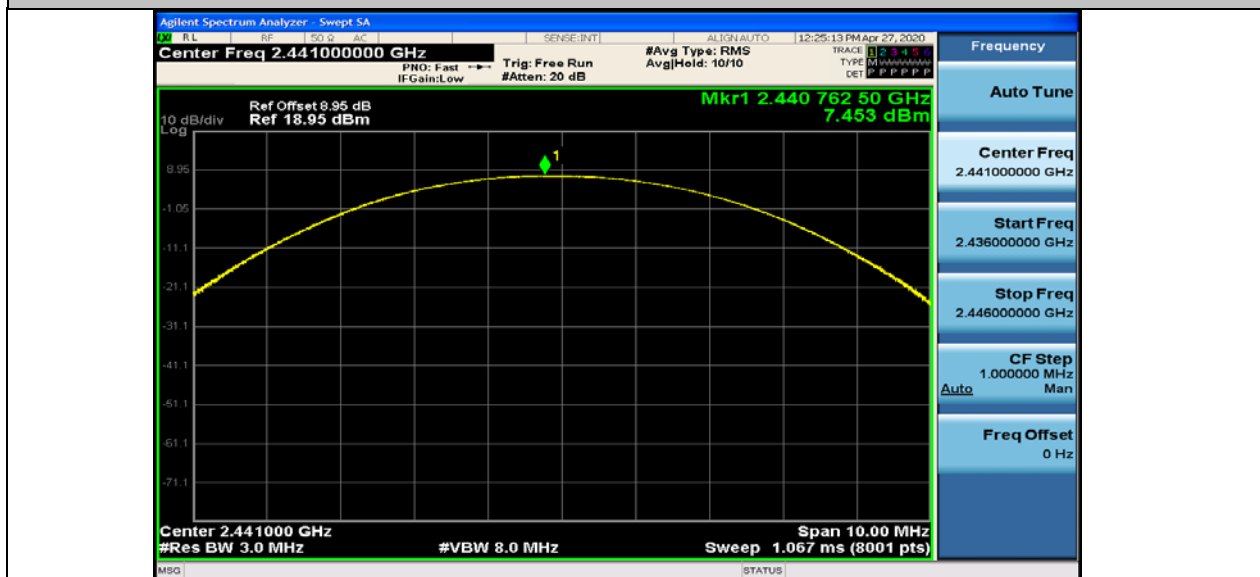
Conducted Peak Output Power_DH5_2480



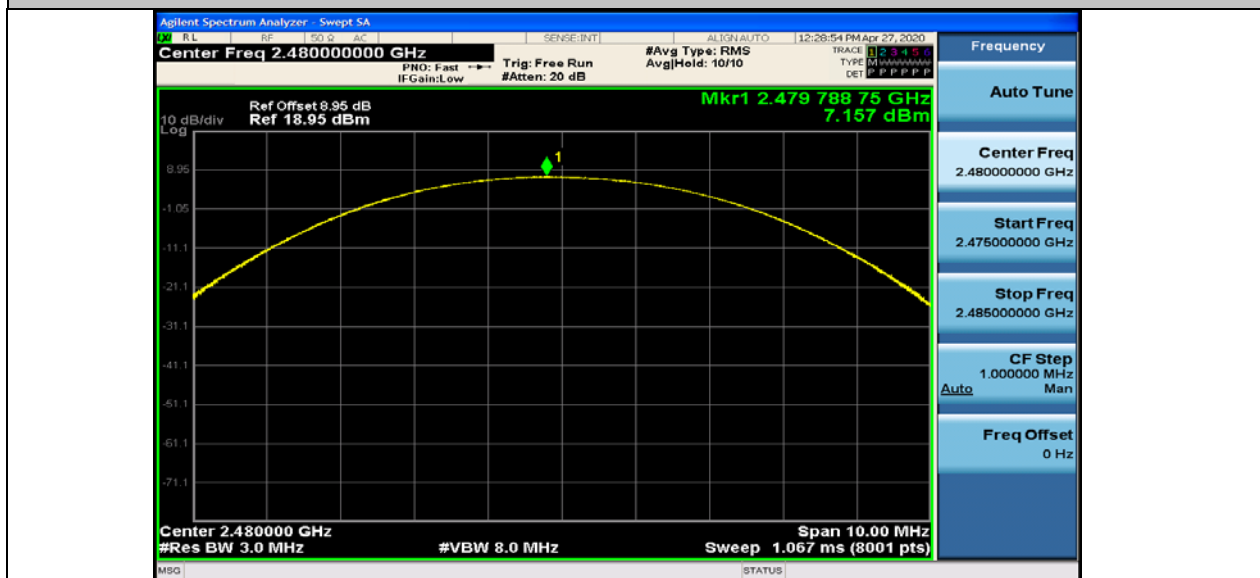
Conducted Peak Output Power_2DH5_2402



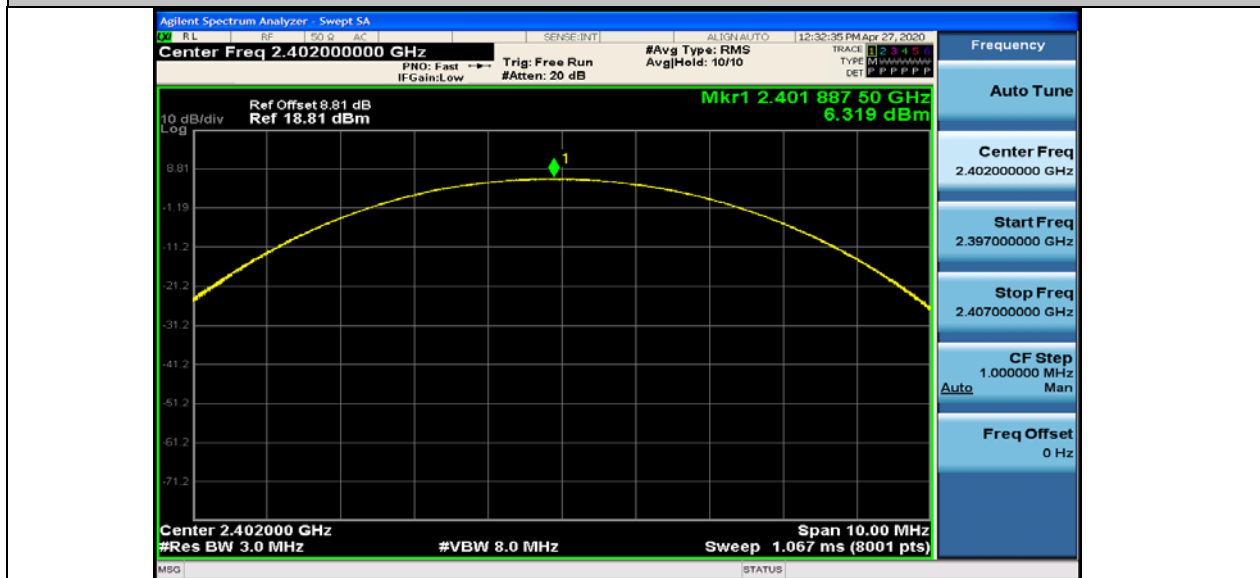
Conducted Peak Output Power_2DH5_2441



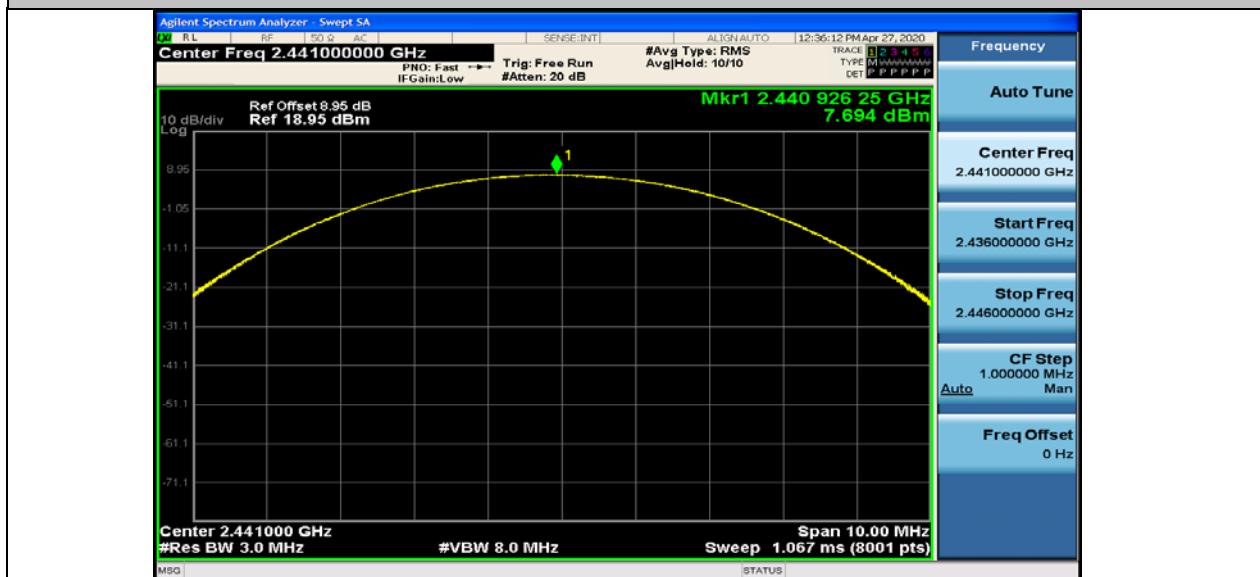
Conducted Peak Output Power_2DH5_2480



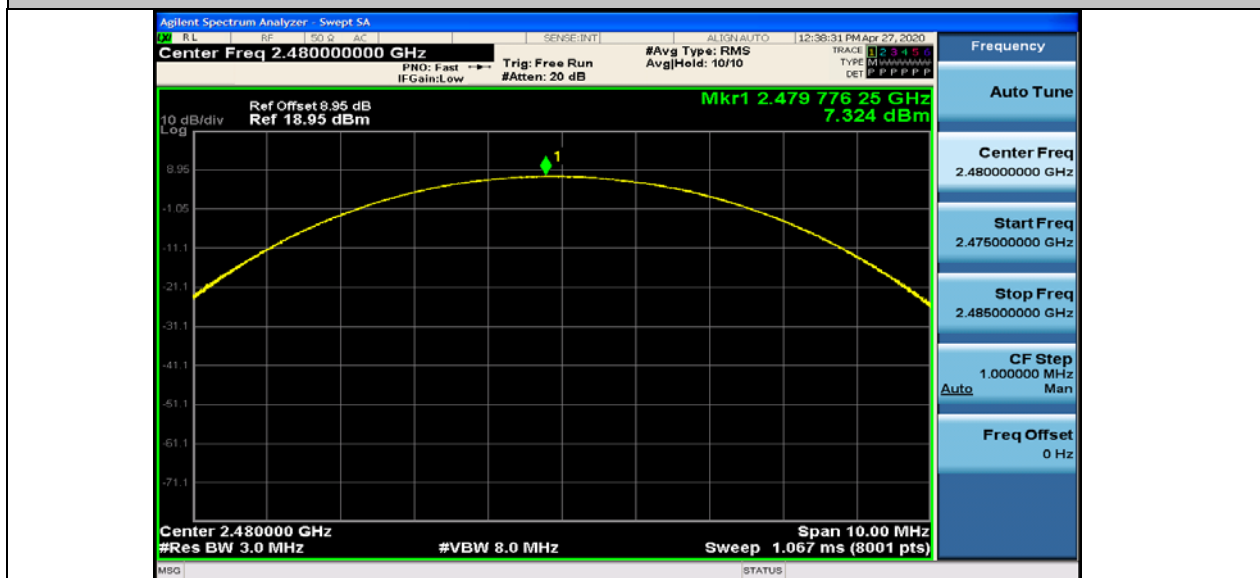
Conducted Peak Output Power_3DH5_2402



Conducted Peak Output Power_3DH5_2441



Conducted Peak Output Power_3DH5_2480





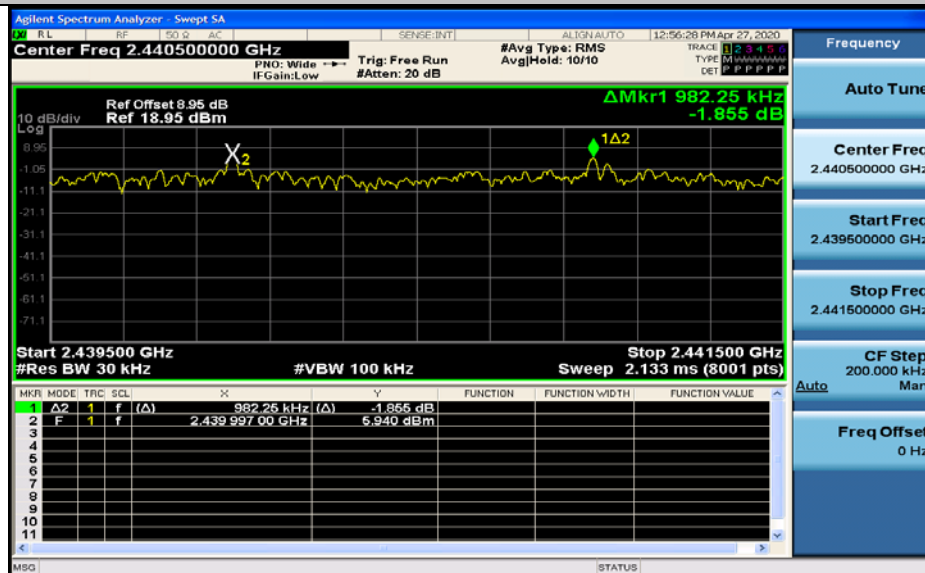
4.Carrier Frequency Separation

Test Mode	Test Channel	Result[MHz]	Limit[MHz]	Verdict
DH5	2441	1.03	0.625	PASS
2DH5	2441	0.98	0.822	PASS
3DH5	2441	1.16	0.841	PASS

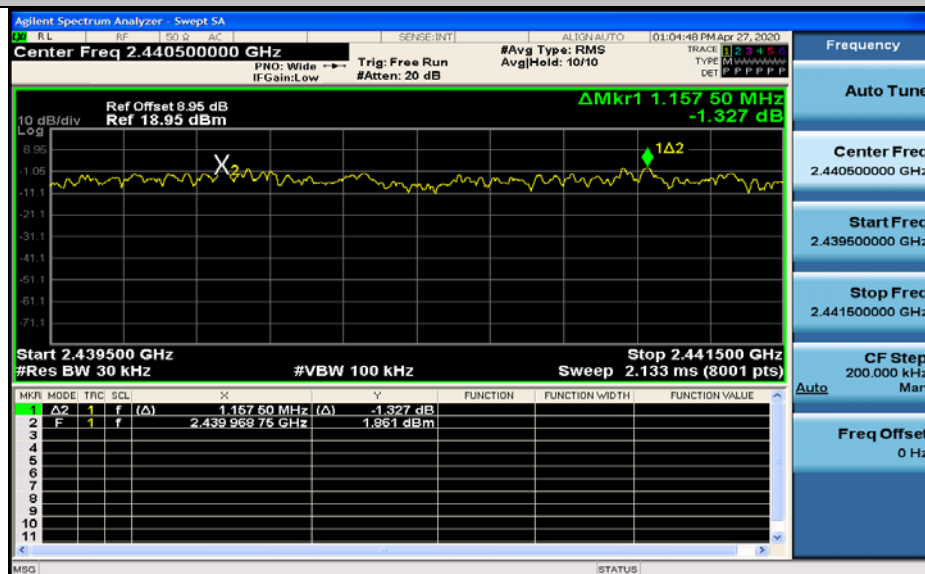
Carrier Frequency Separation_DH5_2441



Carrier Frequency Separation_2DH5_2441



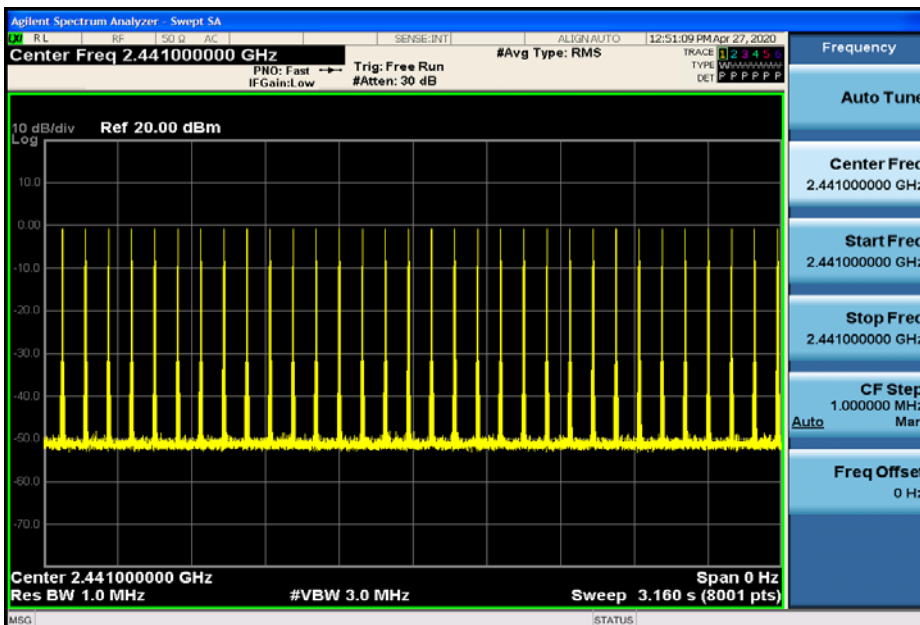
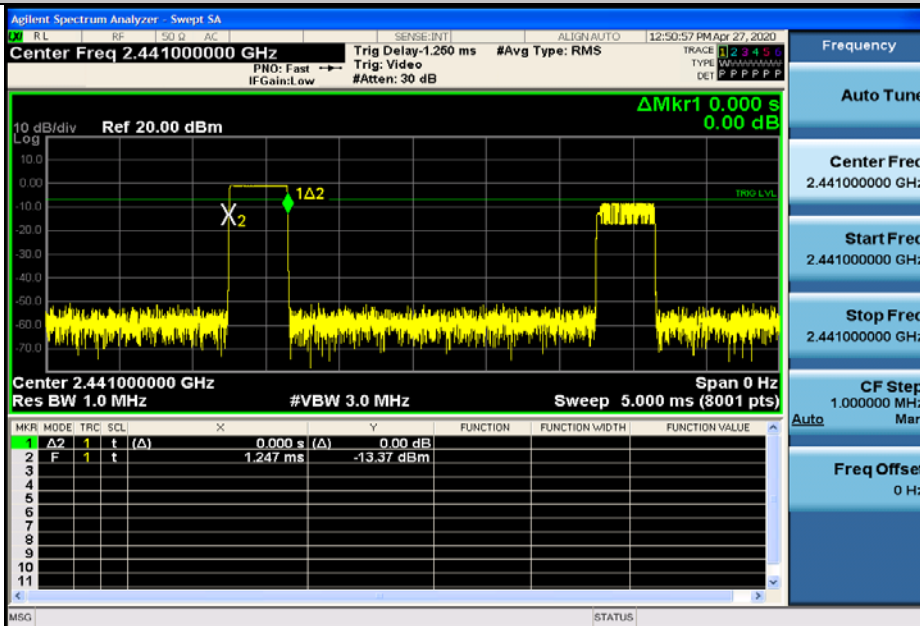
Carrier Frequency Separation_3DH5_2441



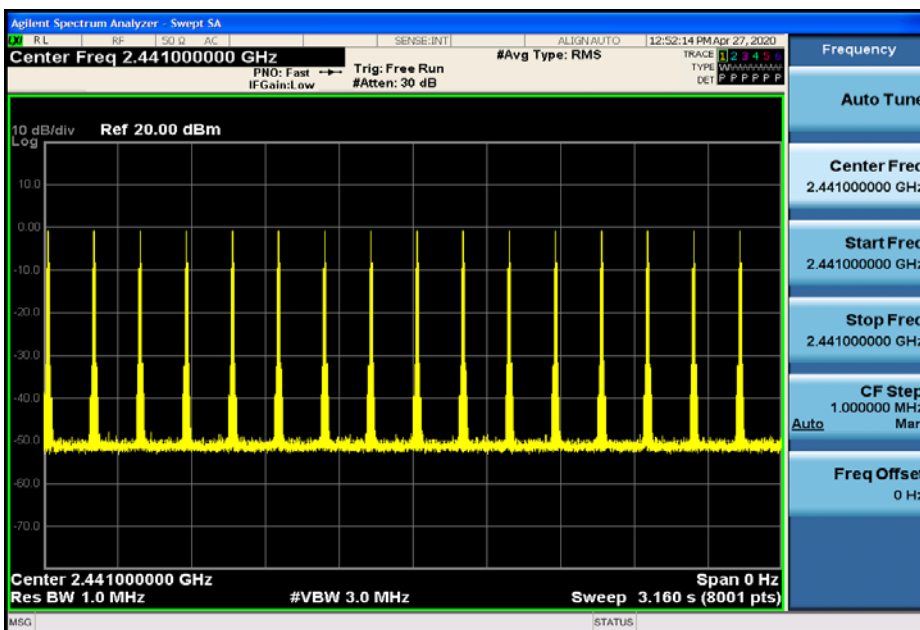
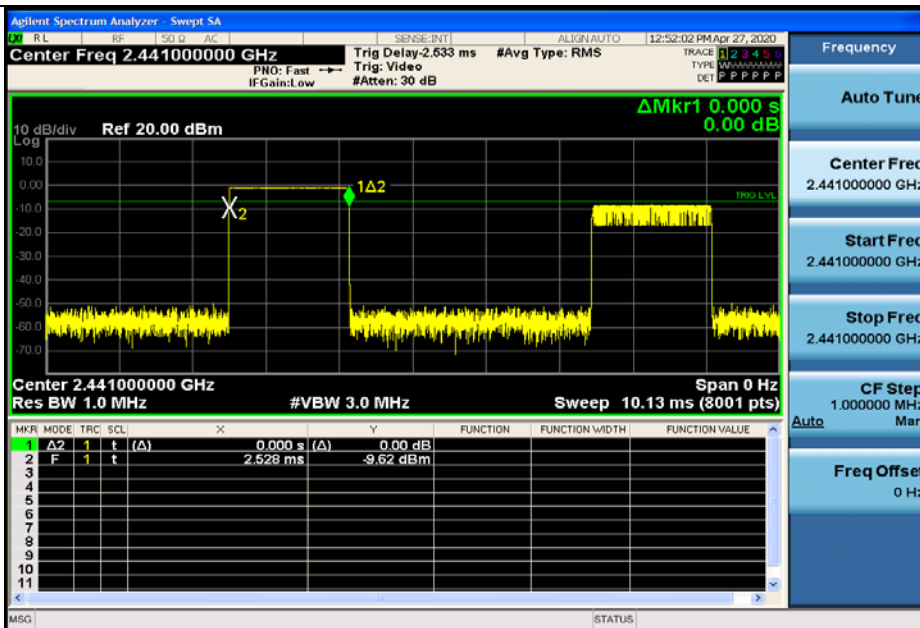
5.Dwell Time

Test Mode	Test Channel	Burst Width[ms/hop/ch]	Total Hops[hop*ch]	Dwell Time[s]	Limit[s]	Verdict
DH1	2441	0.40	320	0.13	0.4	PASS
DH3	2441	1.66	160	0.27	0.4	PASS
DH5	2441	2.90	100	0.29	0.4	PASS
2DH1	2441	0.41	310	0.13	0.4	PASS
2DH3	2441	1.66	160	0.27	0.4	PASS
2DH5	2441	2.91	110	0.32	0.4	PASS
3DH1	2441	0.41	320	0.13	0.4	PASS
3DH3	2441	1.66	160	0.27	0.4	PASS
3DH5	2441	2.91	100	0.29	0.4	PASS

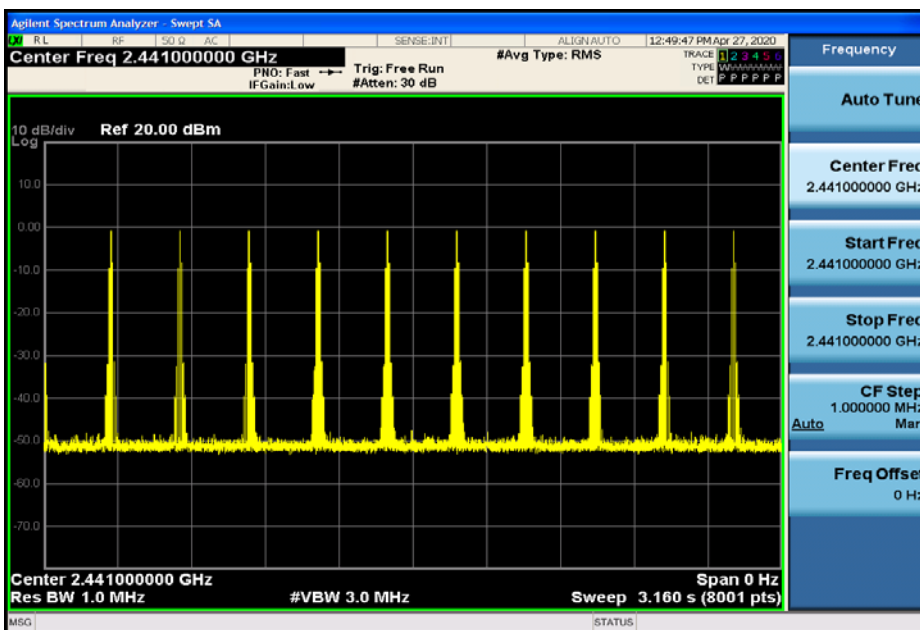
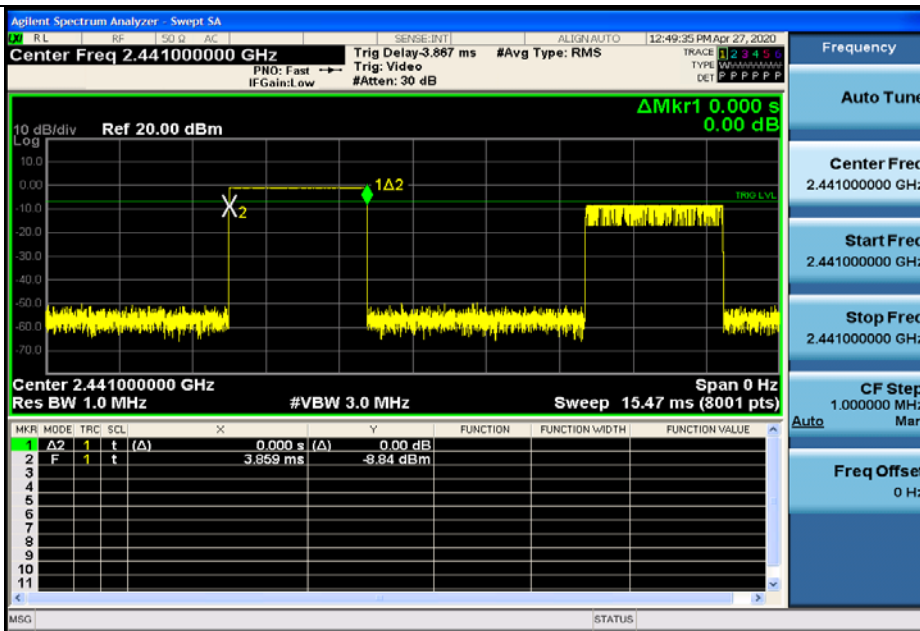
Dwell Time_DH1_2441



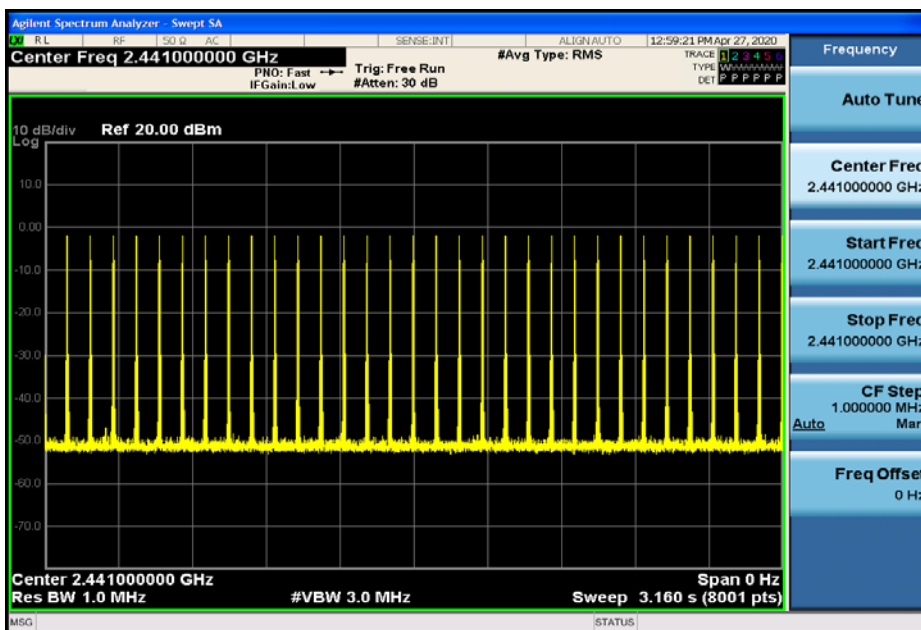
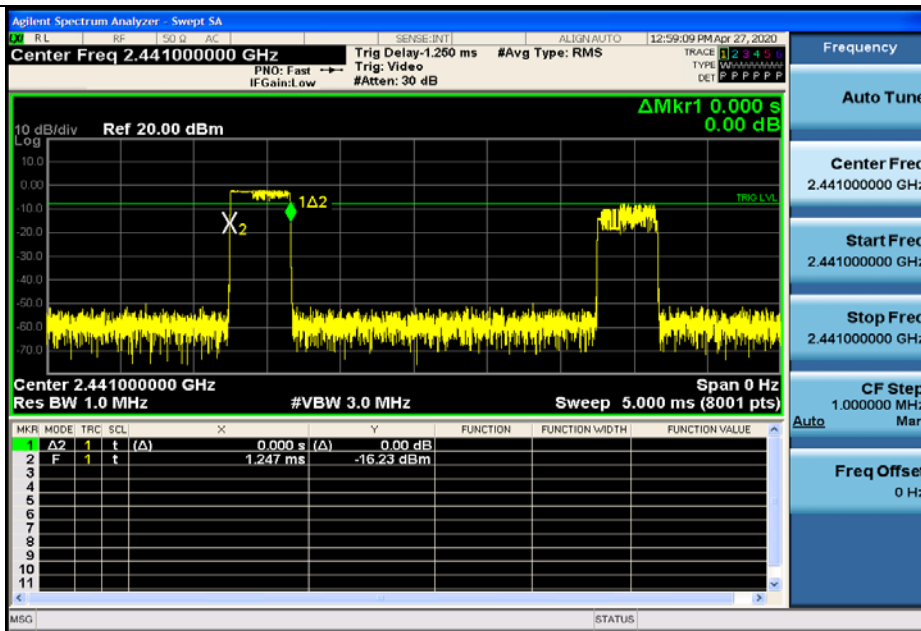
Dwell Time_DH3_2441



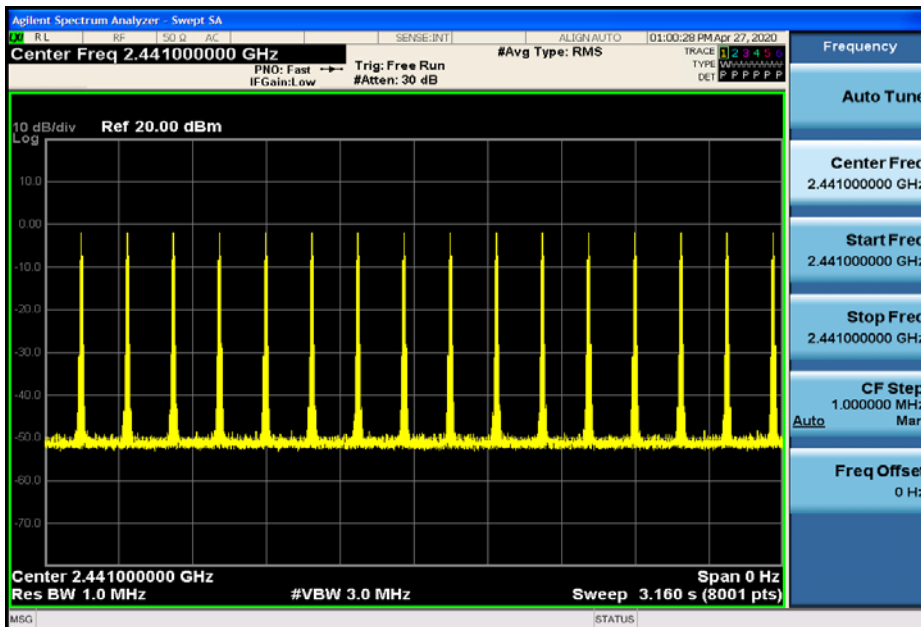
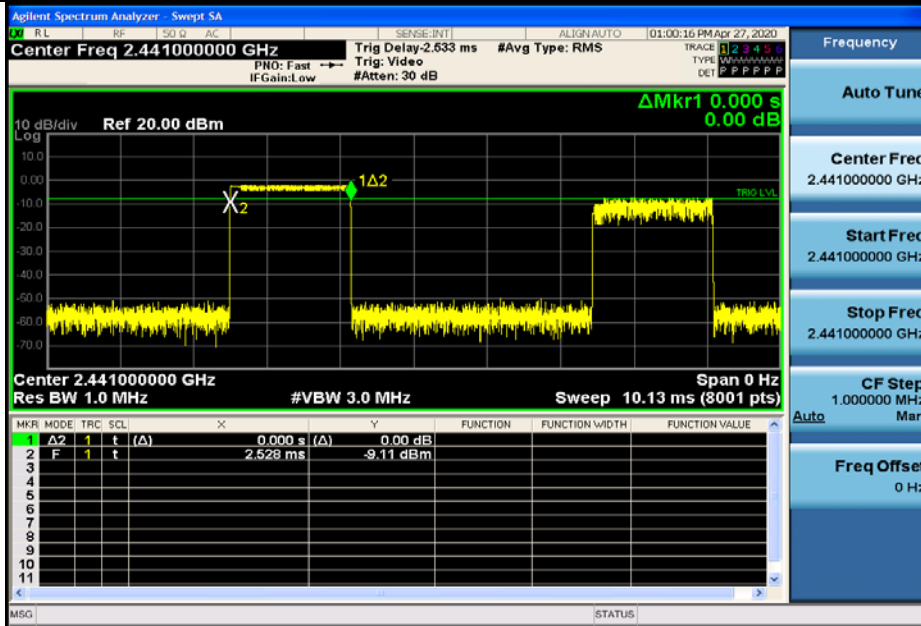
Dwell Time_DH5_2441



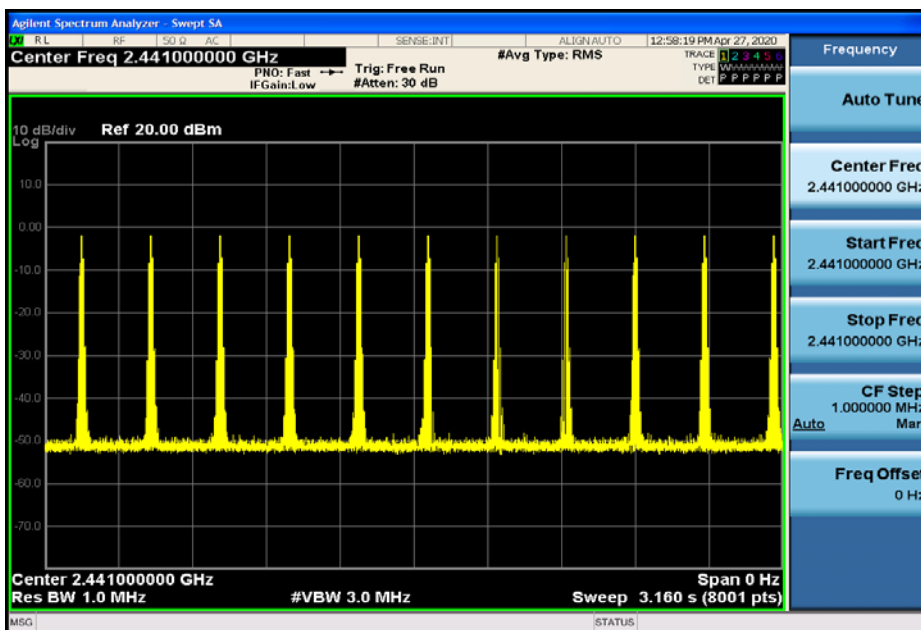
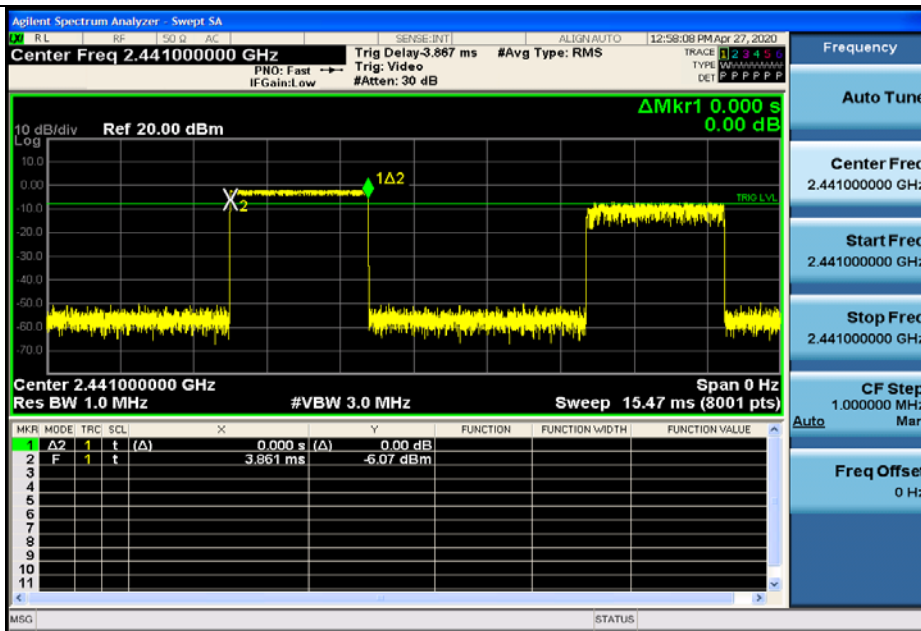
Dwell Time_2DH1_2441



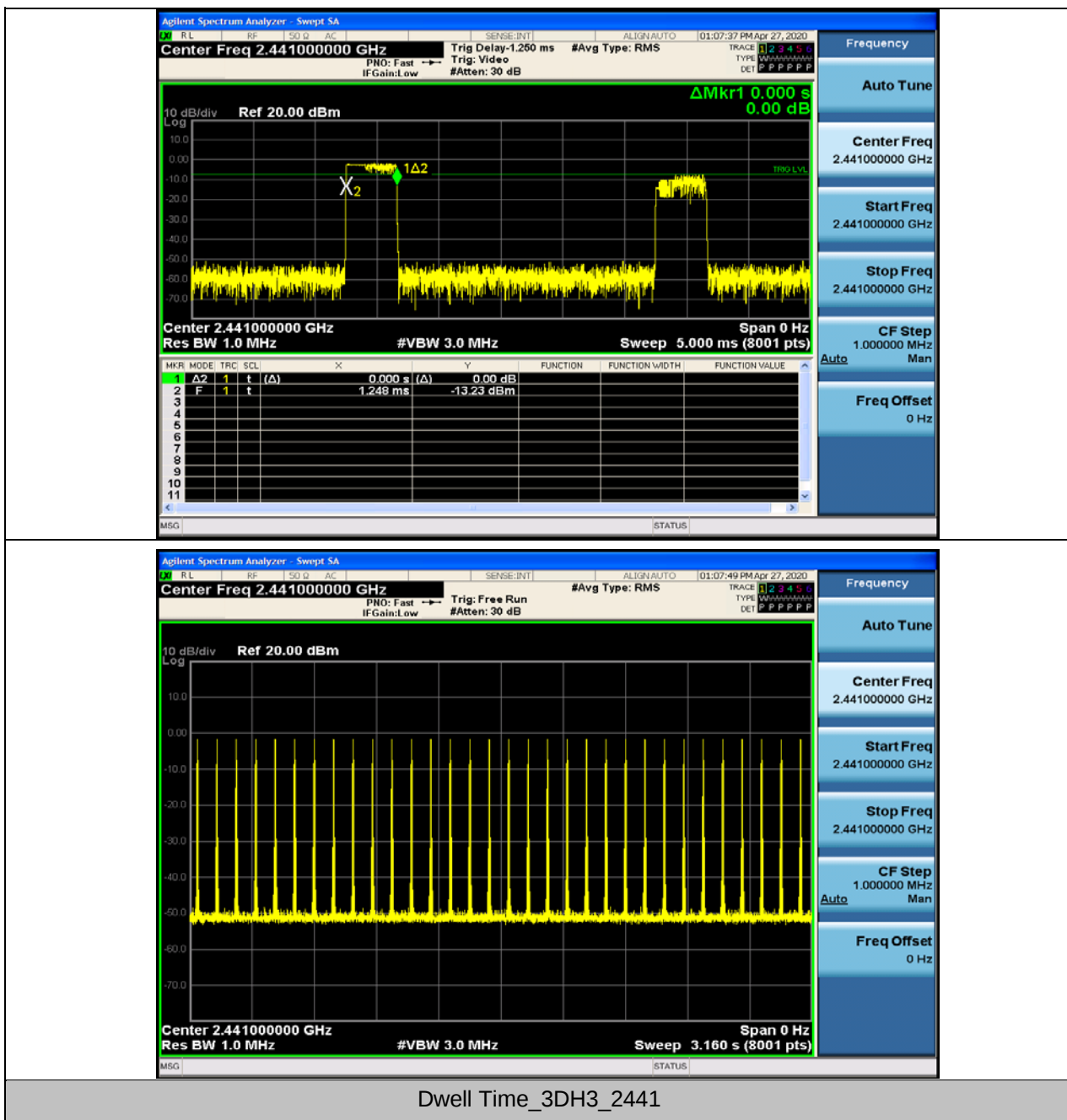
Dwell Time_2DH3_2441



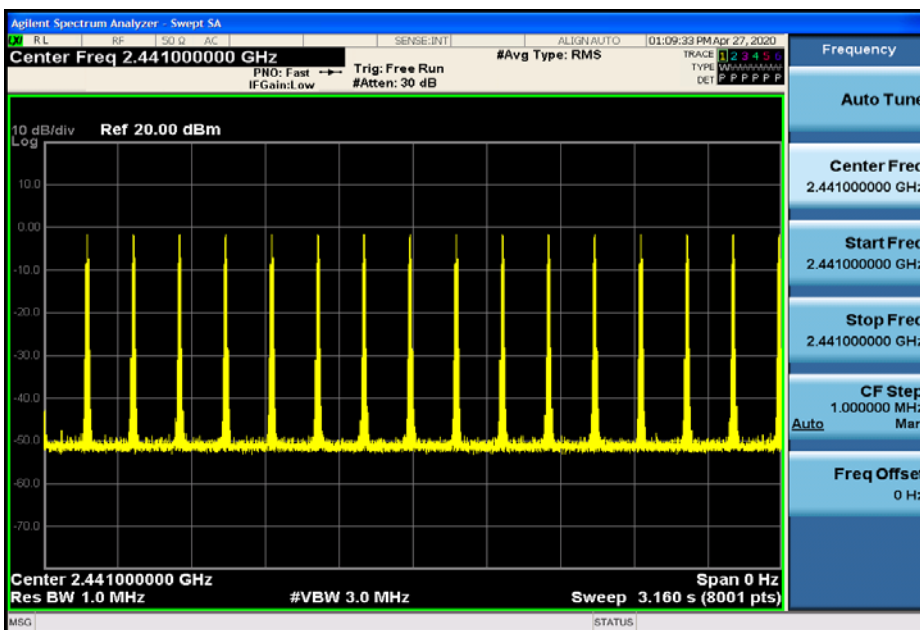
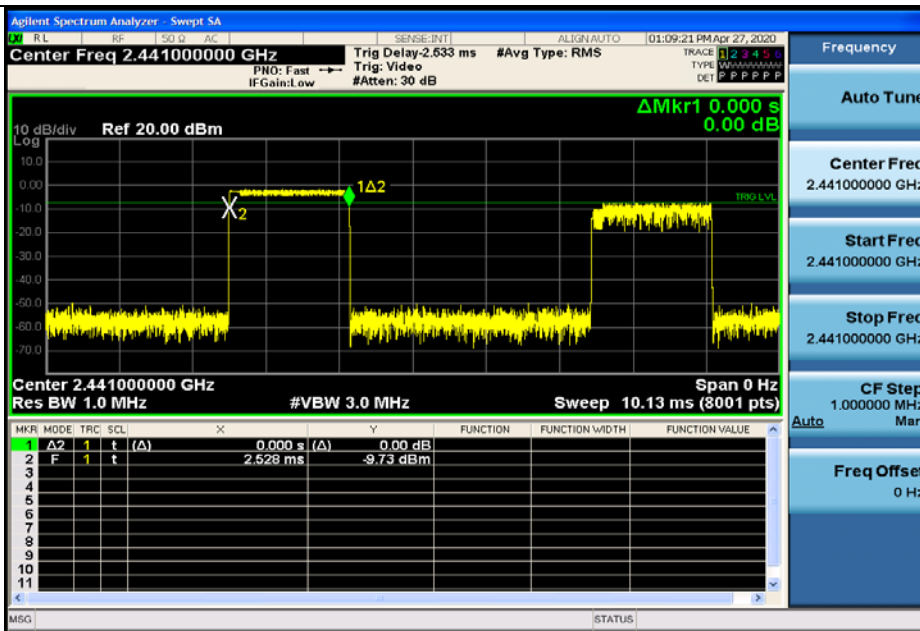
Dwell Time_2DH5_2441



Dwell Time_3DH1_2441



Dwell Time_3DH3_2441



Dwell Time_3DH5_2441

