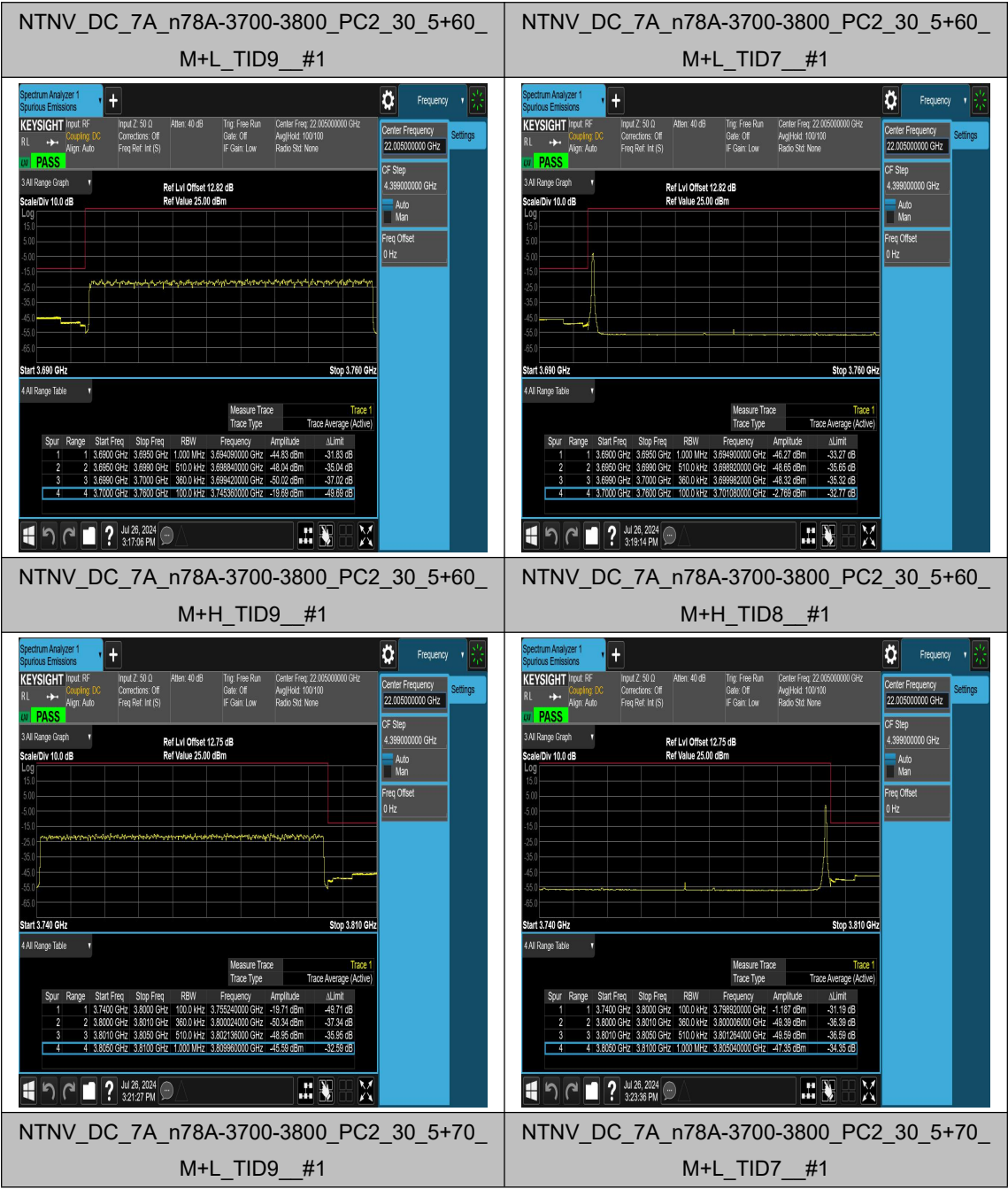




NTNV_DC_7A_n78A-3700-3800_PC2_30_5+60_

M+H_TID9_#1

Spectrum Analyzer 1

Spurious Emissions

+

KEYSIGHT

Input RF

Input Z: 50 Ω

Att: 40 dB

Trig: Free Run

Gate: Off

Center Freq: 22.00500000 GHz

CF Step

22.00500000 GHz

Settings

Align: Auto

Corrections: Off

Freq Ref: Int (S)

AugHld: 100/100

Radio Std: None

IF Gain: Low

4.399000000 GHz

Auto

Man

0 Hz

0 Hz

3 All Range Graph

Ref Lvl Offset 12.75 dB

Ref Value 25.00 dBm

Scale Div 10.0 dB

Log

Start 3.740 GHz

Stop 3.810 GHz

4 All Range Table

Measure Trace

Trace Type

Trace Average (Active)

Trace 1

Spur

Range

Start Freq

Stop Freq

RBW

Frequency

Amplitude

μ Limit

1

1

3.7400 GHz

3.8000 GHz

100.0 kHz

3.795240000 GHz

-19.71 dBm

-49.71 dB

2

2

3.8000 GHz

3.8010 GHz

360.0 kHz

3.800240000 GHz

-58.34 dBm

-57.34 dB

3

3

3.8010 GHz

3.8050 GHz

510.0 kHz

3.801180000 GHz

-49.86 dBm

-35.86 dB

4

4

3.8050 GHz

3.8100 GHz

1.000 MHz

3.809900000 GHz

-45.59 dBm

-32.59 dB

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NTNV_DC_7A_n78A-3700-3800_PC2_30_5+60_

M+H_TID8_#1

Spectrum Analyzer 1

Spurious Emissions

+

KEYSIGHT

Input RF

Input Z: 50 Ω

Att: 40 dB

Trig: Free Run

Gate: Off

Center Freq: 22.00500000 GHz

CF Step

22.00500000 GHz

Settings

Align: Auto

Corrections: Off

Freq Ref: Int (S)

AugHld: 100/100

Radio Std: None

IF Gain: Low

4.399000000 GHz

Auto

Man

0 Hz

0 Hz

3 All Range Graph

Ref Lvl Offset 12.75 dB

Ref Value 25.00 dBm

Scale Div 10.0 dB

Log

Start 3.740 GHz

Stop 3.810 GHz

4 All Range Table

Measure Trace

Trace Type

Trace Average (Active)

Trace 1

Spur

Range

Start Freq

Stop Freq

RBW

Frequency

Amplitude

μ Limit

1

1

3.7400 GHz

3.8000 GHz

100.0 kHz

3.799320000 GHz

-1.167 dBm

-31.16 dB

2

2

3.8000 GHz

3.8010 GHz

360.0 kHz

3.800020000 GHz

-49.59 dBm

-36.59 dB

3

3

3.8010 GHz

3.8050 GHz

510.0 kHz

3.801264000 GHz

-48.59 dBm

-35.59 dB

4

4

3.8050 GHz

3.8100 GHz

1.000 MHz

3.805240000 GHz

-47.35 dBm

-34.35 dB

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NTNV_DC_7A_n78A-3700-3800_PC2_30_5+70_

M+L_TID9_#1

Spectrum Analyzer 1

Spurious Emissions

+

KEYSIGHT

Input RF

Input Z: 50 Ω

Att: 40 dB

Trig: Free Run

Gate: Off

Center Freq: 22.00500000 GHz

CF Step

22.00500000 GHz

Settings

Align: Auto

Corrections: Off

Freq Ref: Int (S)

AugHld: 100/100

Radio Std: None

IF Gain: Low

4.399000000 GHz

Auto

Man

0 Hz

0 Hz

3 All Range Graph

Ref Lvl Offset 12.82 dB

Ref Value 25.00 dBm

Scale Div 10.0 dB

Log

Start 3.690 GHz

Stop 3.700 GHz

4 All Range Table

Measure Trace

Trace Type

Trace Average (Active)

Trace 1

Spur

Range

Start Freq

Stop Freq

RBW

Frequency

Amplitude

μ Limit

1

1

3.6900 GHz

3.6900 GHz

1.000 MHz

3.694200000 GHz

-44.83 dBm

-31.83 dB

2

2

3.6950 GHz

3.6960 GHz

510.0 kHz

3.698400000 GHz

-48.04 dBm

-35.04 dB

3

3

3.6960 GHz

3.7000 GHz

360.0 kHz

3.699420000 GHz

-50.02 dBm

-37.02 dB

4

4

3.7000 GHz

3.7600 GHz

100.0 kHz

3.745300000 GHz

-19.69 dBm

-49.69 dB

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NTNV_DC_7A_n78A-3700-3800_PC2_30_5+70_

M+L_TID7_#1

Spectrum Analyzer 1

Spurious Emissions

+

KEYSIGHT

Input RF

Input Z: 50 Ω

Att: 40 dB

Trig: Free Run

Gate: Off

Center Freq: 22.00500000 GHz

CF Step

22.00500000 GHz

Settings

Align: Auto

Corrections: Off

Freq Ref: Int (S)

AugHld: 100/100

Radio Std: None

IF Gain: Low

4.399000000 GHz

Auto

Man

0 Hz

0 Hz

3 All Range Graph

Ref Lvl Offset 12.82 dB

Ref Value 25.00 dBm

Scale Div 10.0 dB

Log

Start 3.690 GHz

Stop 3.700 GHz

4 All Range Table

Measure Trace

Trace Type

Trace Average (Active)

Trace 1

Spur

Range

Start Freq

Stop Freq

RBW

Frequency

Amplitude

μ Limit

1

1

3.6900 GHz

3.6950 GHz

1.000 MHz

3.694200000 GHz

-46.87 dBm

-33.87 dB

2

2

3.6960 GHz

3.6980 GHz

510.0 kHz

3.698200000 GHz

-48.66 dBm

-35.66 dB

3

3

3.6960 GHz

3.7000 GHz

360.0 kHz

3.698420000 GHz

-48.32 dBm

-35.32 dB

4

4

3.7000 GHz

3.7600 GHz

100.0 kHz

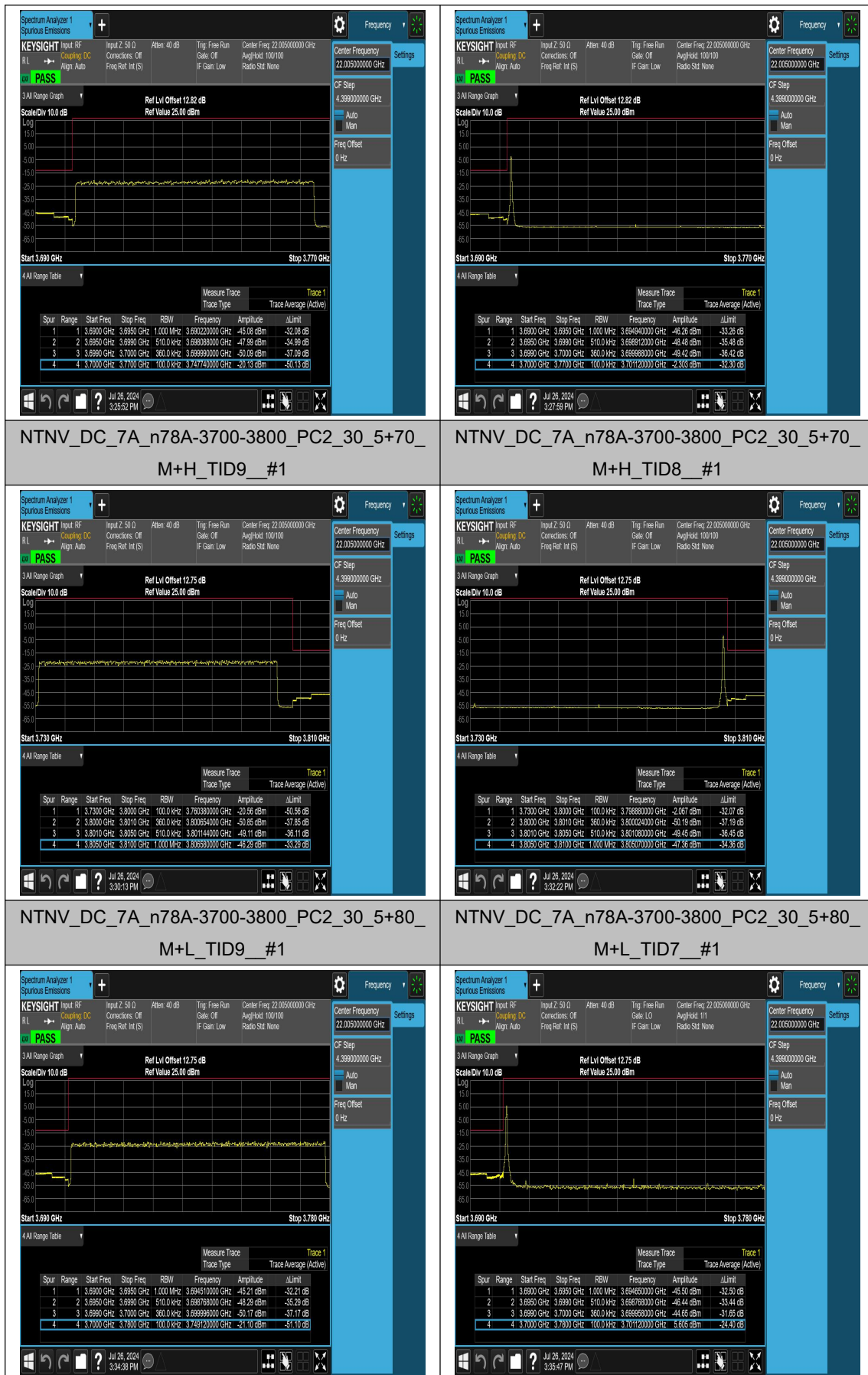
3.710200000 GHz

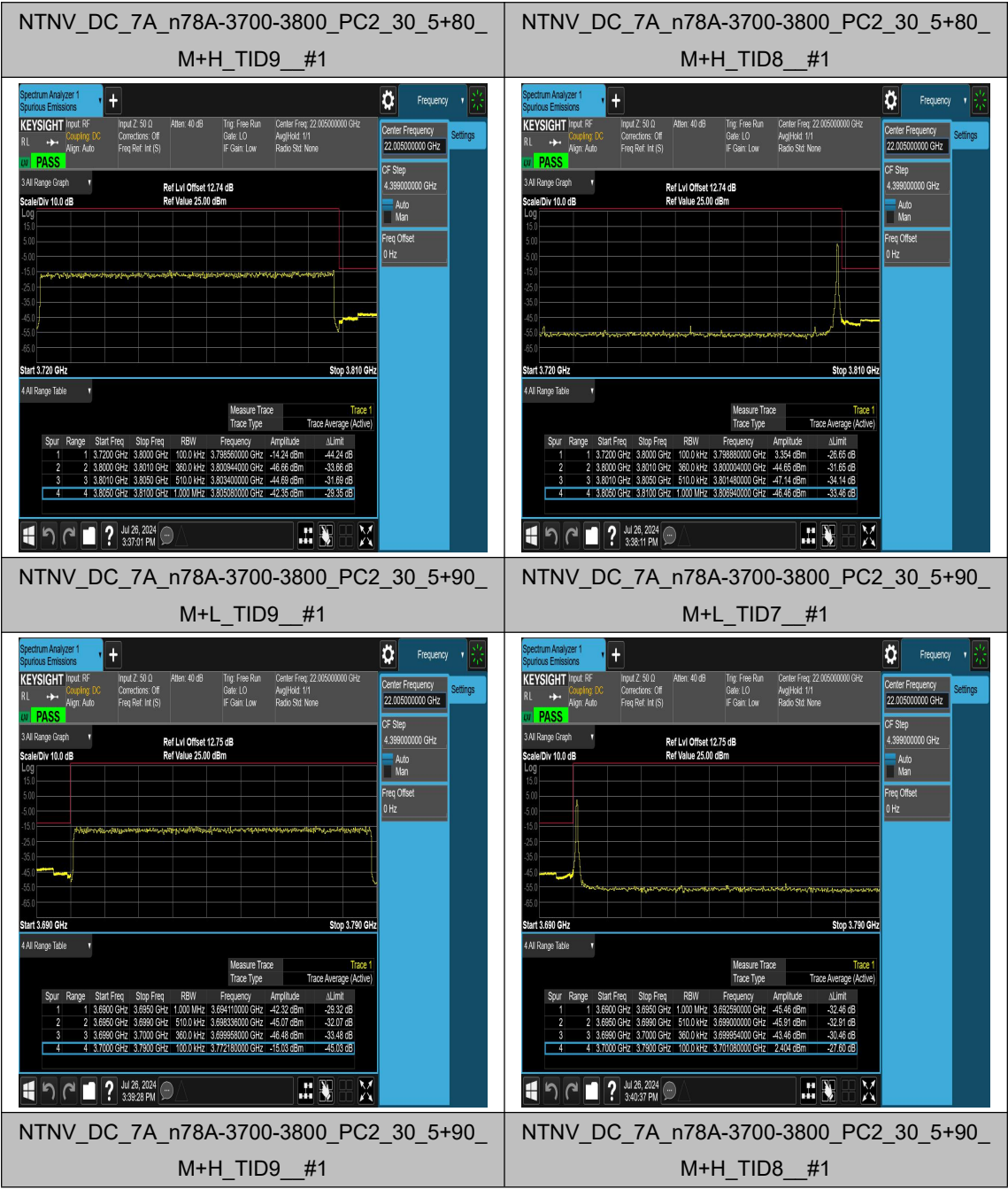
-2.769 dBm

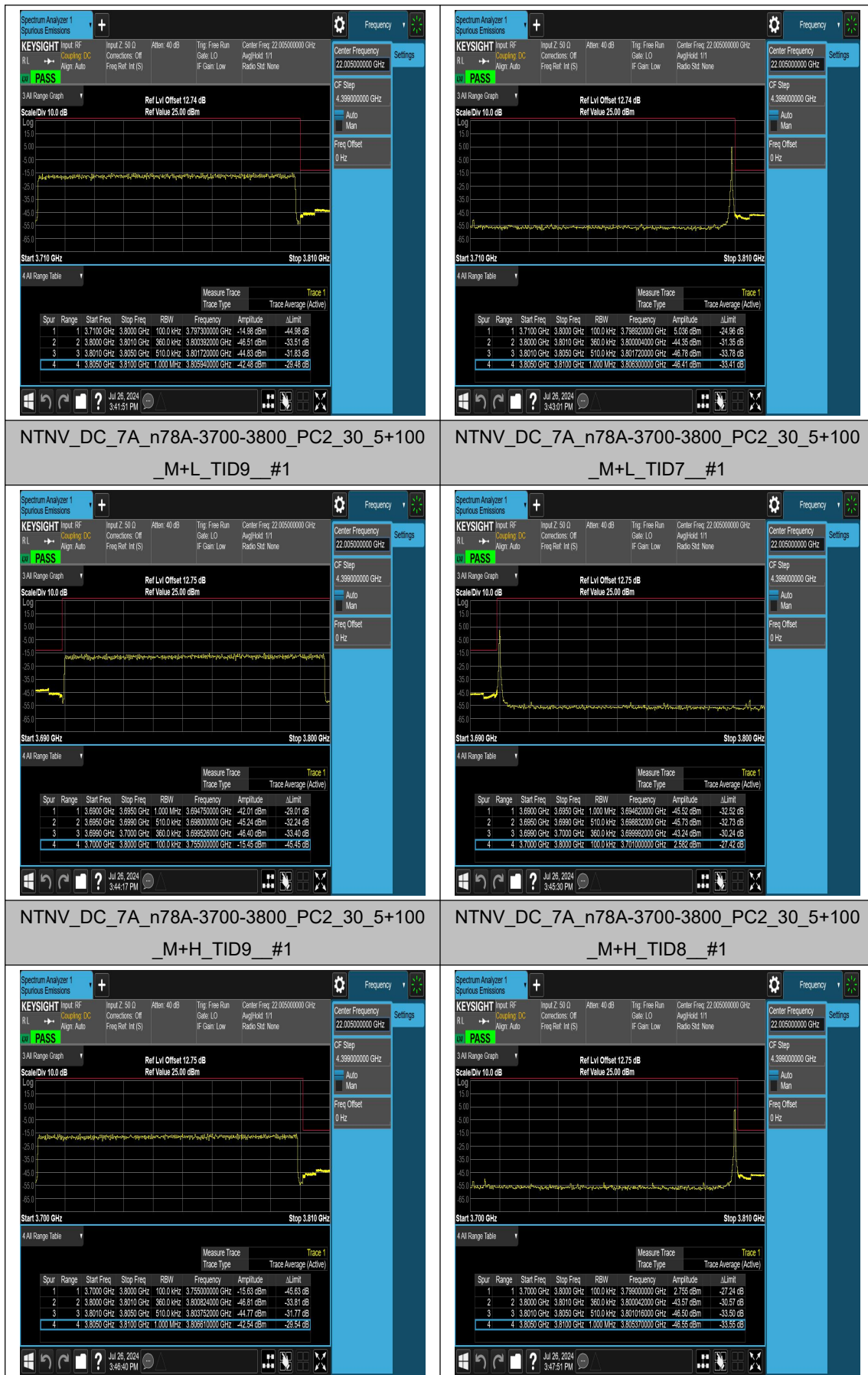
-32.77 dB

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Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SC S	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-63.06	-43	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	0.15	30	-72.99	-33	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	30	1000	-61.60	-13	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	1000	6000	-40.70	-13	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	6000	18000	-49.99	-13	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	18000	40000	-44.45	-13	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-62.84	-43	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-73.65	-33	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-61.60	-13	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-40.76	-13	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-49.96	-13	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-44.26	-13	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-62.62	-43	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-73.01	-33	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-61.92	-13	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-40.81	-13	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-49.91	-13	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-44.31	-13	PASS

3700-3800										
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-62.27	-43	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Outer_Full	0.15	30	-73.22	-33	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Outer_Full	30	1000	-61.69	-13	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Outer_Full	1000	6000	-40.69	-13	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Outer_Full	6000	18000	-49.79	-13	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Outer_Full	18000	40000	-44.22	-13	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-63.57	-43	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-73.20	-33	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-61.85	-13	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-40.74	-13	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-49.93	-13	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-44.51	-13	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-62.69	-43	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-73.41	-33	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-61.69	-13	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-40.77	-13	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-49.93	-13	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-44.42	-13	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-63.03	-43	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	0.15	30	-73.30	-33	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	30	1000	-61.65	-13	PASS

DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	1000	6000	-40.77	-13	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	6000	18000	-50.02	-13	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	18000	40000	-44.20	-13	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-62.89	-43	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.32	-33	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-61.84	-13	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-40.78	-13	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-49.87	-13	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-44.22	-13	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-63.33	-43	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.24	-33	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-61.56	-13	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-40.70	-13	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-49.86	-13	PASS
DC_7A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-44.07	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-63.54	-43	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	0.15	30	-74.11	-33	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	30	1000	-61.65	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	1000	6000	-38.47	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	6000	18000	-49.87	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	18000	40000	-44.16	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-63.63	-43	PASS

3700-3800										
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-73.51	-33	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-61.60	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-40.78	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-49.89	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-44.22	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-62.58	-43	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-73.14	-33	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-61.68	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-40.67	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-49.82	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-44.42	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-63.09	-43	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Outer_Full	0.15	30	-73.48	-33	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Outer_Full	30	1000	-61.73	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Outer_Full	1000	6000	-40.82	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Outer_Full	6000	18000	-49.94	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Outer_Full	18000	40000	-44.15	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-63.52	-43	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-73.37	-33	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-61.87	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-40.76	-13	PASS

DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-49.81	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-44.27	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-62.82	-43	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-73.87	-33	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-61.59	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-40.59	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-49.86	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-44.13	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-63.17	-43	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	0.15	30	-73.65	-33	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	30	1000	-61.90	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	1000	6000	-40.66	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	6000	18000	-50.00	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	18000	40000	-44.38	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-62.45	-43	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.49	-33	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-62.02	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-40.73	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-50.02	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-44.41	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-63.06	-43	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.13	-33	PASS

3700-3800										
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-61.89	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-40.74	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-49.78	-13	PASS
DC_7A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-44.32	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-62.60	-43	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	0.15	30	-73.43	-33	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	30	1000	-61.91	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	1000	6000	-39.66	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	6000	18000	-49.88	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	18000	40000	-44.31	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-63.34	-43	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-73.48	-33	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-61.83	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-40.73	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-49.99	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-44.27	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-62.54	-43	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-73.41	-33	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-61.38	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-40.73	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-49.83	-13	PASS

DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-44.35	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-63.18	-43	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	0.15	30	-73.23	-33	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	30	1000	-62.14	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	1000	6000	-40.71	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	6000	18000	-49.92	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	18000	40000	-44.47	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-64.16	-43	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-73.80	-33	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-61.86	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-40.64	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-49.97	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-44.37	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-63.09	-43	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-73.12	-33	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-61.62	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-40.71	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-49.82	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-44.22	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-63.28	-43	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	0.15	30	-73.16	-33	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	30	1000	-62.14	-13	PASS

3700-3800										
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	1000	6000	-38.90	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	6000	18000	-50.05	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	18000	40000	-44.27	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-62.60	-43	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-72.68	-33	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-62.03	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-40.75	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-49.87	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-44.48	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-63.84	-43	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.34	-33	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-61.90	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-40.68	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-49.82	-13	PASS
DC_7A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-44.27	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-63.53	-43	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	0.15	30	-73.16	-33	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	30	1000	-61.89	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	1000	6000	-38.90	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	6000	18000	-49.92	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	18000	40000	-44.12	-13	PASS

DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-63.65	-43	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-72.62	-33	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-62.07	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-40.56	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-49.95	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-44.43	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-62.53	-43	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-73.12	-33	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-61.50	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-40.63	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-49.95	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-44.42	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-62.79	-43	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	0.15	30	-73.29	-33	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	30	1000	-61.89	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	1000	6000	-40.78	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	6000	18000	-50.00	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	18000	40000	-44.22	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-62.36	-43	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-73.49	-33	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-61.87	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-40.74	-13	PASS

3700-3800										
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-49.89	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-44.42	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-63.07	-43	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-73.31	-33	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-61.58	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-40.75	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-49.92	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-44.18	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-63.12	-43	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	0.15	30	-73.11	-33	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	30	1000	-61.81	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	1000	6000	-39.06	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	6000	18000	-49.87	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	18000	40000	-44.37	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-63.41	-43	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-72.85	-33	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-61.86	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-40.72	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-49.94	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-44.21	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-62.87	-43	PASS

DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.43	-33	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-61.73	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-40.70	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-49.89	-13	PASS
DC_7A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-44.51	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-63.04	-43	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	0.15	30	-72.43	-33	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	30	1000	-62.04	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	1000	6000	-39.30	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	6000	18000	-49.94	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	18000	40000	-44.31	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-63.67	-43	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-72.63	-33	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-61.83	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-40.82	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-49.91	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-44.47	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-63.04	-43	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-72.82	-33	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-61.99	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-40.72	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-49.87	-13	PASS

3700-3800										
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-44.30	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-63.10	-43	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	0.15	30	-72.77	-33	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	30	1000	-61.94	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	1000	6000	-40.41	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	6000	18000	-49.84	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	18000	40000	-44.21	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-63.50	-43	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-72.93	-33	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-61.60	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-40.76	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-49.72	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-44.44	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-62.99	-43	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-73.90	-33	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-61.76	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-40.65	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-49.90	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-44.10	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-63.45	-43	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	0.15	30	-73.04	-33	PASS

DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	30	1000	-61.63	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	1000	6000	-40.61	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	6000	18000	-49.89	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	18000	40000	-44.32	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-63.88	-43	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.26	-33	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-61.27	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-40.72	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-49.78	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-44.38	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-63.20	-43	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.47	-33	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-62.01	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-40.79	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-49.86	-13	PASS
DC_7A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-44.22	-13	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-63.71	-43	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	0.15	30	-73.17	-33	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	30	1000	-61.80	-13	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	1000	6000	-39.57	-13	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	6000	18000	-50.01	-13	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	18000	40000	-44.13	-13	PASS

3700-3800										
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-63.21	-43	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-72.59	-33	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-61.76	-13	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-40.48	-13	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-49.93	-13	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-44.15	-13	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.11	-43	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-73.26	-33	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-61.73	-13	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-40.76	-13	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-50.01	-13	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-44.21	-13	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-63.85	-43	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	0.15	30	-72.94	-33	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	30	1000	-62.01	-13	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	1000	6000	-40.51	-13	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	6000	18000	-49.90	-13	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	18000	40000	-44.13	-13	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-63.97	-43	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-73.01	-33	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-61.88	-13	PASS

DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-40.65	-13	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-49.97	-13	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-44.54	-13	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.22	-43	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-73.68	-33	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-61.51	-13	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-40.64	-13	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-49.75	-13	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-44.23	-13	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-63.68	-43	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	0.15	30	-73.25	-33	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	30	1000	-61.85	-13	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	1000	6000	-40.52	-13	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	6000	18000	-49.94	-13	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	18000	40000	-44.44	-13	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.28	-43	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.47	-33	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-61.38	-13	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-40.64	-13	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-49.88	-13	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-44.21	-13	PASS
DC_7A_n78A-	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-63.63	-43	PASS

3700-3800										
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.13	-33	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-61.70	-13	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-40.78	-13	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-49.90	-13	PASS
DC_7A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-44.51	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-64.30	-43	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	0.15	30	-73.25	-33	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	30	1000	-61.93	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	1000	6000	-38.95	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	6000	18000	-49.72	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	18000	40000	-44.18	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.16	-43	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-73.24	-33	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-61.53	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-40.79	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-49.81	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-44.08	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-63.57	-43	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-73.85	-33	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-61.71	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-40.77	-13	PASS

DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-50.01	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-44.43	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-63.36	-43	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	0.15	30	-73.82	-33	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	30	1000	-61.75	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	1000	6000	-40.19	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	6000	18000	-49.88	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	18000	40000	-44.11	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-63.93	-43	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-74.14	-33	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-61.82	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-40.73	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-49.67	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-44.35	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-64.40	-43	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-73.69	-33	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-61.55	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-40.81	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-49.91	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-44.31	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.03	-43	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	0.15	30	-73.17	-33	PASS

3700-3800										
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	30	1000	-61.44	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	1000	6000	-40.52	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	6000	18000	-49.72	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	18000	40000	-44.46	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.27	-43	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-72.78	-33	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-61.19	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-40.72	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-50.00	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-44.29	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-63.84	-43	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-72.83	-33	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-61.97	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-40.75	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-49.88	-13	PASS
DC_7A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-44.28	-13	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-64.45	-43	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	0.15	30	-73.87	-33	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	30	1000	-61.92	-13	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	1000	6000	-40.21	-13	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	6000	18000	-49.85	-13	PASS

DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	18000	40000	-44.39	-13	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-63.39	-43	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-73.43	-33	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-61.86	-13	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-40.61	-13	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-49.83	-13	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-44.35	-13	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-63.88	-43	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-73.99	-33	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-61.58	-13	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-40.78	-13	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-49.94	-13	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-44.47	-13	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-63.54	-43	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	0.15	30	-72.95	-33	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	30	1000	-61.71	-13	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	1000	6000	-39.90	-13	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	6000	18000	-49.80	-13	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	18000	40000	-44.46	-13	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-63.29	-43	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-73.01	-33	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-61.43	-13	PASS

3700-3800										
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-40.65	-13	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-49.84	-13	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-44.32	-13	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-63.89	-43	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-73.59	-33	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-61.53	-13	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-40.73	-13	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-49.78	-13	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-44.06	-13	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.05	-43	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	0.15	30	-73.71	-33	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	30	1000	-62.01	-13	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	1000	6000	-40.70	-13	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	6000	18000	-49.64	-13	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	18000	40000	-44.27	-13	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-63.97	-43	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.27	-33	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-61.63	-13	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-40.72	-13	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-49.77	-13	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-44.37	-13	PASS

DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-63.79	-43	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.55	-33	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-61.73	-13	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-40.73	-13	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-49.91	-13	PASS
DC_7A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-44.48	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-62.78	-43	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	0.15	30	-73.80	-33	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	30	1000	-61.73	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	1000	6000	-40.02	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	6000	18000	-49.98	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	18000	40000	-44.30	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.18	-43	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-72.89	-33	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-61.75	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-40.68	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-49.71	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-44.17	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-63.79	-43	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-73.38	-33	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-61.84	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-40.69	-13	PASS

3700-3800										
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-49.99	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-44.31	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-63.45	-43	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	0.15	30	-73.66	-33	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	30	1000	-61.75	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	1000	6000	-39.78	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	6000	18000	-49.80	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	18000	40000	-44.28	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-63.56	-43	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-74.08	-33	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-62.08	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-40.79	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-49.94	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-44.06	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-63.25	-43	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-74.26	-33	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-62.05	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-40.66	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-50.00	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-44.42	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.37	-43	PASS

DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	0.15	30	-73.11	-33	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	30	1000	-61.82	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	1000	6000	-39.91	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	6000	18000	-49.71	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	18000	40000	-44.37	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-63.23	-43	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.46	-33	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-61.25	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-40.73	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-49.97	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-44.32	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-63.97	-43	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-72.58	-33	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-62.09	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-40.73	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-49.81	-13	PASS
DC_7A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-44.39	-13	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-63.60	-43	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.15	30	-73.62	-33	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	30	1000	-61.76	-13	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	1000	6000	-39.66	-13	PASS
DC_7A_n78A-	30	5+10	DFT-QPSK	M+L	Outer_Full	6000	18000	-49.89	-13	PASS

3700-3800		0								
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	18000	40000	-44.26	-13	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-63.47	-43	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-74.12	-33	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-61.66	-13	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-40.79	-13	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-49.78	-13	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-44.10	-13	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-64.10	-43	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-73.76	-33	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-61.86	-13	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-40.70	-13	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-49.90	-13	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-44.09	-13	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-63.57	-43	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.15	30	-73.71	-33	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	30	1000	-61.68	-13	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	1000	6000	-40.28	-13	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	6000	18000	-49.97	-13	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	18000	40000	-44.37	-13	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-63.81	-43	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-73.12	-33	PASS

DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-61.88	-13	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-40.68	-13	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-49.87	-13	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-44.30	-13	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-63.87	-43	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-73.27	-33	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-61.96	-13	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-40.76	-13	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-50.03	-13	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-44.41	-13	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-63.93	-43	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.15	30	-73.29	-33	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	30	1000	-61.59	-13	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	1000	6000	-39.98	-13	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	6000	18000	-49.99	-13	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	18000	40000	-44.17	-13	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.02	-43	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.56	-33	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-61.76	-13	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-40.82	-13	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-49.78	-13	PASS
DC_7A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-43.87	-13	PASS

3700-3800		0								
DC_7A_n78A- 3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.16	-43	PASS
DC_7A_n78A- 3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.43	-33	PASS
DC_7A_n78A- 3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-61.46	-13	PASS
DC_7A_n78A- 3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-40.79	-13	PASS
DC_7A_n78A- 3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-50.08	-13	PASS
DC_7A_n78A- 3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-44.16	-13	PASS

Test Graphs

