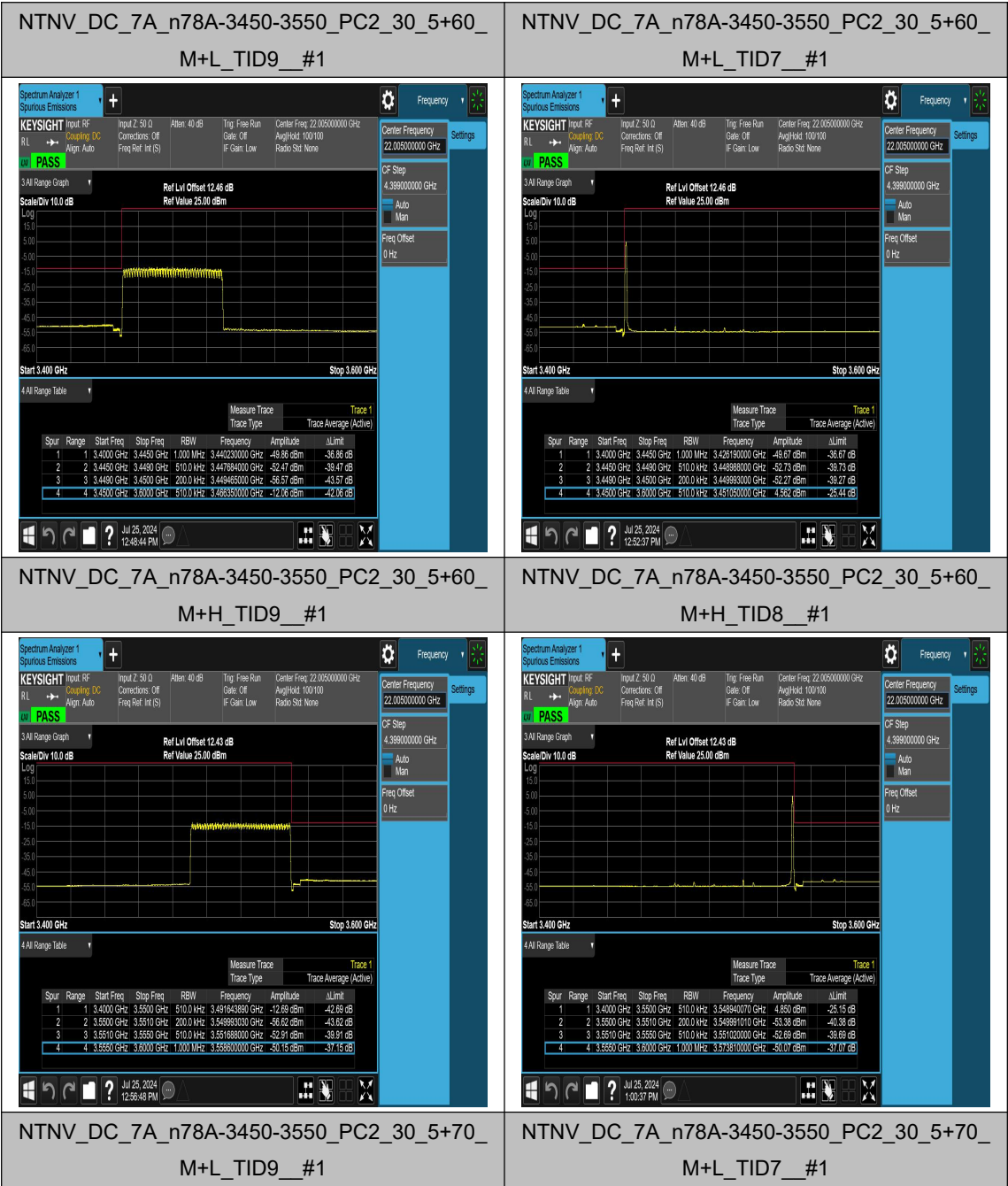
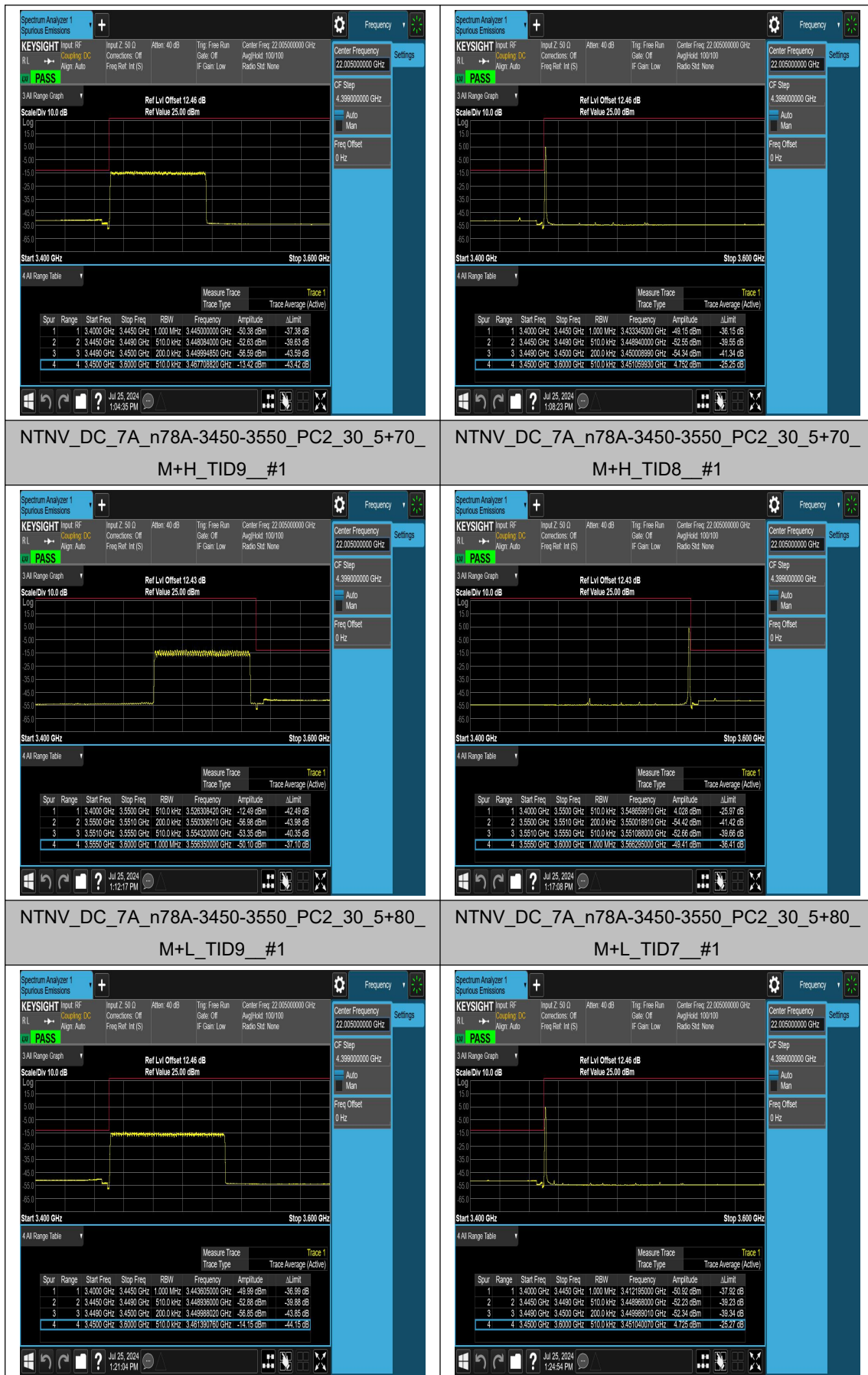
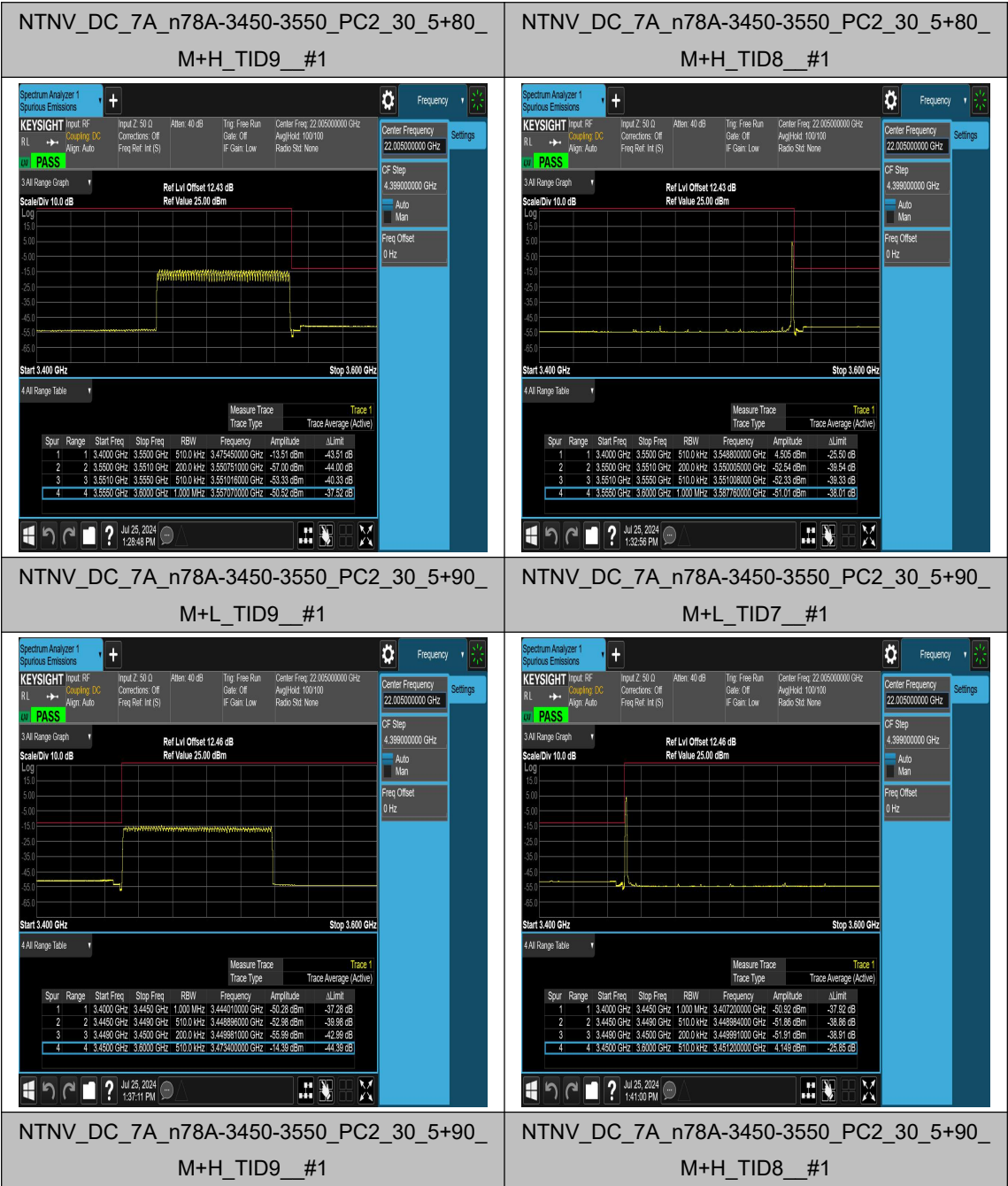
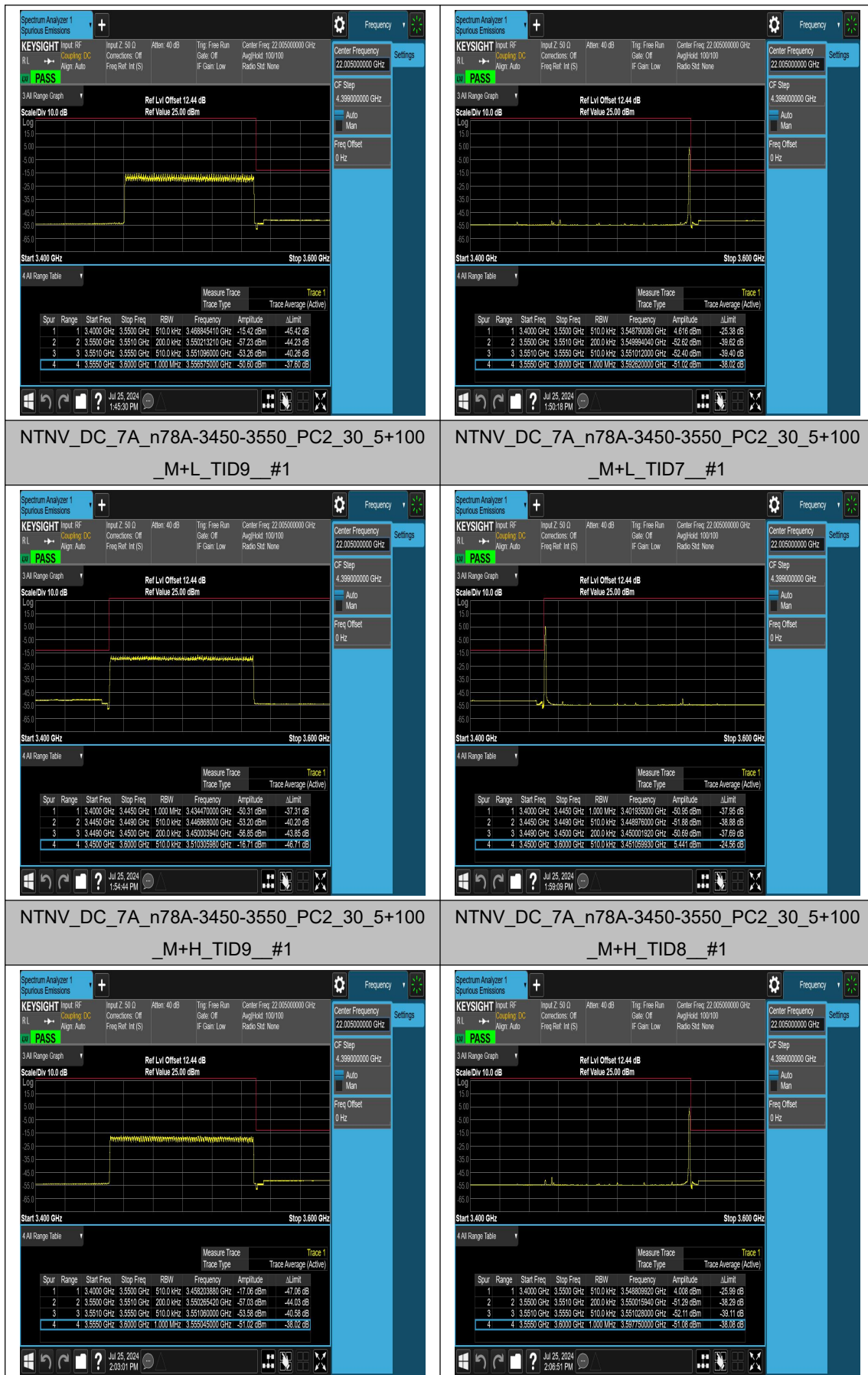










Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SC S	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-69.21	-43	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	0.15	30	-81.08	-33	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	30	1000	-62.30	-23	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	1000	3000	-46.38	-13	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	3000	6000	-40.58	-13	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	6000	26000	-40.69	-13	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	26000	40000	-44.43	-13	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-66.93	-43	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-79.75	-33	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-62.36	-23	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-47.07	-13	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-40.60	-13	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-40.64	-13	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-44.14	-13	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-67.91	-43	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-81.03	-33	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-62.13	-23	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-46.23	-13	PASS

3450-3550										
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-40.30	-13	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-40.77	-13	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-44.45	-13	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-68.90	-43	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	0.15	30	-82.32	-33	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	30	1000	-62.13	-23	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	1000	3000	-46.85	-13	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	3000	6000	-40.52	-13	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	6000	26000	-40.76	-13	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	26000	40000	-44.33	-13	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-68.02	-43	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-80.32	-33	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-62.35	-23	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-46.57	-13	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-40.49	-13	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-40.90	-13	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-44.55	-13	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-68.39	-43	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-80.27	-33	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-62.41	-23	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-46.75	-13	PASS

DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-40.40	-13	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-40.78	-13	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-44.50	-13	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-67.53	-43	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	0.15	30	-82.82	-33	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	30	1000	-62.11	-23	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	1000	3000	-45.96	-13	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	3000	6000	-40.50	-13	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	6000	26000	-40.66	-13	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	26000	40000	-44.57	-13	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-69.46	-43	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-80.64	-33	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-62.46	-23	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-46.66	-13	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-40.44	-13	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-40.73	-13	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-44.51	-13	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-68.90	-43	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-81.11	-33	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-62.17	-23	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-46.51	-13	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-40.50	-13	PASS

3450-3550										
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-40.77	-13	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-44.49	-13	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-68.72	-43	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	0.15	30	-82.21	-33	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	30	1000	-62.33	-23	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	1000	3000	-46.17	-13	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	3000	6000	-40.40	-13	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	6000	26000	-40.64	-13	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	26000	40000	-44.55	-13	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-67.42	-43	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-80.98	-33	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-62.25	-23	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-45.50	-13	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-40.58	-13	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-40.87	-13	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-44.52	-13	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-68.69	-43	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-81.24	-33	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-62.24	-23	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-45.65	-13	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-40.42	-13	PASS

DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-40.86	-13	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-44.47	-13	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-69.24	-43	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	0.15	30	-81.25	-33	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	30	1000	-62.20	-23	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	1000	3000	-46.82	-13	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	3000	6000	-40.46	-13	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	6000	26000	-40.80	-13	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	26000	40000	-44.49	-13	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-67.56	-43	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-82.14	-33	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-62.10	-23	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-46.79	-13	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-40.29	-13	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-40.87	-13	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-44.32	-13	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-68.57	-43	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-82.55	-33	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-62.15	-23	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-46.17	-13	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-40.53	-13	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-40.78	-13	PASS

3450-3550										
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-44.53	-13	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-68.91	-43	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	0.15	30	-81.97	-33	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	30	1000	-62.28	-23	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	1000	3000	-46.12	-13	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	3000	6000	-40.50	-13	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	6000	26000	-40.81	-13	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	26000	40000	-44.43	-13	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-69.21	-43	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-81.93	-33	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-62.16	-23	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-47.05	-13	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-40.59	-13	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-40.78	-13	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-44.45	-13	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-67.26	-43	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-79.99	-33	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-62.14	-23	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-45.87	-13	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-40.39	-13	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-40.70	-13	PASS

DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-44.45	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-68.34	-43	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	0.15	30	-81.07	-33	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	30	1000	-62.20	-23	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	1000	3000	-46.07	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	3000	6000	-40.51	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	6000	26000	-40.82	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	26000	40000	-44.45	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-68.98	-43	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-82.15	-33	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-62.23	-23	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-46.81	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-40.62	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-40.74	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-44.40	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-69.24	-43	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-81.14	-33	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-62.10	-23	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-46.77	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-40.54	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-40.62	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-44.44	-13	PASS

3450-3550										
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-66.56	-43	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	0.15	30	-79.37	-33	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	30	1000	-62.17	-23	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	1000	3000	-46.51	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	3000	6000	-40.40	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	6000	26000	-40.68	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	26000	40000	-44.32	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-68.12	-43	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-81.12	-33	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-62.07	-23	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-45.95	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-40.41	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-40.76	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-44.44	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-68.28	-43	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-80.23	-33	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-62.37	-23	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-46.13	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-40.54	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-40.69	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-44.59	-13	PASS

DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-68.44	-43	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	0.15	30	-81.11	-33	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	30	1000	-62.23	-23	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	1000	3000	-46.64	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	3000	6000	-40.52	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	6000	26000	-40.78	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	26000	40000	-44.57	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-67.71	-43	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-81.26	-33	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-62.25	-23	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-46.30	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-40.51	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-40.87	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-44.28	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-69.88	-43	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-82.24	-33	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-62.12	-23	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-45.86	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-40.33	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-40.79	-13	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-44.34	-13	PASS
DC_7A_n78A-	30	5+40	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-68.97	-43	PASS

3450-3550										
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	0.15	30	-81.60	-33	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	30	1000	-62.24	-23	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	1000	3000	-46.13	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	3000	6000	-40.38	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	6000	26000	-40.63	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	26000	40000	-44.44	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-69.50	-43	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-81.38	-33	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-62.20	-23	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-46.06	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-40.52	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-40.70	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-44.42	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-69.02	-43	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-81.72	-33	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-62.45	-23	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-46.66	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-40.49	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-40.66	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-44.22	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-68.49	-43	PASS

DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	0.15	30	-81.82	-33	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	30	1000	-62.14	-23	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	1000	3000	-46.16	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	3000	6000	-40.52	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	6000	26000	-40.86	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	26000	40000	-44.46	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-69.41	-43	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-80.51	-33	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-62.10	-23	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-45.96	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-40.43	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-40.75	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-44.42	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-69.07	-43	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-81.74	-33	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-62.06	-23	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-46.83	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-40.55	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-40.79	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-44.41	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-67.85	-43	PASS
DC_7A_n78A-	30	5+40	DFT-QPSK	M+H	Outer_Full	0.15	30	-83.23	-33	PASS

3450-3550										
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	30	1000	-62.11	-23	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	1000	3000	-46.06	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	3000	6000	-40.54	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	6000	26000	-40.65	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	26000	40000	-44.38	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-67.86	-43	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-81.78	-33	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-62.41	-23	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-46.43	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-40.37	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-40.57	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-44.46	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-67.03	-43	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-80.15	-33	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-62.22	-23	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-45.81	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-40.51	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-40.64	-13	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-44.52	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-68.06	-43	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	0.15	30	-79.89	-33	PASS

DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	30	1000	-62.12	-23	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	1000	3000	-46.90	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	3000	6000	-40.42	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	6000	26000	-40.83	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	26000	40000	-44.45	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-67.49	-43	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-82.67	-33	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-62.46	-23	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-45.85	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-40.62	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-40.69	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-44.38	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-68.17	-43	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-81.13	-33	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-62.37	-23	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-46.90	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-40.39	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-40.61	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-44.48	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-67.89	-43	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	0.15	30	-82.00	-33	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	30	1000	-61.93	-23	PASS

3450-3550										
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	1000	3000	-45.88	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	3000	6000	-40.23	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	6000	26000	-40.52	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	26000	40000	-44.45	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-69.11	-43	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-80.12	-33	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-62.22	-23	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-46.61	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-40.54	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-40.57	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-44.46	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-67.79	-43	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-80.46	-33	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-62.18	-23	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-46.61	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-40.61	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-40.70	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-44.27	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-61.22	-43	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	0.15	30	-71.84	-33	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	30	1000	-62.31	-23	PASS

DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	1000	3000	-46.79	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	3000	6000	-40.42	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	6000	26000	-40.73	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	26000	40000	-44.35	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-61.64	-43	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-72.50	-33	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-62.37	-23	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-45.91	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-40.45	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-40.80	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-44.40	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-62.10	-43	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.26	-33	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-62.19	-23	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-47.20	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-40.45	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-40.70	-13	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-44.42	-13	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-61.98	-43	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	0.15	30	-71.20	-33	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	30	1000	-62.08	-23	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	1000	3000	-46.22	-13	PASS

3450-3550										
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	3000	6000	-40.43	-13	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	6000	26000	-40.80	-13	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	26000	40000	-44.43	-13	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-59.82	-43	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-72.45	-33	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-60.47	-23	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-46.20	-13	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-40.43	-13	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-40.87	-13	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-44.34	-13	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-62.42	-43	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-71.52	-33	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-62.04	-23	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-46.96	-13	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-40.31	-13	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-40.81	-13	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-44.54	-13	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-62.88	-43	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	0.15	30	-71.72	-33	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	30	1000	-62.70	-23	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	1000	3000	-47.14	-13	PASS

DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	3000	6000	-40.57	-13	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	6000	26000	-41.14	-13	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	26000	40000	-44.87	-13	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-62.69	-43	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-71.94	-33	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-62.15	-23	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-45.93	-13	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-40.69	-13	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-41.02	-13	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-44.95	-13	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-62.45	-43	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-71.66	-33	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-62.58	-23	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-23.27	-13	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-40.62	-13	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-41.30	-13	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-44.67	-13	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-62.51	-43	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	0.15	30	-72.58	-33	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	30	1000	-62.28	-23	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	1000	3000	-46.94	-13	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	3000	6000	-40.74	-13	PASS

3450-3550										
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	6000	26000	-40.89	-13	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	26000	40000	-44.92	-13	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-63.19	-43	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-71.44	-33	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-62.48	-23	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-46.75	-13	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-40.57	-13	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-41.07	-13	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-44.82	-13	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-62.30	-43	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-72.12	-33	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-62.37	-23	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-45.70	-13	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-40.59	-13	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-41.01	-13	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-44.83	-13	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-62.34	-43	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	0.15	30	-71.92	-33	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	30	1000	-62.25	-23	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	1000	3000	-47.14	-13	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	3000	6000	-40.69	-13	PASS

DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	6000	26000	-41.01	-13	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	26000	40000	-44.56	-13	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-62.29	-43	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-70.77	-33	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-62.21	-23	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-46.82	-13	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-40.67	-13	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-41.14	-13	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-44.58	-13	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-62.38	-43	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-73.15	-33	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-62.30	-23	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-46.85	-13	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-40.62	-13	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-40.76	-13	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-44.64	-13	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-61.83	-43	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	0.15	30	-72.10	-33	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	30	1000	-62.35	-23	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	1000	3000	-46.49	-13	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	3000	6000	-40.67	-13	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	6000	26000	-40.89	-13	PASS

3450-3550										
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	26000	40000	-44.47	-13	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-62.67	-43	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-71.99	-33	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-62.32	-23	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-46.25	-13	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-40.62	-13	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-40.80	-13	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-44.54	-13	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-61.92	-43	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-72.79	-33	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-62.39	-23	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-47.01	-13	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-40.59	-13	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-40.96	-13	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-44.69	-13	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-61.64	-43	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	0.15	30	-72.93	-33	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	30	1000	-62.13	-23	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	1000	3000	-46.34	-13	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	3000	6000	-40.66	-13	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	6000	26000	-40.82	-13	PASS

DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	26000	40000	-44.78	-13	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-62.76	-43	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-72.31	-33	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-62.22	-23	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-46.65	-13	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-40.60	-13	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-41.05	-13	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-44.36	-13	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-62.35	-43	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-72.02	-33	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-62.36	-23	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-46.55	-13	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-40.60	-13	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-40.82	-13	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-44.59	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-61.26	-43	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	0.15	30	-73.28	-33	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	30	1000	-62.26	-23	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	1000	3000	-46.54	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	3000	6000	-40.64	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	6000	26000	-40.81	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	26000	40000	-44.57	-13	PASS

3450-3550										
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-63.50	-43	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-72.86	-33	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-62.35	-23	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-46.16	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-40.75	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-40.98	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-44.46	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-62.16	-43	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-72.25	-33	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-62.36	-23	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-46.85	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-40.59	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-40.96	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-44.69	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-61.74	-43	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	0.15	30	-71.61	-33	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	30	1000	-61.92	-23	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	1000	3000	-46.19	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	3000	6000	-40.48	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	6000	26000	-40.86	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	26000	40000	-44.62	-13	PASS

DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-62.64	-43	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-72.15	-33	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-62.09	-23	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-46.60	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-40.49	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-40.84	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-44.49	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-61.86	-43	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-70.15	-33	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-62.27	-23	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-46.57	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-40.50	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-40.83	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-44.43	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-63.04	-43	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	0.15	30	-71.95	-33	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	30	1000	-62.21	-23	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	1000	3000	-47.07	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	3000	6000	-40.67	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	6000	26000	-40.91	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	26000	40000	-44.66	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-61.81	-43	PASS

3450-3550										
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.92	-33	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-62.43	-23	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-46.62	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-40.62	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-40.87	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-44.37	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-62.13	-43	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-71.94	-33	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-62.26	-23	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-45.97	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-40.55	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-40.87	-13	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-44.57	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-61.78	-43	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	0.15	30	-72.72	-33	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	30	1000	-62.33	-23	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	1000	3000	-46.95	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	3000	6000	-40.68	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	6000	26000	-40.53	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	26000	40000	-44.56	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-62.13	-43	PASS

DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-71.89	-33	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-62.27	-23	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-46.30	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-40.55	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-40.85	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-44.52	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-62.11	-43	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-72.65	-33	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-62.00	-23	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-46.92	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-40.53	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-40.60	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-44.67	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-61.87	-43	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	0.15	30	-72.71	-33	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	30	1000	-62.35	-23	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	1000	3000	-46.35	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	3000	6000	-40.54	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	6000	26000	-40.78	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	26000	40000	-44.60	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-62.01	-43	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-71.41	-33	PASS

3450-3550										
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-61.75	-23	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-45.57	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-40.59	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-40.84	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-44.55	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-62.40	-43	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-72.15	-33	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-62.24	-23	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-46.26	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-40.60	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-40.66	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-44.58	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-60.66	-43	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	0.15	30	-70.68	-33	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	30	1000	-62.01	-23	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	1000	3000	-47.09	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	3000	6000	-40.50	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	6000	26000	-40.88	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	26000	40000	-44.73	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-62.68	-43	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-71.89	-33	PASS

DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-62.06	-23	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-45.56	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-40.54	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-40.80	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-44.59	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-61.96	-43	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.79	-33	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-62.45	-23	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-46.87	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-40.59	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-40.80	-13	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-44.72	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-61.37	-43	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.15	30	-71.94	-33	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	30	1000	-62.37	-23	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	1000	3000	-47.02	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	3000	6000	-40.58	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	6000	26000	-40.79	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	26000	40000	-44.51	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-62.81	-43	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-72.69	-33	PASS
DC_7A_n78A-	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-62.26	-23	PASS

3450-3550		0								
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-46.71	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-40.60	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-40.91	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-44.34	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-62.28	-43	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-71.80	-33	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-62.27	-23	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-46.56	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-40.66	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-40.87	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-44.42	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-62.19	-43	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.15	30	-71.95	-33	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	30	1000	-62.35	-23	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	1000	3000	-46.28	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	3000	6000	-40.60	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	6000	26000	-40.95	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	26000	40000	-44.47	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-62.13	-43	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-72.74	-33	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-61.89	-23	PASS

DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-47.04	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-40.64	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-40.80	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-44.61	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-62.68	-43	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-70.44	-33	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-62.20	-23	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-45.47	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-40.47	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-40.72	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-44.62	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-62.39	-43	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.15	30	-72.30	-33	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	30	1000	-62.16	-23	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	1000	3000	-45.27	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	3000	6000	-40.75	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	6000	26000	-40.74	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	26000	40000	-44.66	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-62.15	-43	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-71.89	-33	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-62.11	-23	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-46.52	-13	PASS

3450-3550		0								
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-40.48	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-40.88	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-44.42	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-62.16	-43	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-70.79	-33	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-61.93	-23	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-47.10	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-40.38	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-40.72	-13	PASS
DC_7A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-44.41	-13	PASS

Test Graphs

