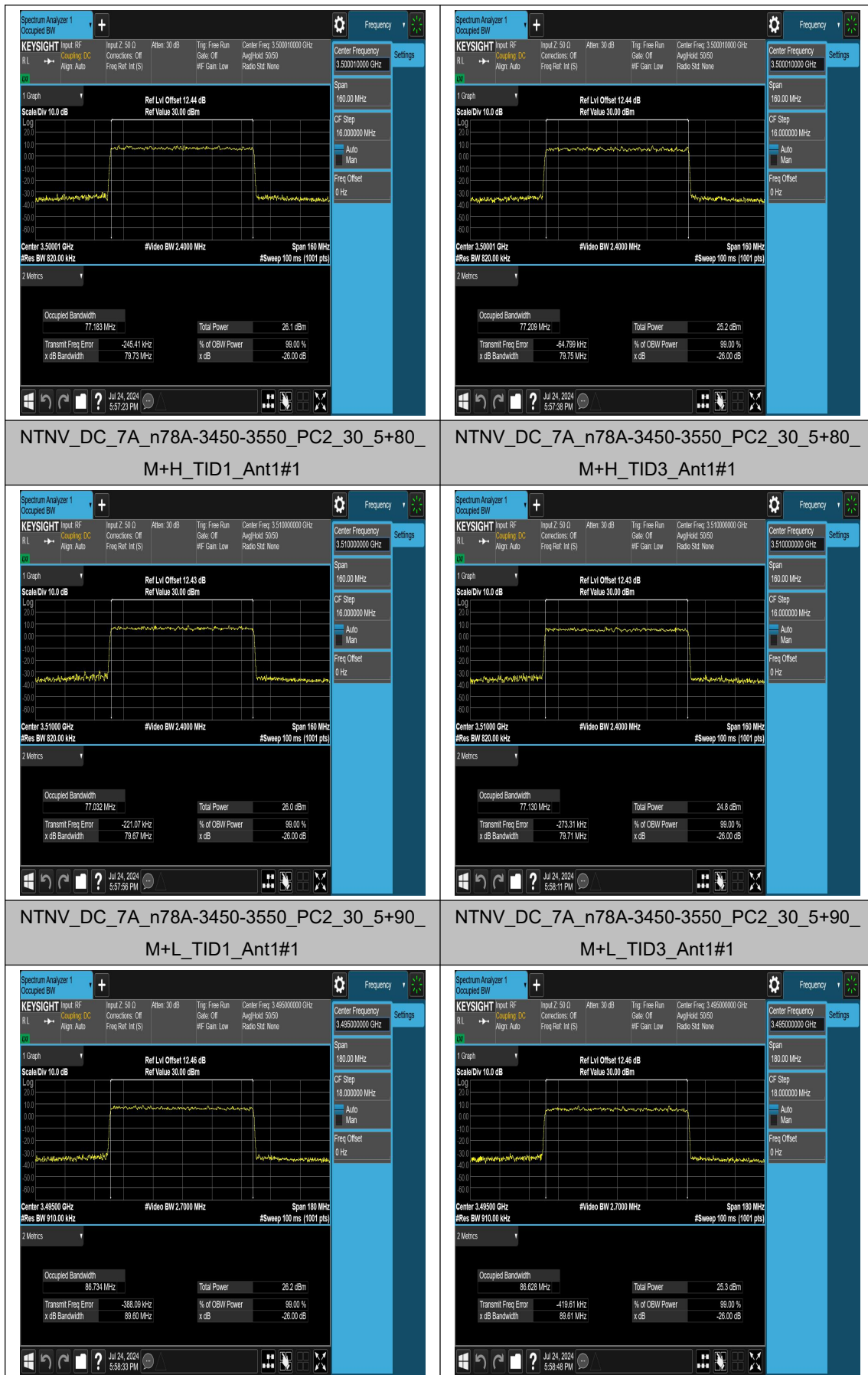
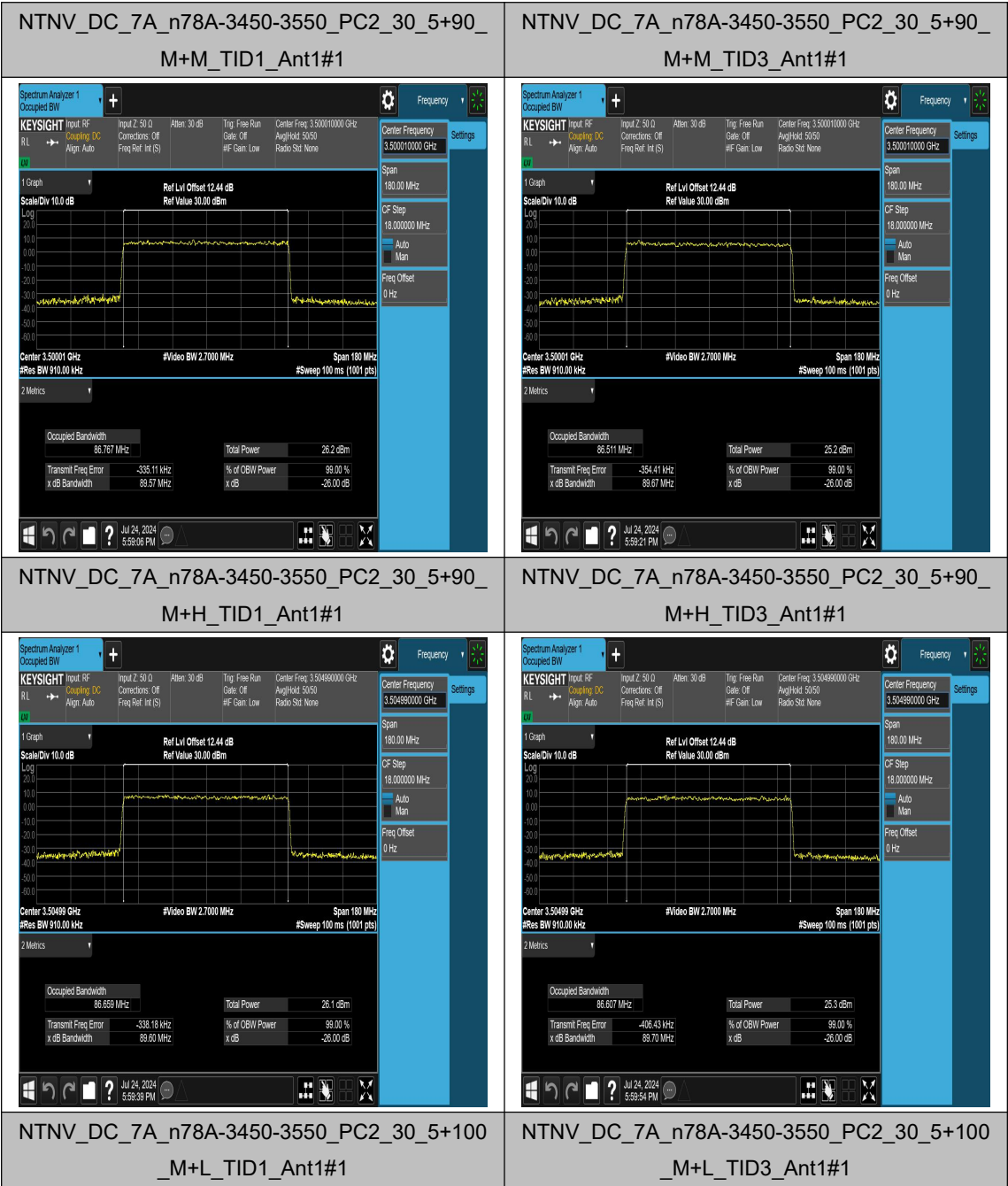
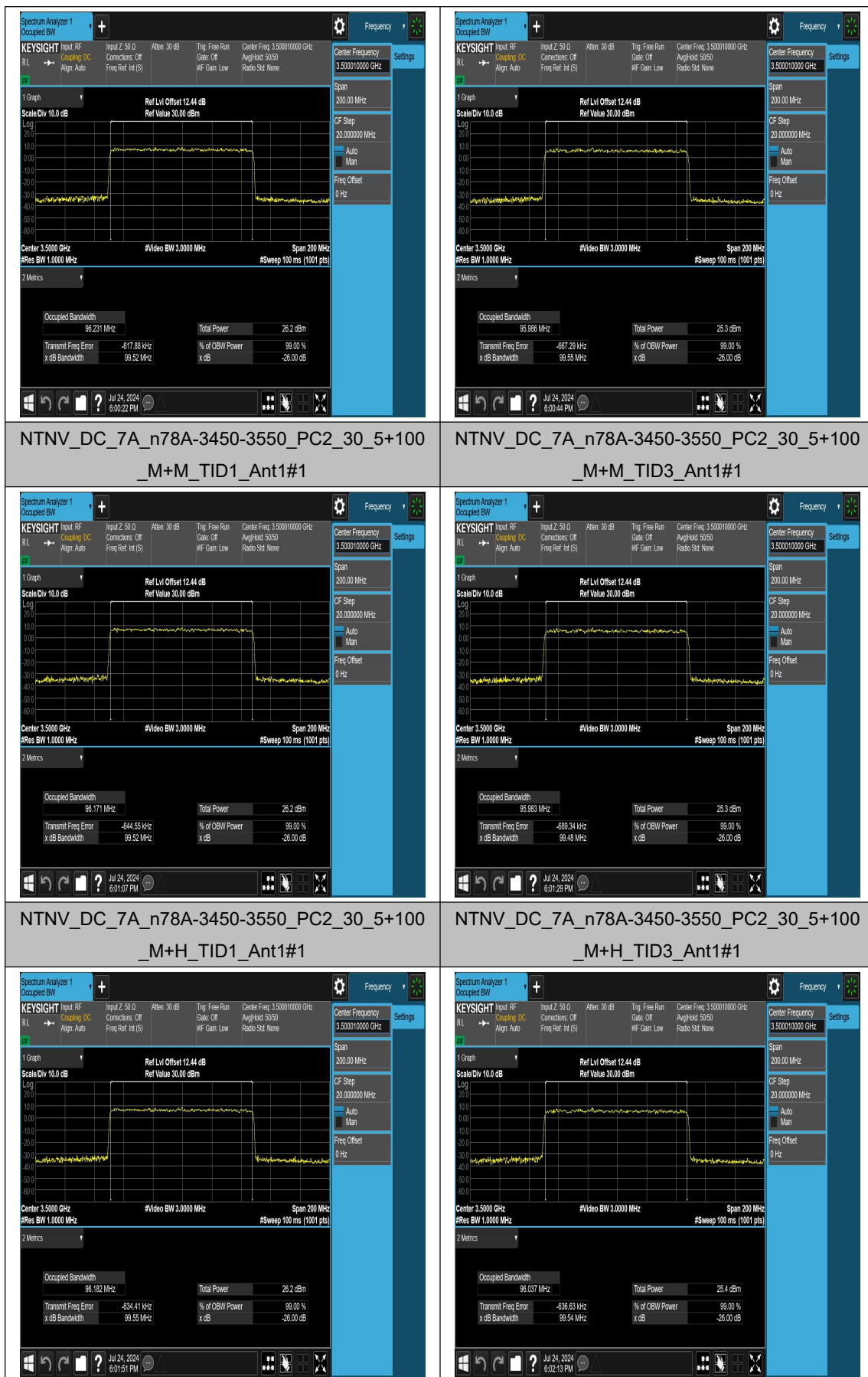








Appendix D: Band Edge for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Margin	Verdict
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	-49.03	36.03	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	-51.47	38.47	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	-49.12	36.12	PASS
DC_7A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	-51.15	38.15	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	-48.67	35.67	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	-50.32	37.32	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	-49.52	36.52	PASS
DC_7A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	-50.41	37.41	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	-49.92	36.92	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	-51.14	38.14	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	-49.70	36.70	PASS
DC_7A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	-50.67	37.67	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	-49.88	36.88	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	-51.19	38.19	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	-50.04	37.04	PASS
DC_7A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	-51.02	38.02	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	-50.25	37.25	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	-51.25	38.25	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	-50.19	37.19	PASS
DC_7A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	-51.01	38.01	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	-49.86	36.86	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	-49.67	36.67	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	-50.15	37.15	PASS
DC_7A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	-50.07	37.07	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	-50.38	37.38	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	-49.15	36.15	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	-50.10	37.10	PASS
DC_7A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	-49.41	36.41	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	-49.99	36.99	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	-50.92	37.92	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	-50.52	37.52	PASS
DC_7A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	-51.01	38.01	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	-50.28	37.28	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	-50.92	37.92	PASS
DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	-50.60	37.60	PASS

DC_7A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	-51.02	38.02	PASS
DC_7A_n78A-3450-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	-50.31	37.31	PASS
DC_7A_n78A-3450-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	-50.69	37.69	PASS
DC_7A_n78A-3450-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	-51.02	38.02	PASS
DC_7A_n78A-3450-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	-51.08	38.08	PASS

Test Graphs

