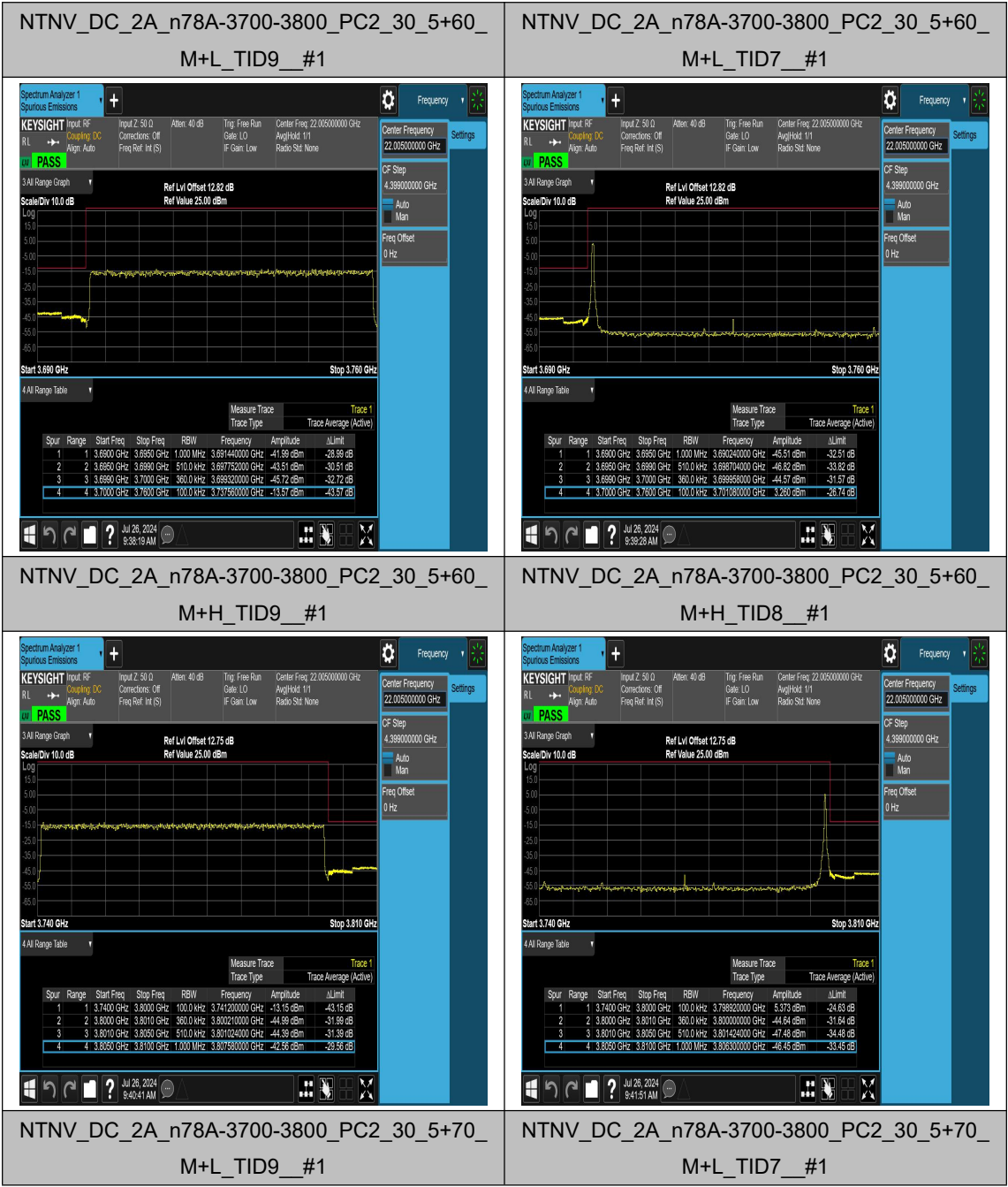
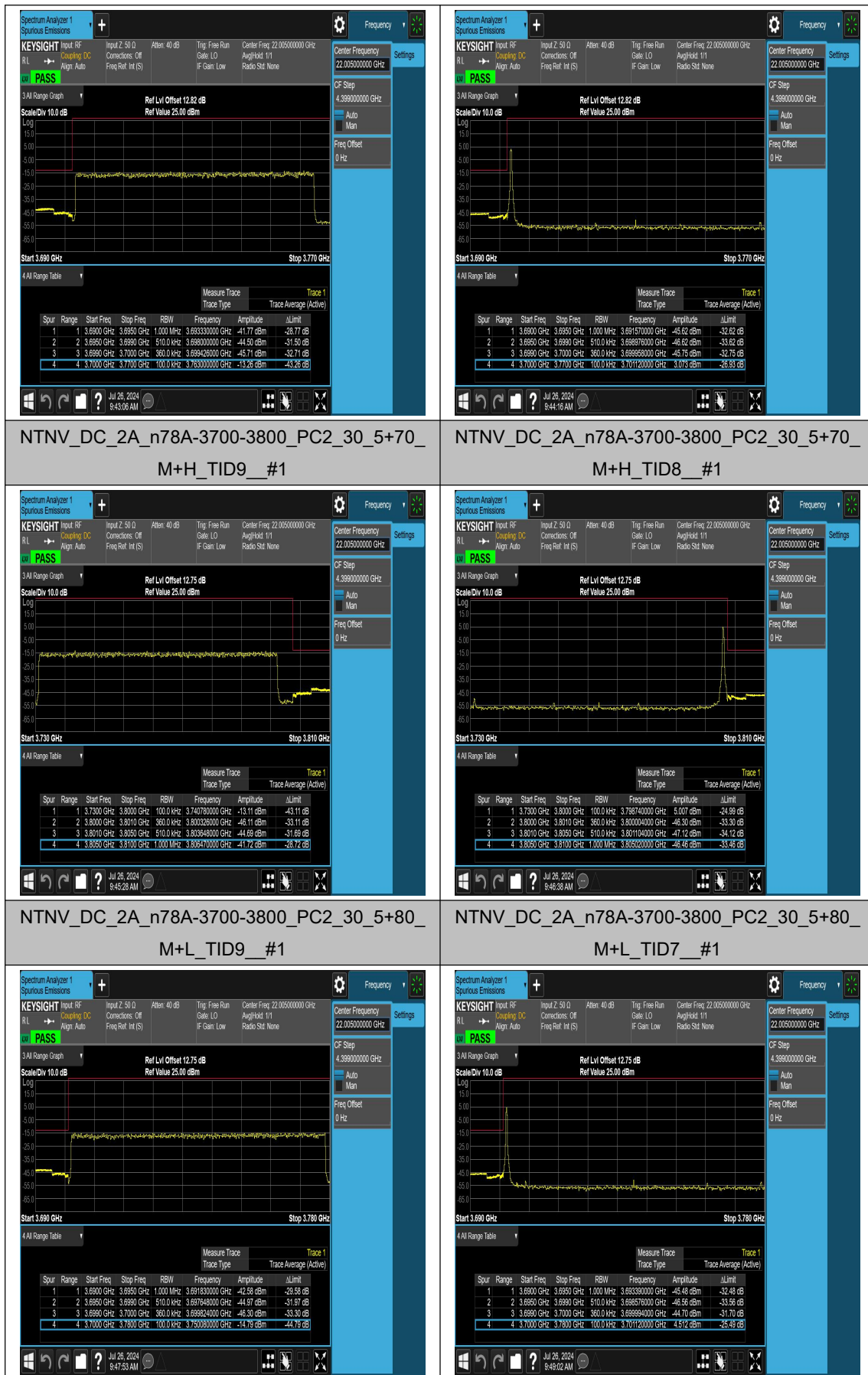
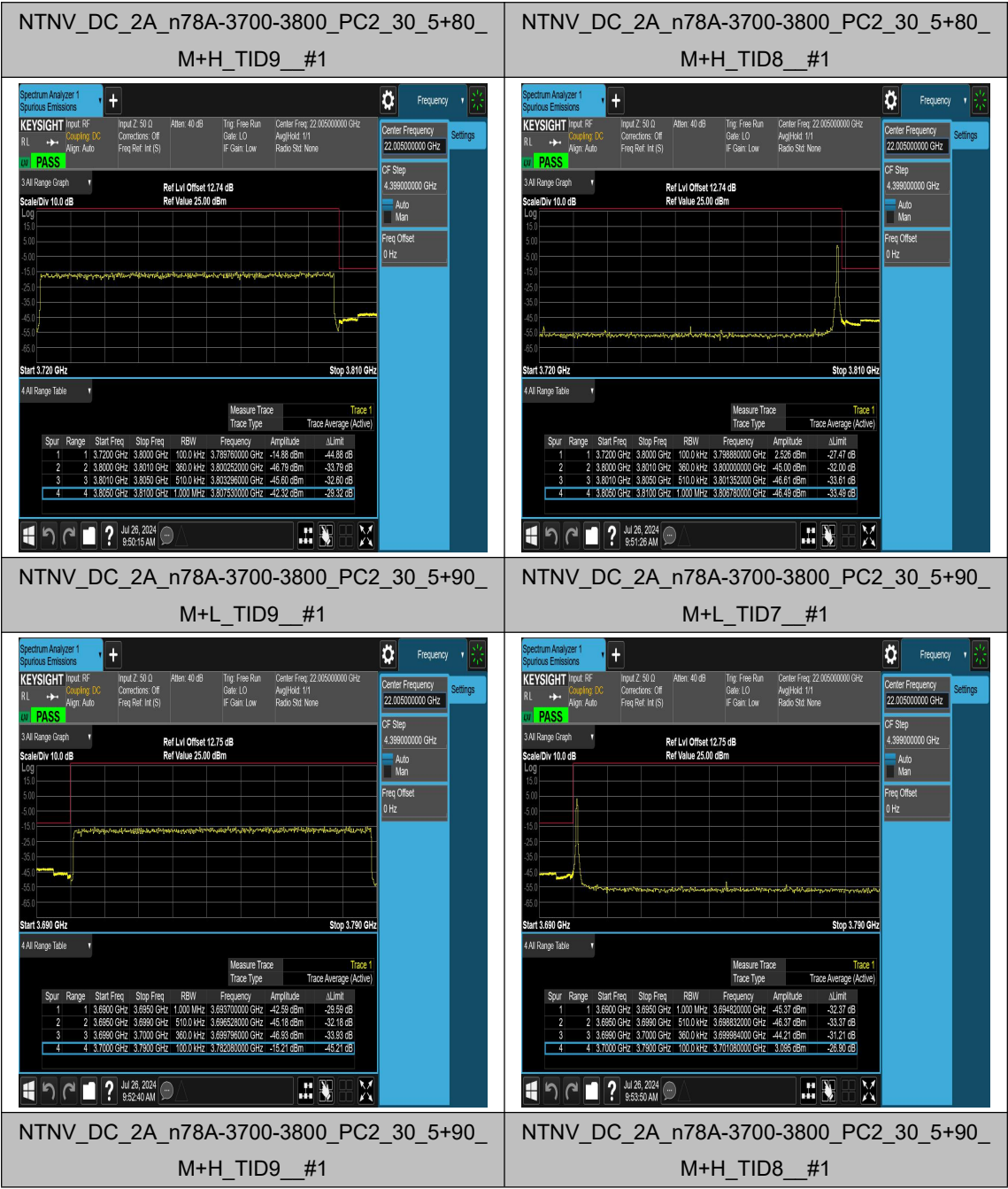
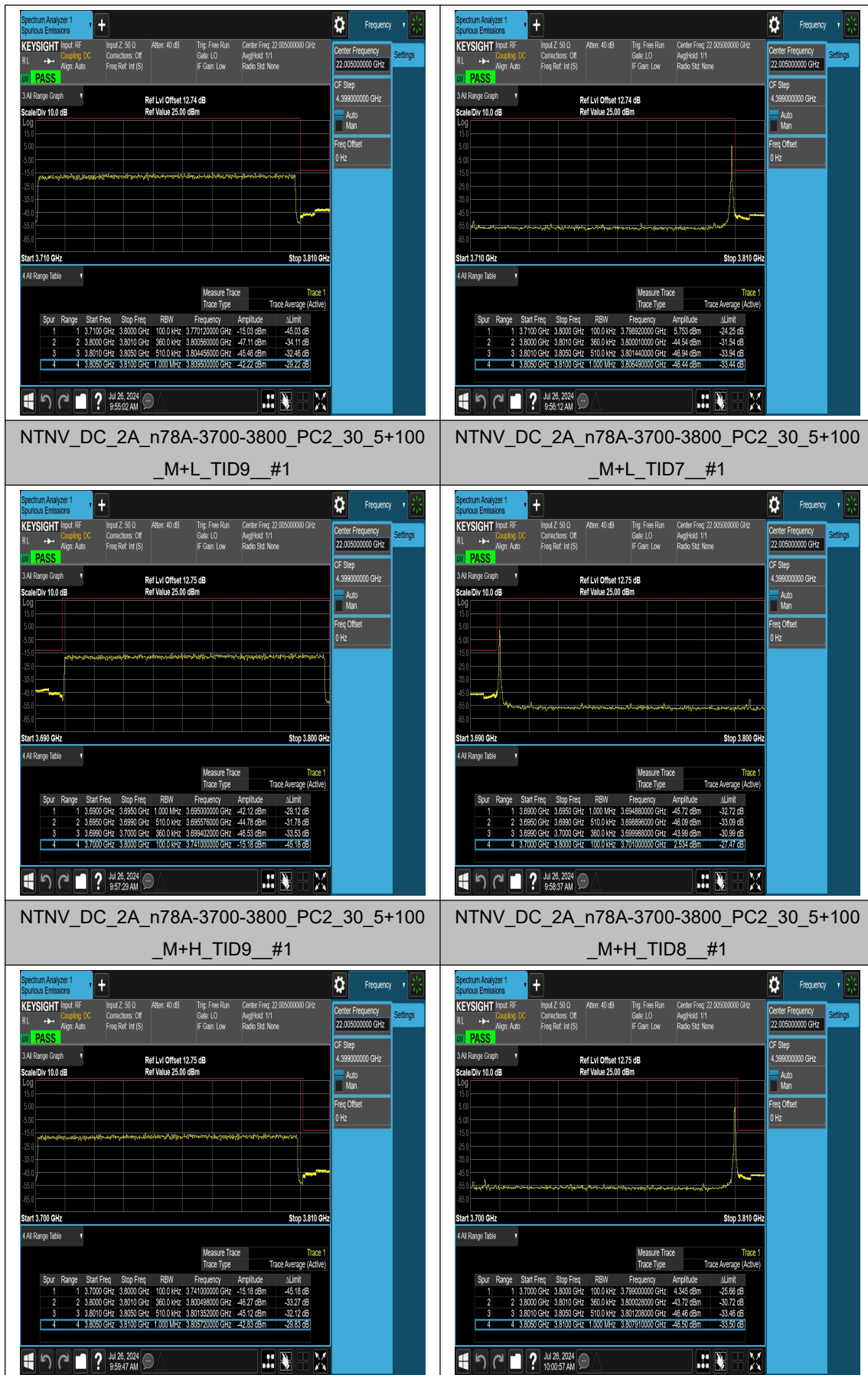










Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SC S	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-69.83	-43	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	0.15	30	-83.09	-33	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	30	1000	-61.80	-13	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	1000	6000	-40.64	-13	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	6000	18000	-49.87	-13	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	18000	40000	-44.28	-13	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-69.32	-43	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-82.94	-33	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-61.74	-13	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-40.53	-13	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-49.89	-13	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-44.24	-13	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-69.73	-43	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-83.14	-33	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-61.76	-13	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-40.62	-13	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-49.95	-13	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-44.38	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-69.21	-43	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Outer_Full	0.15	30	-83.12	-33	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Outer_Full	30	1000	-61.58	-13	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Outer_Full	1000	6000	-40.53	-13	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Outer_Full	6000	18000	-49.82	-13	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Outer_Full	18000	40000	-44.38	-13	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-68.42	-43	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-83.36	-33	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-61.95	-13	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-40.70	-13	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-49.92	-13	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-44.31	-13	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-70.44	-43	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-82.45	-33	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-61.29	-13	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-40.68	-13	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-49.94	-13	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-44.54	-13	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-70.10	-43	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	0.15	30	-82.65	-33	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	30	1000	-61.80	-13	PASS

DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	1000	6000	-40.57	-13	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	6000	18000	-50.00	-13	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Outer_Full	18000	40000	-44.19	-13	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-68.48	-43	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-83.06	-33	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-61.92	-13	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-40.58	-13	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-49.79	-13	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-44.38	-13	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-70.02	-43	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-83.21	-33	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-61.86	-13	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-40.62	-13	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-49.92	-13	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-44.43	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-70.00	-43	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	0.15	30	-82.92	-33	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	30	1000	-61.63	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	1000	6000	-40.08	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	6000	18000	-49.77	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Outer_Full	18000	40000	-44.17	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-70.16	-43	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-82.99	-33	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-61.75	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-40.41	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-49.78	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-44.43	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-69.40	-43	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-82.57	-33	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-61.88	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-40.57	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-49.67	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-44.29	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-69.54	-43	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Outer_Full	0.15	30	-82.82	-33	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Outer_Full	30	1000	-61.76	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Outer_Full	1000	6000	-40.67	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Outer_Full	6000	18000	-49.77	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Outer_Full	18000	40000	-44.41	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-70.02	-43	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-82.55	-33	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-61.82	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-40.66	-13	PASS

DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-49.80	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-44.39	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-70.15	-43	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-83.28	-33	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-61.95	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-39.66	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-49.96	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-44.22	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-69.64	-43	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	0.15	30	-82.20	-33	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	30	1000	-61.68	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	1000	6000	-39.41	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	6000	18000	-49.94	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Outer_Full	18000	40000	-44.39	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-69.61	-43	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-82.79	-33	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-61.85	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-40.60	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-49.89	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-44.15	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-68.92	-43	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-82.04	-33	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-61.92	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-40.48	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-49.87	-13	PASS
DC_2A_n78A-3700-3800	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-44.25	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-69.47	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	0.15	30	-83.13	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	30	1000	-61.94	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	1000	6000	-40.09	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	6000	18000	-49.91	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	18000	40000	-44.40	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-74.04	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-83.66	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-61.85	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-40.70	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-49.83	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-44.26	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-73.92	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-83.11	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-61.67	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-39.83	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-50.01	-13	PASS

DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-44.44	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-73.68	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	0.15	30	-83.06	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	30	1000	-62.00	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	1000	6000	-40.58	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	6000	18000	-49.98	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	18000	40000	-44.28	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-73.34	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-83.28	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-61.98	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-40.64	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-49.94	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-43.80	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-73.81	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-83.03	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-61.62	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-40.57	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-49.95	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-44.23	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-73.72	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	0.15	30	-82.94	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	30	1000	-61.75	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	1000	6000	-39.14	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	6000	18000	-49.91	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	18000	40000	-44.13	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-74.06	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-82.78	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-61.87	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-40.20	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-49.81	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-44.46	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-73.38	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-82.81	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-61.93	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-40.47	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-49.90	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-44.36	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-73.62	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	0.15	30	-82.87	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	30	1000	-61.99	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	1000	6000	-39.09	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	6000	18000	-49.82	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	18000	40000	-44.39	-13	PASS

DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-73.52	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-82.72	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-61.03	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-40.62	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-49.84	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-44.36	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-73.74	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-83.45	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-61.75	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-40.51	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-49.72	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-44.14	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-73.80	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	0.15	30	-83.62	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	30	1000	-61.84	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	1000	6000	-40.63	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	6000	18000	-49.92	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	18000	40000	-44.29	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-74.13	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-83.01	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-61.78	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-40.58	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-49.90	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-44.32	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-73.77	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-82.37	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-61.78	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-40.52	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-49.90	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-44.46	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-73.59	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	0.15	30	-83.02	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	30	1000	-61.87	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	1000	6000	-39.67	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	6000	18000	-49.87	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	18000	40000	-44.44	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-73.38	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-83.16	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-61.56	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-40.60	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-49.93	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-44.36	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-73.57	-43	PASS

DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-82.89	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-61.41	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-40.59	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-49.89	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-43.92	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-74.20	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	0.15	30	-82.87	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	30	1000	-61.72	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	1000	6000	-38.97	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	6000	18000	-50.05	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	18000	40000	-44.43	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-73.38	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-83.29	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-61.84	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-40.63	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-49.92	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-44.42	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-74.62	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-82.62	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-61.73	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-40.72	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-49.82	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-44.32	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-74.18	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	0.15	30	-83.09	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	30	1000	-61.94	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	1000	6000	-40.42	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	6000	18000	-49.91	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	18000	40000	-44.19	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-73.50	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-82.51	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-61.81	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-40.71	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-49.97	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-44.28	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-73.36	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-82.45	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-61.83	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-40.37	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-50.00	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-44.28	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-73.17	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	0.15	30	-82.95	-33	PASS

DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	30	1000	-61.77	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	1000	6000	-40.47	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	6000	18000	-49.98	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	18000	40000	-44.04	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-74.08	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-82.93	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-61.62	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-40.67	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-49.70	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-44.41	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-73.27	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-82.84	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-61.75	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-40.56	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-49.97	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-44.25	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-74.75	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	0.15	30	-82.94	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	30	1000	-61.81	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	1000	6000	-39.68	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	6000	18000	-49.85	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	18000	40000	-44.26	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-73.95	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-82.39	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-61.32	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-40.66	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-49.73	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-44.41	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-73.57	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-83.11	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-61.99	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-40.62	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-49.78	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-43.93	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-74.09	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	0.15	30	-83.45	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	30	1000	-61.60	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	1000	6000	-39.21	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	6000	18000	-49.99	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	18000	40000	-44.34	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-74.29	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-83.33	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-61.81	-13	PASS

DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-40.53	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-49.87	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-44.20	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-73.35	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-82.81	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-61.81	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-40.59	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-50.01	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-44.40	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-73.59	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	0.15	30	-83.60	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	30	1000	-61.89	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	1000	6000	-40.48	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	6000	18000	-49.95	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	18000	40000	-44.30	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-72.76	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-82.86	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-61.47	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-40.60	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-49.84	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-44.43	-13	PASS
DC_2A_n78A-	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-74.34	-43	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-83.03	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-61.44	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-40.53	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-49.99	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-44.46	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-73.12	-43	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	0.15	30	-82.92	-33	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	30	1000	-61.95	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	1000	6000	-39.80	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	6000	18000	-49.95	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Outer_Full	18000	40000	-44.37	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.05	-43	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-73.09	-33	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-61.81	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-40.39	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-49.82	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-44.34	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-63.37	-43	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-73.20	-33	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-61.85	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-40.65	-13	PASS

DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-49.93	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-44.24	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-63.60	-43	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	0.15	30	-73.17	-33	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	30	1000	-61.79	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	1000	6000	-40.46	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	6000	18000	-49.91	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Outer_Full	18000	40000	-44.49	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-63.74	-43	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-73.50	-33	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-62.09	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-40.72	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-49.80	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-44.48	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-63.64	-43	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-73.73	-33	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-61.67	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-40.73	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-49.87	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-44.30	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.13	-43	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	0.15	30	-72.91	-33	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	30	1000	-61.70	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	1000	6000	-40.46	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	6000	18000	-49.82	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Outer_Full	18000	40000	-44.20	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-63.58	-43	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.13	-33	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-61.88	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-40.78	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-49.91	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-44.35	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-63.82	-43	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.92	-33	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-61.83	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-40.73	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-49.94	-13	PASS
DC_2A_n78A-3700-3800	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-44.29	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-64.00	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	0.15	30	-73.08	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	30	1000	-61.98	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	1000	6000	-39.78	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	6000	18000	-49.81	-13	PASS

DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	18000	40000	-44.14	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-64.52	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-73.93	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-61.44	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-40.76	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-49.75	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-44.31	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-63.77	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-72.41	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-61.63	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-40.75	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-49.85	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-44.21	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-63.68	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	0.15	30	-73.09	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	30	1000	-61.96	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	1000	6000	-40.66	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	6000	18000	-49.91	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	18000	40000	-44.17	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-63.00	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-73.16	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-61.58	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-40.73	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-49.99	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-44.39	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-63.44	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-73.09	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-61.28	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-40.76	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-49.92	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-44.39	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-63.38	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	0.15	30	-73.56	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	30	1000	-61.91	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	1000	6000	-40.18	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	6000	18000	-49.83	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	18000	40000	-44.35	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-62.77	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.55	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-61.91	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-40.79	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-49.86	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-44.35	-13	PASS

DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-64.11	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.35	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-61.86	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-40.76	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-49.95	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-44.11	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-64.35	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	0.15	30	-73.34	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	30	1000	-61.98	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	1000	6000	-40.12	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	6000	18000	-49.95	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	18000	40000	-44.39	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-63.62	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-74.04	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-61.54	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-40.75	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-49.67	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-44.32	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-63.85	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-72.81	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-61.50	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-40.82	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-49.81	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-44.30	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-64.50	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	0.15	30	-73.17	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	30	1000	-61.67	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	1000	6000	-40.21	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	6000	18000	-49.99	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	18000	40000	-44.14	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-63.62	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-73.58	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-61.99	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-40.77	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-50.00	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-44.11	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-63.71	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-73.66	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-61.52	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-40.75	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-49.91	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-44.21	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-63.68	-43	PASS

DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	0.15	30	-73.50	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	30	1000	-61.70	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	1000	6000	-40.57	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	6000	18000	-49.92	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	18000	40000	-43.94	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-64.09	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-72.73	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-62.09	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-40.81	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-49.80	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-44.12	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-63.46	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.25	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-61.67	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-40.69	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-49.96	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-44.36	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-62.98	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.15	30	-73.77	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	30	1000	-61.65	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	1000	6000	-39.97	-13	PASS
DC_2A_n78A-	30	5+10	DFT-QPSK	M+L	Outer_Full	6000	18000	-49.98	-13	PASS

3700-3800		0								
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	18000	40000	-44.35	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-63.45	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-73.63	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-61.69	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-40.73	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	6000	18000	-49.79	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	18000	40000	-44.26	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-63.68	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-72.87	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-61.42	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-40.67	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	6000	18000	-49.95	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	18000	40000	-44.41	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-63.50	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.15	30	-72.60	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	30	1000	-61.83	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	1000	6000	-40.04	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	6000	18000	-49.84	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	18000	40000	-44.32	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-62.93	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-72.74	-33	PASS

DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-61.87	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-40.78	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	6000	18000	-49.74	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	18000	40000	-44.14	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-63.80	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-73.66	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-61.65	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-40.62	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	6000	18000	-49.81	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	18000	40000	-44.25	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.07	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.15	30	-72.94	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	30	1000	-61.82	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	1000	6000	-40.26	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	6000	18000	-49.80	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	18000	40000	-44.33	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-63.69	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.33	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-61.61	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-40.69	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	6000	18000	-49.88	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	18000	40000	-44.34	-13	PASS

3700-3800		0								
DC_2A_n78A- 3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-63.36	-43	PASS
DC_2A_n78A- 3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-73.15	-33	PASS
DC_2A_n78A- 3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-61.38	-13	PASS
DC_2A_n78A- 3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-40.68	-13	PASS
DC_2A_n78A- 3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	6000	18000	-49.93	-13	PASS
DC_2A_n78A- 3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	18000	40000	-44.04	-13	PASS

Test Graphs

