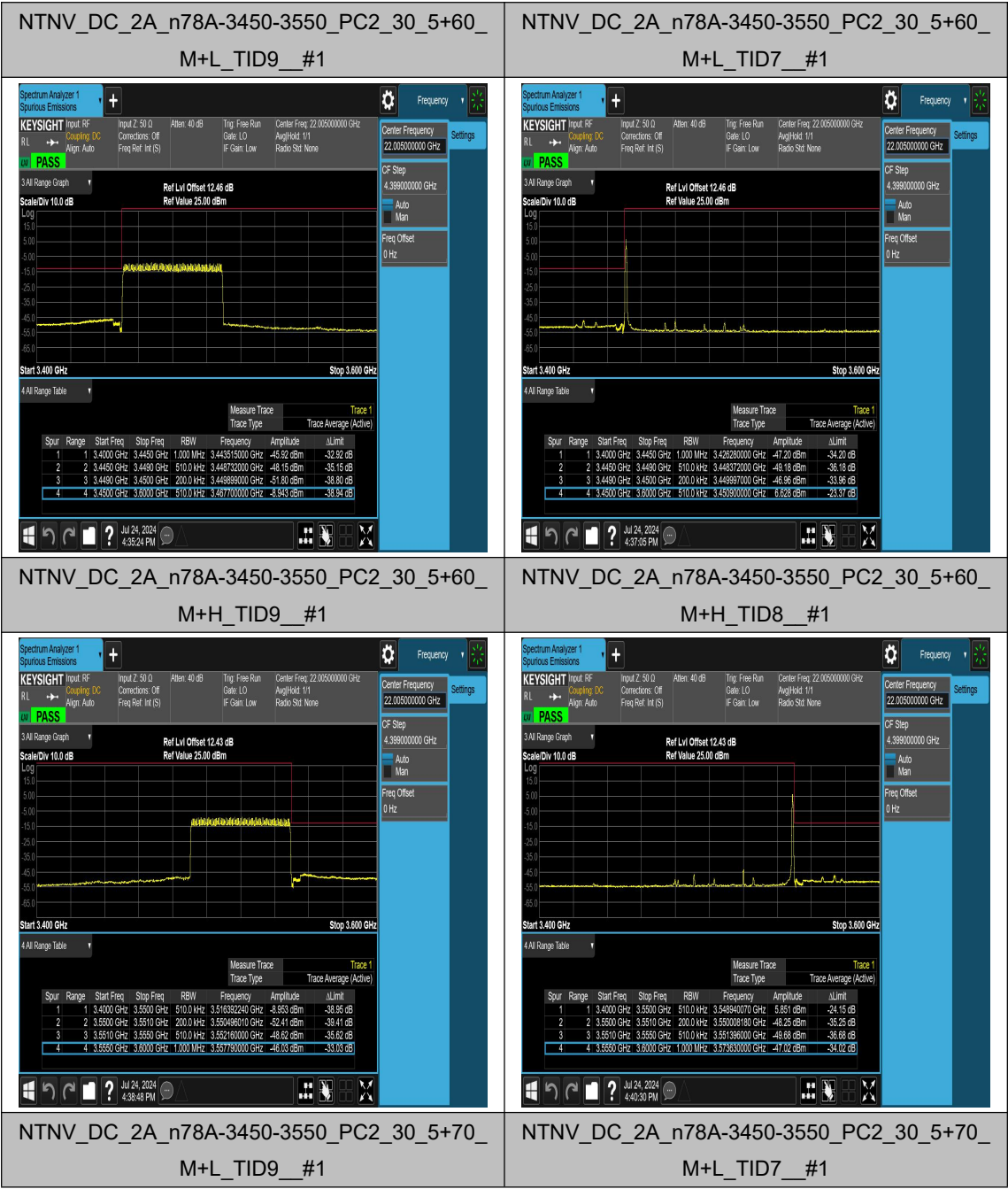
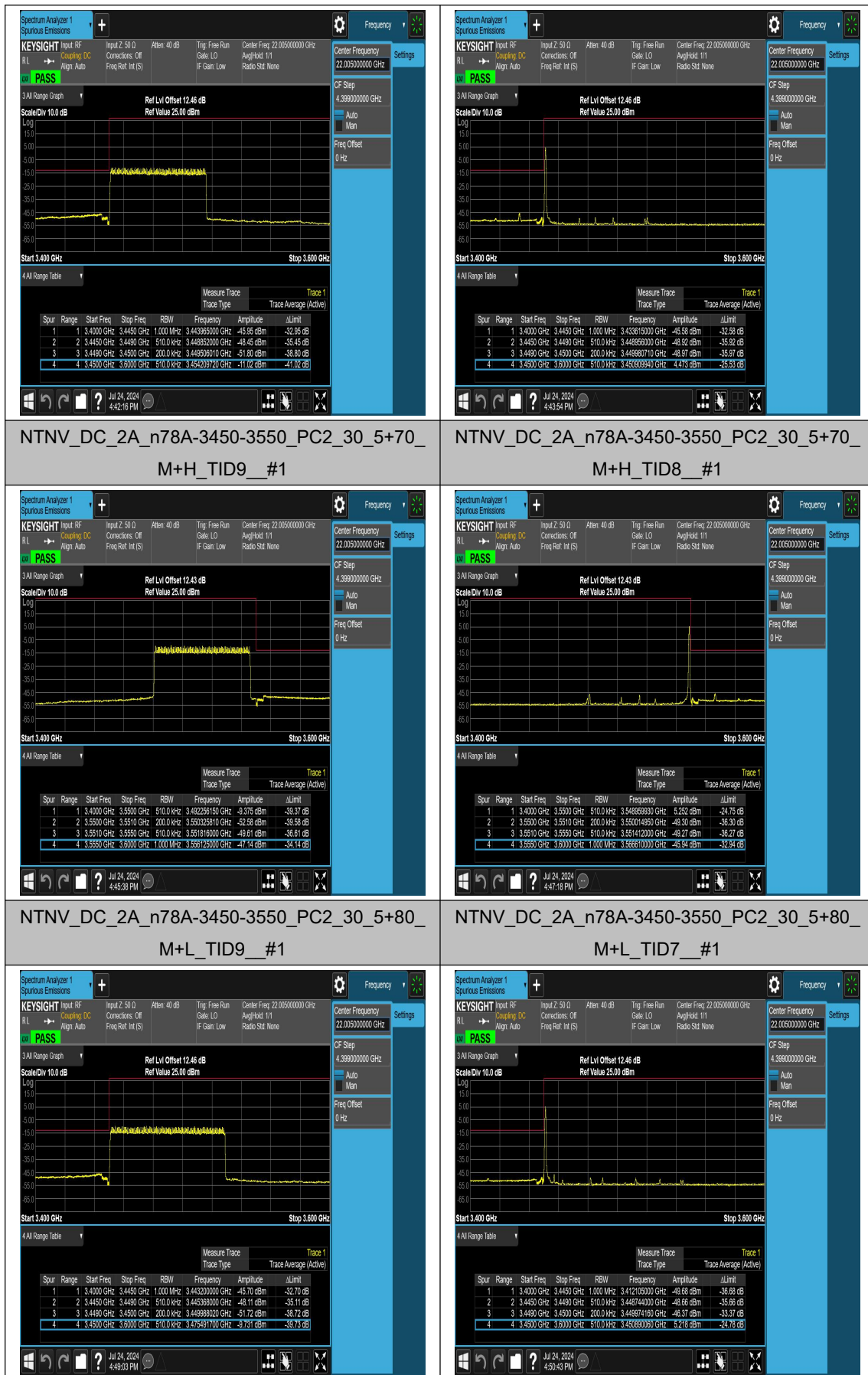
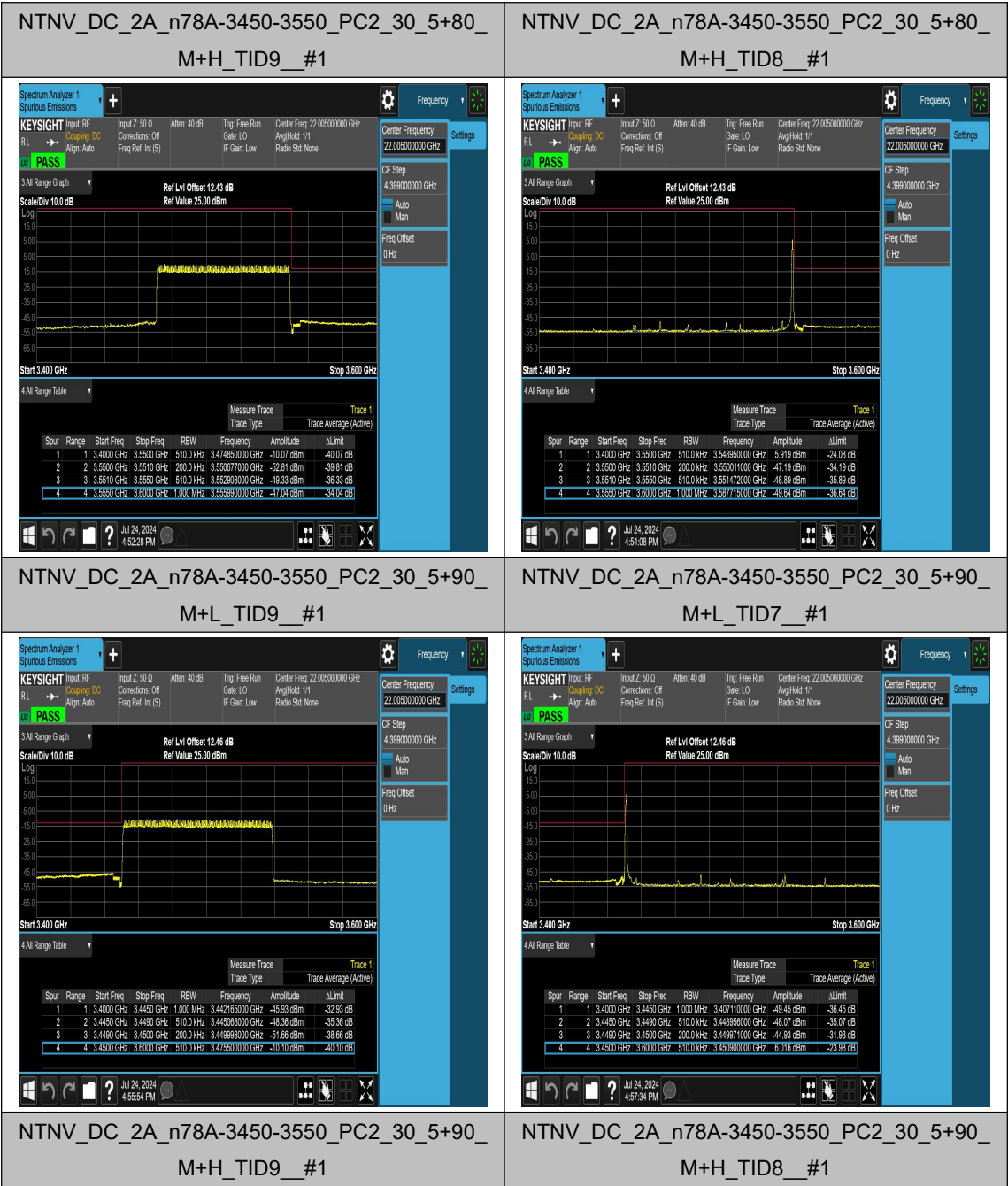
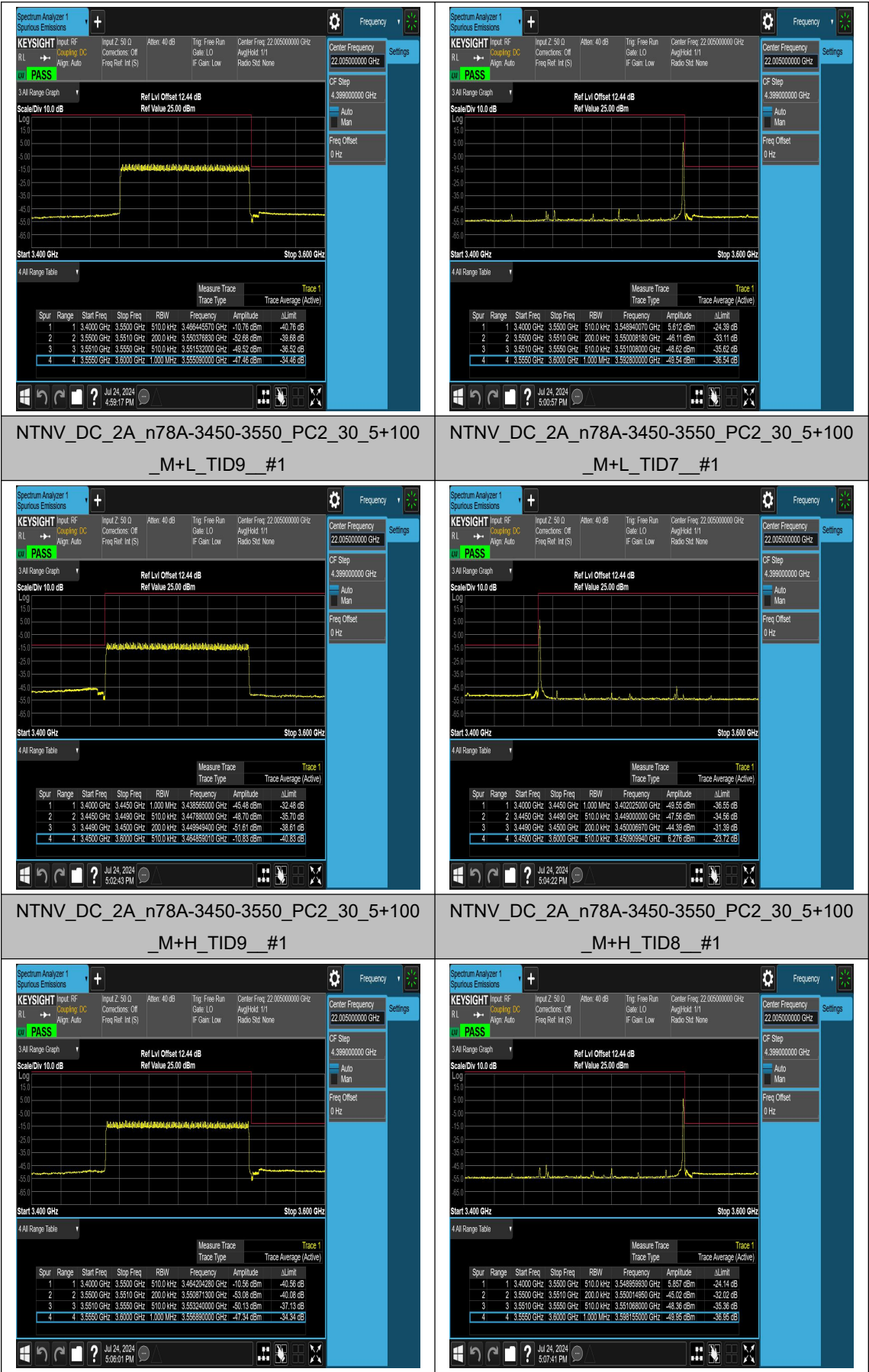










Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SC S	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_2A_n78A- 3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-68.29	-43	PASS
DC_2A_n78A- 3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	0.15	30	-81.97	-33	PASS
DC_2A_n78A- 3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	30	1000	-61.84	-23	PASS
DC_2A_n78A- 3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	1000	3000	-44.88	-13	PASS
DC_2A_n78A- 3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	3000	6000	-40.60	-13	PASS
DC_2A_n78A- 3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	6000	26000	-40.70	-13	PASS
DC_2A_n78A- 3450-3550	30	5+10	DFT-QPSK	M+L	Outer_Full	26000	40000	-44.37	-13	PASS
DC_2A_n78A- 3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-68.09	-43	PASS
DC_2A_n78A- 3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-80.31	-33	PASS
DC_2A_n78A- 3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-61.82	-23	PASS
DC_2A_n78A- 3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-44.63	-13	PASS
DC_2A_n78A- 3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-40.59	-13	PASS
DC_2A_n78A- 3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-40.65	-13	PASS
DC_2A_n78A- 3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-44.56	-13	PASS
DC_2A_n78A- 3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-68.33	-43	PASS
DC_2A_n78A- 3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-81.78	-33	PASS
DC_2A_n78A- 3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-62.35	-23	PASS
DC_2A_n78A- 3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-46.82	-13	PASS

DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-40.48	-13	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-40.79	-13	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-44.57	-13	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-68.76	-43	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	0.15	30	-82.37	-33	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	30	1000	-62.23	-23	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	1000	3000	-45.09	-13	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	3000	6000	-40.46	-13	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	6000	26000	-40.85	-13	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Outer_Full	26000	40000	-44.56	-13	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-69.26	-43	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-82.55	-33	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-62.31	-23	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-45.63	-13	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-40.59	-13	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-40.64	-13	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-44.46	-13	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-68.79	-43	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-80.78	-33	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-62.07	-23	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-43.42	-13	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-40.57	-13	PASS

3450-3550										
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-40.81	-13	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-44.39	-13	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-67.42	-43	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	0.15	30	-82.12	-33	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	30	1000	-61.97	-23	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	1000	3000	-45.53	-13	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	3000	6000	-40.52	-13	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	6000	26000	-40.59	-13	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Outer_Full	26000	40000	-44.44	-13	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-69.16	-43	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-79.62	-33	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-62.16	-23	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-44.89	-13	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-40.50	-13	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-40.44	-13	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-44.56	-13	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-67.54	-43	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-80.54	-33	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-62.12	-23	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-44.71	-13	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-40.38	-13	PASS

DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-40.79	-13	PASS
DC_2A_n78A-3450-3550	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-44.44	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-67.20	-43	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	0.15	30	-82.42	-33	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	30	1000	-62.08	-23	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	1000	3000	-43.27	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	3000	6000	-40.29	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	6000	26000	-40.70	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Outer_Full	26000	40000	-44.45	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-68.34	-43	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-80.58	-33	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-62.27	-23	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-44.16	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-40.61	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-40.67	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-44.57	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-68.81	-43	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-80.71	-33	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-62.42	-23	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-45.41	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-40.48	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-40.71	-13	PASS

3450-3550										
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-44.27	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-67.59	-43	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	0.15	30	-81.04	-33	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	30	1000	-62.19	-23	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	1000	3000	-44.87	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	3000	6000	-40.43	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	6000	26000	-40.67	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Outer_Full	26000	40000	-44.44	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-67.44	-43	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-82.18	-33	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-62.26	-23	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-45.95	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-40.44	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-40.71	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-44.30	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-69.20	-43	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-82.29	-33	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-62.27	-23	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-46.27	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-40.41	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-40.63	-13	PASS

DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-44.32	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-67.93	-43	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	0.15	30	-81.19	-33	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	30	1000	-62.22	-23	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	1000	3000	-45.26	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	3000	6000	-40.44	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	6000	26000	-40.85	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Outer_Full	26000	40000	-44.53	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-67.96	-43	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-81.22	-33	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-61.90	-23	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-45.28	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-40.50	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-40.54	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-44.40	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-68.89	-43	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-81.31	-33	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-62.26	-23	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-45.19	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-40.43	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-40.76	-13	PASS
DC_2A_n78A-3450-3550	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-44.56	-13	PASS

3450-3550										
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-68.91	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	0.15	30	-81.21	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	30	1000	-62.25	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	1000	3000	-47.12	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	3000	6000	-40.64	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	6000	26000	-40.88	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	26000	40000	-44.47	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-68.49	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-81.14	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-62.24	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-43.54	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-40.54	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-40.69	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-44.60	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-67.36	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-80.96	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-62.13	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-43.43	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-40.50	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-40.73	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-44.57	-13	PASS

DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-67.20	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	0.15	30	-81.36	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	30	1000	-62.10	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	1000	3000	-44.87	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	3000	6000	-40.36	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	6000	26000	-40.80	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	26000	40000	-44.33	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-68.28	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-80.42	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-62.24	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-43.48	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-40.48	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-40.79	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-44.52	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-67.84	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-81.12	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-62.19	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-45.51	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-40.60	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-40.68	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-44.57	-13	PASS
DC_2A_n78A-	30	5+20	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-68.92	-43	PASS

3450-3550										
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	0.15	30	-81.88	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	30	1000	-62.30	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	1000	3000	-44.08	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	3000	6000	-40.47	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	6000	26000	-40.64	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	26000	40000	-44.58	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-69.05	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-80.22	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-62.13	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-45.33	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-40.62	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-40.66	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-44.50	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-67.90	-43	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-80.08	-33	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-62.14	-23	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-45.57	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-40.60	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-40.67	-13	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-44.49	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-68.91	-43	PASS

DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	0.15	30	-81.82	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	30	1000	-61.87	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	1000	3000	-45.29	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	3000	6000	-40.52	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	6000	26000	-40.70	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	26000	40000	-44.36	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-67.53	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-79.51	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-62.15	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-45.31	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-40.56	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-40.70	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-44.38	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-68.15	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-81.57	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-61.95	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-44.72	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-40.42	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-40.70	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-44.56	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-69.20	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	0.15	30	-82.34	-33	PASS

3450-3550										
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	30	1000	-61.96	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	1000	3000	-45.05	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	3000	6000	-40.53	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	6000	26000	-40.72	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	26000	40000	-44.33	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-68.83	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-82.56	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-62.45	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-45.47	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-40.64	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-40.76	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-44.50	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-68.14	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-80.22	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-61.87	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-45.91	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-40.51	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-40.52	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-44.47	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-67.87	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	0.15	30	-81.57	-33	PASS

DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	30	1000	-62.18	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	1000	3000	-44.43	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	3000	6000	-40.42	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	6000	26000	-40.74	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	26000	40000	-44.40	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-68.86	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-81.30	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-62.07	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-45.29	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-40.49	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-40.83	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-44.49	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-68.96	-43	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-80.28	-33	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-61.23	-23	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-45.42	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-40.65	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-40.88	-13	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-44.51	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-67.77	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	0.15	30	-81.12	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	30	1000	-62.25	-23	PASS

3450-3550										
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	1000	3000	-45.59	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	3000	6000	-40.52	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	6000	26000	-40.76	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	26000	40000	-44.25	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-68.63	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-81.13	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-62.10	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-47.19	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-40.55	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-40.70	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-44.53	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-69.65	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-80.08	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-62.11	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-46.58	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-40.58	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-40.63	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-44.48	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-69.13	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	0.15	30	-81.59	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	30	1000	-62.44	-23	PASS

DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	1000	3000	-46.28	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	3000	6000	-40.49	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	6000	26000	-40.63	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	26000	40000	-44.32	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-68.61	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-81.56	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-62.25	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-43.84	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-40.53	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-40.72	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-44.53	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-69.42	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-80.62	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-62.30	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-47.17	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-40.59	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-40.74	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-44.51	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-67.90	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	0.15	30	-81.05	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	30	1000	-61.30	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	1000	3000	-46.37	-13	PASS

3450-3550										
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	3000	6000	-40.43	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	6000	26000	-40.89	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	26000	40000	-44.33	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-68.36	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-79.75	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-61.61	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-45.09	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-40.60	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-40.65	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-44.56	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-68.05	-43	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-80.95	-33	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-62.27	-23	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-44.44	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-40.52	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-40.73	-13	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-44.48	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-68.27	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	0.15	30	-80.93	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	30	1000	-62.01	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	1000	3000	-44.93	-13	PASS

DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	3000	6000	-40.48	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	6000	26000	-40.75	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	26000	40000	-44.58	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-68.54	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-80.16	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-61.84	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-44.99	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-40.46	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-40.69	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-44.54	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-69.00	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-80.78	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-62.06	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-43.10	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-40.46	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-40.79	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-44.59	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-68.14	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	0.15	30	-80.25	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	30	1000	-62.36	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	1000	3000	-46.97	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	3000	6000	-40.48	-13	PASS

3450-3550										
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	6000	26000	-40.70	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	26000	40000	-44.50	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-68.74	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-82.62	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-62.11	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-45.91	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-40.55	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-40.91	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-44.31	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-68.23	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-80.65	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-62.13	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-45.09	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-40.58	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-40.61	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-44.52	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-68.09	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	0.15	30	-80.54	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	30	1000	-61.71	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	1000	3000	-42.41	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	3000	6000	-40.63	-13	PASS

DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	6000	26000	-40.80	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	26000	40000	-44.16	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-68.28	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-81.97	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-62.27	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-46.53	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-40.53	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-40.83	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-44.68	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-68.31	-43	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-81.43	-33	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-62.18	-23	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-47.14	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-40.36	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-40.88	-13	PASS
DC_2A_n78A-3450-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-44.58	-13	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-67.68	-43	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	0.15	30	-82.31	-33	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	30	1000	-62.16	-23	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	1000	3000	-44.07	-13	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	3000	6000	-40.65	-13	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	6000	26000	-40.75	-13	PASS

3450-3550										
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Outer_Full	26000	40000	-44.60	-13	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-68.21	-43	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-79.82	-33	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-62.20	-23	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-45.03	-13	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-40.53	-13	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-40.78	-13	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-44.36	-13	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-68.64	-43	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-80.37	-33	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-62.15	-23	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-44.00	-13	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-40.55	-13	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-40.84	-13	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-44.46	-13	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-69.05	-43	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	0.15	30	-81.02	-33	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	30	1000	-62.19	-23	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	1000	3000	-46.13	-13	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	3000	6000	-40.46	-13	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	6000	26000	-40.80	-13	PASS

DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	26000	40000	-44.35	-13	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-69.19	-43	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-80.29	-33	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-61.89	-23	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-44.86	-13	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-40.46	-13	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-40.82	-13	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-44.48	-13	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-68.99	-43	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-81.33	-33	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-61.74	-23	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-45.23	-13	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-40.58	-13	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-40.60	-13	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-44.45	-13	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-69.23	-43	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	0.15	30	-81.79	-33	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	30	1000	-62.30	-23	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	1000	3000	-45.87	-13	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	3000	6000	-40.65	-13	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	6000	26000	-40.77	-13	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Outer_Full	26000	40000	-44.37	-13	PASS

3450-3550										
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-68.63	-43	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-81.76	-33	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-61.37	-23	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-45.30	-13	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-40.34	-13	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-40.81	-13	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-44.43	-13	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-69.19	-43	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-81.73	-33	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-62.23	-23	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-46.10	-13	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-40.50	-13	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-40.80	-13	PASS
DC_2A_n78A-3450-3550	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-44.50	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-68.97	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	0.15	30	-82.15	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	30	1000	-61.96	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	1000	3000	-44.59	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	3000	6000	-40.38	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	6000	26000	-40.72	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	26000	40000	-44.47	-13	PASS

DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-69.75	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-82.06	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-62.20	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-46.65	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-40.30	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-40.75	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-44.54	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-69.97	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-81.98	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-62.09	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-45.15	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-40.50	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-40.84	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-44.49	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-68.05	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	0.15	30	-82.48	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	30	1000	-62.20	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	1000	3000	-44.77	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	3000	6000	-40.35	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	6000	26000	-40.49	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	26000	40000	-44.38	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-68.85	-43	PASS

3450-3550										
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-81.95	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-62.34	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-44.47	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-40.46	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-40.66	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-44.42	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-68.88	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-81.44	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-62.31	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-45.89	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-40.61	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-40.79	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-44.46	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-68.08	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	0.15	30	-81.79	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	30	1000	-61.54	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	1000	3000	-46.63	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	3000	6000	-40.56	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	6000	26000	-40.83	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	26000	40000	-44.44	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-68.66	-43	PASS

DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-82.16	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-61.80	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-46.71	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-40.42	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-40.71	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-44.47	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-68.10	-43	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-80.60	-33	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-62.26	-23	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-45.57	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-40.55	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-40.85	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-44.41	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-68.10	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	0.15	30	-81.84	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	30	1000	-62.23	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	1000	3000	-46.54	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	3000	6000	-40.56	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	6000	26000	-40.69	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	26000	40000	-44.32	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-69.46	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-82.13	-33	PASS

3450-3550										
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-62.31	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-46.59	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-40.68	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-40.77	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-44.21	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-69.55	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-82.25	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-62.26	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-46.36	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-40.53	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-40.76	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-44.43	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-69.73	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	0.15	30	-81.64	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	30	1000	-62.20	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	1000	3000	-44.83	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	3000	6000	-40.58	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	6000	26000	-40.82	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	26000	40000	-44.43	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-68.69	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-81.33	-33	PASS

DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-62.26	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-43.93	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-40.58	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-40.88	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-44.43	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-68.07	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-80.21	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-61.82	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-45.67	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-40.45	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-40.86	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-44.43	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-67.76	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	0.15	30	-81.52	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	30	1000	-62.06	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	1000	3000	-46.53	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	3000	6000	-40.46	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	6000	26000	-40.62	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	26000	40000	-44.44	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-67.49	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-80.98	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-62.15	-23	PASS

3450-3550										
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-47.49	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-40.48	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-40.77	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-44.43	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-69.48	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-80.07	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-62.17	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-44.06	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-40.51	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-40.82	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-44.56	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-68.79	-43	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.15	30	-83.20	-33	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	30	1000	-62.23	-23	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	1000	3000	-46.15	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	3000	6000	-40.44	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	6000	26000	-40.86	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Outer_Full	26000	40000	-44.48	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-69.46	-43	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-82.76	-33	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-62.21	-23	PASS

DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-45.16	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-40.36	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-40.86	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-44.66	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-69.46	-43	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-82.33	-33	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-62.11	-23	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-44.08	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-40.56	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-40.87	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-44.33	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-67.95	-43	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.15	30	-79.18	-33	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	30	1000	-62.25	-23	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	1000	3000	-45.80	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	3000	6000	-40.62	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	6000	26000	-40.77	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Outer_Full	26000	40000	-44.44	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-68.82	-43	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-81.87	-33	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-61.89	-23	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-44.35	-13	PASS

3450-3550		0								
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-40.52	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-40.82	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-44.45	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-68.67	-43	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-81.49	-33	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-62.07	-23	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-45.25	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-40.14	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-40.94	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-44.33	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-69.02	-43	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.15	30	-82.73	-33	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	30	1000	-62.24	-23	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	1000	3000	-46.64	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	3000	6000	-40.62	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	6000	26000	-40.74	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Outer_Full	26000	40000	-44.44	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-69.52	-43	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-82.57	-33	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-62.16	-23	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-45.64	-13	PASS

DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-40.59	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-40.80	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-44.12	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-69.28	-43	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-81.28	-33	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-62.23	-23	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-46.07	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-40.16	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-40.81	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-44.38	-13	PASS

Test Graphs

