

Appendix for N78 (3700-3800MHz)

Product Name: Smart phone

Model No: Black 5

Appendix A: Average Power Output Data for SA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
N78-3700-3800	30	10	DFT-QPSK	L	Outer_Full	25.00	PC2	PASS
N78-3700-3800	30	10	DFT-QPSK	L	Inner_Full	25.99	PC2	PASS
N78-3700-3800	30	10	DFT-QPSK	L	Edge_1RB_Left	22.80	PC2	PASS
N78-3700-3800	30	10	DFT-QPSK	L	Edge_1RB_Right	22.90	PC2	PASS
N78-3700-3800	30	10	DFT-16QAM	L	Outer_Full	23.81	PC2	PASS
N78-3700-3800	30	10	DFT-16QAM	L	Inner_Full	24.82	PC2	PASS
N78-3700-3800	30	10	DFT-16QAM	L	Edge_1RB_Left	22.88	PC2	PASS
N78-3700-3800	30	10	DFT-16QAM	L	Edge_1RB_Right	22.96	PC2	PASS
N78-3700-3800	30	10	DFT-QPSK	M	Outer_Full	25.54	PC2	PASS
N78-3700-3800	30	10	DFT-QPSK	M	Inner_Full	26.48	PC2	PASS
N78-3700-3800	30	10	DFT-QPSK	M	Edge_1RB_Left	23.31	PC2	PASS
N78-3700-3800	30	10	DFT-QPSK	M	Edge_1RB_Right	23.21	PC2	PASS
N78-3700-3800	30	10	DFT-16QAM	M	Outer_Full	24.70	PC2	PASS
N78-3700-3800	30	10	DFT-16QAM	M	Inner_Full	25.47	PC2	PASS
N78-3700-3800	30	10	DFT-16QAM	M	Edge_1RB_Left	23.49	PC2	PASS
N78-3700-3800	30	10	DFT-16QAM	M	Edge_1RB_Right	23.44	PC2	PASS
N78-3700-3800	30	10	DFT-QPSK	H	Outer_Full	25.12	PC2	PASS
N78-3700-3800	30	10	DFT-QPSK	H	Inner_Full	26.09	PC2	PASS

N78-3700 -3800	30	10	DFT-QPSK	H	Edge_1RB_Left	22.78	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	H	Edge_1RB_Right	22.84	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	H	Outer_Full	24.16	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	H	Inner_Full	25.12	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	H	Edge_1RB_Left	22.69	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	H	Edge_1RB_Right	22.76	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	L	Outer_Full	25.14	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	L	Inner_Full	26.00	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	L	Edge_1RB_Left	22.56	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	L	Edge_1RB_Right	22.73	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	L	Outer_Full	24.11	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	L	Inner_Full	25.10	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	L	Edge_1RB_Left	22.32	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	L	Edge_1RB_Right	22.50	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	M	Outer_Full	25.66	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	M	Inner_Full	26.58	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	M	Edge_1RB_Left	23.07	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	M	Edge_1RB_Right	23.02	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	M	Outer_Full	24.66	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	M	Inner_Full	25.66	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	M	Edge_1RB_Left	23.36	PC2	PASS
N78-3700	30	15	DFT-16QAM	M	Edge_1RB_Right	23.36	PC2	PASS

-3800								
N78-3700 -3800	30	15	DFT-QPSK	H	Outer_Full	25.12	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	H	Inner_Full	26.03	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	H	Edge_1RB_Left	22.80	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	H	Edge_1RB_Right	22.89	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	H	Outer_Full	24.11	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	H	Inner_Full	25.10	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	H	Edge_1RB_Left	22.58	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	H	Edge_1RB_Right	22.71	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	L	Outer_Full	25.11	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	L	Inner_Full	26.05	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	L	Edge_1RB_Left	22.64	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	L	Edge_1RB_Right	22.82	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	L	Outer_Full	24.06	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	L	Inner_Full	24.96	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	L	Edge_1RB_Left	22.62	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	L	Edge_1RB_Right	22.82	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	M	Outer_Full	25.59	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	M	Inner_Full	26.61	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	M	Edge_1RB_Left	23.18	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	M	Edge_1RB_Right	23.18	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	M	Outer_Full	24.61	PC2	PASS

N78-3700 -3800	30	20	DFT-16QAM	M	Inner_Full	25.55	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	M	Edge_1RB_Left	23.55	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	M	Edge_1RB_Right	23.50	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	H	Outer_Full	25.16	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	H	Inner_Full	26.09	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	H	Edge_1RB_Left	22.83	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	H	Edge_1RB_Right	22.90	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	H	Outer_Full	24.09	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	H	Inner_Full	25.03	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	H	Edge_1RB_Left	22.78	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	H	Edge_1RB_Right	22.90	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	L	Outer_Full	25.19	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	L	Inner_Full	26.21	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	L	Edge_1RB_Left	22.55	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	L	Edge_1RB_Right	22.81	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	L	Outer_Full	24.14	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	L	Inner_Full	25.18	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	L	Edge_1RB_Left	22.47	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	L	Edge_1RB_Right	22.66	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	M	Outer_Full	25.58	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	M	Inner_Full	26.59	PC2	PASS
N78-3700	30	40	DFT-QPSK	M	Edge_1RB_Left	23.14	PC2	PASS

-3800								
N78-3700 -3800	30	40	DFT-QPSK	M	Edge_1RB_Right	23.21	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	M	Outer_Full	24.62	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	M	Inner_Full	25.63	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	M	Edge_1RB_Left	23.48	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	M	Edge_1RB_Right	23.50	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	H	Outer_Full	25.29	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	H	Inner_Full	26.22	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	H	Edge_1RB_Left	22.91	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	H	Edge_1RB_Right	22.97	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	H	Outer_Full	24.18	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	H	Inner_Full	25.30	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	H	Edge_1RB_Left	22.57	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	H	Edge_1RB_Right	22.63	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	L	Outer_Full	25.27	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	L	Inner_Full	26.27	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	L	Edge_1RB_Left	22.70	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	L	Edge_1RB_Right	22.91	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	L	Outer_Full	24.21	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	L	Inner_Full	25.29	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	L	Edge_1RB_Left	22.64	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	L	Edge_1RB_Right	22.83	PC2	PASS

N78-3700 -3800	30	50	DFT-QPSK	M	Outer_Full	25.60	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	M	Inner_Full	26.59	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	M	Edge_1RB_Left	23.14	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	M	Edge_1RB_Right	23.19	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	M	Outer_Full	24.61	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	M	Inner_Full	25.66	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	M	Edge_1RB_Left	23.34	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	M	Edge_1RB_Right	23.41	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	H	Outer_Full	25.27	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	H	Inner_Full	26.31	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	H	Edge_1RB_Left	23.01	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	H	Edge_1RB_Right	23.07	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	H	Outer_Full	24.27	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	H	Inner_Full	25.26	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	H	Edge_1RB_Left	22.80	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	H	Edge_1RB_Right	22.89	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	L	Outer_Full	25.27	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	L	Inner_Full	26.24	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	L	Edge_1RB_Left	22.54	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	L	Edge_1RB_Right	22.75	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	L	Outer_Full	24.24	PC2	PASS
N78-3700	30	60	DFT-16QAM	L	Inner_Full	25.29	PC2	PASS

-3800								
N78-3700 -3800	30	60	DFT-16QAM	L	Edge_1RB_Left	22.35	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	L	Edge_1RB_Right	22.55	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	M	Outer_Full	25.61	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	M	Inner_Full	26.61	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	M	Edge_1RB_Left	22.90	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	M	Edge_1RB_Right	23.00	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	M	Outer_Full	24.59	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	M	Inner_Full	25.55	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	M	Edge_1RB_Left	23.15	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	M	Edge_1RB_Right	23.28	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	H	Outer_Full	25.25	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	H	Inner_Full	26.20	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	H	Edge_1RB_Left	22.80	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	H	Edge_1RB_Right	22.82	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	H	Outer_Full	24.27	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	H	Inner_Full	25.24	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	H	Edge_1RB_Left	22.60	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	H	Edge_1RB_Right	22.66	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	L	Outer_Full	25.25	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	L	Inner_Full	26.22	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	L	Edge_1RB_Left	22.67	PC2	PASS

N78-3700 -3800	30	70	DFT-QPSK	L	Edge_1RB_Right	22.86	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	L	Outer_Full	24.20	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	L	Inner_Full	25.20	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	L	Edge_1RB_Left	22.64	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	L	Edge_1RB_Right	22.82	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	M	Outer_Full	25.62	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	M	Inner_Full	26.56	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	M	Edge_1RB_Left	23.11	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	M	Edge_1RB_Right	23.05	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	M	Outer_Full	24.59	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	M	Inner_Full	25.58	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	M	Edge_1RB_Left	23.34	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	M	Edge_1RB_Right	23.30	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	H	Outer_Full	25.29	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	H	Inner_Full	26.26	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	H	Edge_1RB_Left	22.96	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	H	Edge_1RB_Right	23.00	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	H	Outer_Full	24.14	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	H	Inner_Full	25.26	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	H	Edge_1RB_Left	22.89	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	H	Edge_1RB_Right	22.87	PC2	PASS
N78-3700	30	80	DFT-QPSK	L	Outer_Full	25.53	PC2	PASS

-3800								
N78-3700 -3800	30	80	DFT-QPSK	L	Inner_Full	26.57	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	L	Edge_1RB_Left	22.99	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	L	Edge_1RB_Right	23.19	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	L	Outer_Full	24.55	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	L	Inner_Full	25.55	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	L	Edge_1RB_Left	22.86	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	L	Edge_1RB_Right	22.93	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	M	Outer_Full	25.50	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	M	Inner_Full	26.48	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	M	Edge_1RB_Left	23.02	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	M	Edge_1RB_Right	23.02	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	M	Outer_Full	24.52	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	M	Inner_Full	25.55	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	M	Edge_1RB_Left	23.32	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	M	Edge_1RB_Right	23.26	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	H	Outer_Full	25.51	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	H	Inner_Full	26.50	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	H	Edge_1RB_Left	23.14	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	H	Edge_1RB_Right	23.19	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	H	Outer_Full	24.46	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	H	Inner_Full	25.54	PC2	PASS

N78-3700 -3800	30	80	DFT-16QAM	H	Edge_1RB_Left	22.98	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	H	Edge_1RB_Right	22.97	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	L	Outer_Full	25.53	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	L	Inner_Full	26.61	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	L	Edge_1RB_Left	22.96	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	L	Edge_1RB_Right	23.07	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	L	Outer_Full	24.51	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	L	Inner_Full	25.63	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	L	Edge_1RB_Left	22.93	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	L	Edge_1RB_Right	23.03	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	M	Outer_Full	25.52	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	M	Inner_Full	26.51	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	M	Edge_1RB_Left	22.95	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	M	Edge_1RB_Right	23.04	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	M	Outer_Full	24.61	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	M	Inner_Full	25.57	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	M	Edge_1RB_Left	23.20	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	M	Edge_1RB_Right	23.31	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	H	Outer_Full	25.59	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	H	Inner_Full	26.62	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	H	Edge_1RB_Left	23.07	PC2	PASS
N78-3700	30	90	DFT-QPSK	H	Edge_1RB_Right	23.24	PC2	PASS

-3800								
N78-3700 -3800	30	90	DFT-16QAM	H	Outer_Full	24.51	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	H	Inner_Full	25.59	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	H	Edge_1RB_Left	22.95	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	H	Edge_1RB_Right	23.09	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	L	Outer_Full	25.53	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	L	Inner_Full	26.59	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	L	Edge_1RB_Left	22.76	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	L	Edge_1RB_Right	23.01	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	L	Outer_Full	24.59	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	L	Inner_Full	25.53	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	L	Edge_1RB_Left	22.59	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	L	Edge_1RB_Right	22.79	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	M	Outer_Full	25.54	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	M	Inner_Full	26.54	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	M	Edge_1RB_Left	22.76	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	M	Edge_1RB_Right	23.04	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	M	Outer_Full	24.58	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	M	Inner_Full	25.54	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	M	Edge_1RB_Left	22.58	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	M	Edge_1RB_Right	22.81	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	H	Outer_Full	25.55	PC2	PASS

N78-3700 -3800	30	100	DFT-QPSK	H	Inner_Full	26.63	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	H	Edge_1RB_Left	22.76	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	H	Edge_1RB_Right	23.03	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	H	Outer_Full	24.57	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	H	Inner_Full	25.54	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	H	Edge_1RB_Left	22.63	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	H	Edge_1RB_Right	22.81	PC2	PASS

Hotspot ON

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
N78-3700 -3800	30	10	DFT-QPSK	L	Outer_Full	21.09	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	L	Inner_Full	21.20	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	L	Edge_1RB_Left	21.33	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	L	Edge_1RB_Right	21.51	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	L	Outer_Full	20.94	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	L	Inner_Full	20.93	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	L	Edge_1RB_Left	21.39	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	L	Edge_1RB_Right	21.53	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	M	Outer_Full	21.64	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	M	Inner_Full	21.62	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	M	Edge_1RB_Left	21.81	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	M	Edge_1RB_Right	21.74	PC2	PASS

N78-3700 -3800	30	10	DFT-16QAM	M	Outer_Full	21.65	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	M	Inner_Full	21.62	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	M	Edge_1RB_Left	22.03	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	M	Edge_1RB_Right	21.99	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	H	Outer_Full	21.20	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	H	Inner_Full	21.41	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	H	Edge_1RB_Left	21.29	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	H	Edge_1RB_Right	21.37	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	H	Outer_Full	21.14	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	H	Inner_Full	21.25	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	H	Edge_1RB_Left	21.17	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	H	Edge_1RB_Right	21.27	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	L	Outer_Full	21.17	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	L	Inner_Full	21.20	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	L	Edge_1RB_Left	21.11	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	L	Edge_1RB_Right	21.25	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	L	Outer_Full	21.28	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	L	Inner_Full	21.21	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	L	Edge_1RB_Left	20.88	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	L	Edge_1RB_Right	21.04	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	M	Outer_Full	21.74	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	M	Inner_Full	21.74	PC2	PASS

-3800								
N78-3700 -3800	30	15	DFT-QPSK	M	Edge_1RB_Left	21.54	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	M	Edge_1RB_Right	21.45	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	M	Outer_Full	21.68	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	M	Inner_Full	21.70	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	M	Edge_1RB_Left	21.86	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	M	Edge_1RB_Right	21.83	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	H	Outer_Full	21.27	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	H	Inner_Full	21.24	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	H	Edge_1RB_Left	21.27	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	H	Edge_1RB_Right	21.38	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	H	Outer_Full	21.16	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	H	Inner_Full	21.21	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	H	Edge_1RB_Left	21.05	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	H	Edge_1RB_Right	21.16	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	L	Outer_Full	21.22	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	L	Inner_Full	21.25	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	L	Edge_1RB_Left	21.21	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	L	Edge_1RB_Right	21.41	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	L	Outer_Full	21.17	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	L	Inner_Full	21.21	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	L	Edge_1RB_Left	21.20	PC2	PASS

N78-3700 -3800	30	20	DFT-16QAM	L	Edge_1RB_Right	21.38	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	M	Outer_Full	21.68	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	M	Inner_Full	21.69	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	M	Edge_1RB_Left	21.66	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	M	Edge_1RB_Right	21.64	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	M	Outer_Full	21.62	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	M	Inner_Full	21.69	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	M	Edge_1RB_Left	22.05	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	M	Edge_1RB_Right	22.07	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	H	Outer_Full	21.19	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	H	Inner_Full	21.23	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	H	Edge_1RB_Left	21.30	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	H	Edge_1RB_Right	21.42	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	H	Outer_Full	21.14	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	H	Inner_Full	21.19	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	H	Edge_1RB_Left	21.22	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	H	Edge_1RB_Right	21.34	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	L	Outer_Full	21.24	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	L	Inner_Full	21.23	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	L	Edge_1RB_Left	21.05	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	L	Edge_1RB_Right	21.37	PC2	PASS
N78-3700	30	40	DFT-16QAM	L	Outer_Full	21.19	PC2	PASS

-3800								
N78-3700 -3800	30	40	DFT-16QAM	L	Inner_Full	21.24	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	L	Edge_1RB_Left	20.99	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	L	Edge_1RB_Right	21.25	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	M	Outer_Full	21.60	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	M	Inner_Full	21.64	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	M	Edge_1RB_Left	21.66	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	M	Edge_1RB_Right	21.72	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	M	Outer_Full	21.56	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	M	Inner_Full	21.61	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	M	Edge_1RB_Left	22.02	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	M	Edge_1RB_Right	21.97	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	H	Outer_Full	21.27	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	H	Inner_Full	21.28	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	H	Edge_1RB_Left	21.40	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	H	Edge_1RB_Right	21.48	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	H	Outer_Full	21.18	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	H	Inner_Full	21.23	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	H	Edge_1RB_Left	21.07	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	H	Edge_1RB_Right	21.11	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	L	Outer_Full	21.27	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	L	Inner_Full	21.36	PC2	PASS

N78-3700 -3800	30	50	DFT-QPSK	L	Edge_1RB_Left	21.19	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	L	Edge_1RB_Right	21.44	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	L	Outer_Full	21.27	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	L	Inner_Full	21.28	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	L	Edge_1RB_Left	21.15	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	L	Edge_1RB_Right	21.40	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	M	Outer_Full	21.65	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	M	Inner_Full	21.70	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	M	Edge_1RB_Left	21.68	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	M	Edge_1RB_Right	21.67	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	M	Outer_Full	21.58	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	M	Inner_Full	21.65	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	M	Edge_1RB_Left	21.86	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	M	Edge_1RB_Right	21.92	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	H	Outer_Full	21.32	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	H	Inner_Full	21.37	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	H	Edge_1RB_Left	21.52	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	H	Edge_1RB_Right	21.59	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	H	Outer_Full	21.29	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	H	Inner_Full	21.25	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	H	Edge_1RB_Left	21.33	PC2	PASS
N78-3700	30	50	DFT-16QAM	H	Edge_1RB_Right	21.41	PC2	PASS

-3800								
N78-3700 -3800	30	60	DFT-QPSK	L	Outer_Full	21.27	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	L	Inner_Full	21.30	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	L	Edge_1RB_Left	21.04	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	L	Edge_1RB_Right	21.23	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	L	Outer_Full	21.22	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	L	Inner_Full	21.35	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	L	Edge_1RB_Left	20.86	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	L	Edge_1RB_Right	21.04	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	M	Outer_Full	21.60	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	M	Inner_Full	21.65	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	M	Edge_1RB_Left	21.48	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	M	Edge_1RB_Right	21.50	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	M	Outer_Full	21.60	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	M	Inner_Full	21.61	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	M	Edge_1RB_Left	21.74	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	M	Edge_1RB_Right	21.80	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	H	Outer_Full	21.25	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	H	Inner_Full	21.28	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	H	Edge_1RB_Left	21.34	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	H	Edge_1RB_Right	21.36	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	H	Outer_Full	21.27	PC2	PASS

N78-3700 -3800	30	60	DFT-16QAM	H	Inner_Full	21.26	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	H	Edge_1RB_Left	21.11	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	H	Edge_1RB_Right	21.16	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	L	Outer_Full	21.35	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	L	Inner_Full	21.42	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	L	Edge_1RB_Left	21.23	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	L	Edge_1RB_Right	21.39	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	L	Outer_Full	21.28	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	L	Inner_Full	21.39	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	L	Edge_1RB_Left	21.19	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	L	Edge_1RB_Right	21.39	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	M	Outer_Full	21.69	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	M	Inner_Full	21.75	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	M	Edge_1RB_Left	21.64	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	M	Edge_1RB_Right	21.62	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	M	Outer_Full	21.64	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	M	Inner_Full	21.72	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	M	Edge_1RB_Left	21.89	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	M	Edge_1RB_Right	21.91	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	H	Outer_Full	21.45	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	H	Inner_Full	21.41	PC2	PASS
N78-3700	30	70	DFT-QPSK	H	Edge_1RB_Left	21.54	PC2	PASS

-3800								
N78-3700 -3800	30	70	DFT-QPSK	H	Edge_1RB_Right	21.61	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	H	Outer_Full	21.35	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	H	Inner_Full	21.34	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	H	Edge_1RB_Left	21.40	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	H	Edge_1RB_Right	21.45	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	L	Outer_Full	21.71	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	L	Inner_Full	21.84	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	L	Edge_1RB_Left	21.54	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	L	Edge_1RB_Right	21.70	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	L	Outer_Full	21.65	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	L	Inner_Full	21.77	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	L	Edge_1RB_Left	21.40	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	L	Edge_1RB_Right	21.48	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	M	Outer_Full	21.72	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	M	Inner_Full	21.73	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	M	Edge_1RB_Left	21.58	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	M	Edge_1RB_Right	21.64	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	M	Outer_Full	21.70	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	M	Inner_Full	21.65	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	M	Edge_1RB_Left	21.83	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	M	Edge_1RB_Right	21.86	PC2	PASS

N78-3700 -3800	30	80	DFT-QPSK	H	Outer_Full	21.68	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	H	Inner_Full	21.75	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	H	Edge_1RB_Left	21.67	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	H	Edge_1RB_Right	21.76	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	H	Outer_Full	21.62	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	H	Inner_Full	21.69	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	H	Edge_1RB_Left	21.51	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	H	Edge_1RB_Right	21.64	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	L	Outer_Full	21.71	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	L	Inner_Full	21.81	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	L	Edge_1RB_Left	21.51	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	L	Edge_1RB_Right	21.65	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	L	Outer_Full	21.67	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	L	Inner_Full	21.80	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	L	Edge_1RB_Left	21.51	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	L	Edge_1RB_Right	21.63	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	M	Outer_Full	21.72	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	M	Inner_Full	21.74	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	M	Edge_1RB_Left	21.50	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	M	Edge_1RB_Right	21.62	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	M	Outer_Full	21.62	PC2	PASS
N78-3700	30	90	DFT-16QAM	M	Inner_Full	21.70	PC2	PASS

-3800								
N78-3700 -3800	30	90	DFT-16QAM	M	Edge_1RB_Left	21.75	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	M	Edge_1RB_Right	21.95	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	H	Outer_Full	21.75	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	H	Inner_Full	21.78	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	H	Edge_1RB_Left	21.67	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	H	Edge_1RB_Right	21.84	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	H	Outer_Full	21.72	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	H	Inner_Full	21.68	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	H	Edge_1RB_Left	21.49	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	H	Edge_1RB_Right	21.69	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	L	Outer_Full	21.72	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	L	Inner_Full	22.10	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	L	Edge_1RB_Left	21.28	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	L	Edge_1RB_Right	21.67	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	L	Outer_Full	21.66	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	L	Inner_Full	21.72	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	L	Edge_1RB_Left	21.16	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	L	Edge_1RB_Right	21.46	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	M	Outer_Full	21.72	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	M	Inner_Full	21.76	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	M	Edge_1RB_Left	21.35	PC2	PASS

N78-3700 -3800	30	100	DFT-QPSK	M	Edge_1RB_Right	21.66	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	M	Outer_Full	21.67	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	M	Inner_Full	21.71	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	M	Edge_1RB_Left	21.16	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	M	Edge_1RB_Right	21.46	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	H	Outer_Full	21.75	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	H	Inner_Full	21.73	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	H	Edge_1RB_Left	21.30	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	H	Edge_1RB_Right	21.65	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	H	Outer_Full	21.68	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	H	Inner_Full	21.73	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	H	Edge_1RB_Left	21.15	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	H	Edge_1RB_Right	21.47	PC2	PASS

Receiver ON

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
N78-3700 -3800	30	10	DFT-QPSK	L	Outer_Full	18.02	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	L	Inner_Full	18.13	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	L	Edge_1RB_Left	18.36	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	L	Edge_1RB_Right	18.45	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	L	Outer_Full	17.97	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	L	Inner_Full	17.90	PC2	PASS

N78-3700 -3800	30	10	DFT-16QAM	L	Edge_1RB_Left	18.37	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	L	Edge_1RB_Right	18.50	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	M	Outer_Full	18.68	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	M	Inner_Full	18.53	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	M	Edge_1RB_Left	18.85	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	M	Edge_1RB_Right	18.75	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	M	Outer_Full	18.61	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	M	Inner_Full	18.59	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	M	Edge_1RB_Left	19.09	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	M	Edge_1RB_Right	19.06	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	H	Outer_Full	18.21	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	H	Inner_Full	18.08	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	H	Edge_1RB_Left	18.30	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	H	Edge_1RB_Right	18.37	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	H	Outer_Full	17.88	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	H	Inner_Full	18.20	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	H	Edge_1RB_Left	18.30	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	H	Edge_1RB_Right	18.44	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	L	Outer_Full	18.18	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	L	Inner_Full	18.07	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	L	Edge_1RB_Left	18.08	PC2	PASS
N78-3700	30	15	DFT-QPSK	L	Edge_1RB_Right	18.24	PC2	PASS

-3800								
N78-3700 -3800	30	15	DFT-16QAM	L	Outer_Full	18.15	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	L	Inner_Full	18.19	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	L	Edge_1RB_Left	17.89	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	L	Edge_1RB_Right	18.09	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	M	Outer_Full	18.71	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	M	Inner_Full	18.67	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	M	Edge_1RB_Left	18.55	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	M	Edge_1RB_Right	18.49	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	M	Outer_Full	18.70	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	M	Inner_Full	18.62	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	M	Edge_1RB_Left	18.80	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	M	Edge_1RB_Right	18.74	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	H	Outer_Full	18.25	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	H	Inner_Full	18.09	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	H	Edge_1RB_Left	18.29	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	H	Edge_1RB_Right	18.39	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	H	Outer_Full	18.16	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	H	Inner_Full	18.27	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	H	Edge_1RB_Left	17.93	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	H	Edge_1RB_Right	18.12	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	L	Outer_Full	18.21	PC2	PASS

N78-3700 -3800	30	20	DFT-QPSK	L	Inner_Full	18.15	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	L	Edge_1RB_Left	18.19	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	L	Edge_1RB_Right	18.35	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	L	Outer_Full	18.15	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	L	Inner_Full	18.14	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	L	Edge_1RB_Left	18.14	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	L	Edge_1RB_Right	18.34	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	M	Outer_Full	18.64	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	M	Inner_Full	18.66	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	M	Edge_1RB_Left	18.71	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	M	Edge_1RB_Right	18.64	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	M	Outer_Full	18.61	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	M	Inner_Full	18.67	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	M	Edge_1RB_Left	19.13	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	M	Edge_1RB_Right	19.09	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	H	Outer_Full	18.22	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	H	Inner_Full	18.15	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	H	Edge_1RB_Left	18.35	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	H	Edge_1RB_Right	18.42	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	H	Outer_Full	18.18	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	H	Inner_Full	18.19	PC2	PASS
N78-3700	30	20	DFT-16QAM	H	Edge_1RB_Left	18.35	PC2	PASS

-3800								
N78-3700 -3800	30	20	DFT-16QAM	H	Edge_1RB_Right	18.41	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	L	Outer_Full	18.18	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	L	Inner_Full	18.11	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	L	Edge_1RB_Left	18.01	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	L	Edge_1RB_Right	18.34	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	L	Outer_Full	18.18	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	L	Inner_Full	18.26	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	L	Edge_1RB_Left	17.98	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	L	Edge_1RB_Right	18.26	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	M	Outer_Full	18.62	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	M	Inner_Full	18.53	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	M	Edge_1RB_Left	18.72	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	M	Edge_1RB_Right	18.71	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	M	Outer_Full	18.57	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	M	Inner_Full	18.63	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	M	Edge_1RB_Left	18.89	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	M	Edge_1RB_Right	18.88	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	H	Outer_Full	18.27	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	H	Inner_Full	18.15	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	H	Edge_1RB_Left	18.42	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	H	Edge_1RB_Right	18.52	PC2	PASS

N78-3700 -3800	30	40	DFT-16QAM	H	Outer_Full	18.18	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	H	Inner_Full	18.27	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	H	Edge_1RB_Left	18.00	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	H	Edge_1RB_Right	17.99	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	L	Outer_Full	18.28	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	L	Inner_Full	18.25	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	L	Edge_1RB_Left	18.16	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	L	Edge_1RB_Right	18.44	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	L	Outer_Full	18.28	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	L	Inner_Full	18.29	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	L	Edge_1RB_Left	18.12	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	L	Edge_1RB_Right	18.40	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	M	Outer_Full	18.63	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	M	Inner_Full	18.58	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	M	Edge_1RB_Left	18.67	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	M	Edge_1RB_Right	18.70	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	M	Outer_Full	18.60	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	M	Inner_Full	18.62	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	M	Edge_1RB_Left	18.98	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	M	Edge_1RB_Right	18.99	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	H	Outer_Full	18.35	PC2	PASS
N78-3700	30	50	DFT-QPSK	H	Inner_Full	18.22	PC2	PASS

-3800								
N78-3700 -3800	30	50	DFT-QPSK	H	Edge_1RB_Left	18.55	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	H	Edge_1RB_Right	18.56	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	H	Outer_Full	18.28	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	H	Inner_Full	18.25	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	H	Edge_1RB_Left	18.43	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	H	Edge_1RB_Right	18.48	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	L	Outer_Full	18.26	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	L	Inner_Full	18.21	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	L	Edge_1RB_Left	18.00	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	L	Edge_1RB_Right	18.20	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	L	Outer_Full	18.27	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	L	Inner_Full	18.28	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	L	Edge_1RB_Left	17.87	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	L	Edge_1RB_Right	18.04	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	M	Outer_Full	18.61	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	M	Inner_Full	18.56	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	M	Edge_1RB_Left	18.53	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	M	Edge_1RB_Right	18.56	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	M	Outer_Full	18.59	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	M	Inner_Full	18.56	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	M	Edge_1RB_Left	18.64	PC2	PASS

N78-3700 -3800	30	60	DFT-16QAM	M	Edge_1RB_Right	18.70	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	H	Outer_Full	18.23	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	H	Inner_Full	18.17	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	H	Edge_1RB_Left	18.37	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	H	Edge_1RB_Right	18.37	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	H	Outer_Full	18.25	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	H	Inner_Full	18.28	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	H	Edge_1RB_Left	18.06	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	H	Edge_1RB_Right	18.07	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	L	Outer_Full	18.33	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	L	Inner_Full	18.27	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	L	Edge_1RB_Left	18.21	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	L	Edge_1RB_Right	18.40	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	L	Outer_Full	18.34	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	L	Inner_Full	18.38	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	L	Edge_1RB_Left	18.18	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	L	Edge_1RB_Right	18.38	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	M	Outer_Full	18.69	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	M	Inner_Full	18.62	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	M	Edge_1RB_Left	18.65	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	M	Edge_1RB_Right	18.64	PC2	PASS
N78-3700	30	70	DFT-16QAM	M	Outer_Full	18.68	PC2	PASS

-3800								
N78-3700 -3800	30	70	DFT-16QAM	M	Inner_Full	18.72	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	M	Edge_1RB_Left	18.98	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	M	Edge_1RB_Right	18.98	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	H	Outer_Full	18.43	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	H	Inner_Full	18.28	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	H	Edge_1RB_Left	18.59	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	H	Edge_1RB_Right	18.64	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	H	Outer_Full	18.40	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	H	Inner_Full	18.36	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	H	Edge_1RB_Left	18.50	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	H	Edge_1RB_Right	18.58	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	L	Outer_Full	18.69	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	L	Inner_Full	18.69	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	L	Edge_1RB_Left	18.52	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	L	Edge_1RB_Right	18.68	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	L	Outer_Full	18.67	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	L	Inner_Full	18.78	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	L	Edge_1RB_Left	18.43	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	L	Edge_1RB_Right	18.53	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	M	Outer_Full	18.67	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	M	Inner_Full	18.60	PC2	PASS

N78-3700 -3800	30	80	DFT-QPSK	M	Edge_1RB_Left	18.57	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	M	Edge_1RB_Right	18.69	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	M	Outer_Full	18.72	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	M	Inner_Full	18.64	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	M	Edge_1RB_Left	18.77	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	M	Edge_1RB_Right	18.74	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	H	Outer_Full	18.67	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	H	Inner_Full	18.61	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	H	Edge_1RB_Left	18.70	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	H	Edge_1RB_Right	18.81	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	H	Outer_Full	18.66	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	H	Inner_Full	18.68	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	H	Edge_1RB_Left	18.45	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	H	Edge_1RB_Right	18.55	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	L	Outer_Full	18.68	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	L	Inner_Full	18.65	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	L	Edge_1RB_Left	18.53	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	L	Edge_1RB_Right	18.72	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	L	Outer_Full	18.67	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	L	Inner_Full	18.79	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	L	Edge_1RB_Left	18.49	PC2	PASS
N78-3700	30	90	DFT-16QAM	L	Edge_1RB_Right	18.66	PC2	PASS

-3800								
N78-3700 -3800	30	90	DFT-QPSK	M	Outer_Full	18.69	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	M	Inner_Full	18.64	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	M	Edge_1RB_Left	18.52	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	M	Edge_1RB_Right	18.71	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	M	Outer_Full	18.72	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	M	Inner_Full	18.72	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	M	Edge_1RB_Left	18.83	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	M	Edge_1RB_Right	19.07	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	H	Outer_Full	18.73	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	H	Inner_Full	18.61	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	H	Edge_1RB_Left	18.70	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	H	Edge_1RB_Right	18.92	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	H	Outer_Full	18.74	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	H	Inner_Full	18.67	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	H	Edge_1RB_Left	18.58	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	H	Edge_1RB_Right	18.81	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	L	Outer_Full	18.73	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	L	Inner_Full	18.62	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	L	Edge_1RB_Left	18.30	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	L	Edge_1RB_Right	18.63	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	L	Outer_Full	18.68	PC2	PASS

N78-3700 -3800	30	100	DFT-16QAM	L	Inner_Full	18.69	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	L	Edge_1RB_Left	18.27	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	L	Edge_1RB_Right	18.51	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	M	Outer_Full	19.14	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	M	Inner_Full	18.63	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	M	Edge_1RB_Left	18.33	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	M	Edge_1RB_Right	18.64	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	M	Outer_Full	18.68	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	M	Inner_Full	18.68	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	M	Edge_1RB_Left	18.20	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	M	Edge_1RB_Right	18.50	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	H	Outer_Full	18.69	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	H	Inner_Full	18.63	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	H	Edge_1RB_Left	18.30	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	H	Edge_1RB_Right	18.62	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	H	Outer_Full	18.70	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	H	Inner_Full	18.67	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	H	Edge_1RB_Left	18.20	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	H	Edge_1RB_Right	18.49	PC2	PASS

Sensor ON

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
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N78-3700 -3800	30	10	DFT-QPSK	L	Outer_Full	21.67	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	L	Inner_Full	21.79	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	L	Edge_1RB_Left	21.93	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	L	Edge_1RB_Right	22.04	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	L	Outer_Full	21.67	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	L	Inner_Full	21.49	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	L	Edge_1RB_Left	22.02	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	L	Edge_1RB_Right	22.10	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	M	Outer_Full	22.24	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	M	Inner_Full	22.21	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	M	Edge_1RB_Left	22.42	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	M	Edge_1RB_Right	22.37	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	M	Outer_Full	22.39	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	M	Inner_Full	22.23	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	M	Edge_1RB_Left	22.66	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	M	Edge_1RB_Right	22.57	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	H	Outer_Full	21.82	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	H	Inner_Full	21.86	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	H	Edge_1RB_Left	21.84	PC2	PASS
N78-3700 -3800	30	10	DFT-QPSK	H	Edge_1RB_Right	21.93	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	H	Outer_Full	21.95	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	H	Inner_Full	21.79	PC2	PASS

-3800								
N78-3700 -3800	30	10	DFT-16QAM	H	Edge_1RB_Left	21.77	PC2	PASS
N78-3700 -3800	30	10	DFT-16QAM	H	Edge_1RB_Right	21.82	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	L	Outer_Full	21.76	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	L	Inner_Full	21.78	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	L	Edge_1RB_Left	21.66	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	L	Edge_1RB_Right	21.85	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	L	Outer_Full	21.97	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	L	Inner_Full	21.78	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	L	Edge_1RB_Left	21.48	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	L	Edge_1RB_Right	21.67	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	M	Outer_Full	22.29	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	M	Inner_Full	22.34	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	M	Edge_1RB_Left	22.13	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	M	Edge_1RB_Right	22.08	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	M	Outer_Full	22.40	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	M	Inner_Full	22.31	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	M	Edge_1RB_Left	22.49	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	M	Edge_1RB_Right	22.44	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	H	Outer_Full	21.88	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	H	Inner_Full	21.82	PC2	PASS
N78-3700 -3800	30	15	DFT-QPSK	H	Edge_1RB_Left	21.85	PC2	PASS

N78-3700 -3800	30	15	DFT-QPSK	H	Edge_1RB_Right	22.01	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	H	Outer_Full	21.88	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	H	Inner_Full	21.86	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	H	Edge_1RB_Left	21.63	PC2	PASS
N78-3700 -3800	30	15	DFT-16QAM	H	Edge_1RB_Right	21.81	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	L	Outer_Full	21.80	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	L	Inner_Full	21.83	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	L	Edge_1RB_Left	21.82	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	L	Edge_1RB_Right	21.97	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	L	Outer_Full	21.87	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	L	Inner_Full	21.76	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	L	Edge_1RB_Left	21.80	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	L	Edge_1RB_Right	21.99	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	M	Outer_Full	22.29	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	M	Inner_Full	22.32	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	M	Edge_1RB_Left	22.30	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	M	Edge_1RB_Right	22.28	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	M	Outer_Full	22.32	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	M	Inner_Full	22.27	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	M	Edge_1RB_Left	22.61	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	M	Edge_1RB_Right	22.62	PC2	PASS
N78-3700	30	20	DFT-QPSK	H	Outer_Full	21.81	PC2	PASS

-3800								
N78-3700 -3800	30	20	DFT-QPSK	H	Inner_Full	21.80	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	H	Edge_1RB_Left	21.91	PC2	PASS
N78-3700 -3800	30	20	DFT-QPSK	H	Edge_1RB_Right	22.02	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	H	Outer_Full	21.86	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	H	Inner_Full	21.72	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	H	Edge_1RB_Left	21.81	PC2	PASS
N78-3700 -3800	30	20	DFT-16QAM	H	Edge_1RB_Right	21.93	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	L	Outer_Full	21.77	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	L	Inner_Full	21.80	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	L	Edge_1RB_Left	21.63	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	L	Edge_1RB_Right	21.93	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	L	Outer_Full	21.89	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	L	Inner_Full	21.83	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	L	Edge_1RB_Left	21.57	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	L	Edge_1RB_Right	21.80	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	M	Outer_Full	22.21	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	M	Inner_Full	22.21	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	M	Edge_1RB_Left	22.29	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	M	Edge_1RB_Right	22.29	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	M	Outer_Full	22.28	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	M	Inner_Full	22.23	PC2	PASS

N78-3700 -3800	30	40	DFT-16QAM	M	Edge_1RB_Left	22.62	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	M	Edge_1RB_Right	22.60	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	H	Outer_Full	21.89	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	H	Inner_Full	21.87	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	H	Edge_1RB_Left	22.03	PC2	PASS
N78-3700 -3800	30	40	DFT-QPSK	H	Edge_1RB_Right	22.09	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	H	Outer_Full	21.91	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	H	Inner_Full	21.83	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	H	Edge_1RB_Left	21.69	PC2	PASS
N78-3700 -3800	30	40	DFT-16QAM	H	Edge_1RB_Right	21.72	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	L	Outer_Full	21.86	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	L	Inner_Full	21.94	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	L	Edge_1RB_Left	21.77	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	L	Edge_1RB_Right	22.05	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	L	Outer_Full	21.95	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	L	Inner_Full	21.88	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	L	Edge_1RB_Left	21.74	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	L	Edge_1RB_Right	22.02	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	M	Outer_Full	22.23	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	M	Inner_Full	22.31	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	M	Edge_1RB_Left	22.29	PC2	PASS
N78-3700	30	50	DFT-QPSK	M	Edge_1RB_Right	22.26	PC2	PASS

-3800								
N78-3700 -3800	30	50	DFT-16QAM	M	Outer_Full	22.33	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	M	Inner_Full	22.20	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	M	Edge_1RB_Left	22.48	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	M	Edge_1RB_Right	22.47	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	H	Outer_Full	21.92	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	H	Inner_Full	21.93	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	H	Edge_1RB_Left	22.11	PC2	PASS
N78-3700 -3800	30	50	DFT-QPSK	H	Edge_1RB_Right	22.16	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	H	Outer_Full	22.02	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	H	Inner_Full	21.80	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	H	Edge_1RB_Left	21.91	PC2	PASS
N78-3700 -3800	30	50	DFT-16QAM	H	Edge_1RB_Right	22.00	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	L	Outer_Full	21.84	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	L	Inner_Full	21.85	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	L	Edge_1RB_Left	21.62	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	L	Edge_1RB_Right	21.83	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	L	Outer_Full	21.92	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	L	Inner_Full	21.88	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	L	Edge_1RB_Left	21.45	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	L	Edge_1RB_Right	21.63	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	M	Outer_Full	22.18	PC2	PASS

N78-3700 -3800	30	60	DFT-QPSK	M	Inner_Full	22.19	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	M	Edge_1RB_Left	22.07	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	M	Edge_1RB_Right	22.11	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	M	Outer_Full	22.27	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	M	Inner_Full	22.20	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	M	Edge_1RB_Left	22.30	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	M	Edge_1RB_Right	22.38	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	H	Outer_Full	21.84	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	H	Inner_Full	21.88	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	H	Edge_1RB_Left	21.90	PC2	PASS
N78-3700 -3800	30	60	DFT-QPSK	H	Edge_1RB_Right	21.95	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	H	Outer_Full	21.94	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	H	Inner_Full	21.85	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	H	Edge_1RB_Left	21.74	PC2	PASS
N78-3700 -3800	30	60	DFT-16QAM	H	Edge_1RB_Right	21.74	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	L	Outer_Full	21.97	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	L	Inner_Full	21.92	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	L	Edge_1RB_Left	21.81	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	L	Edge_1RB_Right	21.99	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	L	Outer_Full	22.01	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	L	Inner_Full	21.97	PC2	PASS
N78-3700	30	70	DFT-16QAM	L	Edge_1RB_Left	21.82	PC2	PASS

-3800								
N78-3700 -3800	30	70	DFT-16QAM	L	Edge_1RB_Right	21.96	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	M	Outer_Full	22.29	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	M	Inner_Full	22.33	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	M	Edge_1RB_Left	22.22	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	M	Edge_1RB_Right	22.24	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	M	Outer_Full	22.38	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	M	Inner_Full	22.28	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	M	Edge_1RB_Left	22.46	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	M	Edge_1RB_Right	22.45	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	H	Outer_Full	22.02	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	H	Inner_Full	21.99	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	H	Edge_1RB_Left	22.13	PC2	PASS
N78-3700 -3800	30	70	DFT-QPSK	H	Edge_1RB_Right	22.24	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	H	Outer_Full	22.06	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	H	Inner_Full	21.90	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	H	Edge_1RB_Left	21.98	PC2	PASS
N78-3700 -3800	30	70	DFT-16QAM	H	Edge_1RB_Right	22.03	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	L	Outer_Full	22.32	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	L	Inner_Full	22.39	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	L	Edge_1RB_Left	22.13	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	L	Edge_1RB_Right	22.29	PC2	PASS

N78-3700 -3800	30	80	DFT-16QAM	L	Outer_Full	22.37	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	L	Inner_Full	22.38	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	L	Edge_1RB_Left	21.97	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	L	Edge_1RB_Right	22.09	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	M	Outer_Full	22.29	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	M	Inner_Full	22.28	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	M	Edge_1RB_Left	22.14	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	M	Edge_1RB_Right	22.22	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	M	Outer_Full	22.39	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	M	Inner_Full	22.25	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	M	Edge_1RB_Left	22.43	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	M	Edge_1RB_Right	22.47	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	H	Outer_Full	22.26	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	H	Inner_Full	22.34	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	H	Edge_1RB_Left	22.25	PC2	PASS
N78-3700 -3800	30	80	DFT-QPSK	H	Edge_1RB_Right	22.37	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	H	Outer_Full	22.35	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	H	Inner_Full	22.27	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	H	Edge_1RB_Left	22.14	PC2	PASS
N78-3700 -3800	30	80	DFT-16QAM	H	Edge_1RB_Right	22.26	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	L	Outer_Full	22.28	PC2	PASS
N78-3700	30	90	DFT-QPSK	L	Inner_Full	22.37	PC2	PASS

-3800								
N78-3700 -3800	30	90	DFT-QPSK	L	Edge_1RB_Left	22.10	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	L	Edge_1RB_Right	22.28	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	L	Outer_Full	22.37	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	L	Inner_Full	22.38	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	L	Edge_1RB_Left	22.10	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	L	Edge_1RB_Right	22.23	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	M	Outer_Full	22.29	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	M	Inner_Full	22.33	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	M	Edge_1RB_Left	22.07	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	M	Edge_1RB_Right	22.24	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	M	Outer_Full	22.37	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	M	Inner_Full	22.29	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	M	Edge_1RB_Left	22.33	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	M	Edge_1RB_Right	22.50	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	H	Outer_Full	22.30	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	H	Inner_Full	22.34	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	H	Edge_1RB_Left	22.24	PC2	PASS
N78-3700 -3800	30	90	DFT-QPSK	H	Edge_1RB_Right	22.43	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	H	Outer_Full	22.41	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	H	Inner_Full	22.27	PC2	PASS
N78-3700 -3800	30	90	DFT-16QAM	H	Edge_1RB_Left	22.05	PC2	PASS

N78-3700 -3800	30	90	DFT-16QAM	H	Edge_1RB_Right	22.26	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	L	Outer_Full	22.28	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	L	Inner_Full	22.28	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	L	Edge_1RB_Left	21.91	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	L	Edge_1RB_Right	22.23	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	L	Outer_Full	22.36	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	L	Inner_Full	22.25	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	L	Edge_1RB_Left	21.74	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	L	Edge_1RB_Right	22.04	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	M	Outer_Full	22.29	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	M	Inner_Full	22.67	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	M	Edge_1RB_Left	21.91	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	M	Edge_1RB_Right	22.24	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	M	Outer_Full	22.36	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	M	Inner_Full	22.29	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	M	Edge_1RB_Left	21.75	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	M	Edge_1RB_Right	22.05	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	H	Outer_Full	22.30	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	H	Inner_Full	22.28	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	H	Edge_1RB_Left	21.90	PC2	PASS
N78-3700 -3800	30	100	DFT-QPSK	H	Edge_1RB_Right	22.22	PC2	PASS
N78-3700	30	100	DFT-16QAM	H	Outer_Full	22.37	PC2	PASS

-3800								
N78-3700 -3800	30	100	DFT-16QAM	H	Inner_Full	22.25	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	H	Edge_1RB_Left	21.74	PC2	PASS
N78-3700 -3800	30	100	DFT-16QAM	H	Edge_1RB_Right	22.05	PC2	PASS

Appendix B: Peak-to-Average Ratio for SA

Peak-to-Average Ratio(PAPR)

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	DutyCycle	Factor	Result	Limit	Verdict
N78-3700-3800	30	10	DFT-QPSK	L	Outer_Full	100%	0.00	7.35	≤13	PASS
N78-3700-3800	30	10	DFT-16QAM	L	Outer_Full	100%	0.00	7.72	≤13	PASS
N78-3700-3800	30	10	DFT-QPSK	M	Outer_Full	100%	0.00	7.25	≤13	PASS
N78-3700-3800	30	10	DFT-16QAM	M	Outer_Full	100%	0.00	7.88	≤13	PASS
N78-3700-3800	30	10	DFT-QPSK	H	Outer_Full	100%	0.00	7.27	≤13	PASS
N78-3700-3800	30	10	DFT-16QAM	H	Outer_Full	100%	0.00	8.10	≤13	PASS
N78-3700-3800	30	15	DFT-QPSK	L	Outer_Full	100%	0.00	7.66	≤13	PASS
N78-3700-3800	30	15	DFT-16QAM	L	Outer_Full	100%	0.00	8.53	≤13	PASS
N78-3700-3800	30	15	DFT-QPSK	M	Outer_Full	100%	0.00	7.58	≤13	PASS
N78-3700-3800	30	15	DFT-16QAM	M	Outer_Full	100%	0.00	8.50	≤13	PASS
N78-3700-3800	30	15	DFT-QPSK	H	Outer_Full	100%	0.00	7.58	≤13	PASS
N78-3700-3800	30	15	DFT-16QAM	H	Outer_Full	100%	0.00	8.46	≤13	PASS
N78-3700-3800	30	20	DFT-QPSK	L	Outer_Full	100%	0.00	8.59	≤13	PASS
N78-3700-3800	30	20	DFT-16QAM	L	Outer_Full	100%	0.00	7.88	≤13	PASS
N78-3700-3800	30	20	DFT-QPSK	M	Outer_Full	100%	0.00	8.30	≤13	PASS
N78-3700-3800	30	20	DFT-16QAM	M	Outer_Full	100%	0.00	8.28	≤13	PASS
N78-3700-3800	30	20	DFT-QPSK	H	Outer_Full	100%	0.00	8.28	≤13	PASS
N78-3700-3800	30	20	DFT-16QAM	H	Outer_Full	100%	0.00	8.01	≤13	PASS
N78-3700-3800	30	40	DFT-QPSK	L	Outer_Full	100%	0.00	8.88	≤13	PASS
N78-3700-3800	30	40	DFT-16QAM	L	Outer_Full	100%	0.00	8.27	≤13	PASS
N78-3700-3800	30	40	DFT-QPSK	M	Outer_Full	100%	0.00	8.24	≤13	PASS
N78-3700-3800	30	40	DFT-16QAM	M	Outer_Full	100%	0.00	8.60	≤13	PASS
N78-3700-3800	30	40	DFT-QPSK	H	Outer_Full	100%	0.00	7.77	≤13	PASS
N78-3700-3800	30	40	DFT-16QAM	H	Outer_Full	100%	0.00	8.69	≤13	PASS
N78-3700-3800	30	50	DFT-QPSK	L	Outer_Full	100%	0.00	8.02	≤13	PASS
N78-3700-3800	30	50	DFT-16QAM	L	Outer_Full	100%	0.00	8.29	≤13	PASS
N78-3700-3800	30	50	DFT-QPSK	M	Outer_Full	100%	0.00	8.15	≤13	PASS
N78-3700-3800	30	50	DFT-16QAM	M	Outer_Full	100%	0.00	8.77	≤13	PASS
N78-3700-3800	30	50	DFT-QPSK	H	Outer_Full	100%	0.00	8.67	≤13	PASS
N78-3700-3800	30	50	DFT-16QAM	H	Outer_Full	100%	0.00	8.45	≤13	PASS
N78-3700-3800	30	60	DFT-QPSK	L	Outer_Full	100%	0.00	8.47	≤13	PASS
N78-3700-3800	30	60	DFT-16QAM	L	Outer_Full	100%	0.00	8.82	≤13	PASS
N78-3700-3800	30	60	DFT-QPSK	M	Outer_Full	100%	0.00	8.37	≤13	PASS
N78-3700-3800	30	60	DFT-16QAM	M	Outer_Full	100%	0.00	8.76	≤13	PASS
N78-3700-3800	30	60	DFT-QPSK	H	Outer_Full	100%	0.00	8.49	≤13	PASS

N78-3700-3800	30	60	DFT-16QAM	H	Outer_Full	100%	0.00	8.51	≤13	PASS
N78-3700-3800	30	70	DFT-QPSK	L	Outer_Full	100%	0.00	8.61	≤13	PASS
N78-3700-3800	30	70	DFT-16QAM	L	Outer_Full	100%	0.00	8.84	≤13	PASS
N78-3700-3800	30	70	DFT-QPSK	M	Outer_Full	100%	0.00	8.46	≤13	PASS
N78-3700-3800	30	70	DFT-16QAM	M	Outer_Full	100%	0.00	8.95	≤13	PASS
N78-3700-3800	30	70	DFT-QPSK	H	Outer_Full	100%	0.00	8.60	≤13	PASS
N78-3700-3800	30	70	DFT-16QAM	H	Outer_Full	100%	0.00	8.87	≤13	PASS
N78-3700-3800	30	80	DFT-QPSK	L	Outer_Full	100%	0.00	8.32	≤13	PASS
N78-3700-3800	30	80	DFT-16QAM	L	Outer_Full	100%	0.00	8.81	≤13	PASS
N78-3700-3800	30	80	DFT-QPSK	M	Outer_Full	100%	0.00	8.81	≤13	PASS
N78-3700-3800	30	80	DFT-16QAM	M	Outer_Full	100%	0.00	8.58	≤13	PASS
N78-3700-3800	30	80	DFT-QPSK	H	Outer_Full	100%	0.00	8.68	≤13	PASS
N78-3700-3800	30	80	DFT-16QAM	H	Outer_Full	100%	0.00	8.74	≤13	PASS
N78-3700-3800	30	90	DFT-QPSK	L	Outer_Full	100%	0.00	8.27	≤13	PASS
N78-3700-3800	30	90	DFT-16QAM	L	Outer_Full	100%	0.00	8.89	≤13	PASS
N78-3700-3800	30	90	DFT-QPSK	M	Outer_Full	100%	0.00	8.31	≤13	PASS
N78-3700-3800	30	90	DFT-16QAM	M	Outer_Full	100%	0.00	8.72	≤13	PASS
N78-3700-3800	30	90	DFT-QPSK	H	Outer_Full	100%	0.00	8.69	≤13	PASS
N78-3700-3800	30	90	DFT-16QAM	H	Outer_Full	100%	0.00	8.55	≤13	PASS
N78-3700-3800	30	100	DFT-QPSK	L	Outer_Full	100%	0.00	8.67	≤13	PASS
N78-3700-3800	30	100	DFT-16QAM	L	Outer_Full	100%	0.00	8.53	≤13	PASS
N78-3700-3800	30	100	DFT-QPSK	M	Outer_Full	100%	0.00	8.29	≤13	PASS
N78-3700-3800	30	100	DFT-16QAM	M	Outer_Full	100%	0.00	8.36	≤13	PASS
N78-3700-3800	30	100	DFT-QPSK	H	Outer_Full	100%	0.00	8.19	≤13	PASS
N78-3700-3800	30	100	DFT-16QAM	H	Outer_Full	100%	0.00	8.12	≤13	PASS

Test Graphs









