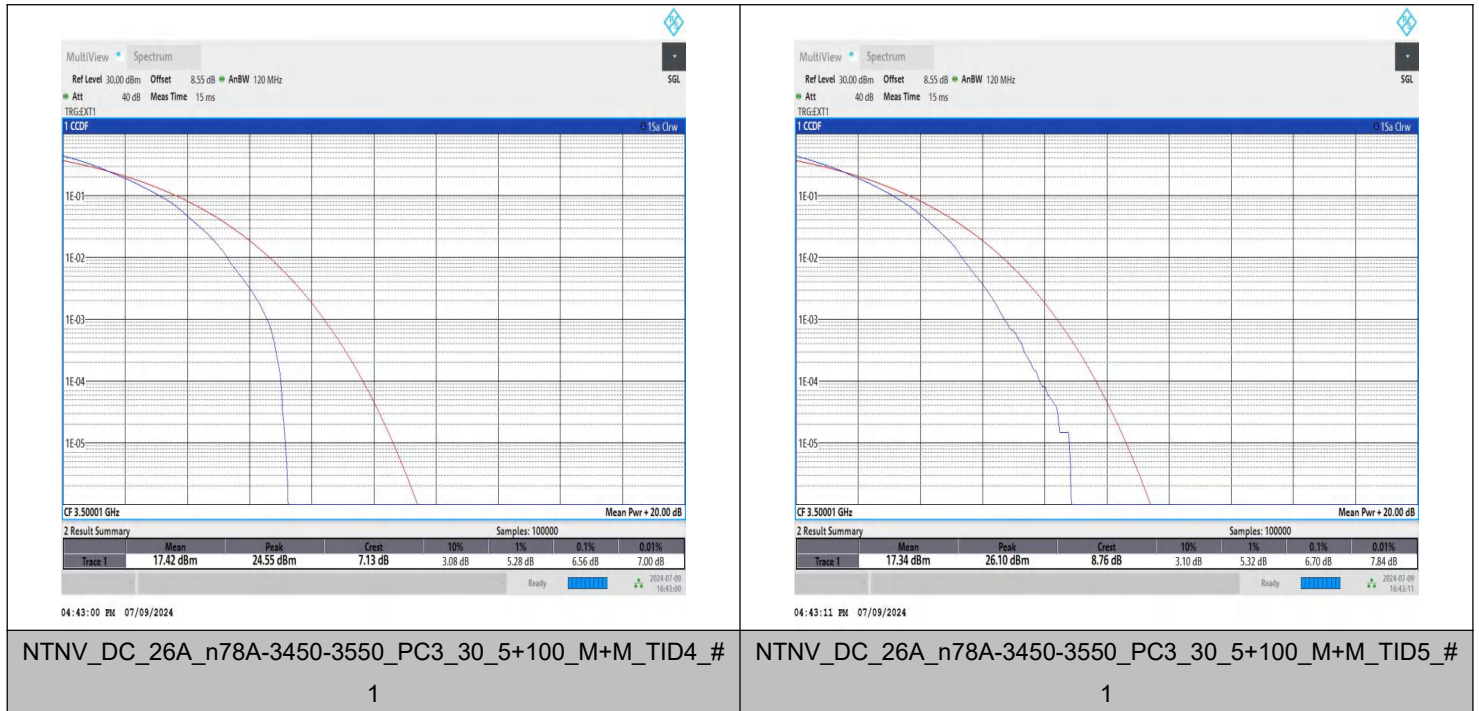




Peak-to-Average Ratio for NSA

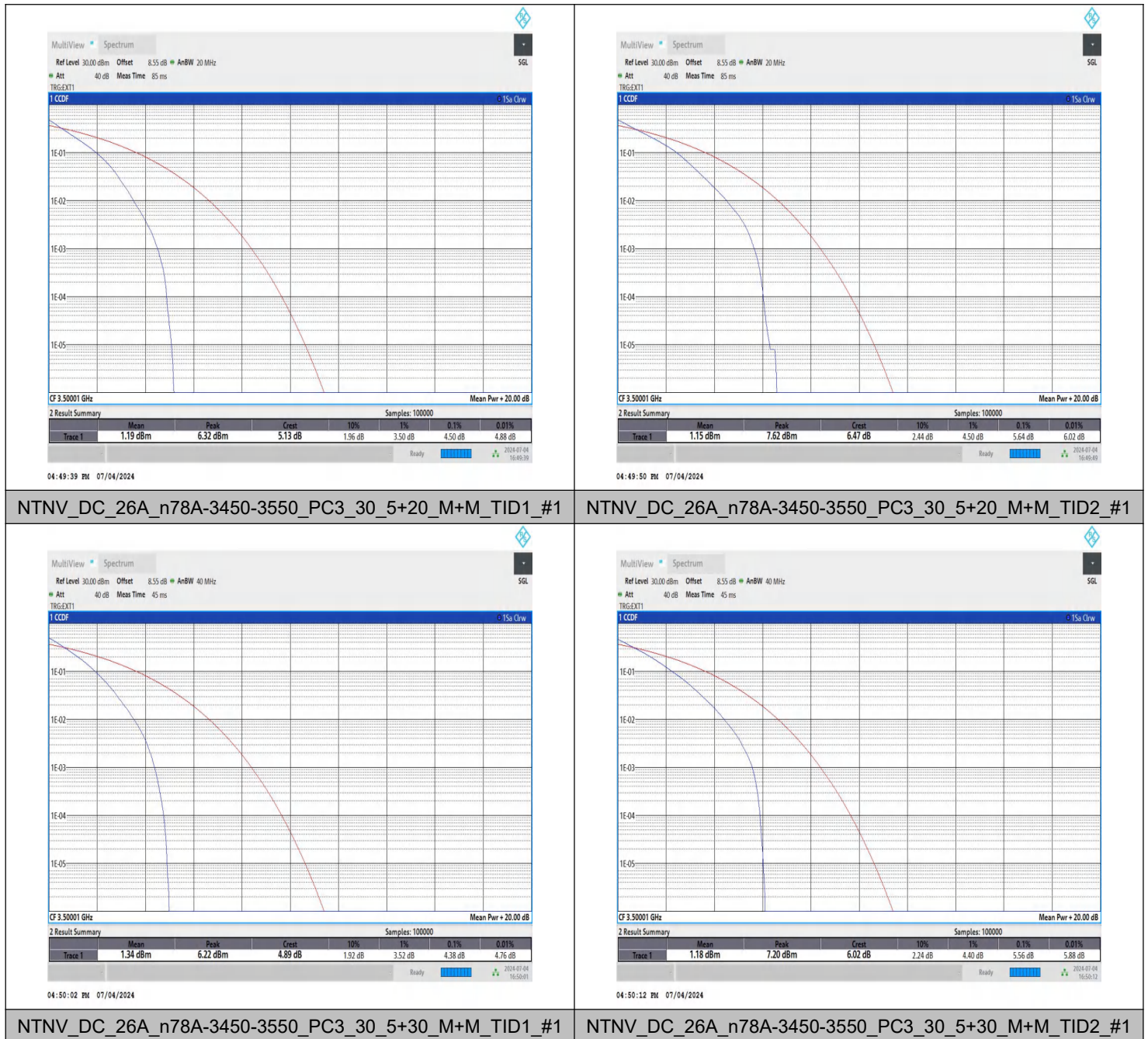
Peak-to-Average Ratio(CCDF)

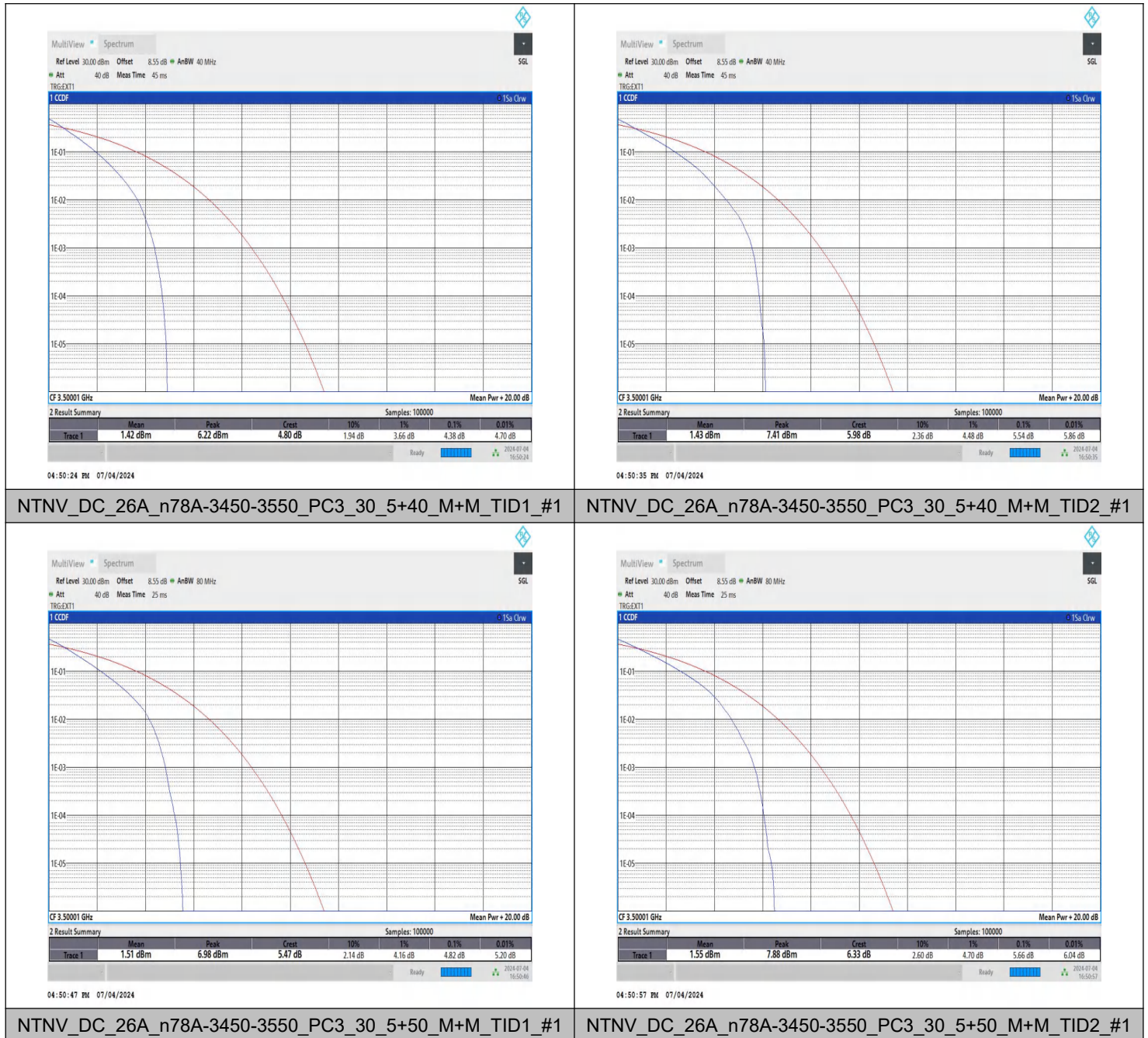
Test Result

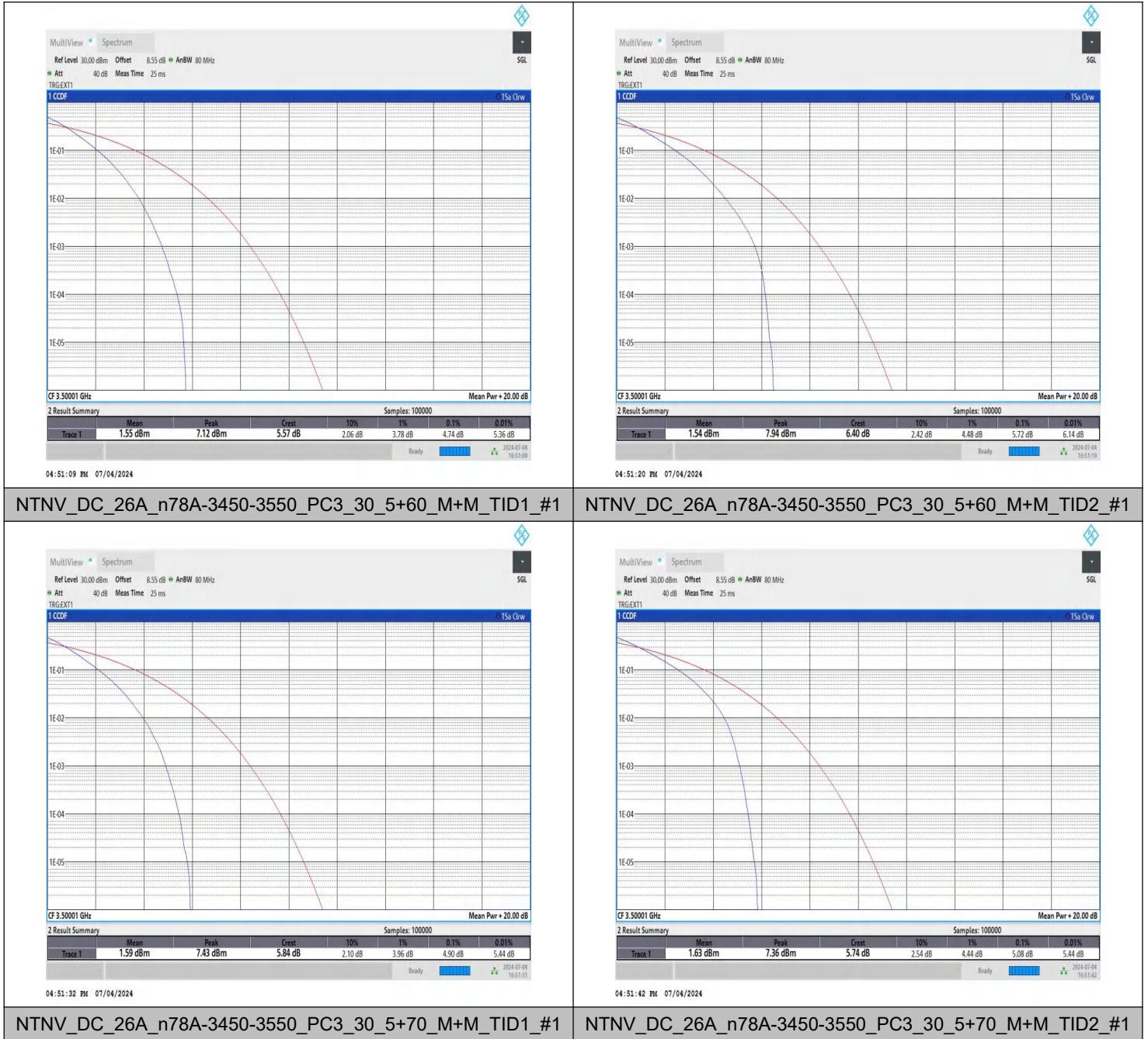
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_26A_n78A-3450-3550	30	5+20	DFT-PI2BPSK	M+M	Outer_Full	4.50	≤13	PASS
DC_26A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	5.64	≤13	PASS
DC_26A_n78A-3450-3550	30	5+30	DFT-PI2BPSK	M+M	Outer_Full	4.38	≤13	PASS
DC_26A_n78A-3450-3550	30	5+30	DFT-QPSK	M+M	Outer_Full	5.56	≤13	PASS
DC_26A_n78A-3450-3550	30	5+40	DFT-PI2BPSK	M+M	Outer_Full	4.38	≤13	PASS
DC_26A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	5.54	≤13	PASS
DC_26A_n78A-3450-3550	30	5+50	DFT-PI2BPSK	M+M	Outer_Full	4.82	≤13	PASS
DC_26A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	5.66	≤13	PASS
DC_26A_n78A-3450-3550	30	5+60	DFT-PI2BPSK	M+M	Outer_Full	4.74	≤13	PASS
DC_26A_n78A-3450-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	5.72	≤13	PASS
DC_26A_n78A-3450-3550	30	5+70	DFT-PI2BPSK	M+M	Outer_Full	4.90	≤13	PASS
DC_26A_n78A-3450-3550	30	5+70	DFT-QPSK	M+M	Outer_Full	5.08	≤13	PASS
DC_26A_n78A-3450-3550	30	5+80	DFT-PI2BPSK	M+M	Outer_Full	5.04	≤13	PASS
DC_26A_n78A-3450-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	5.16	≤13	PASS
DC_26A_n78A-3450-3550	30	5+90	DFT-PI2BPSK	M+M	Outer_Full	5.06	≤13	PASS
DC_26A_n78A-3450-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	5.32	≤13	PASS
DC_26A_n78A-3450-3550	30	5+100	DFT-PI2BPSK	M+M	Outer_Full	5.16	≤13	PASS
DC_26A_n78A-3450-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	5.34	≤13	PASS
DC_26A_n78A-3450-3550	30	5+20	DFT-16QAM	M+M	Outer_Full	6.52	≤13	PASS

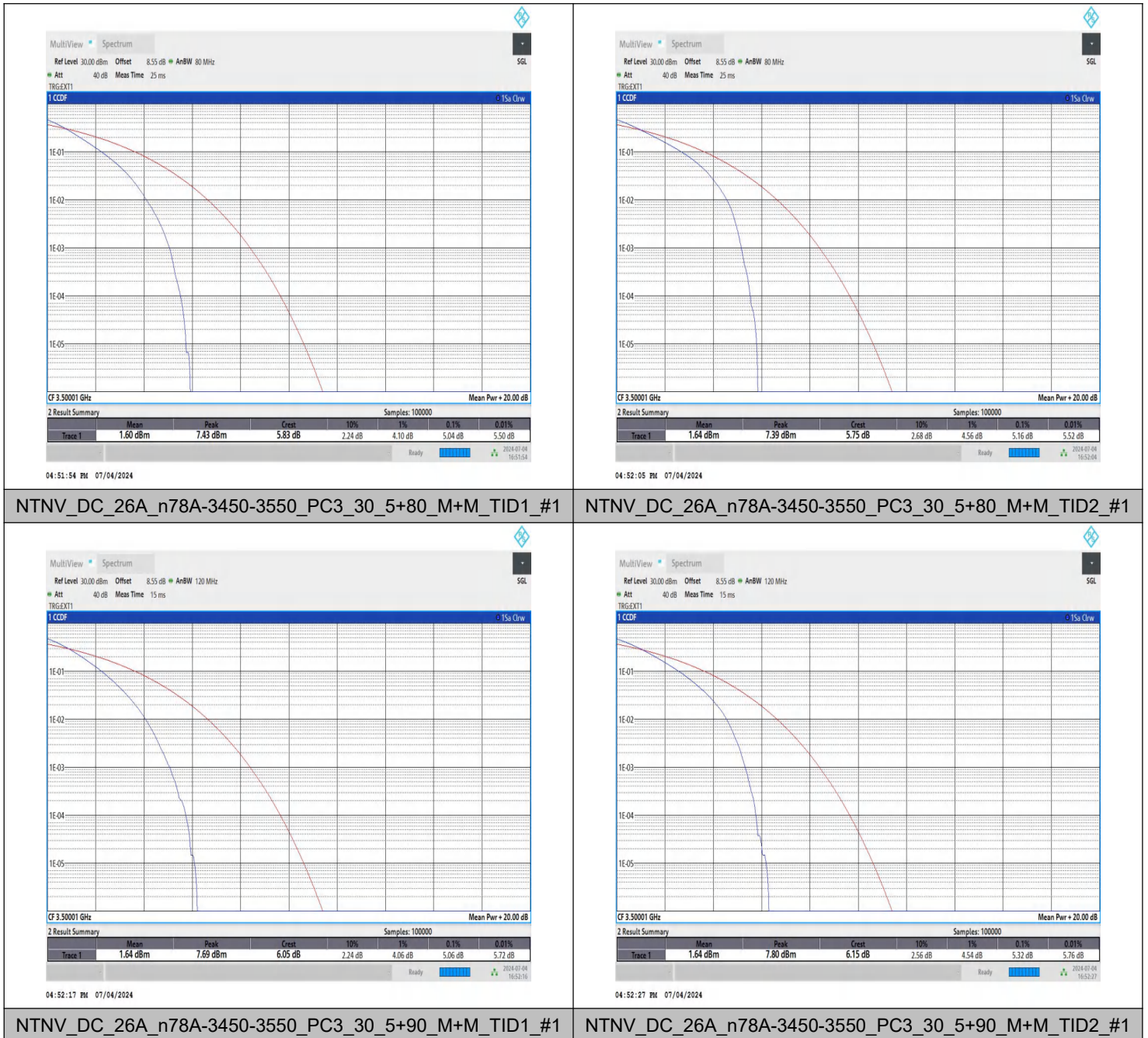
DC_26A_n78A-3450-3550	30	5+20	DFT-64QAM	M+M	Outer_Full	6.66	≤13	PASS
DC_26A_n78A-3450-3550	30	5+20	DFT-256QAM	M+M	Outer_Full	6.80	≤13	PASS
DC_26A_n78A-3450-3550	30	5+30	DFT-16QAM	M+M	Outer_Full	6.34	≤13	PASS
DC_26A_n78A-3450-3550	30	5+30	DFT-64QAM	M+M	Outer_Full	6.58	≤13	PASS
DC_26A_n78A-3450-3550	30	5+30	DFT-256QAM	M+M	Outer_Full	6.76	≤13	PASS
DC_26A_n78A-3450-3550	30	5+40	DFT-16QAM	M+M	Outer_Full	6.36	≤13	PASS
DC_26A_n78A-3450-3550	30	5+40	DFT-64QAM	M+M	Outer_Full	6.50	≤13	PASS
DC_26A_n78A-3450-3550	30	5+40	DFT-256QAM	M+M	Outer_Full	6.82	≤13	PASS
DC_26A_n78A-3450-3550	30	5+50	DFT-16QAM	M+M	Outer_Full	6.50	≤13	PASS
DC_26A_n78A-3450-3550	30	5+50	DFT-64QAM	M+M	Outer_Full	6.62	≤13	PASS
DC_26A_n78A-3450-3550	30	5+50	DFT-256QAM	M+M	Outer_Full	6.74	≤13	PASS
DC_26A_n78A-3450-3550	30	5+60	DFT-16QAM	M+M	Outer_Full	6.48	≤13	PASS
DC_26A_n78A-3450-3550	30	5+60	DFT-64QAM	M+M	Outer_Full	6.68	≤13	PASS
DC_26A_n78A-3450-3550	30	5+60	DFT-256QAM	M+M	Outer_Full	6.76	≤13	PASS
DC_26A_n78A-3450-3550	30	5+80	DFT-16QAM	M+M	Outer_Full	6.40	≤13	PASS
DC_26A_n78A-3450-3550	30	5+80	DFT-64QAM	M+M	Outer_Full	6.64	≤13	PASS
DC_26A_n78A-3450-3550	30	5+80	DFT-256QAM	M+M	Outer_Full	6.74	≤13	PASS
DC_26A_n78A-3450-3550	30	5+90	DFT-16QAM	M+M	Outer_Full	6.46	≤13	PASS
DC_26A_n78A-3450-3550	30	5+90	DFT-64QAM	M+M	Outer_Full	6.72	≤13	PASS
DC_26A_n78A-3450-3550	30	5+90	DFT-256QAM	M+M	Outer_Full	6.68	≤13	PASS
DC_26A_n78A-3450-3550	30	5+100	DFT-16QAM	M+M	Outer_Full	6.46	≤13	PASS
DC_26A_n78A-3450-3550	30	5+100	DFT-64QAM	M+M	Outer_Full	6.56	≤13	PASS
DC_26A_n78A-3450-3550	30	5+100	DFT-256QAM	M+M	Outer_Full	6.70	≤13	PASS

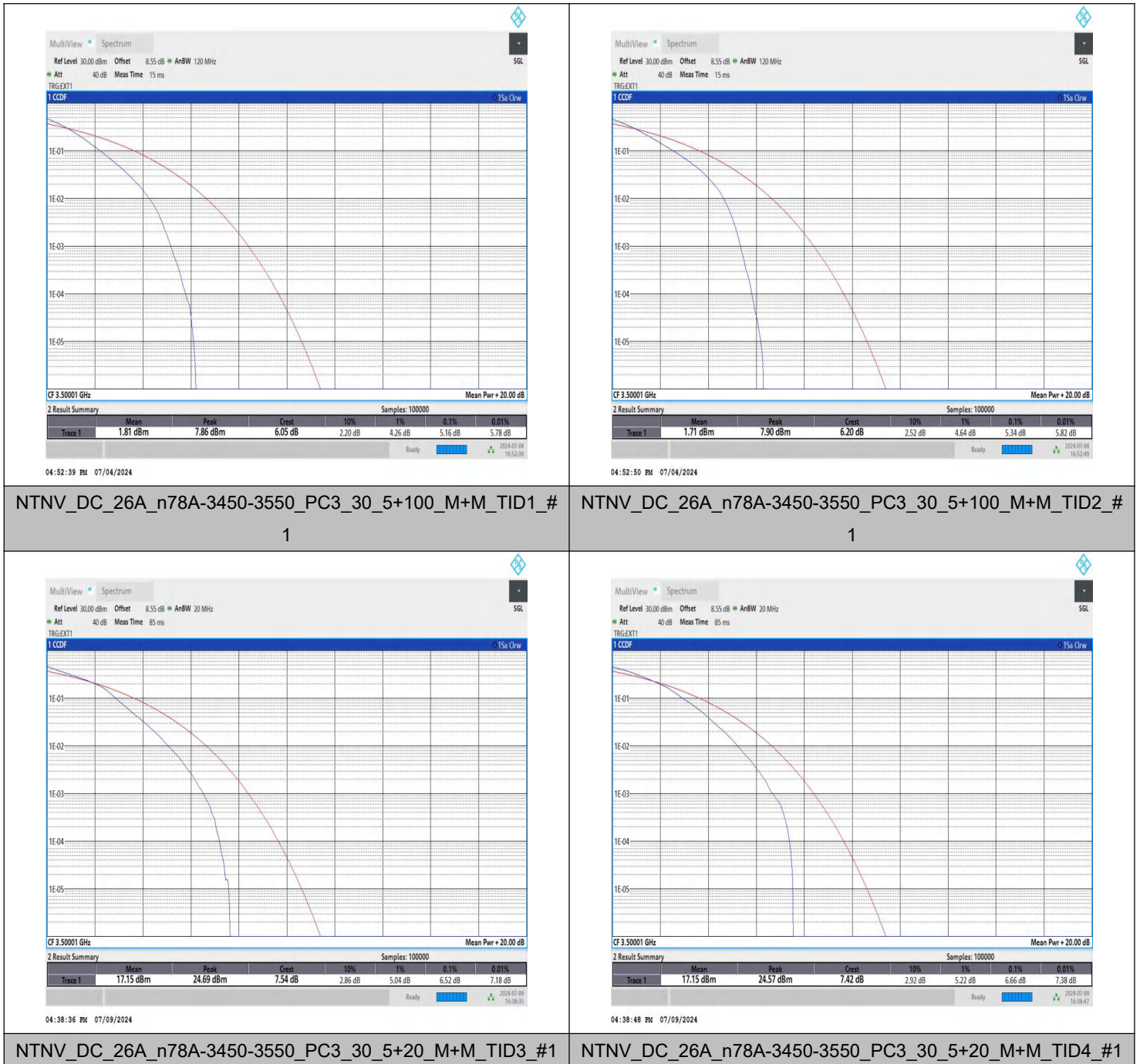
Test Graphs

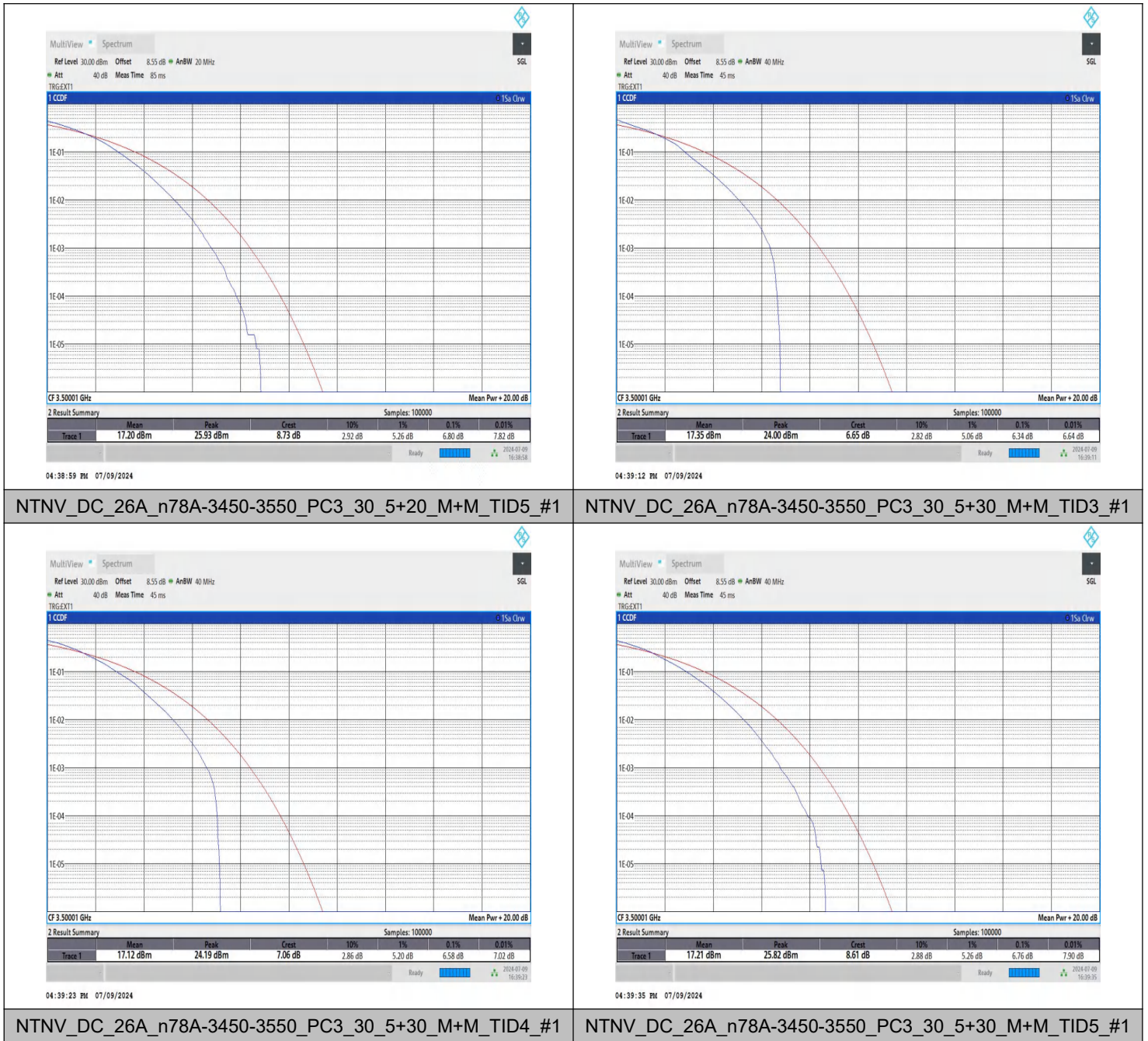


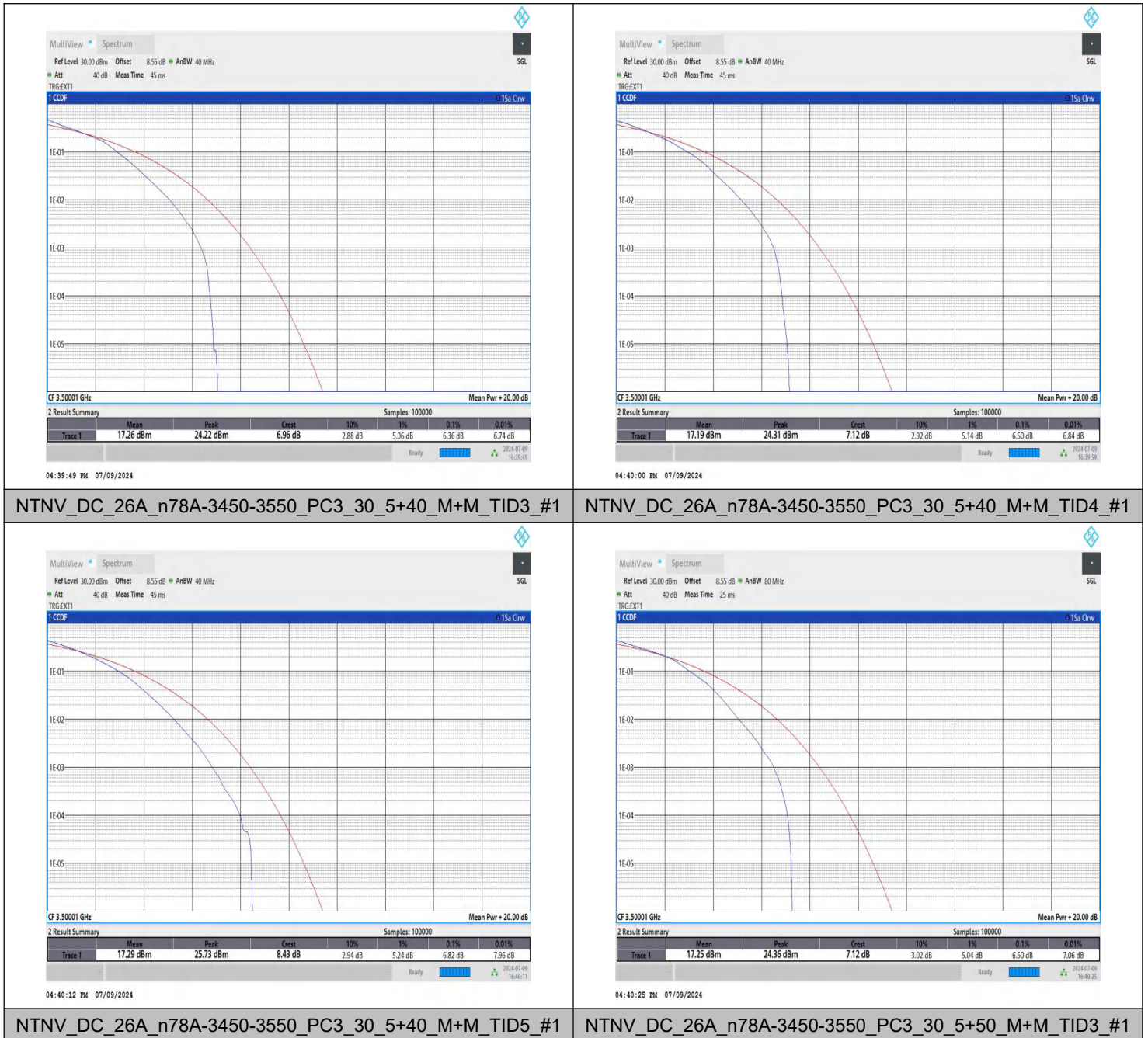


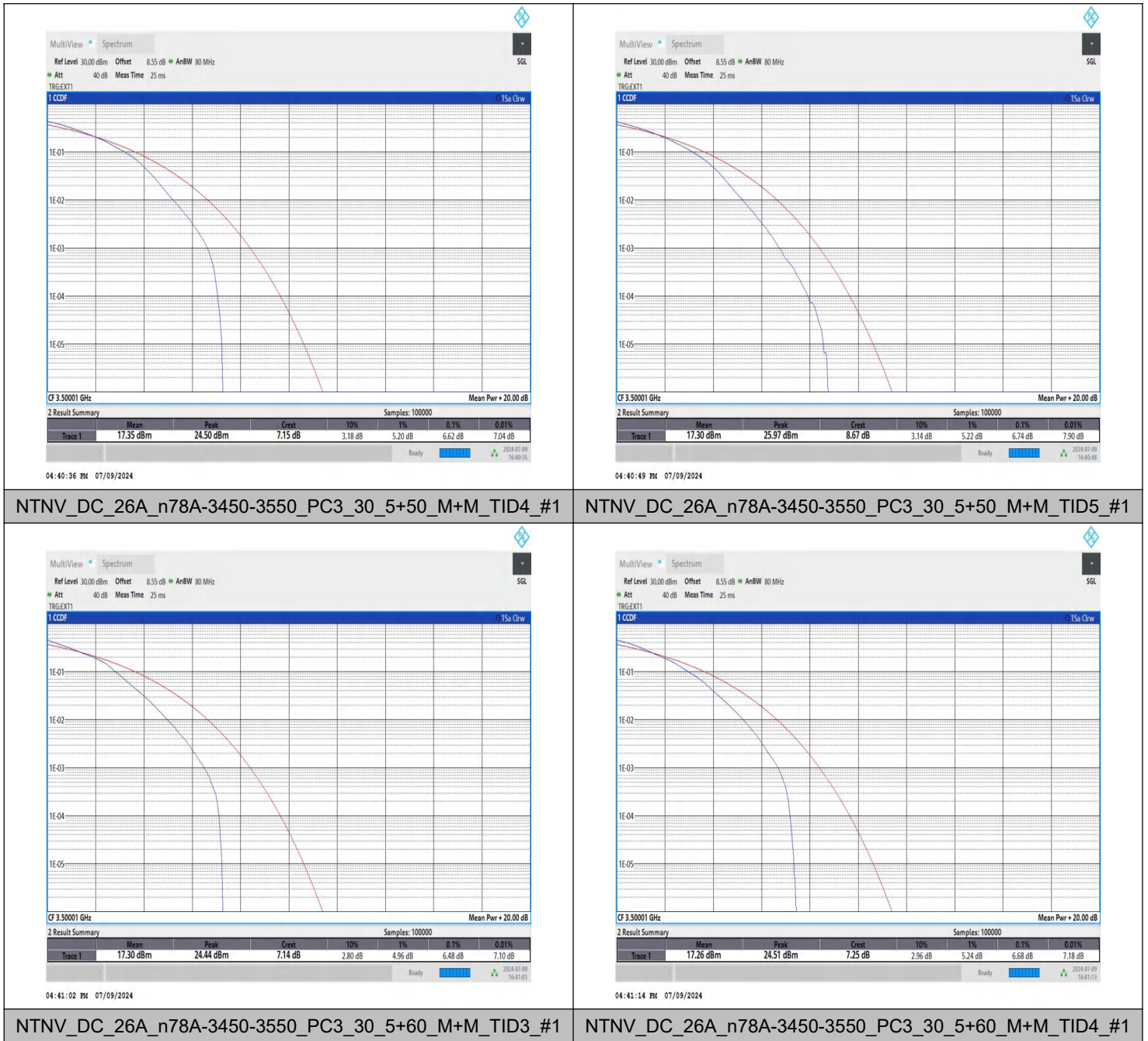


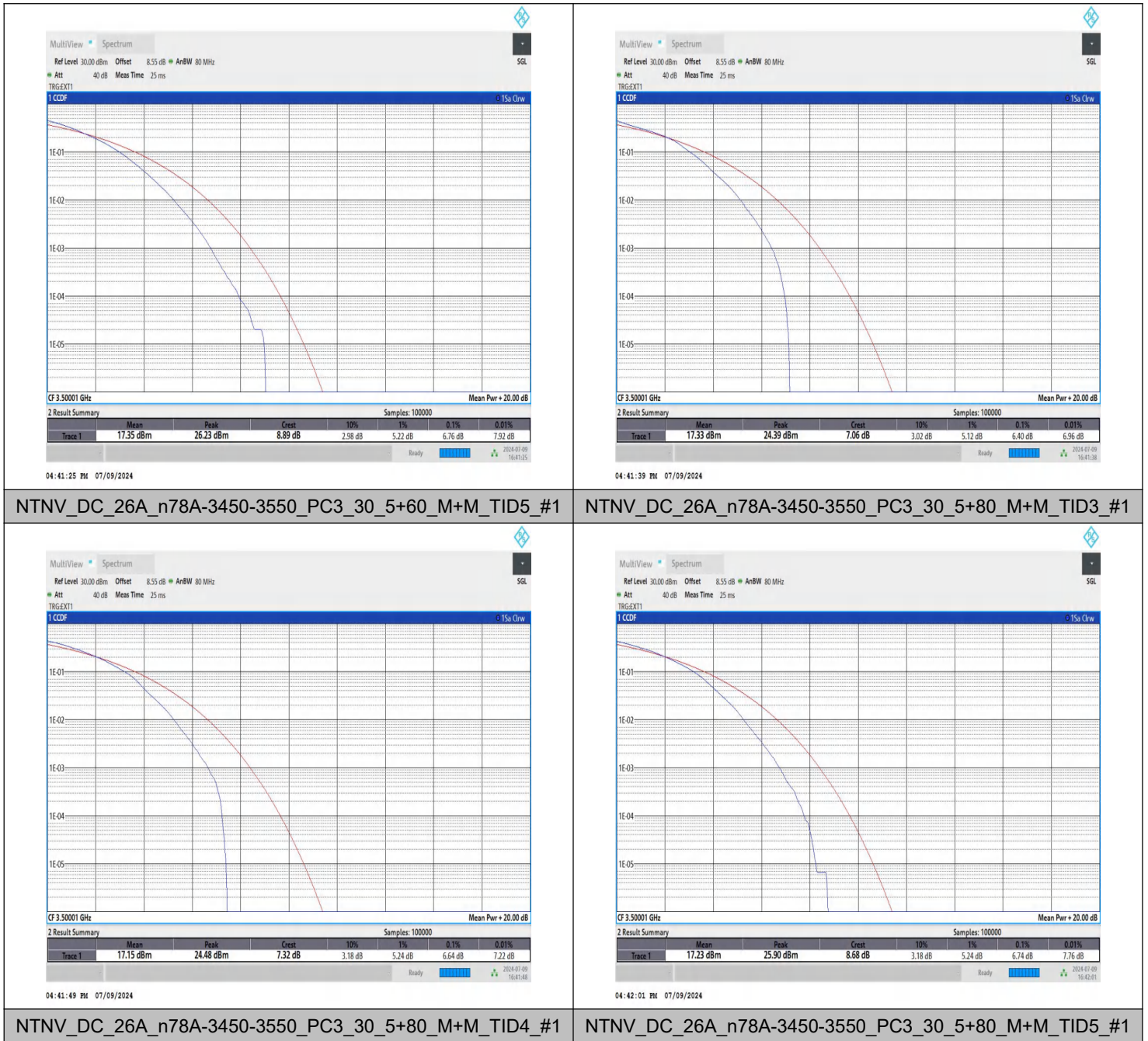


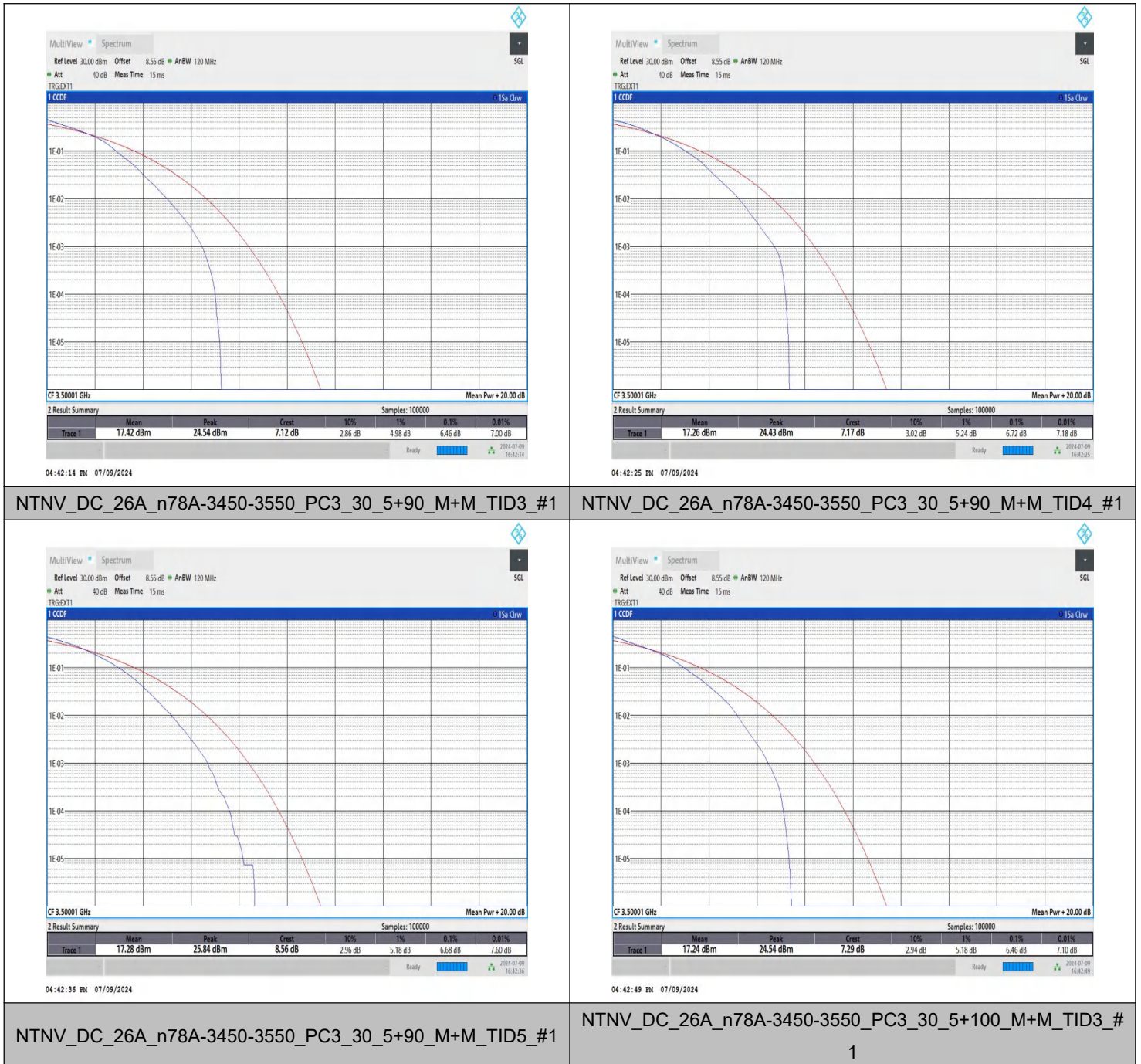


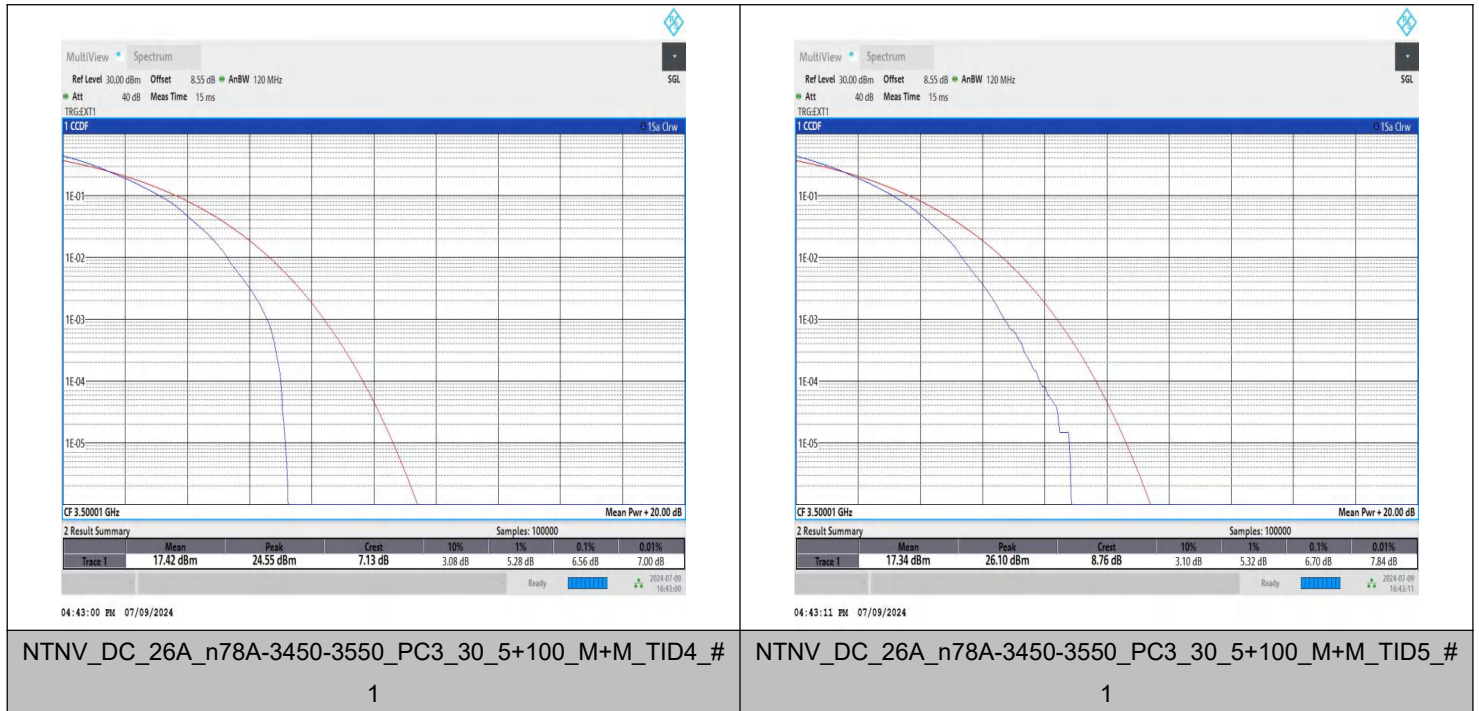










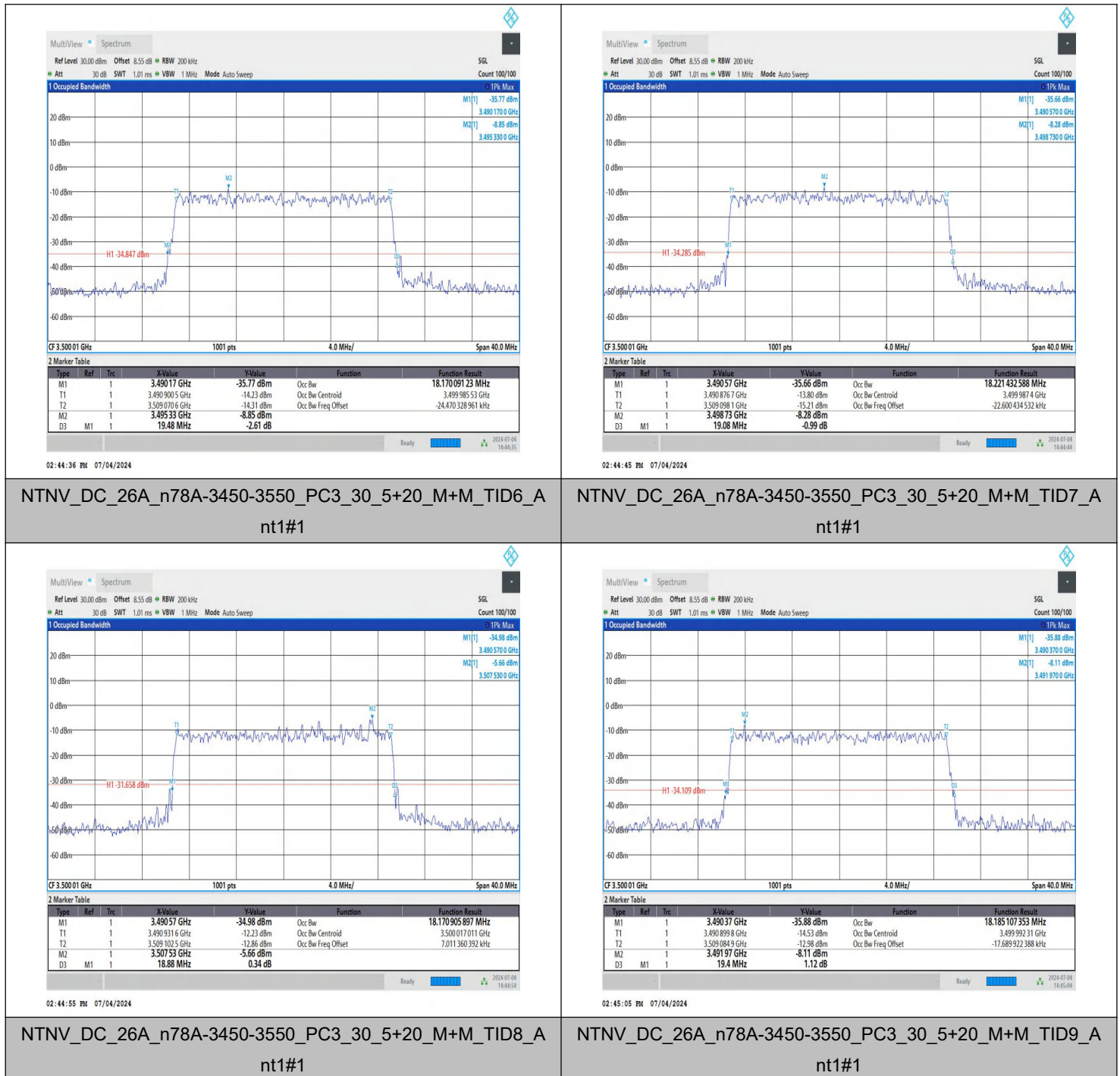


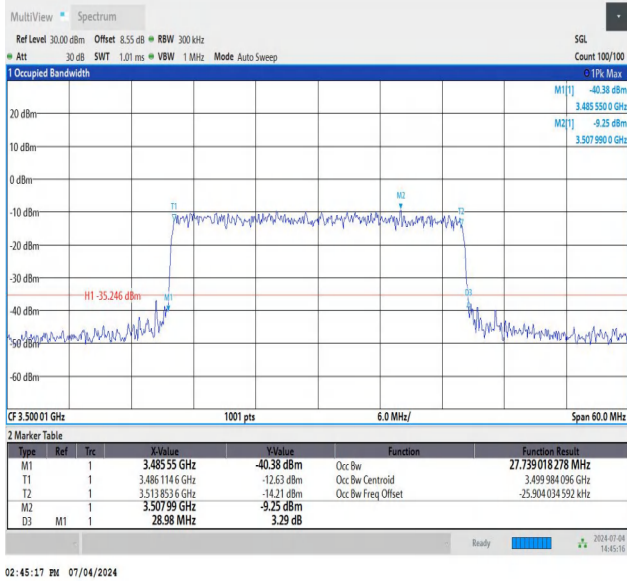
26dB Bandwidth and Occupied Bandwidth for NSA

Test Result

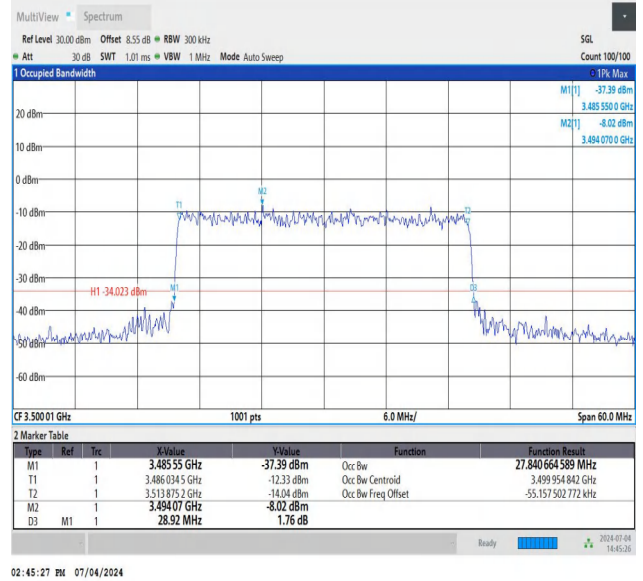
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_26A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Outer_Full	18.17	19.480	PASS
DC_26A_n78A-3450-3550	30	5+20	CP-16QAM	M+M	Outer_Full	18.221	19.080	PASS
DC_26A_n78A-3450-3550	30	5+20	CP-64QAM	M+M	Outer_Full	18.171	18.880	PASS
DC_26A_n78A-3450-3550	30	5+20	CP-256QAM	M+M	Outer_Full	18.185	19.400	PASS
DC_26A_n78A-3450-3550	30	5+30	CP-QPSK	M+M	Outer_Full	27.739	28.980	PASS
DC_26A_n78A-3450-3550	30	5+30	CP-16QAM	M+M	Outer_Full	27.841	28.920	PASS
DC_26A_n78A-3450-3550	30	5+30	CP-64QAM	M+M	Outer_Full	27.916	29.640	PASS
DC_26A_n78A-3450-3550	30	5+30	CP-256QAM	M+M	Outer_Full	27.845	28.860	PASS
DC_26A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Outer_Full	37.918	39.760	PASS
DC_26A_n78A-3450-3550	30	5+40	CP-16QAM	M+M	Outer_Full	37.847	39.440	PASS
DC_26A_n78A-3450-3550	30	5+40	CP-64QAM	M+M	Outer_Full	37.774	39.840	PASS
DC_26A_n78A-3450-3550	30	5+40	CP-256QAM	M+M	Outer_Full	37.807	39.680	PASS
DC_26A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Outer_Full	47.56	49.300	PASS
DC_26A_n78A-3450-3550	30	5+50	CP-16QAM	M+M	Outer_Full	47.47	49.100	PASS
DC_26A_n78A-3450-3550	30	5+50	CP-64QAM	M+M	Outer_Full	47.561	49.500	PASS
DC_26A_n78A-3450-3550	30	5+50	CP-256QAM	M+M	Outer_Full	47.481	49.400	PASS
DC_26A_n78A-3450-3550	30	5+60	CP-QPSK	M+M	Outer_Full	57.871	60.720	PASS
DC_26A_n78A-3450-3550	30	5+60	CP-16QAM	M+M	Outer_Full	57.901	60.720	PASS
DC_26A_n78A-3450-3550	30	5+60	CP-64QAM	M+M	Outer_Full	57.779	60.720	PASS
DC_26A_n78A-3450-3550	30	5+60	CP-256QAM	M+M	Outer_Full	57.901	60.720	PASS
DC_26A_n78A-3450-3550	30	5+70	CP-QPSK	M+M	Outer_Full	67.479	70.420	PASS
DC_26A_n78A-3450-3550	30	5+70	CP-16QAM	M+M	Outer_Full	67.433	70.420	PASS
DC_26A_n78A-3450-3550	30	5+70	CP-64QAM	M+M	Outer_Full	67.6	70.560	PASS
DC_26A_n78A-3450-3550	30	5+70	CP-256QAM	M+M	Outer_Full	67.495	70.420	PASS
DC_26A_n78A-3450-3550	30	5+80	CP-QPSK	M+M	Outer_Full	77.641	80.640	PASS
DC_26A_n78A-3450-3550	30	5+80	CP-16QAM	M+M	Outer_Full	77.447	80.640	PASS
DC_26A_n78A-3450-3550	30	5+80	CP-64QAM	M+M	Outer_Full	77.369	80.640	PASS
DC_26A_n78A-3450-3550	30	5+80	CP-256QAM	M+M	Outer_Full	77.391	80.640	PASS
DC_26A_n78A-3450-3550	30	5+90	CP-QPSK	M+M	Outer_Full	87.632	90.720	PASS
DC_26A_n78A-3450-3550	30	5+90	CP-16QAM	M+M	Outer_Full	87.621	90.900	PASS
DC_26A_n78A-3450-3550	30	5+90	CP-64QAM	M+M	Outer_Full	87.442	90.900	PASS
DC_26A_n78A-3450-3550	30	5+90	CP-256QAM	M+M	Outer_Full	87.51	90.720	PASS
DC_26A_n78A-3450-3550	30	5+100	CP-QPSK	M+M	Outer_Full	97.547	101.200	PASS
DC_26A_n78A-3450-3550	30	5+100	CP-16QAM	M+M	Outer_Full	97.493	101.000	PASS
DC_26A_n78A-3450-3550	30	5+100	CP-64QAM	M+M	Outer_Full	97.648	100.800	PASS
DC_26A_n78A-3450-3550	30	5+100	CP-256QAM	M+M	Outer_Full	97.765	100.800	PASS

Test Graphs

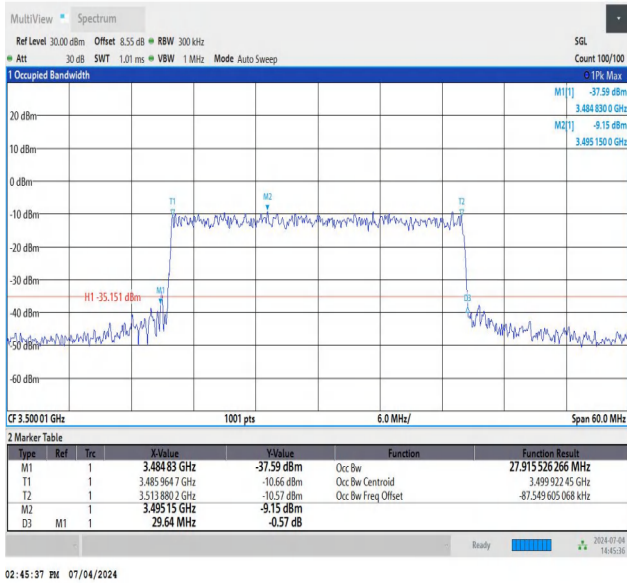




NTNV_DC_26A_n78A-3450-3550_PC3_30_5+30_M+M_TID6_A
nt1#1



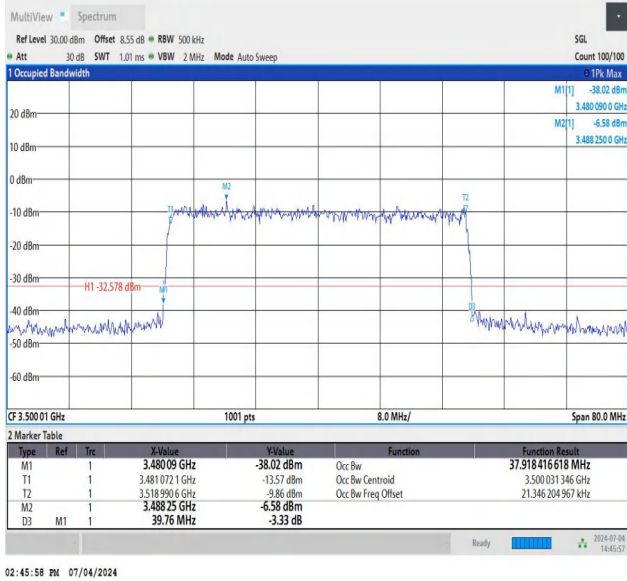
NTNV_DC_26A_n78A-3450-3550_PC3_30_5+30_M+M_TID7_A
nt1#1



NTNV_DC_26A_n78A-3450-3550_PC3_30_5+30_M+M_TID8_A
nt1#1



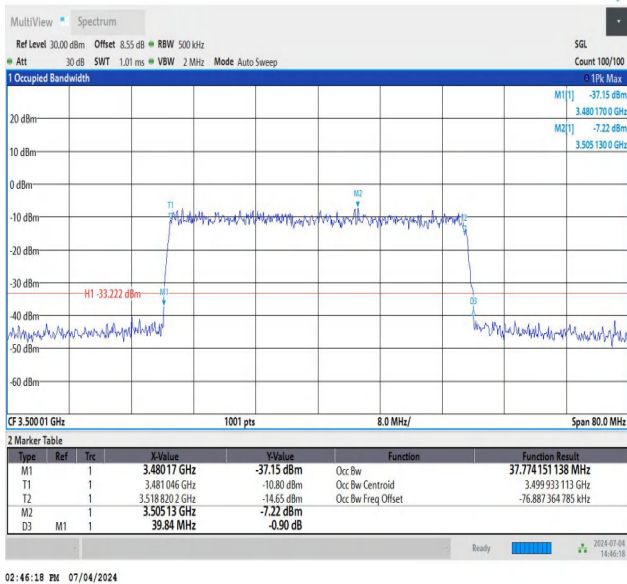
NTNV_DC_26A_n78A-3450-3550_PC3_30_5+30_M+M_TID9_A
nt1#1



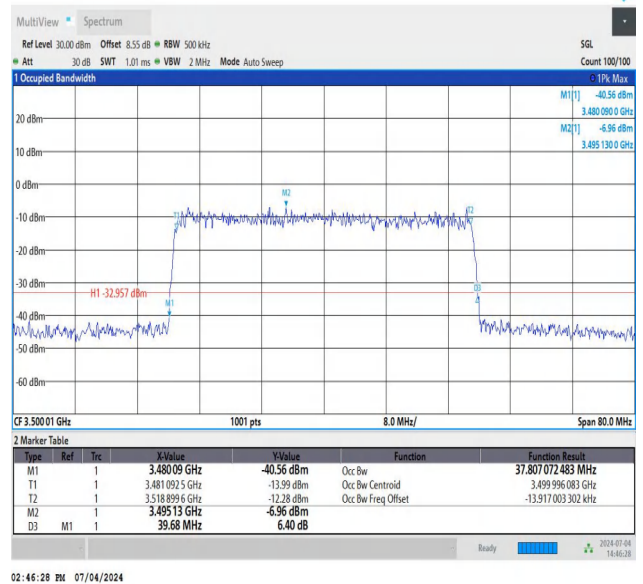
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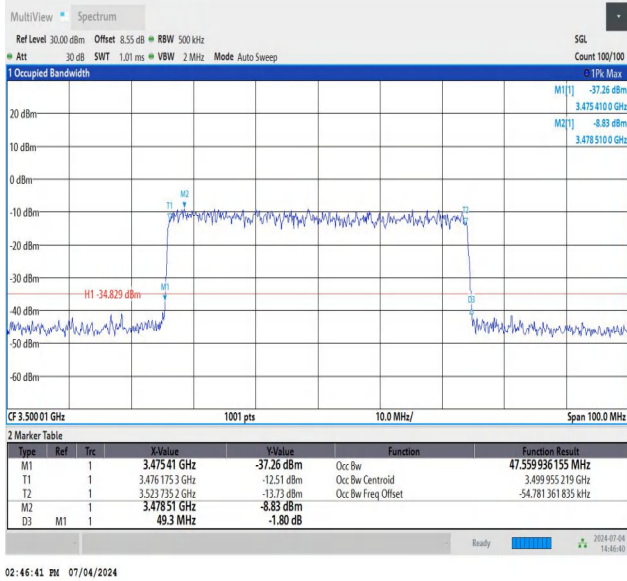
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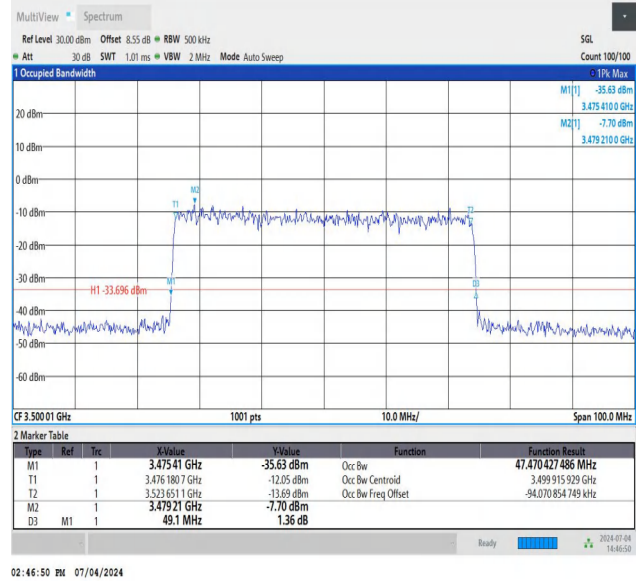
NTNV_DC_26A_n78A-3450-3550_PC3_30_5+40_M+M_TID8_A
nt1#1



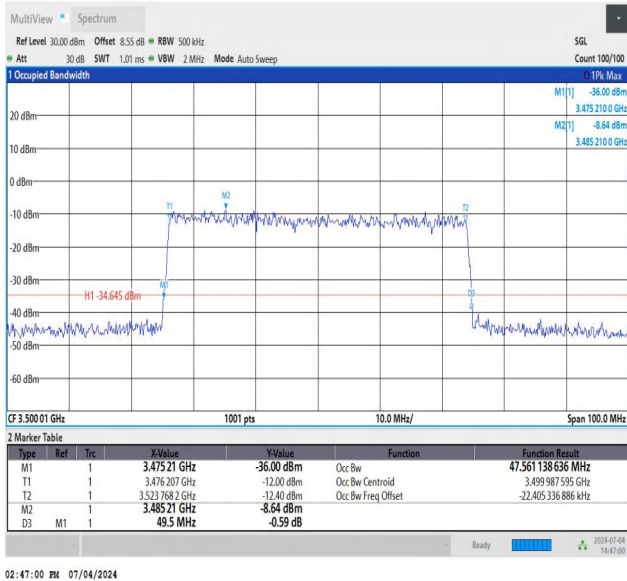
NTNV_DC_26A_n78A-3450-3550_PC3_30_5+40_M+M_TID9_A
nt1#1



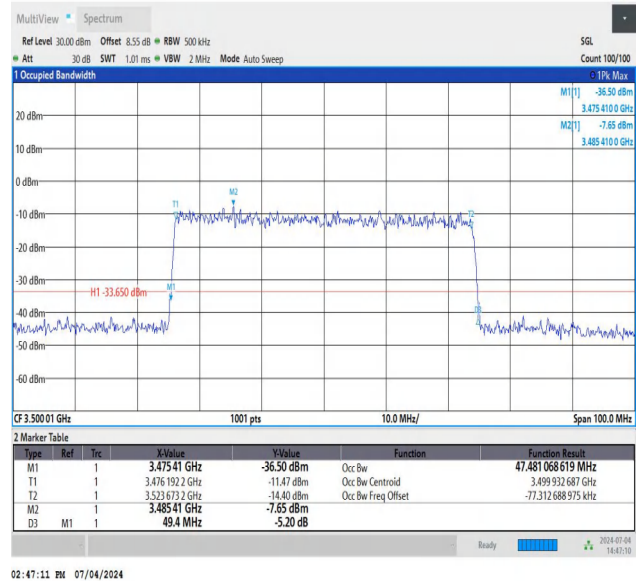
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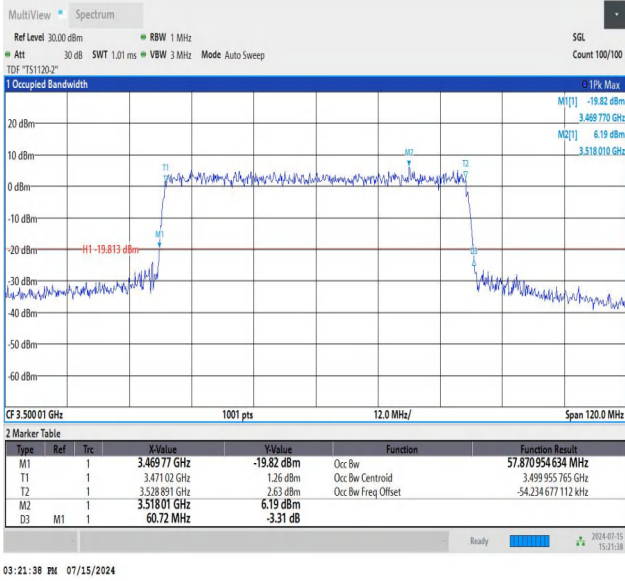
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nt1#1



NTNV_DC_26A_n78A-3450-3550_PC3_30_5+50_M+M_TID8_A
nt1#1

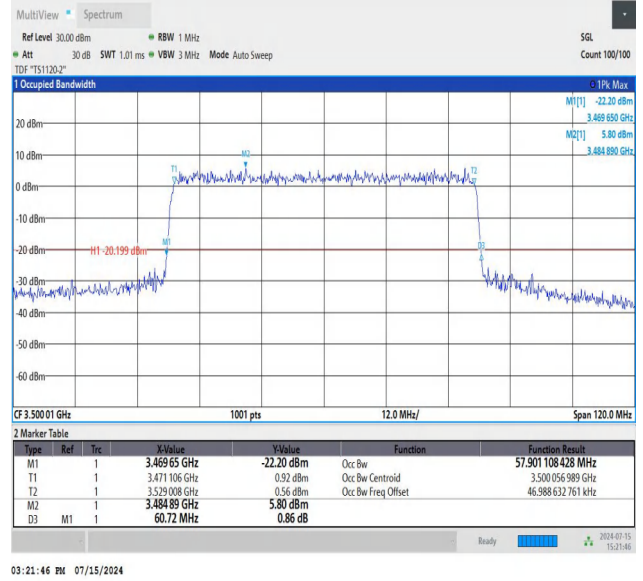


NTNV_DC_26A_n78A-3450-3550_PC3_30_5+50_M+M_TID9_A
nt1#1



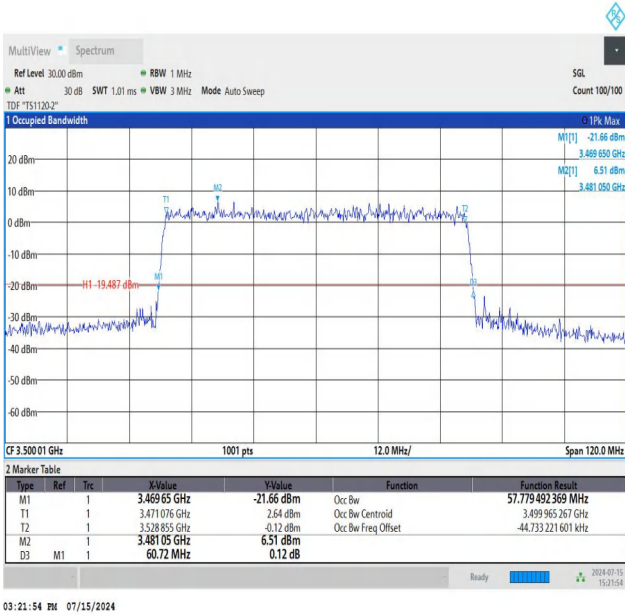
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nt1#1



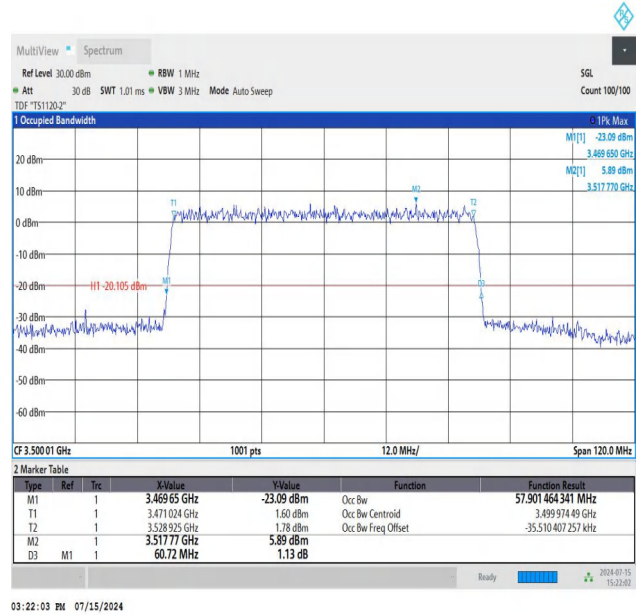
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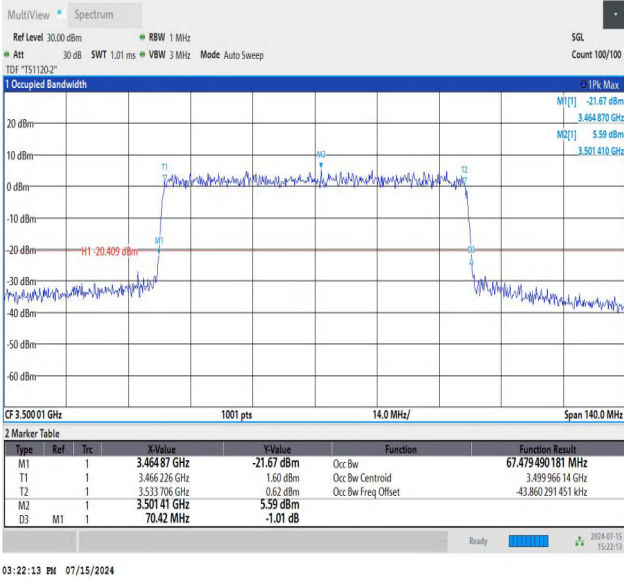
NTNV_DC_26A_n78A-3450-3550_PC3_30_5+60_M+M_TID8_A

nt1#1

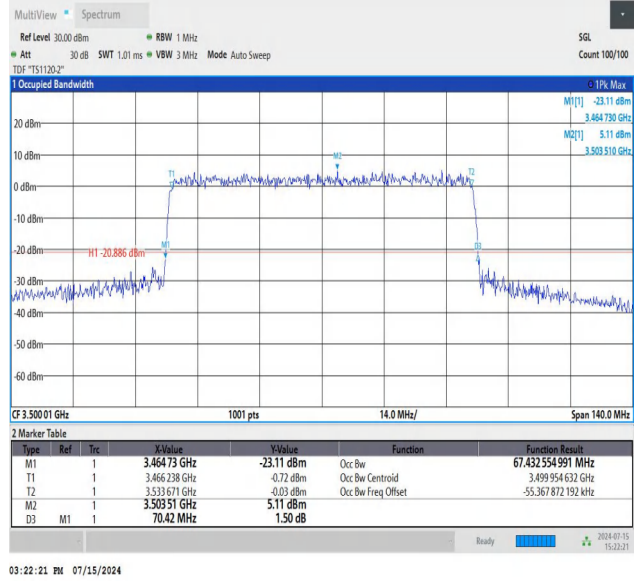


NTNV_DC_26A_n78A-3450-3550_PC3_30_5+60_M+M_TID9_A

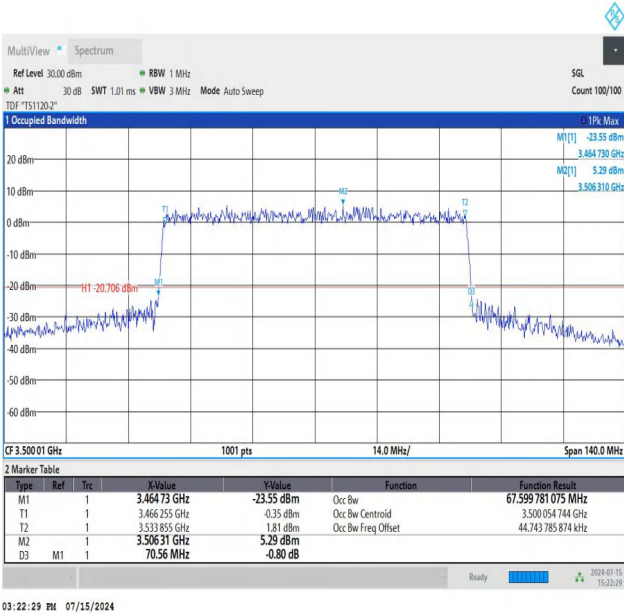
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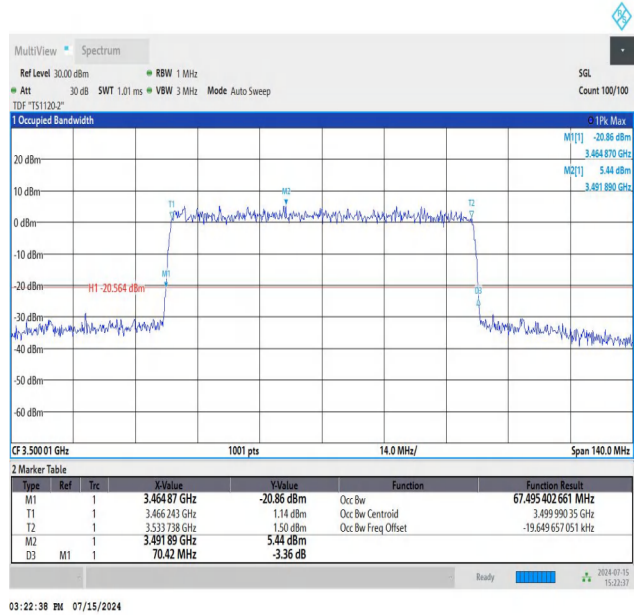
NTNV_DC_26A_n78A-3450-3550_PC3_30_5+70_M+M_TID6_A
nt1#1



NTNV_DC_26A_n78A-3450-3550_PC3_30_5+70_M+M_TID7_A
nt1#1



NTNV_DC_26A_n78A-3450-3550_PC3_30_5+70_M+M_TID8_A
nt1#1



NTNV_DC_26A_n78A-3450-3550_PC3_30_5+70_M+M_TID9_A
nt1#1