

DC_26A_n77A-3700-398 0	30	5+100	DFT-QPSK	M+H	Inner_Full	25.63	28.22	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	22.78	25.37	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	22.56	25.15	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-16QAM	M+H	Outer_Full	23.77	26.36	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-16QAM	M+H	Inner_Full	24.76	27.35	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	21.8	24.39	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	21.61	24.2	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-64QAM	M+H	Outer_Full	23.37	25.96	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-64QAM	M+H	Inner_Full	23.25	25.84	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-64QAM	M+H	Edge_1RB_Left	21.41	24	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-64QAM	M+H	Edge_1RB_Right	21.22	23.81	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-256QAM	M+H	Outer_Full	21.35	23.94	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-256QAM	M+H	Inner_Full	21.21	23.8	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-256QAM	M+H	Edge_1RB_Left	21.41	24	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-256QAM	M+H	Edge_1RB_Right	21.08	23.67	30	PASS

Peak-to-Average Ratio for NSA

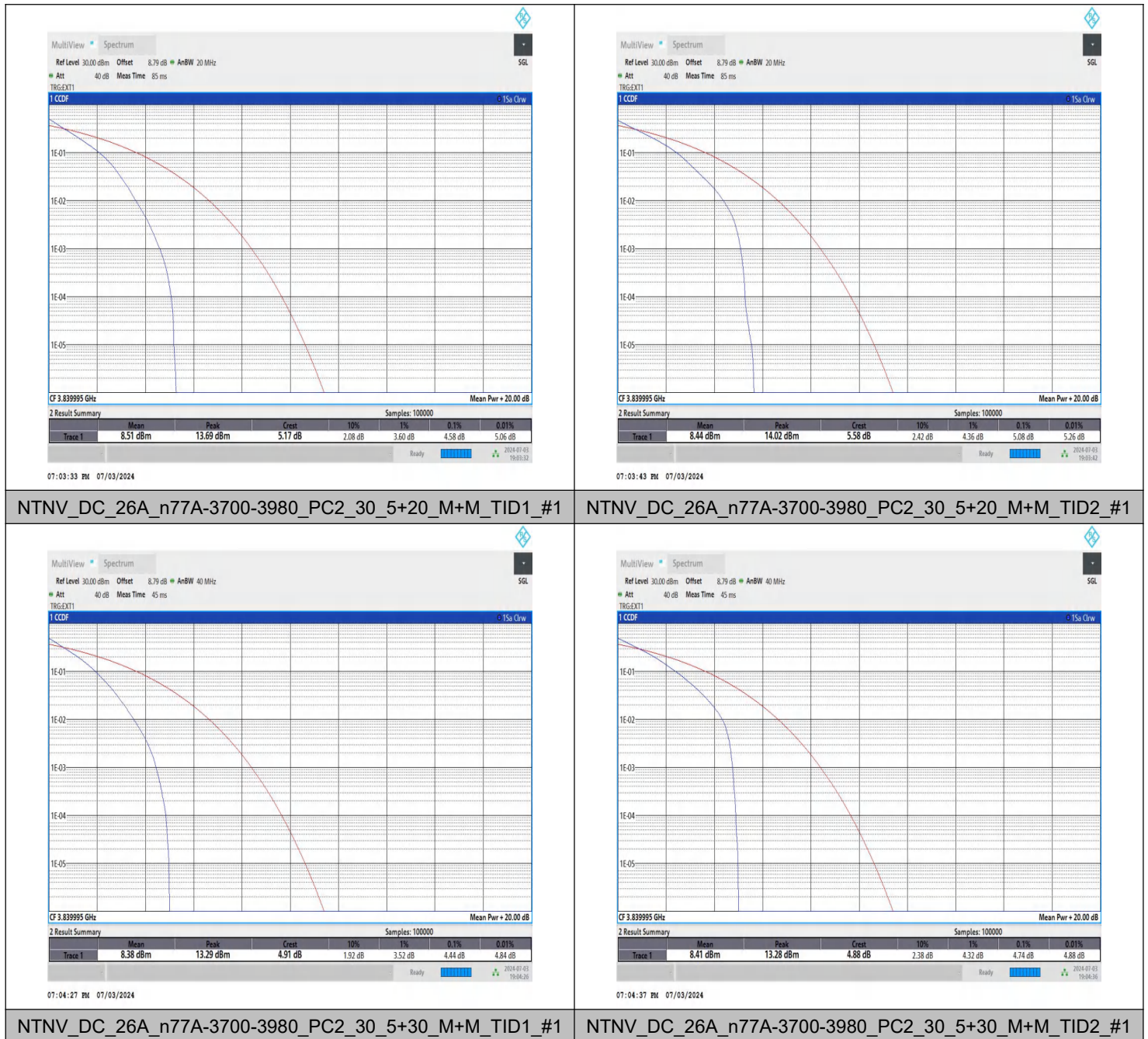
Peak-to-Average Ratio(CCDF)

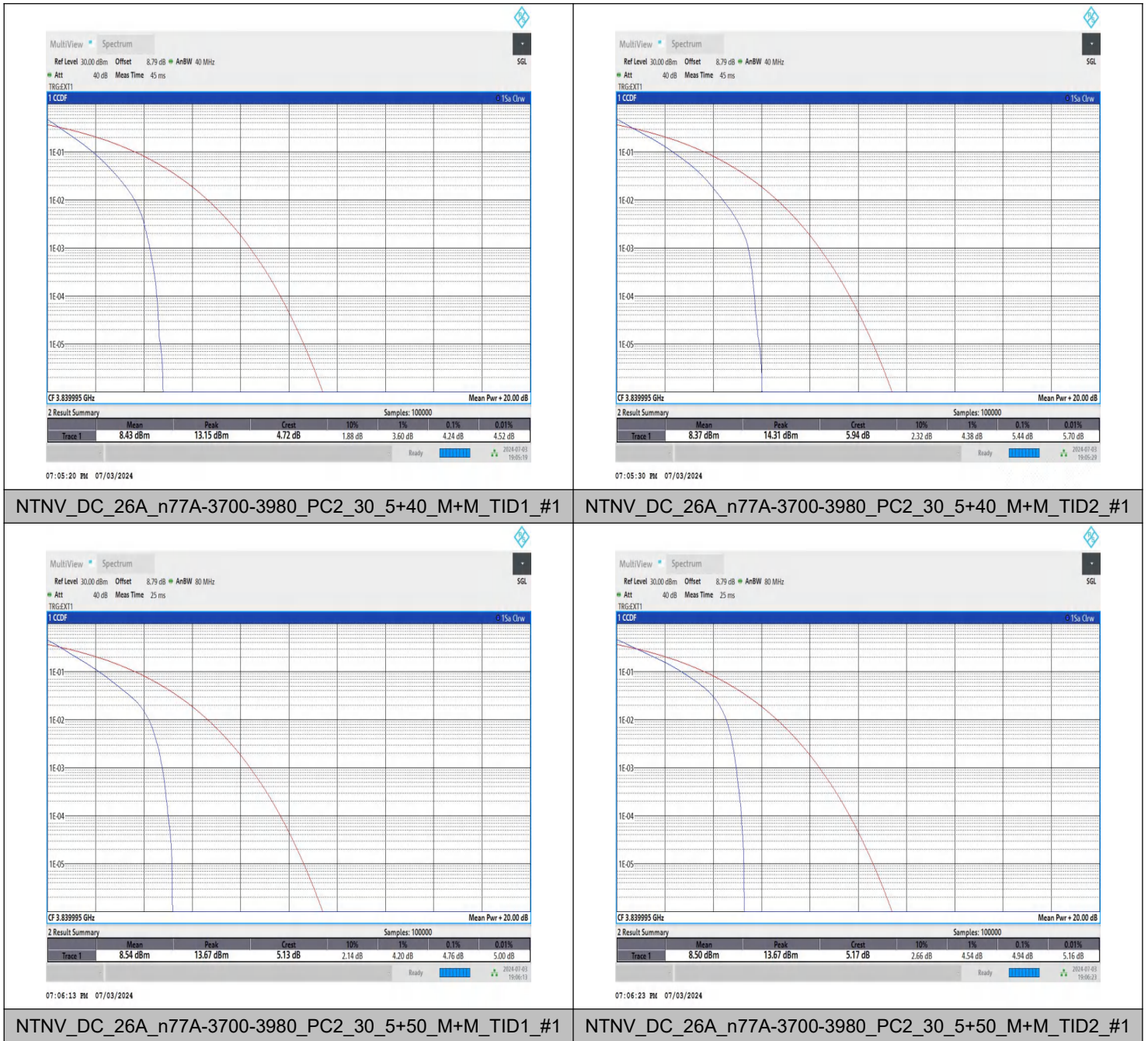
Test Result

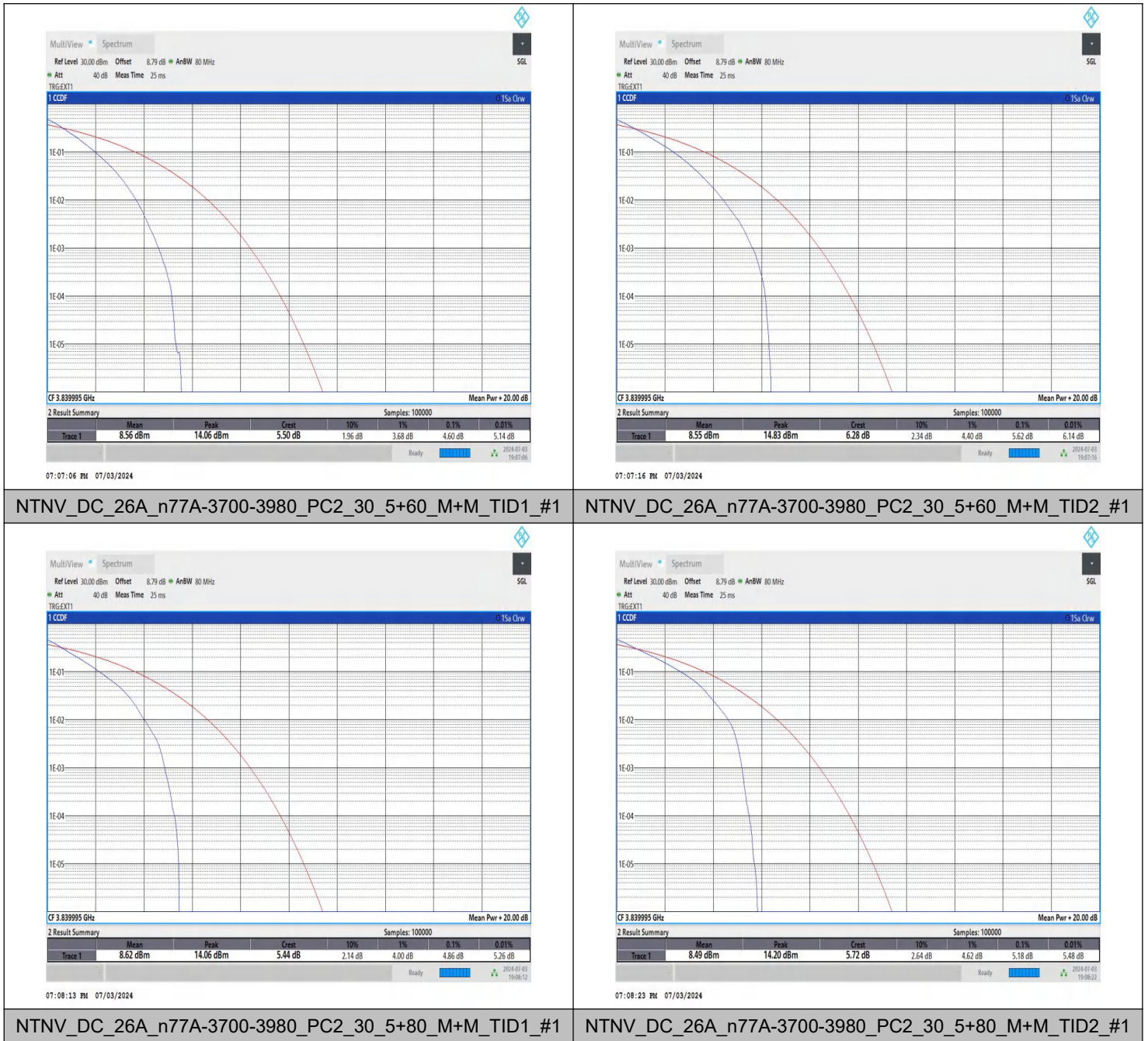
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_26A_n77A-3700-3980	30	5+20	DFT-PI2BPSK	M+M	Outer_Full	4.58	≤13	PASS
DC_26A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	5.08	≤13	PASS
DC_26A_n77A-3700-3980	30	5+30	DFT-PI2BPSK	M+M	Outer_Full	4.44	≤13	PASS
DC_26A_n77A-3700-3980	30	5+30	DFT-QPSK	M+M	Outer_Full	4.74	≤13	PASS
DC_26A_n77A-3700-3980	30	5+40	DFT-PI2BPSK	M+M	Outer_Full	4.24	≤13	PASS
DC_26A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	5.44	≤13	PASS
DC_26A_n77A-3700-3980	30	5+50	DFT-PI2BPSK	M+M	Outer_Full	4.76	≤13	PASS
DC_26A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	4.94	≤13	PASS
DC_26A_n77A-3700-3980	30	5+60	DFT-PI2BPSK	M+M	Outer_Full	4.60	≤13	PASS
DC_26A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	5.62	≤13	PASS
DC_26A_n77A-3700-3980	30	5+80	DFT-PI2BPSK	M+M	Outer_Full	4.86	≤13	PASS
DC_26A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	5.18	≤13	PASS
DC_26A_n77A-3700-3980	30	5+90	DFT-PI2BPSK	M+M	Outer_Full	4.78	≤13	PASS
DC_26A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	5.14	≤13	PASS
DC_26A_n77A-3700-3980	30	5+100	DFT-PI2BPSK	M+M	Outer_Full	4.90	≤13	PASS
DC_26A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Outer_Full	5.10	≤13	PASS
DC_26A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	5.90	≤13	PASS
DC_26A_n77A-3700-3980	30	5+20	DFT-64QAM	M+M	Outer_Full	6.68	≤13	PASS
DC_26A_n77A-3700-3980	30	5+20	DFT-256QAM	M+M	Outer_Full	6.84	≤13	PASS
DC_26A_n77A-3700-3980	30	5+30	DFT-16QAM	M+M	Outer_Full	5.84	≤13	PASS
DC_26A_n77A-3700-3980	30	5+30	DFT-64QAM	M+M	Outer_Full	6.46	≤13	PASS
DC_26A_n77A-3700-3980	30	5+30	DFT-256QAM	M+M	Outer_Full	7.02	≤13	PASS
DC_26A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	6.02	≤13	PASS
DC_26A_n77A-3700-3980	30	5+40	DFT-64QAM	M+M	Outer_Full	6.54	≤13	PASS
DC_26A_n77A-3700-3980	30	5+40	DFT-256QAM	M+M	Outer_Full	6.90	≤13	PASS
DC_26A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Outer_Full	6.20	≤13	PASS
DC_26A_n77A-3700-3980	30	5+50	DFT-64QAM	M+M	Outer_Full	6.16	≤13	PASS
DC_26A_n77A-3700-3980	30	5+50	DFT-256QAM	M+M	Outer_Full	6.78	≤13	PASS
DC_26A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	6.08	≤13	PASS
DC_26A_n77A-3700-3980	30	5+60	DFT-64QAM	M+M	Outer_Full	6.24	≤13	PASS
DC_26A_n77A-3700-3980	30	5+60	DFT-256QAM	M+M	Outer_Full	6.90	≤13	PASS
DC_26A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	6.28	≤13	PASS
DC_26A_n77A-3700-3980	30	5+80	DFT-64QAM	M+M	Outer_Full	6.36	≤13	PASS
DC_26A_n77A-3700-3980	30	5+80	DFT-256QAM	M+M	Outer_Full	6.76	≤13	PASS
DC_26A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Outer_Full	6.14	≤13	PASS

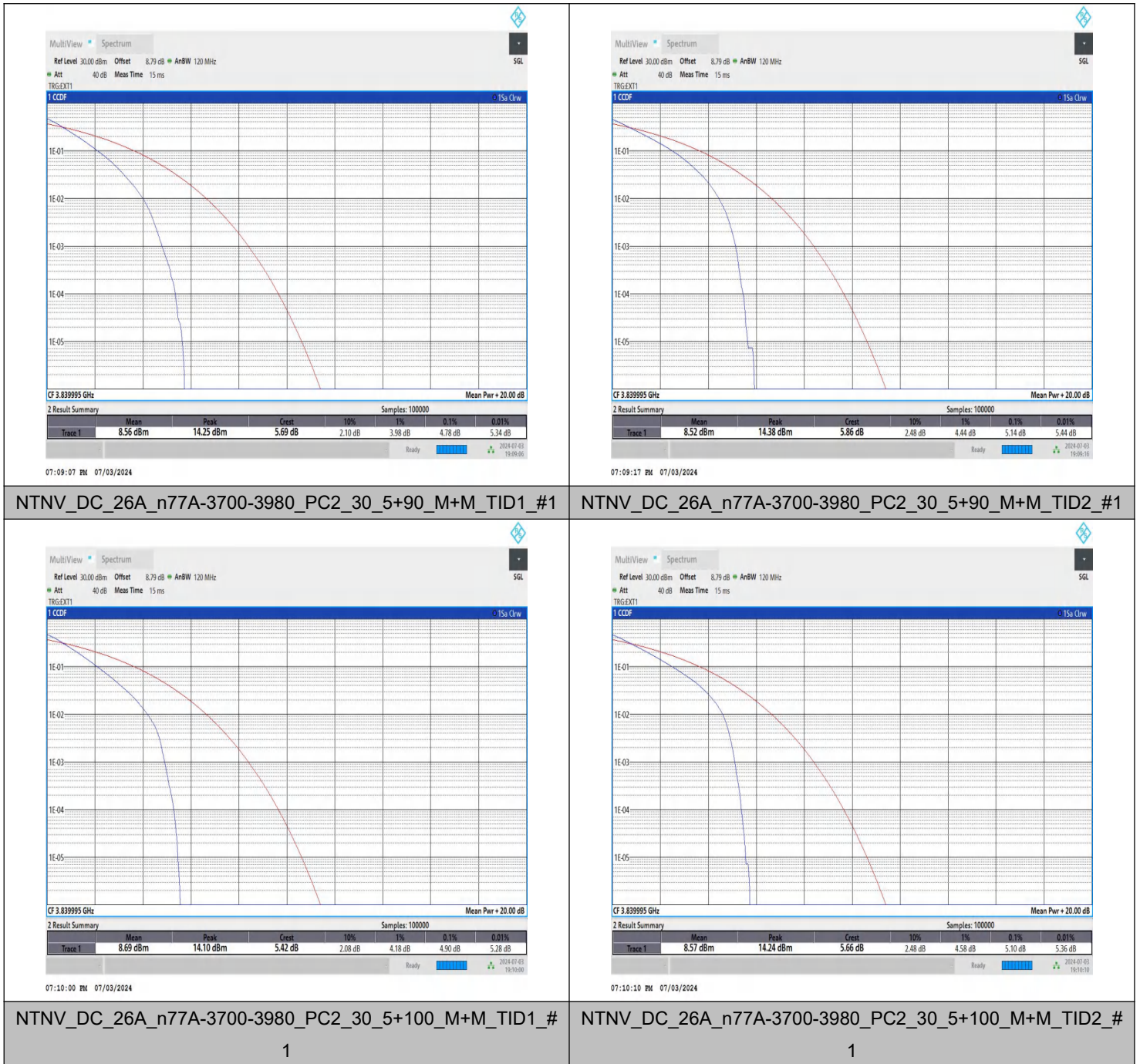
DC_26A_n77A-3700-3980	30	5+90	DFT-64QAM	M+M	Outer_Full	6.30	≤ 13	PASS
DC_26A_n77A-3700-3980	30	5+90	DFT-256QAM	M+M	Outer_Full	6.70	≤ 13	PASS
DC_26A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Outer_Full	6.22	≤ 13	PASS
DC_26A_n77A-3700-3980	30	5+100	DFT-64QAM	M+M	Outer_Full	6.26	≤ 13	PASS
DC_26A_n77A-3700-3980	30	5+100	DFT-256QAM	M+M	Outer_Full	6.86	≤ 13	PASS

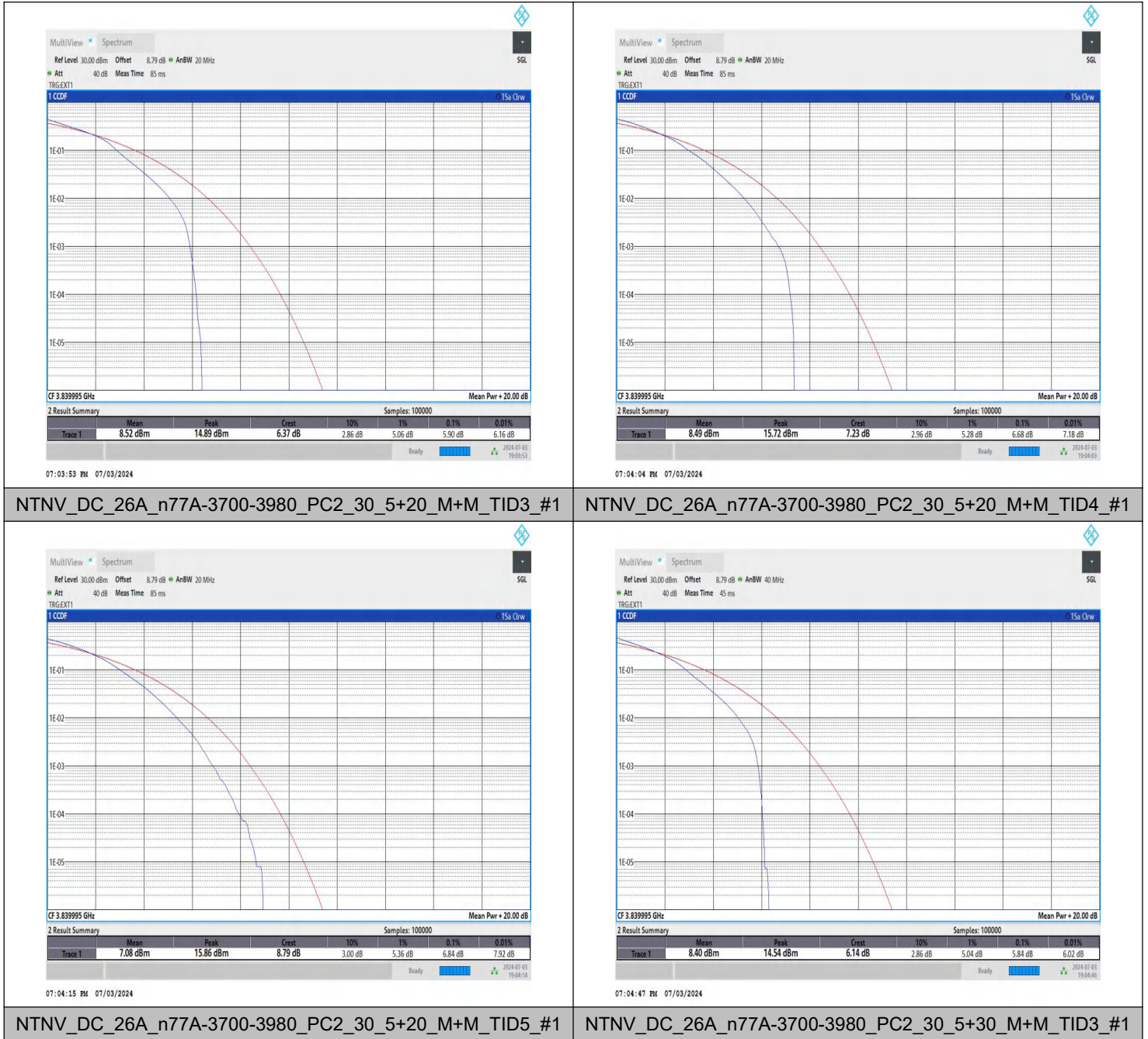
Test Graphs

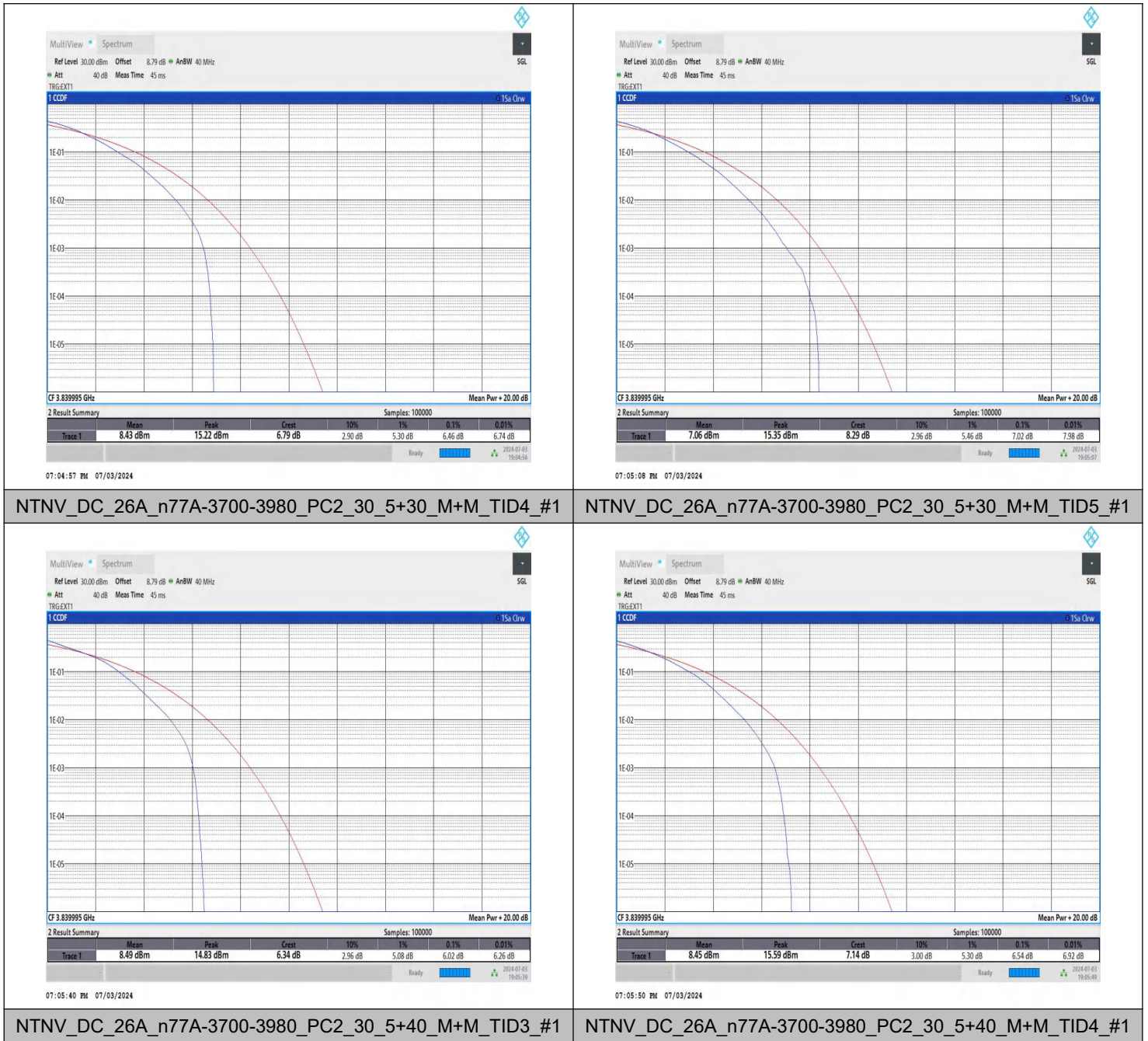


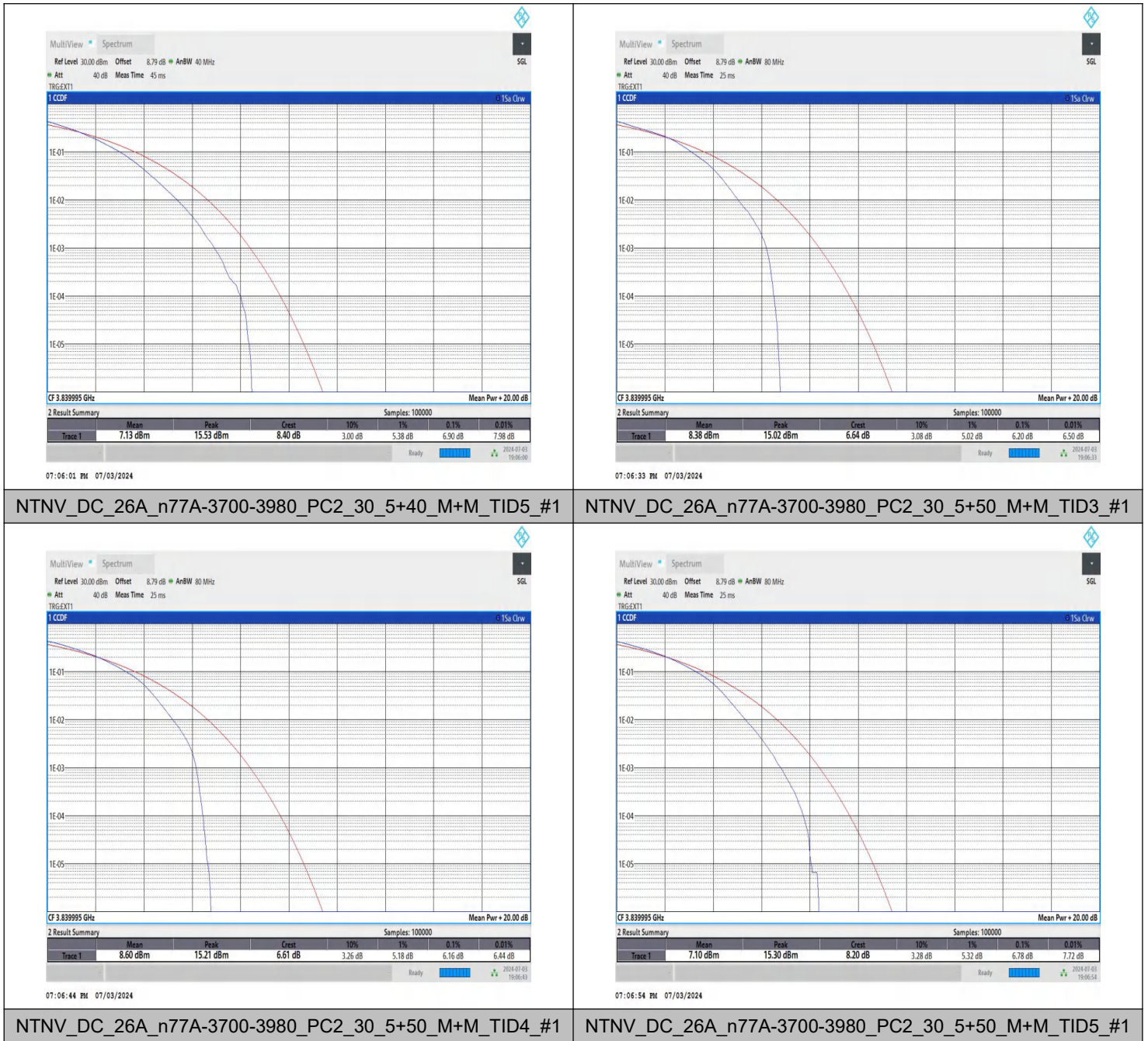


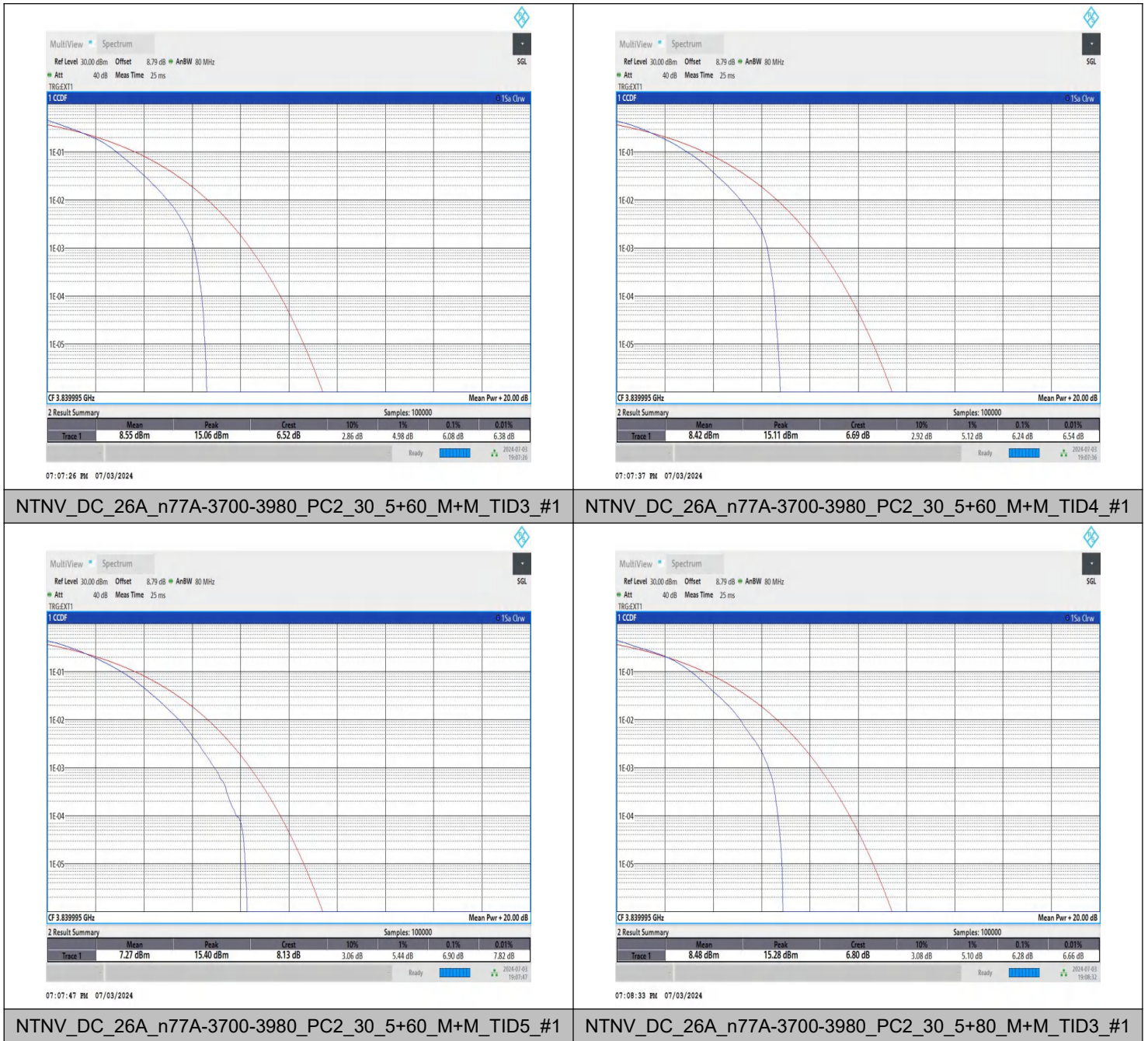


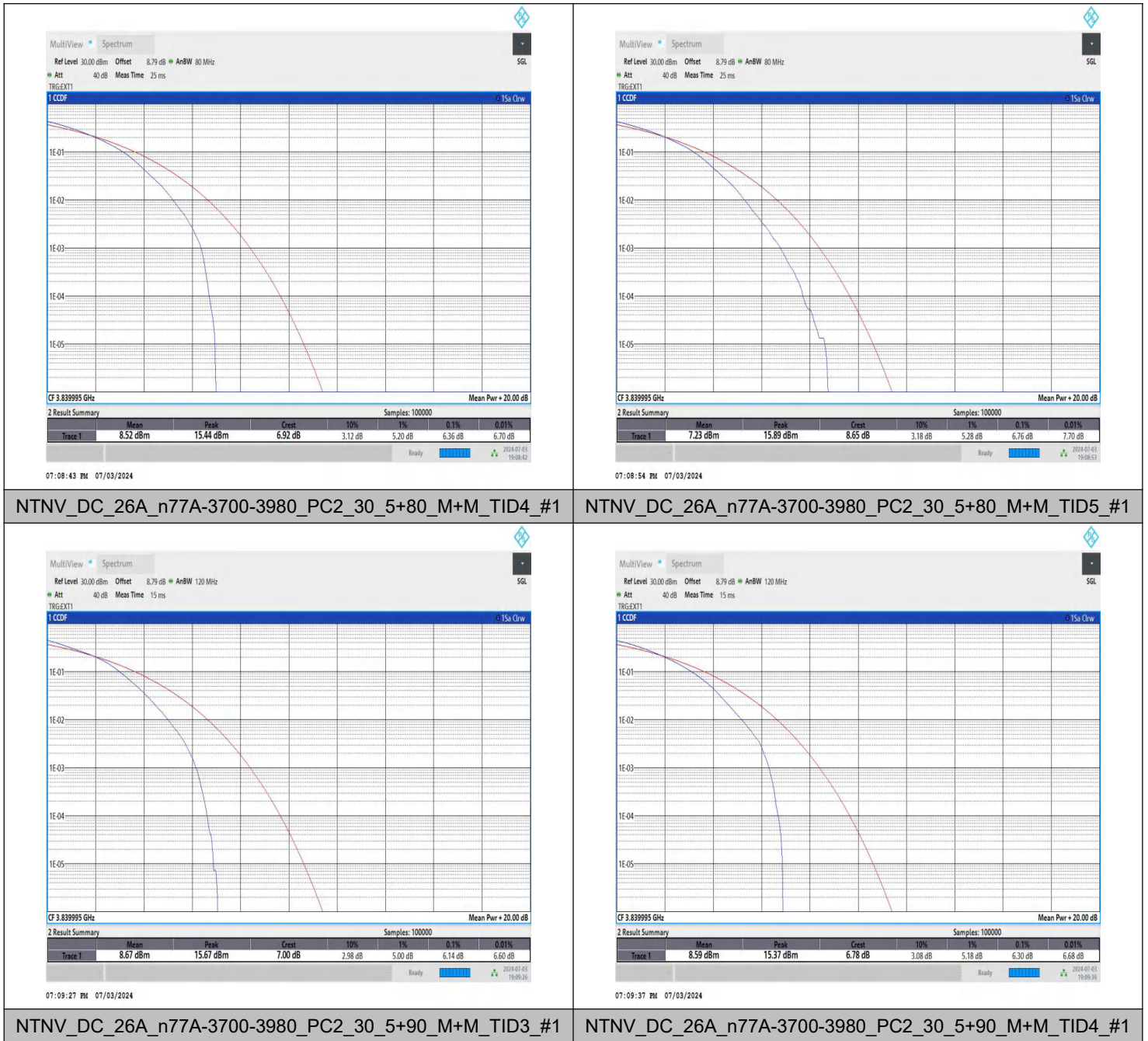


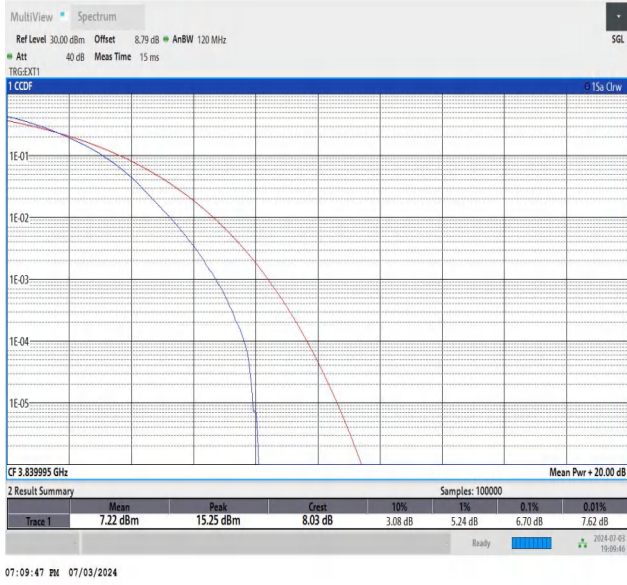










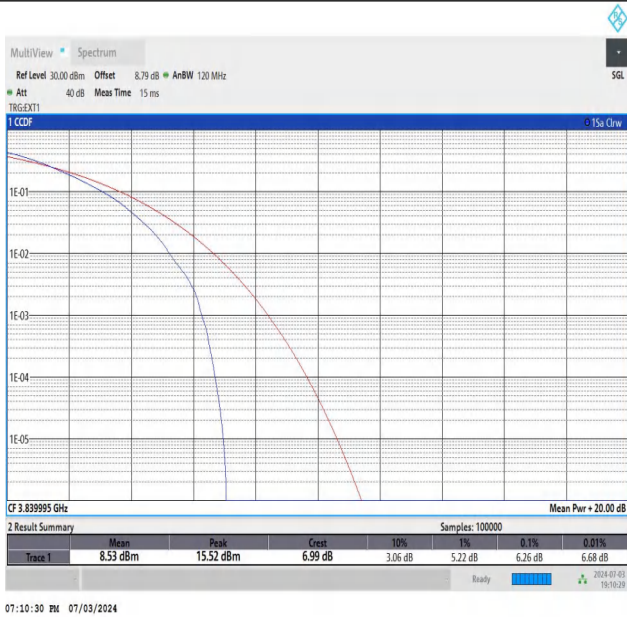


NTNV_DC_26A_n77A-3700-3980_PC2_30_5+90_M+M_TID5_#1



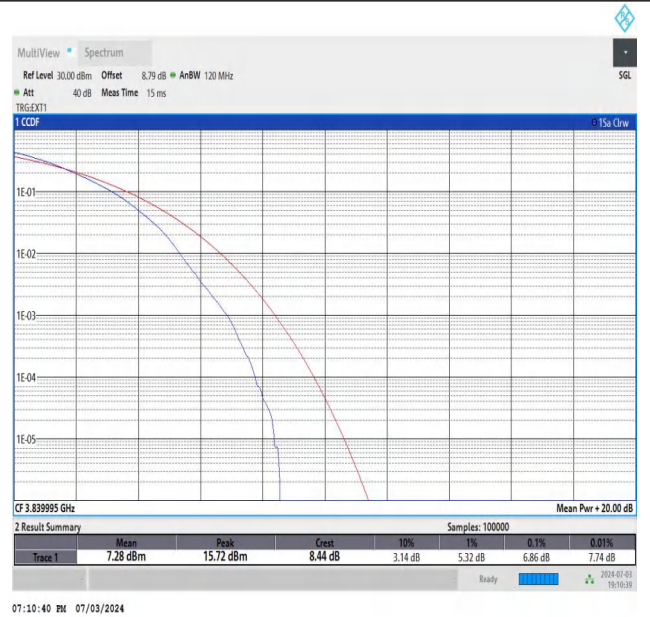
NTNV_DC_26A_n77A-3700-3980_PC2_30_5+100_M+M_TID3_#

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NTNV_DC_26A_n77A-3700-3980_PC2_30_5+100_M+M_TID4_#

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NTNV_DC_26A_n77A-3700-3980_PC2_30_5+100_M+M_TID5_#

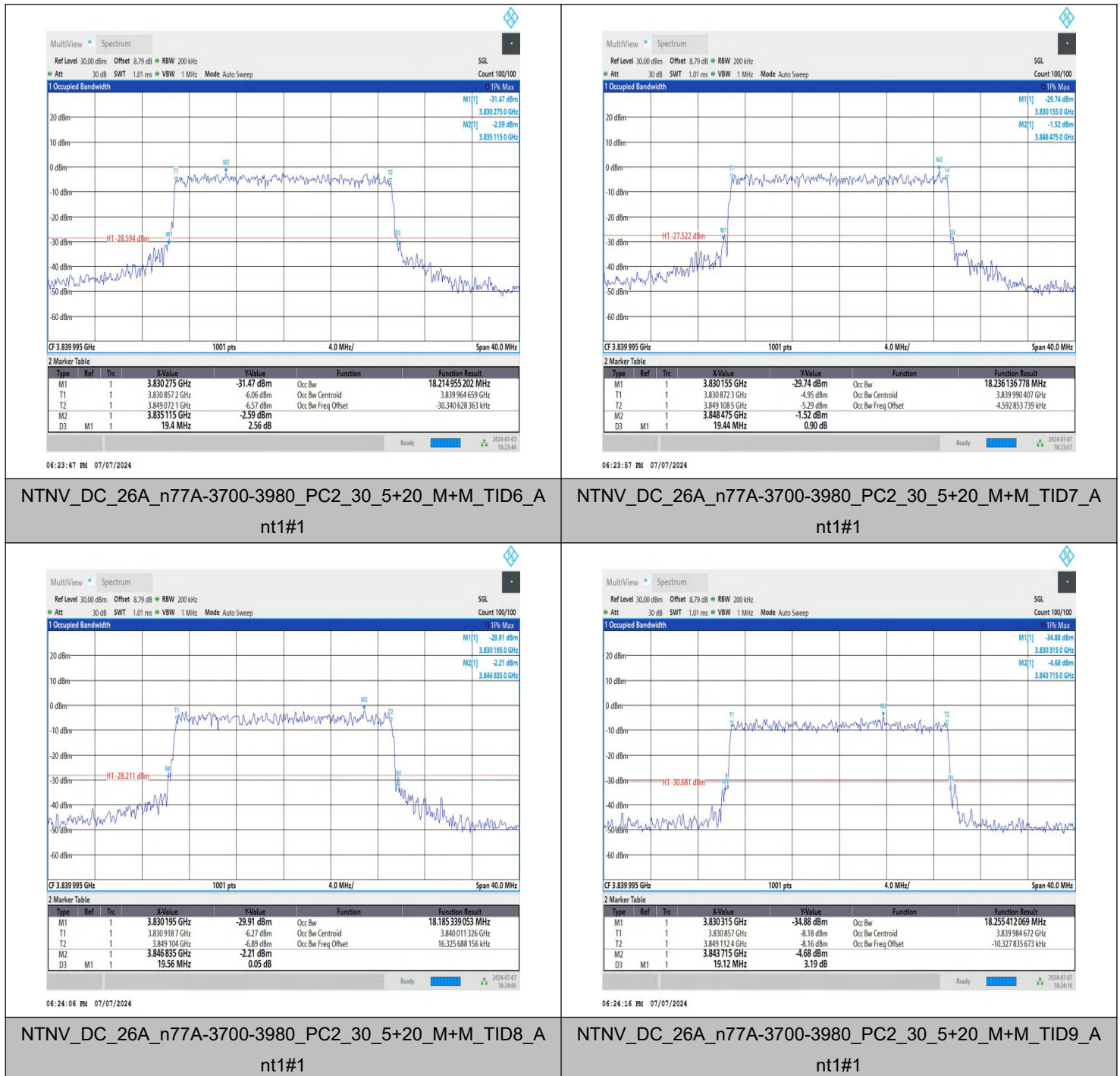
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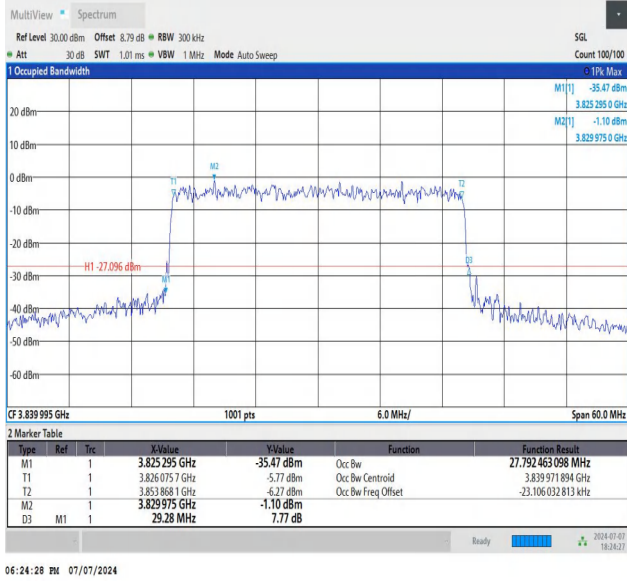
26dB Bandwidth and Occupied Bandwidth for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_26A_n77A-3700-3980	30	5+20	CP-QPSK	M+M	Outer_Full	18.215	19.400	PASS
DC_26A_n77A-3700-3980	30	5+20	CP-16QAM	M+M	Outer_Full	18.236	19.440	PASS
DC_26A_n77A-3700-3980	30	5+20	CP-64QAM	M+M	Outer_Full	18.185	19.560	PASS
DC_26A_n77A-3700-3980	30	5+20	CP-256QAM	M+M	Outer_Full	18.255	19.120	PASS
DC_26A_n77A-3700-3980	30	5+30	CP-QPSK	M+M	Outer_Full	27.792	29.280	PASS
DC_26A_n77A-3700-3980	30	5+30	CP-16QAM	M+M	Outer_Full	27.829	29.220	PASS
DC_26A_n77A-3700-3980	30	5+30	CP-64QAM	M+M	Outer_Full	27.776	29.280	PASS
DC_26A_n77A-3700-3980	30	5+30	CP-256QAM	M+M	Outer_Full	27.803	28.980	PASS
DC_26A_n77A-3700-3980	30	5+40	CP-QPSK	M+M	Outer_Full	37.87	39.520	PASS
DC_26A_n77A-3700-3980	30	5+40	CP-16QAM	M+M	Outer_Full	37.826	39.360	PASS
DC_26A_n77A-3700-3980	30	5+40	CP-64QAM	M+M	Outer_Full	37.822	39.360	PASS
DC_26A_n77A-3700-3980	30	5+40	CP-256QAM	M+M	Outer_Full	37.834	39.360	PASS
DC_26A_n77A-3700-3980	30	5+50	CP-QPSK	M+M	Outer_Full	47.59	49.300	PASS
DC_26A_n77A-3700-3980	30	5+50	CP-16QAM	M+M	Outer_Full	47.423	49.300	PASS
DC_26A_n77A-3700-3980	30	5+50	CP-64QAM	M+M	Outer_Full	47.353	49.400	PASS
DC_26A_n77A-3700-3980	30	5+50	CP-256QAM	M+M	Outer_Full	47.555	49.200	PASS
DC_26A_n77A-3700-3980	30	5+60	CP-QPSK	M+M	Outer_Full	57.762	60.720	PASS
DC_26A_n77A-3700-3980	30	5+60	CP-16QAM	M+M	Outer_Full	57.85	60.720	PASS
DC_26A_n77A-3700-3980	30	5+60	CP-64QAM	M+M	Outer_Full	57.914	60.840	PASS
DC_26A_n77A-3700-3980	30	5+60	CP-256QAM	M+M	Outer_Full	58.003	60.720	PASS
DC_26A_n77A-3700-3980	30	5+80	CP-QPSK	M+M	Outer_Full	77.227	80.640	PASS
DC_26A_n77A-3700-3980	30	5+80	CP-16QAM	M+M	Outer_Full	77.345	80.640	PASS
DC_26A_n77A-3700-3980	30	5+80	CP-64QAM	M+M	Outer_Full	77.554	80.640	PASS
DC_26A_n77A-3700-3980	30	5+80	CP-256QAM	M+M	Outer_Full	77.455	80.640	PASS
DC_26A_n77A-3700-3980	30	5+90	CP-QPSK	M+M	Outer_Full	87.281	90.720	PASS
DC_26A_n77A-3700-3980	30	5+90	CP-16QAM	M+M	Outer_Full	87.469	90.720	PASS
DC_26A_n77A-3700-3980	30	5+90	CP-64QAM	M+M	Outer_Full	87.463	90.720	PASS
DC_26A_n77A-3700-3980	30	5+90	CP-256QAM	M+M	Outer_Full	87.654	90.900	PASS
DC_26A_n77A-3700-3980	30	5+100	CP-QPSK	M+M	Outer_Full	97.326	101.000	PASS
DC_26A_n77A-3700-3980	30	5+100	CP-16QAM	M+M	Outer_Full	97.29	100.800	PASS
DC_26A_n77A-3700-3980	30	5+100	CP-64QAM	M+M	Outer_Full	97.385	100.600	PASS
DC_26A_n77A-3700-3980	30	5+100	CP-256QAM	M+M	Outer_Full	97.332	101.000	PASS

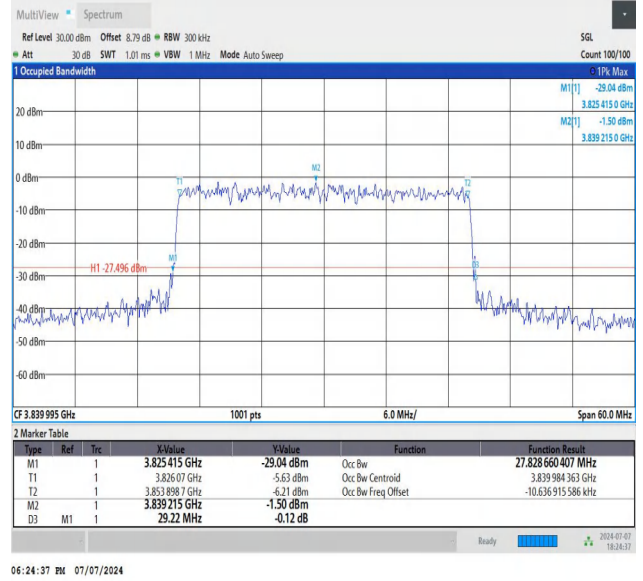
Test Graphs





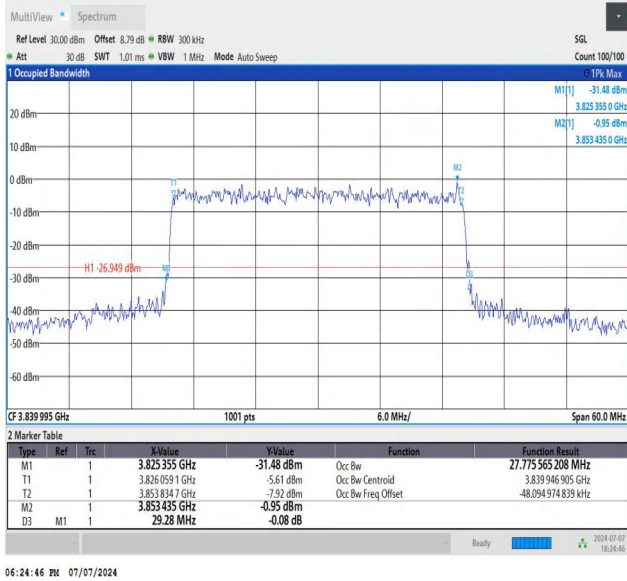
NTNV_DC_26A_n77A-3700-3980_PC2_30_5+30_M+M_TID6_A

nt1#1



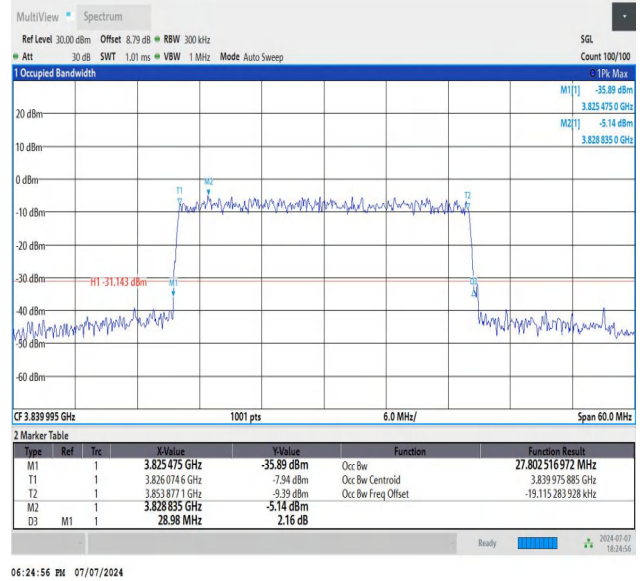
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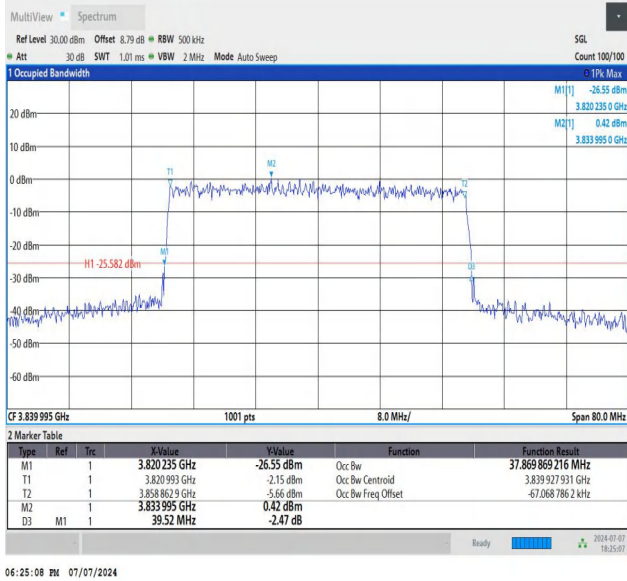
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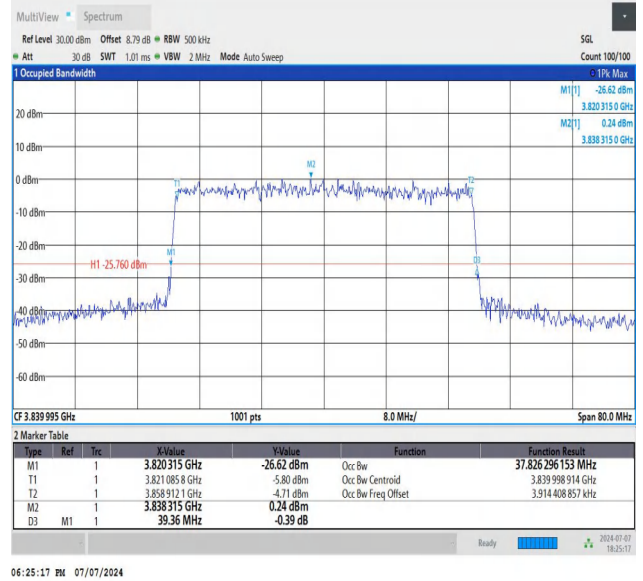


NTNV_DC_26A_n77A-3700-3980_PC2_30_5+30_M+M_TID9_A

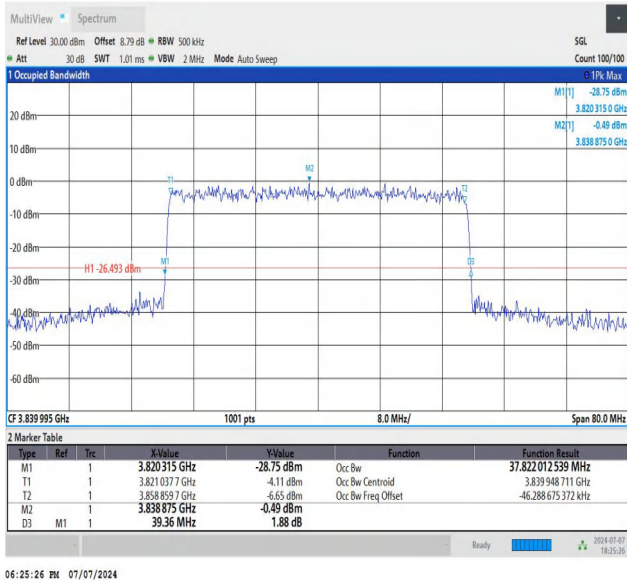
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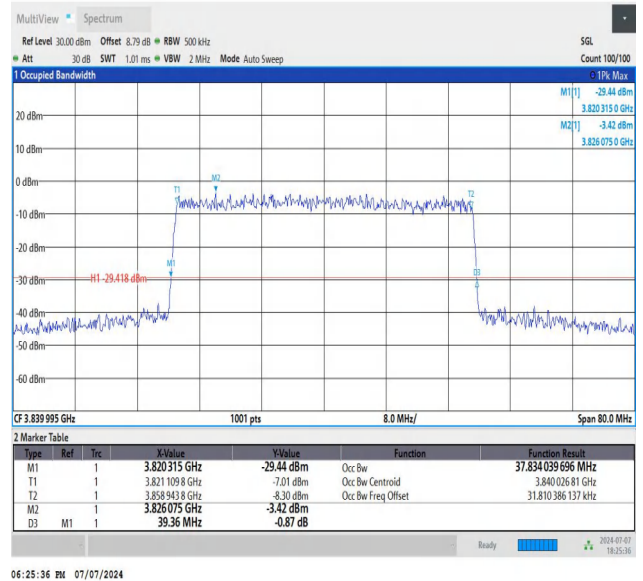
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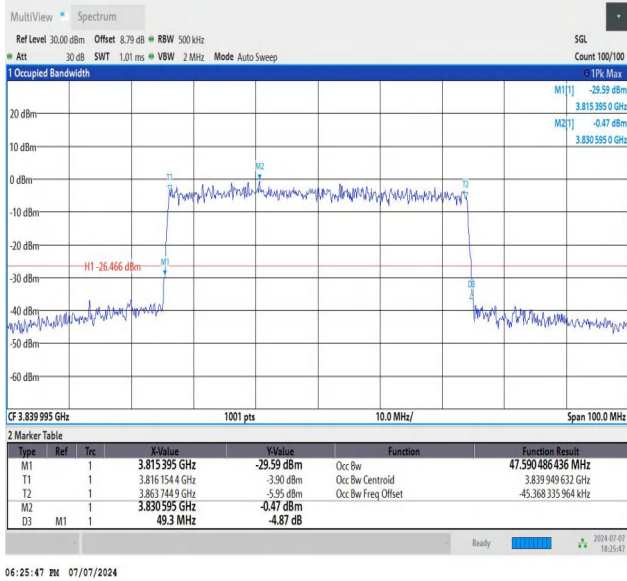
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nt1#1



NTNV_DC_26A_n77A-3700-3980_PC2_30_5+40_M+M_TID8_A
nt1#1

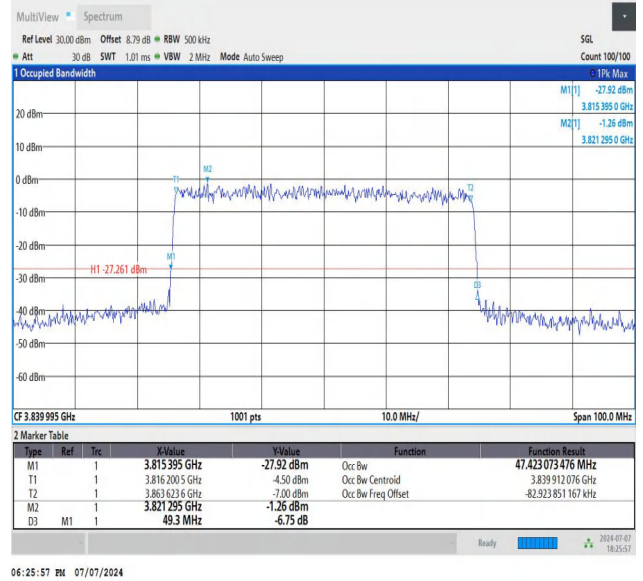


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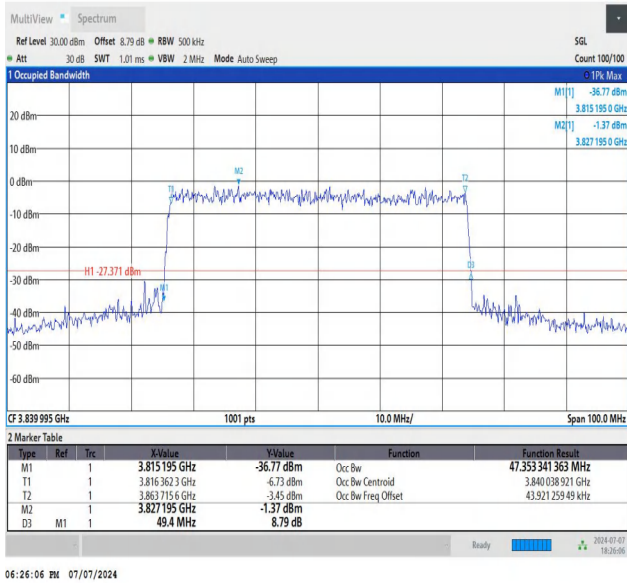
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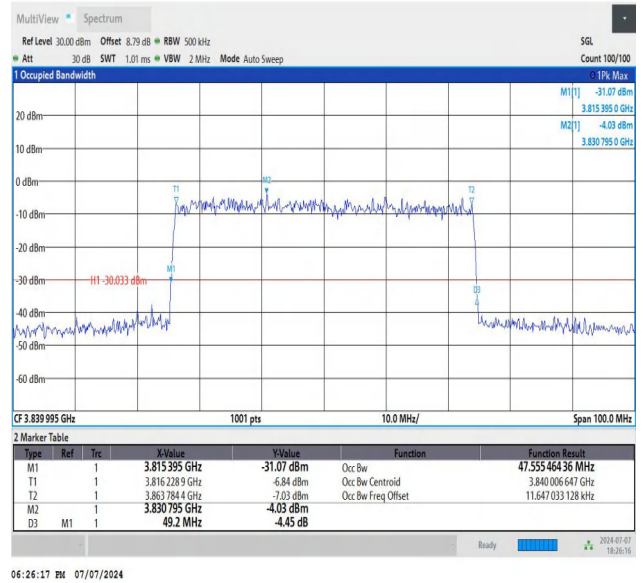
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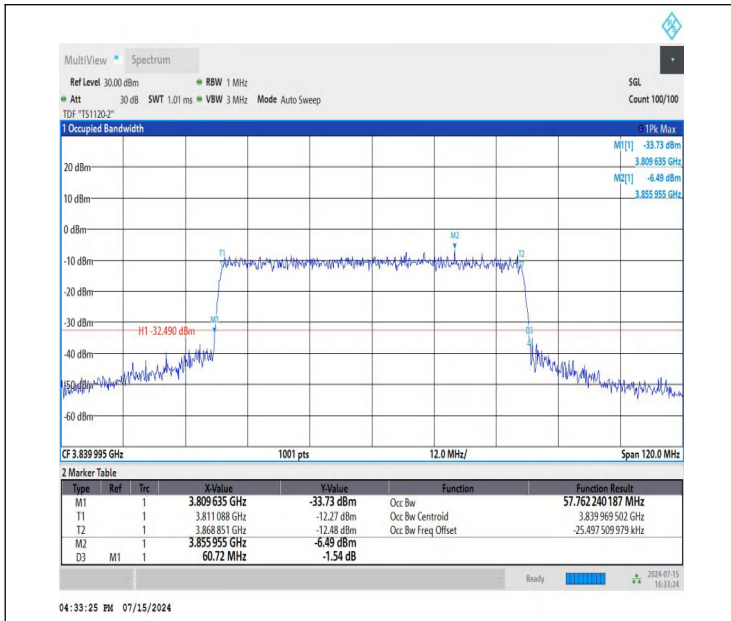
NTNV_DC_26A_n77A-3700-3980_PC2_30_5+50_M+M_TID8_A

nt1#1



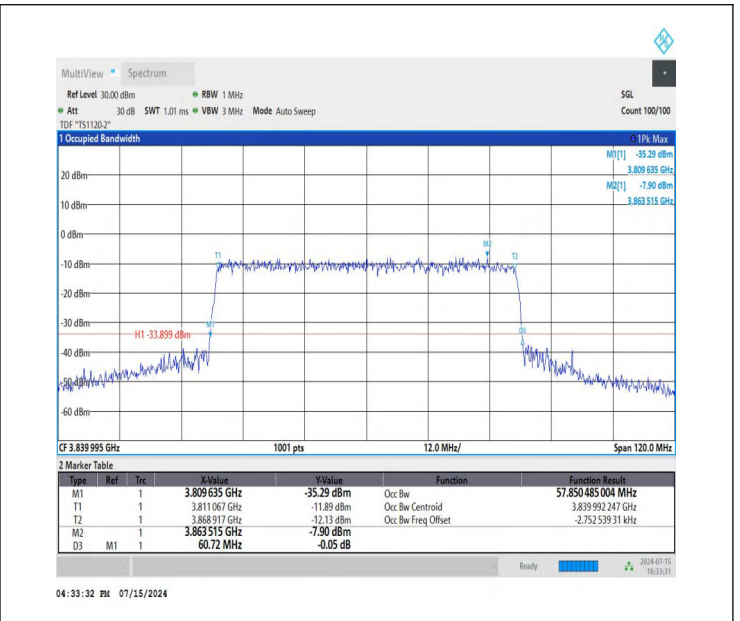
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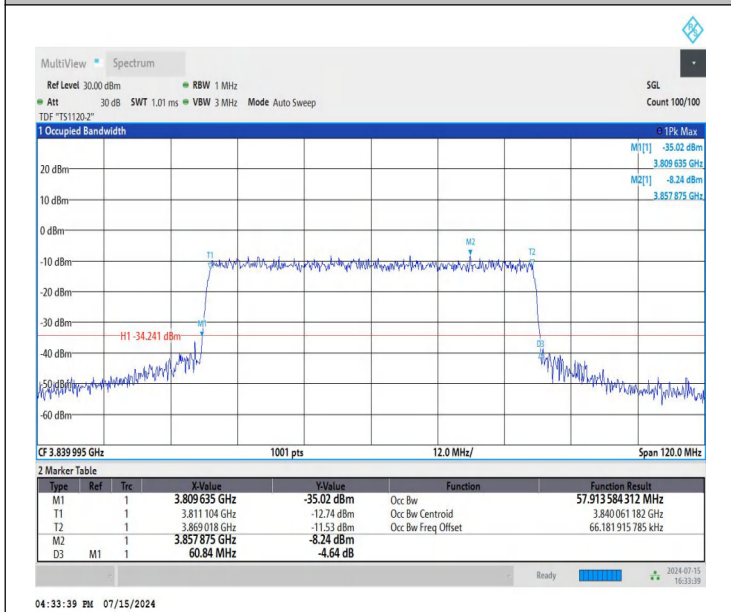
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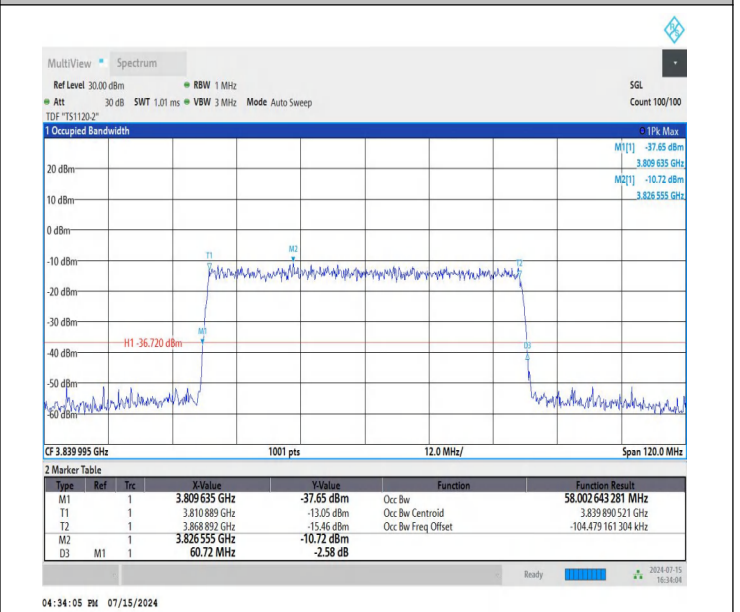
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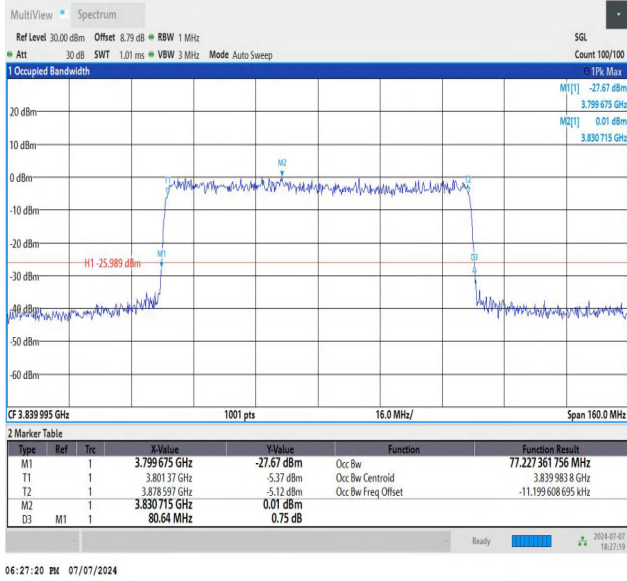
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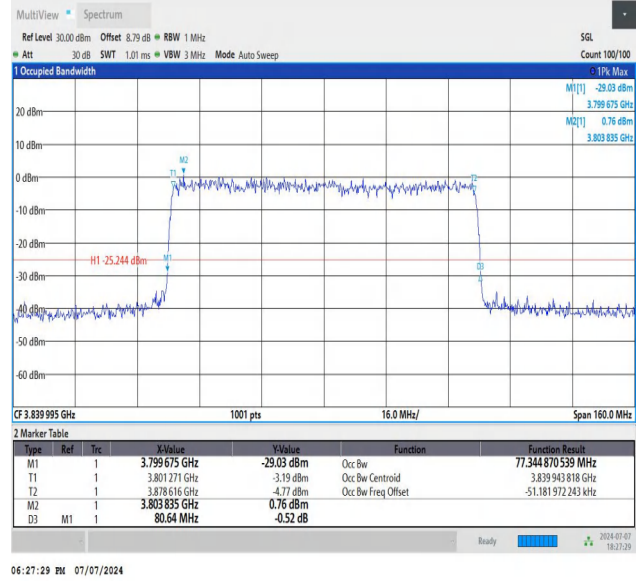


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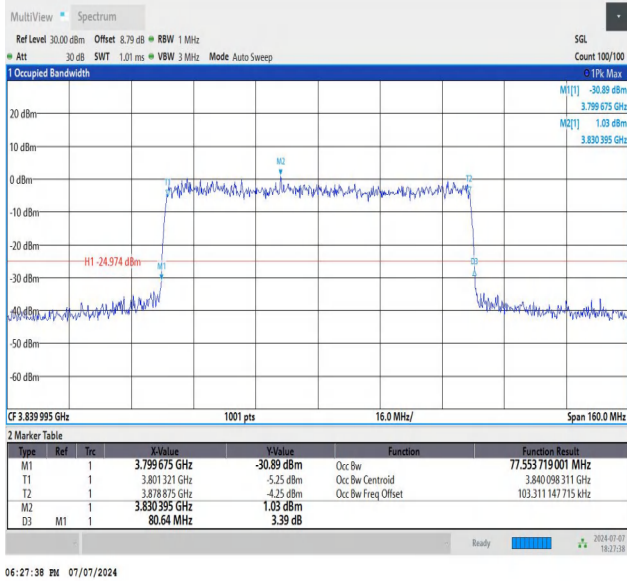
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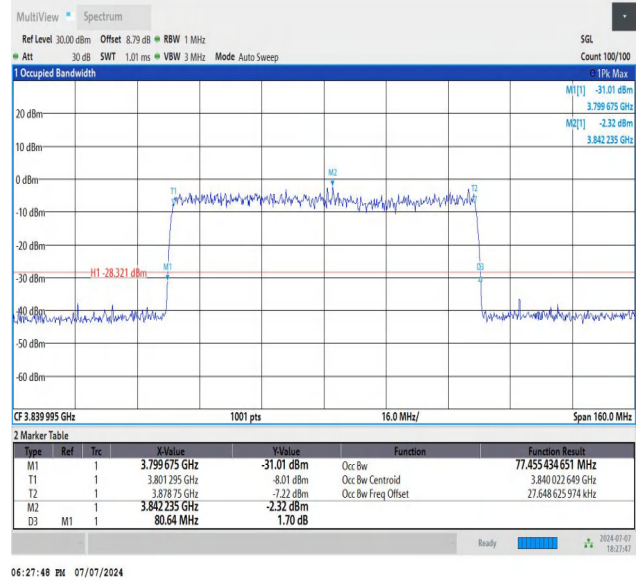
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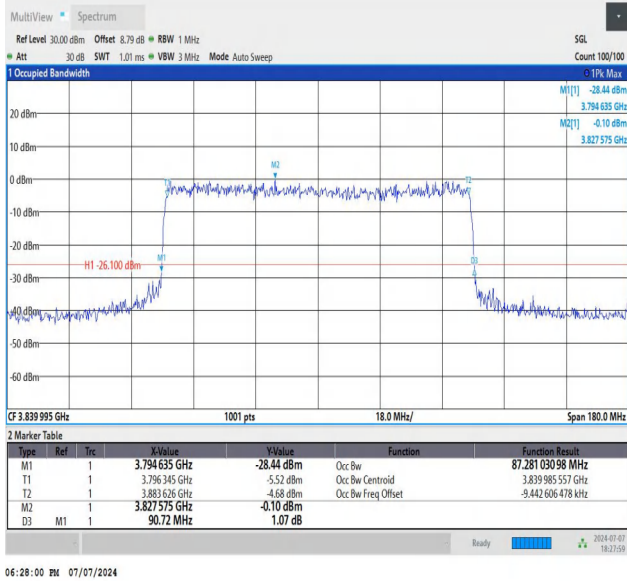
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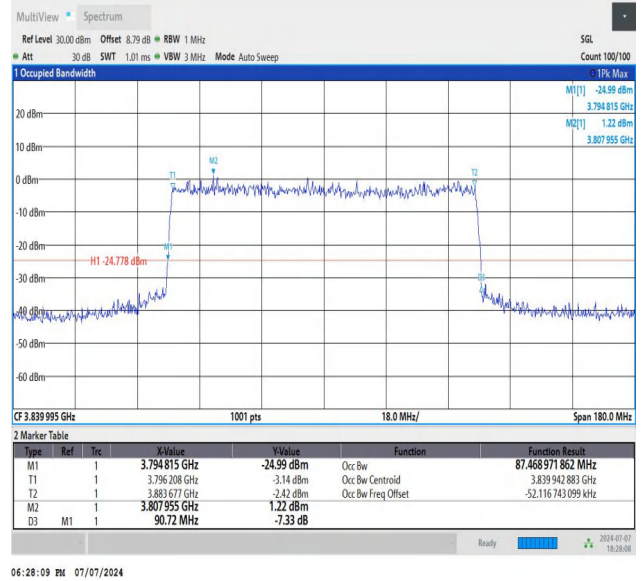


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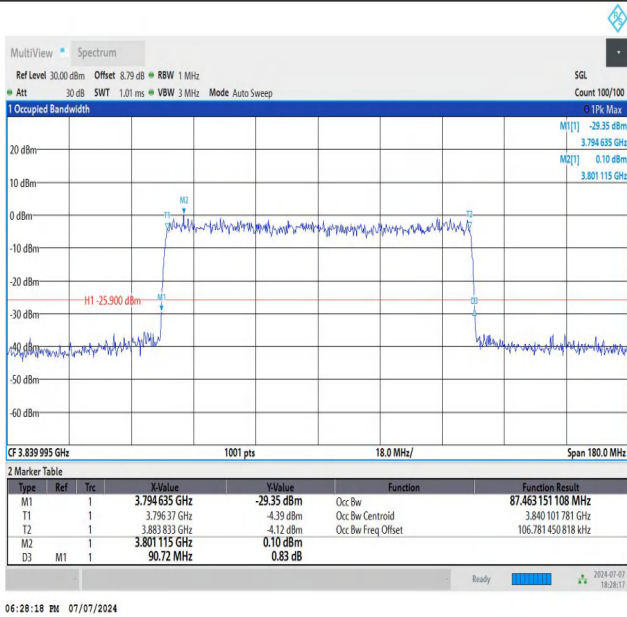
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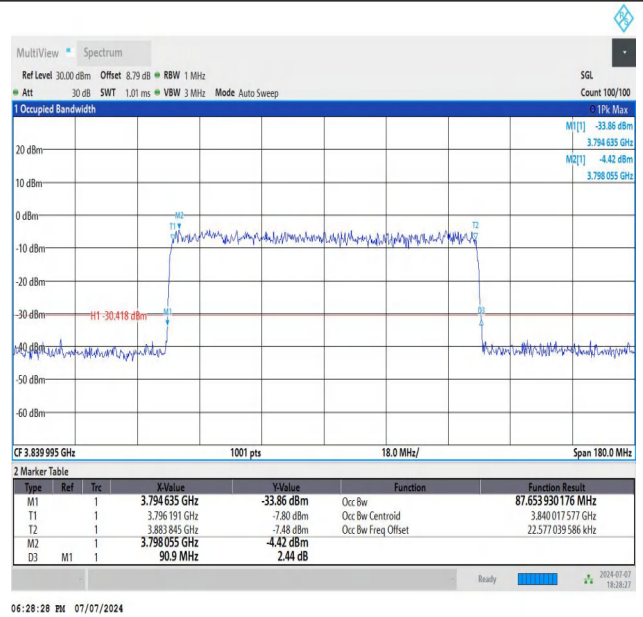
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nt1#1



NTNV_DC_26A_n77A-3700-3980_PC2_30_5+90_M+M_TID9_A

nt1#1