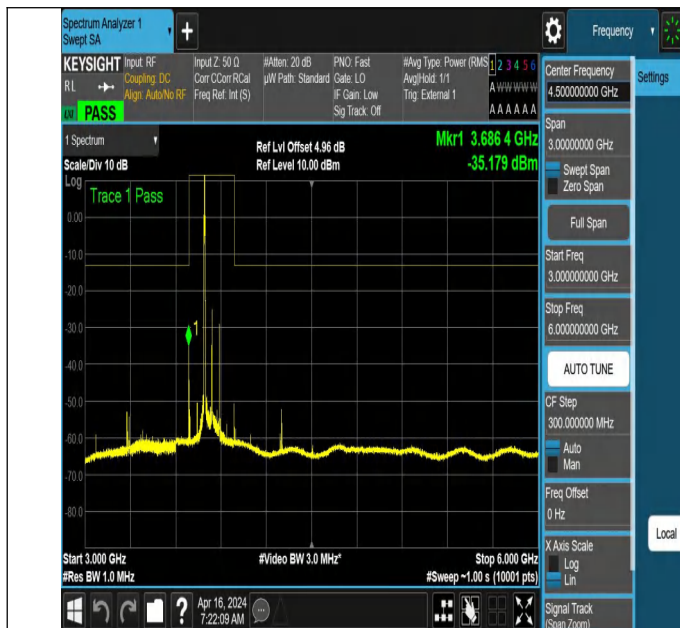
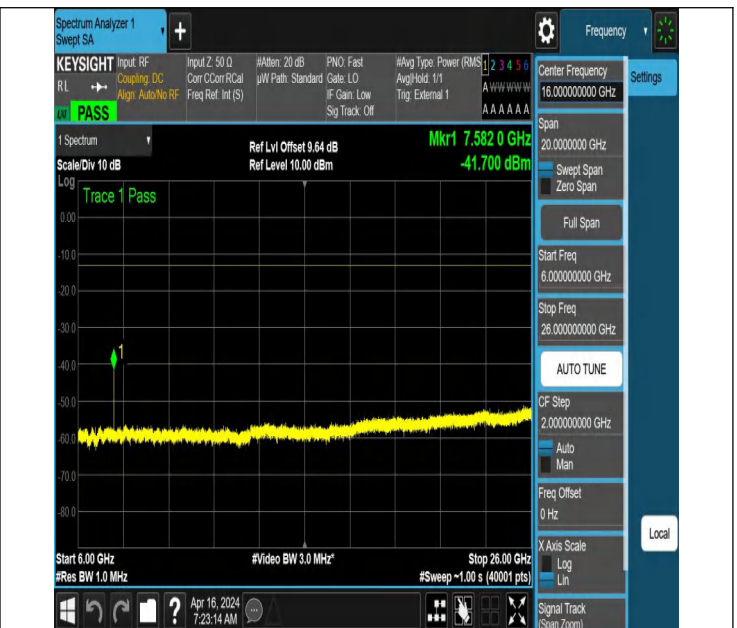
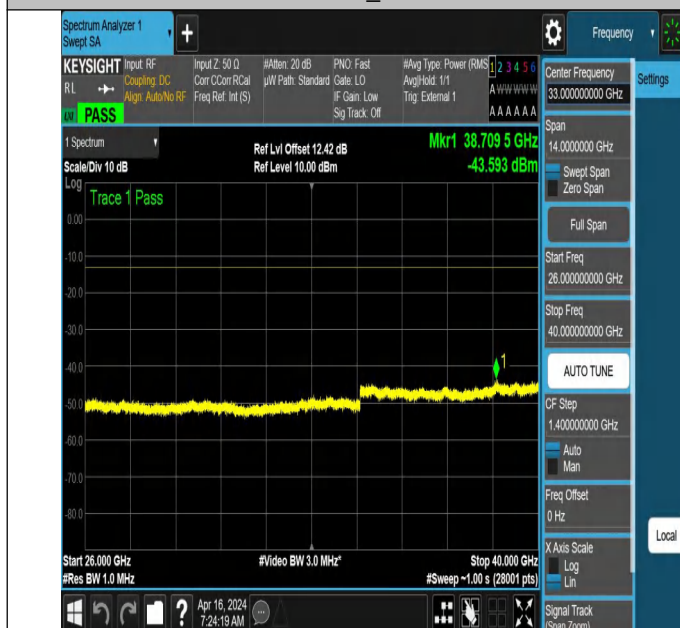
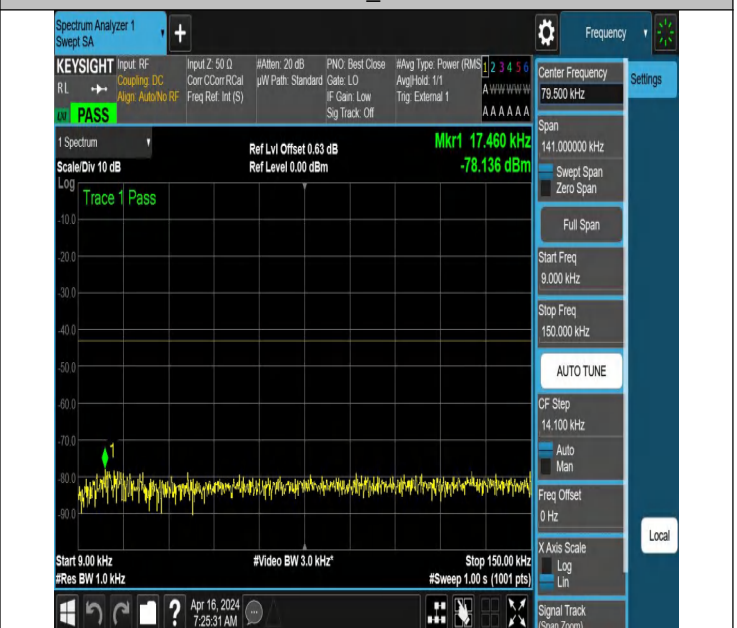




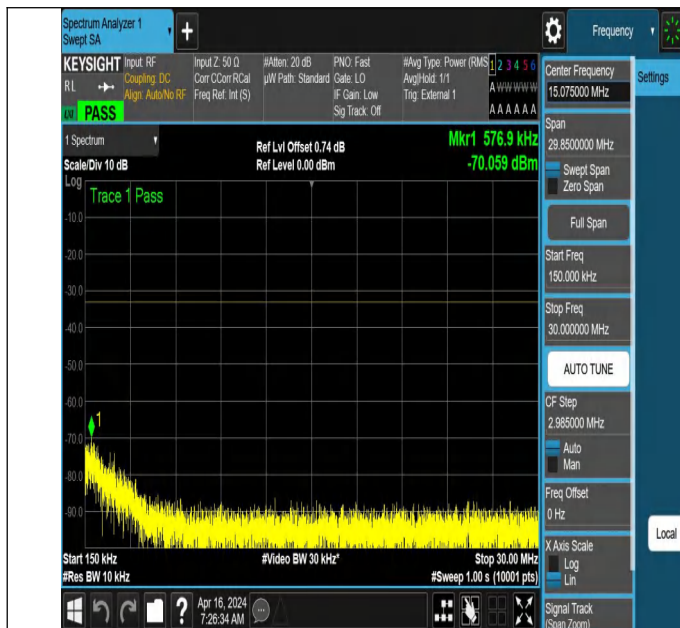
NTNV_N77-3700-3980_PC2_30_100_M_TID2_NS_01_3000_60
00 #1



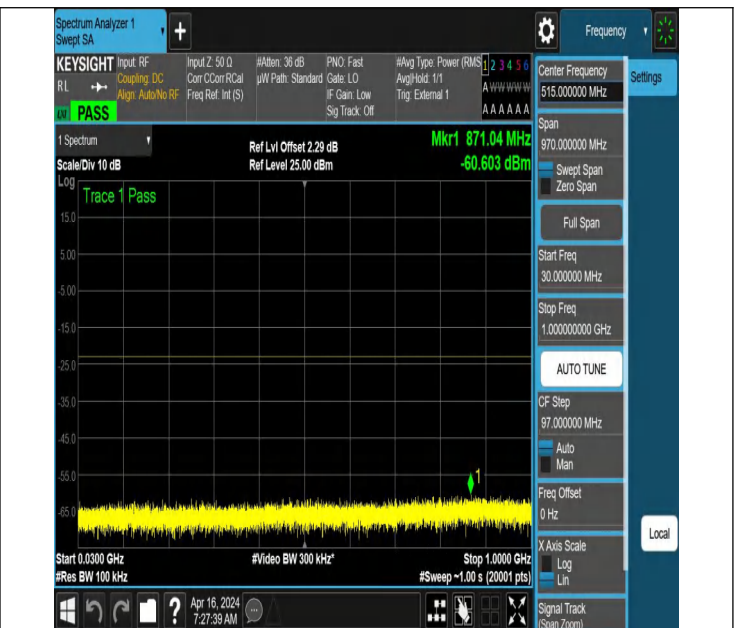
NTNV_N77-3700-3980_PC2_30_100_M_TID2_NS_01_6000_26
000 #1

NTNV_N77-3700-3980_PC2_30_100_M_TID2_NS_01_26000_4
0000_#1

NTNV_N77-3700-3980_PC2_30_100_M_TID3_NS_01_0.009_0.
15_#1



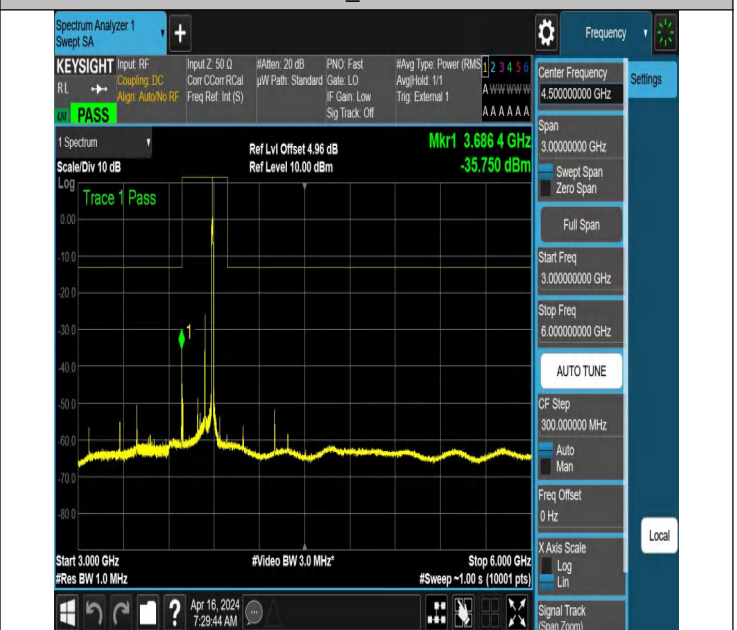
NTNV_N77-3700-3980_PC2_30_100_M_TID3_NS_01_0.15_30_#1



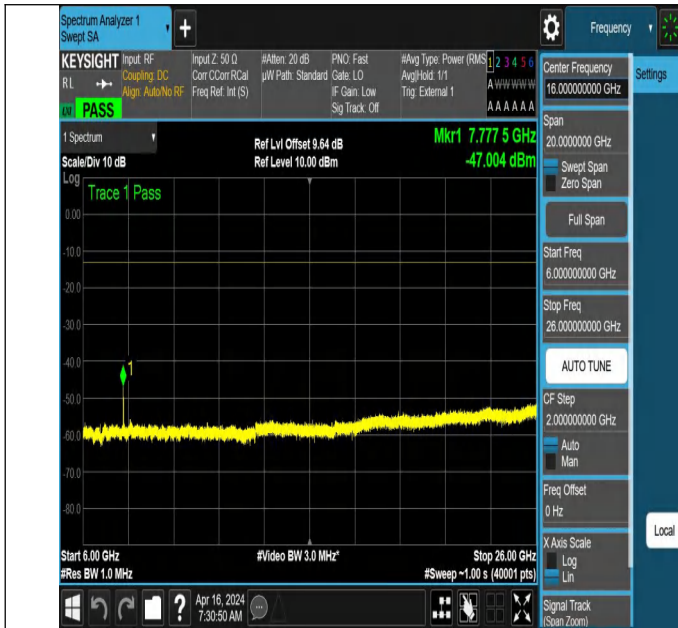
NTNV_N77-3700-3980_PC2_30_100_M_TID3_NS_01_30_1000



NTNV_N77-3700-3980_PC2_30_100_M_TID3_NS_01_1000_30
00 #1



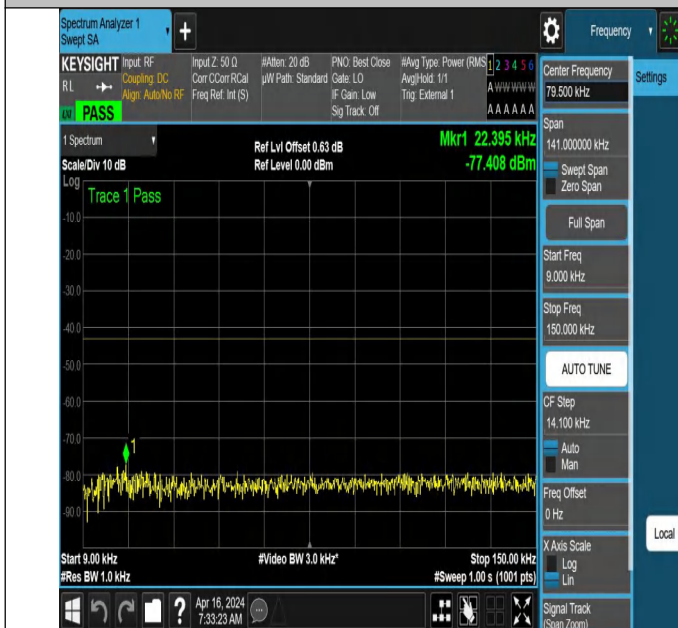
NTNV_N77-3700-3980_PC2_30_100_M_TID3_NS_01_3000_60
00 #1



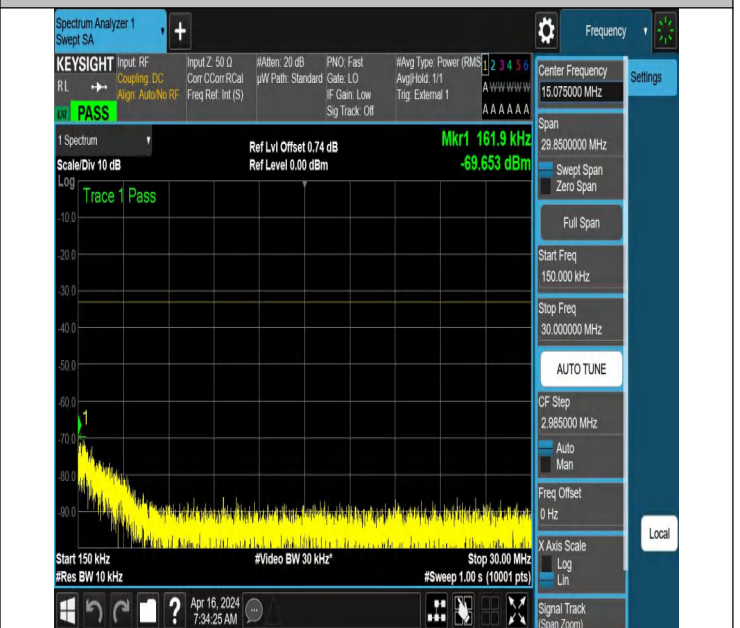
NTNV_N77-3700-3980_PC2_30_100_M_TID3_NS_01_6000_26
000_#1



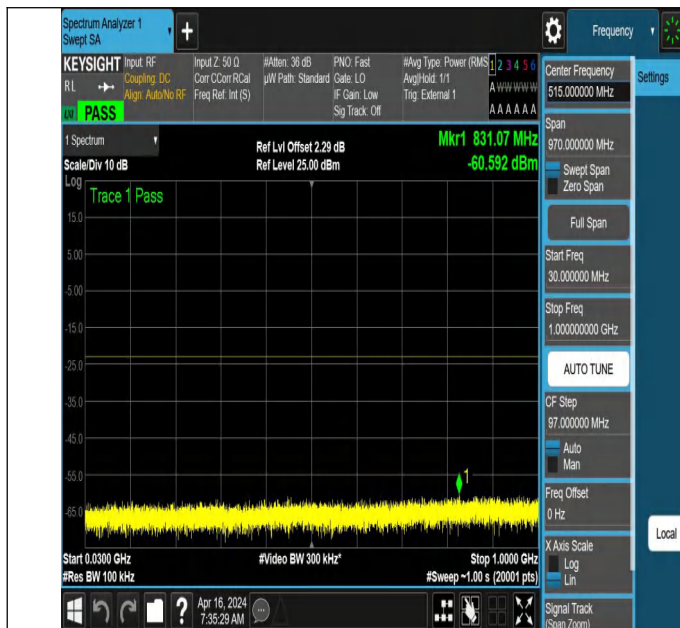
NTNV_N77-3700-3980_PC2_30_100_M_TID3_NS_01_26000_4
0000_#1



NTNV_N77-3700-3980_PC2_30_100_M_TID5_NS_01_0.009_0.
15_#1



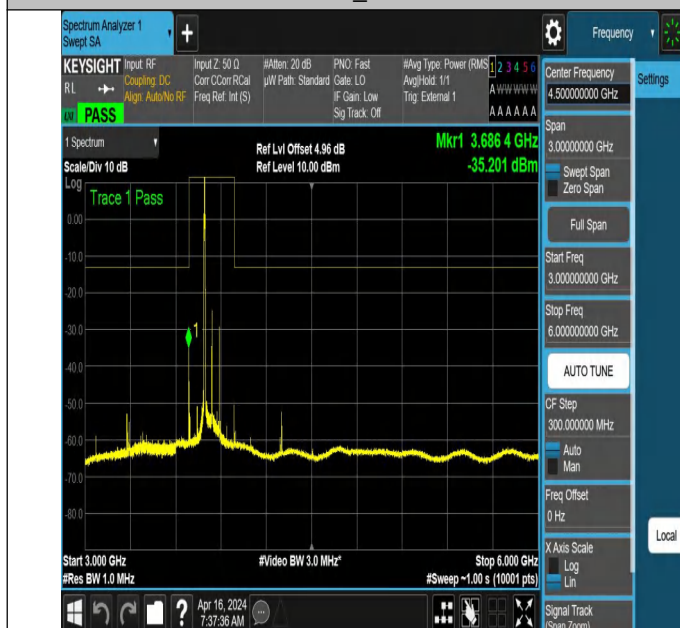
NTNV_N77-3700-3980_PC2_30_100_M_TID5_NS_01_0.15_30_
#1



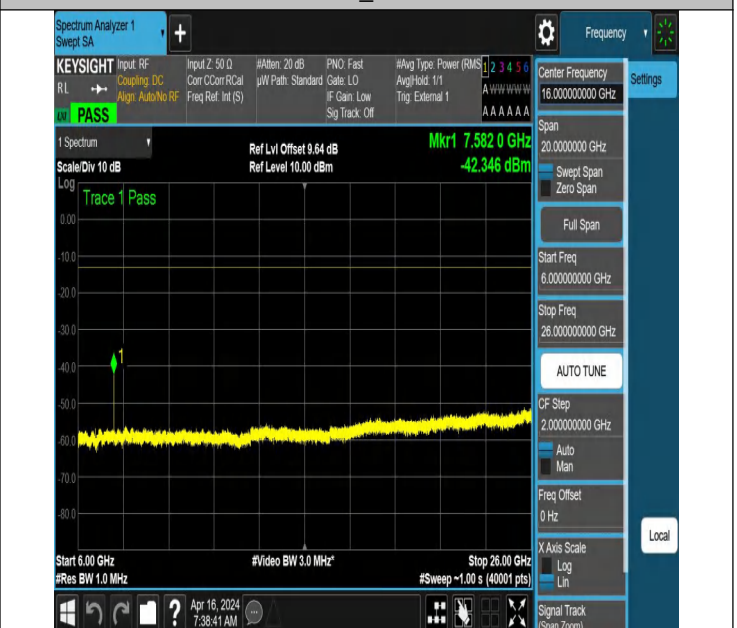
NTNV_N77-3700-3980_PC2_30_100_M_TID5_NS_01_30_1000
#1



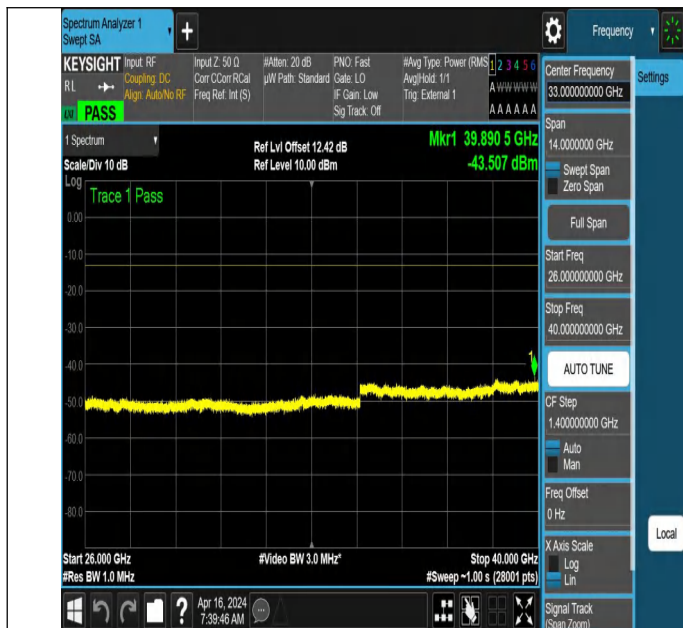
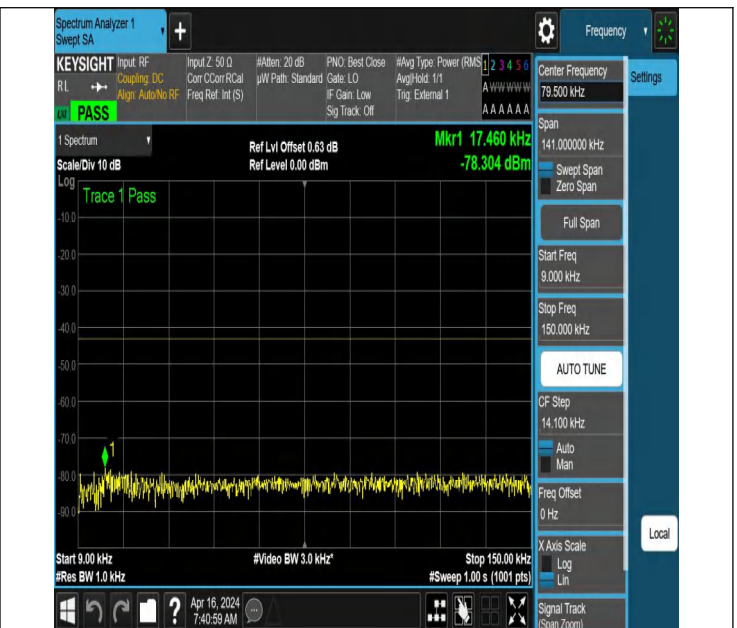
NTNV_N77-3700-3980_PC2_30_100_M_TID5_NS_01_1000_30
00 #1



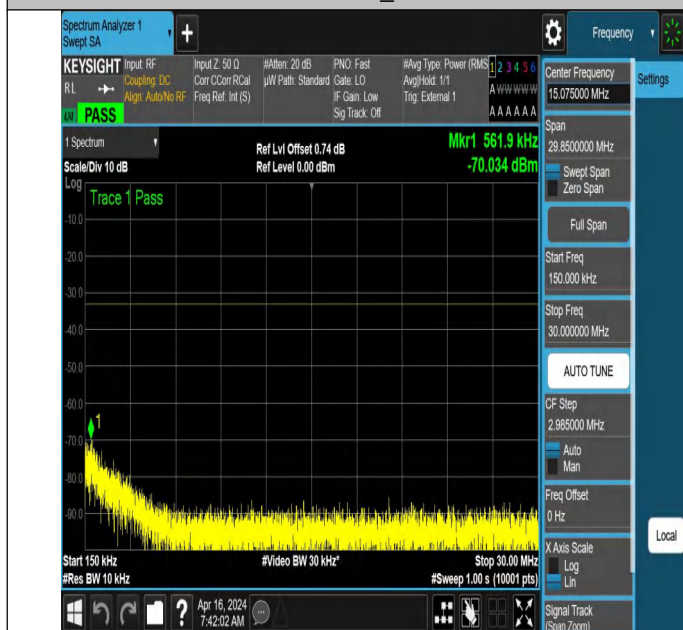
NTNV_N77-3700-3980_PC2_30_100_M_TID5_NS_01_3000_60
00 #1



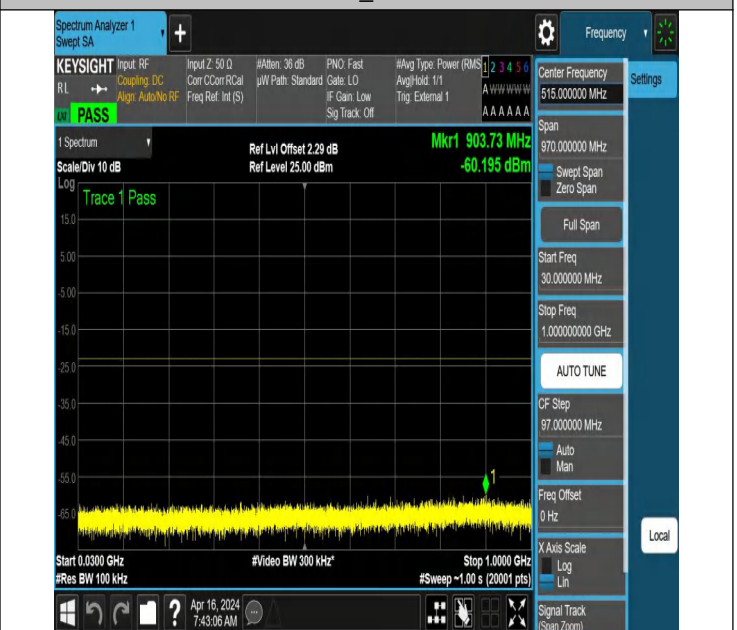
NTNV_N77-3700-3980_PC2_30_100_M_TID5_NS_01_6000_26
000 #1

NTNV_N77-3700-3980_PC2_30_100_M_TID5_NS_01_26000_4
0000_#1

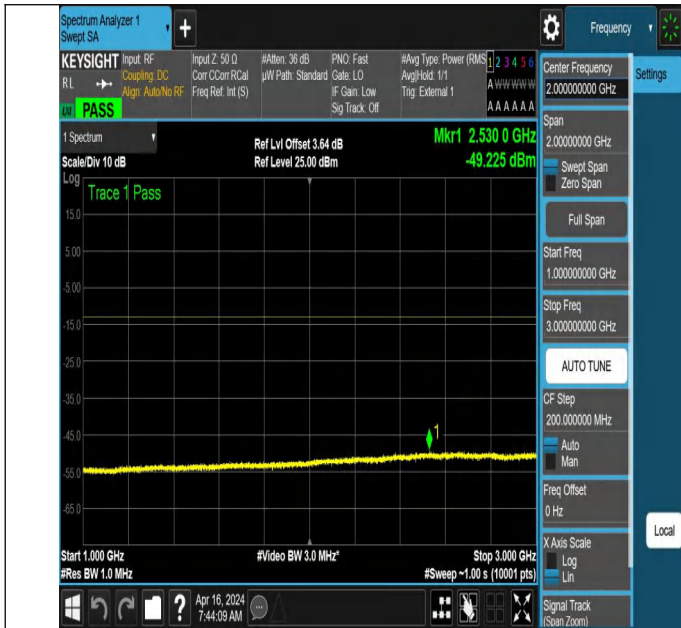
NTNV_N77-3700-3980_PC2_30_100_M_TID6_NS_01_0.009_0.
15 #1



NTNV_N77-3700-3980_PC2_30_100_M_TID6_NS_01_0.15_30_#1



NTNV_N77-3700-3980_PC2_30_100_M_TID6_NS_01_30_1000



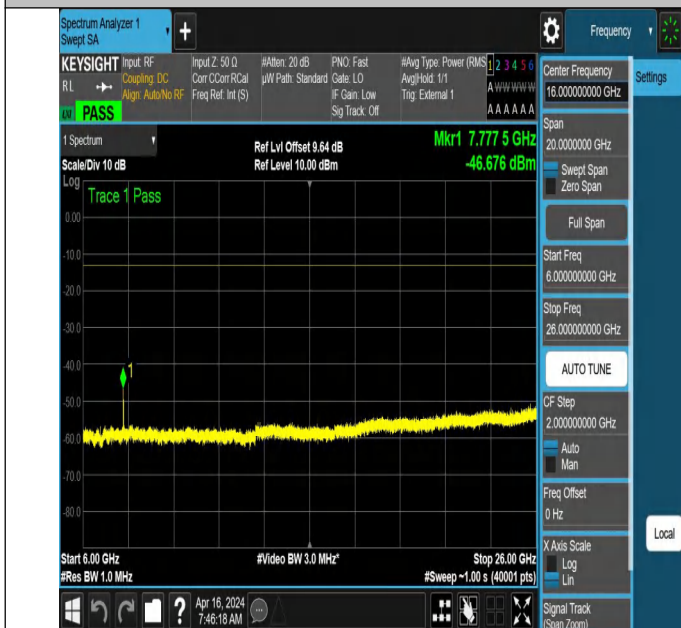
NTNV_N77-3700-3980_PC2_30_100_M_TID6_NS_01_1000_30

00_#1



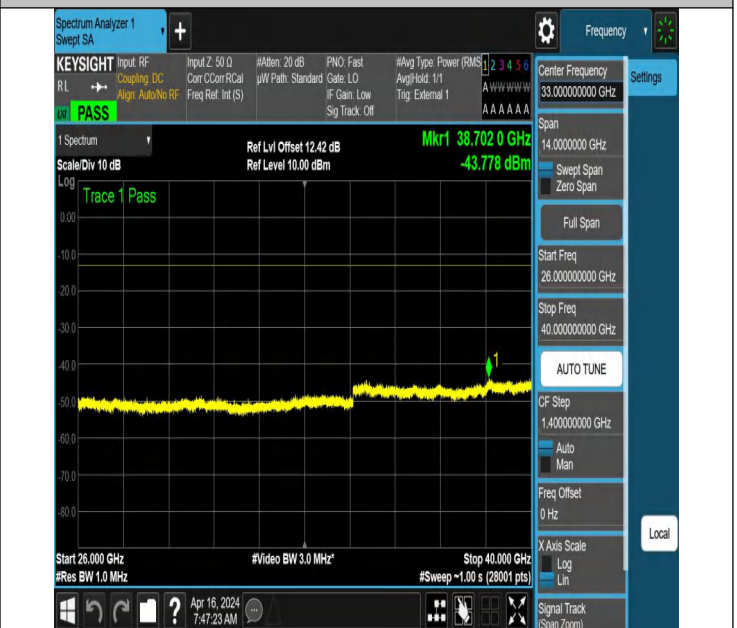
NTNV_N77-3700-3980_PC2_30_100_M_TID6_NS_01_3000_60

00_#1



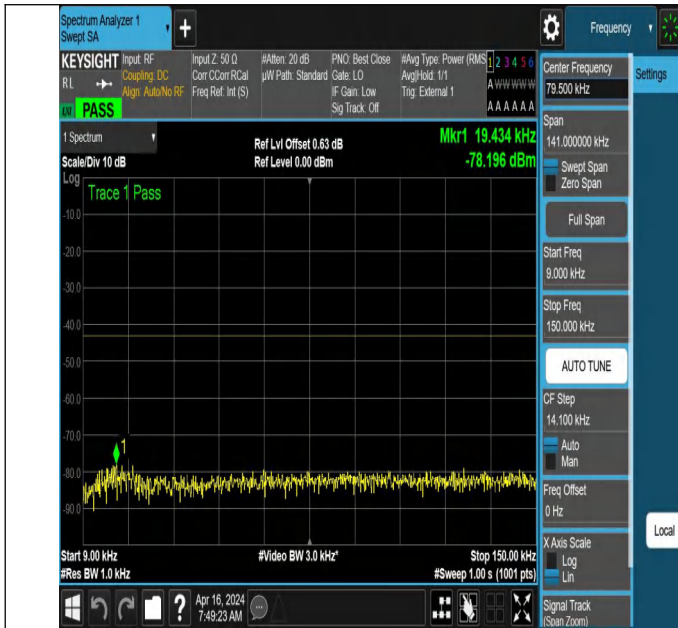
NTNV_N77-3700-3980_PC2_30_100_M_TID6_NS_01_6000_26

000_#1



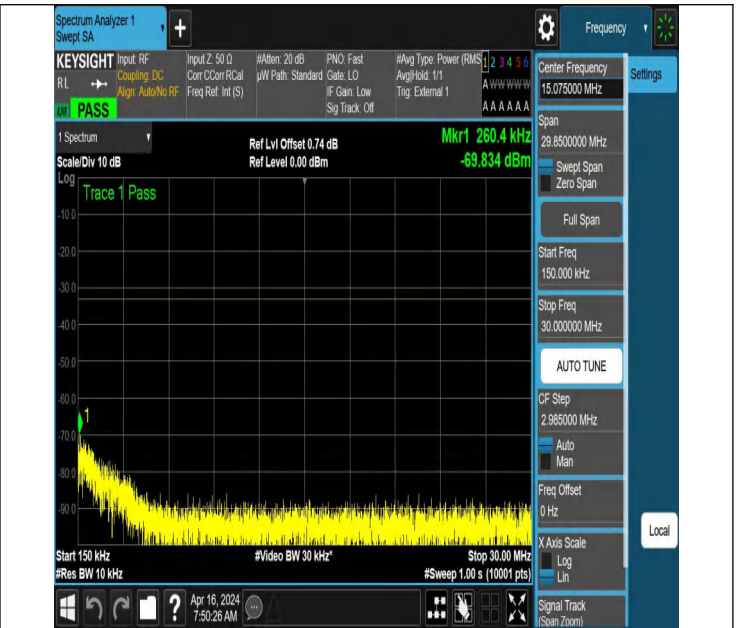
NTNV_N77-3700-3980_PC2_30_100_M_TID6_NS_01_26000_4

0000_#1



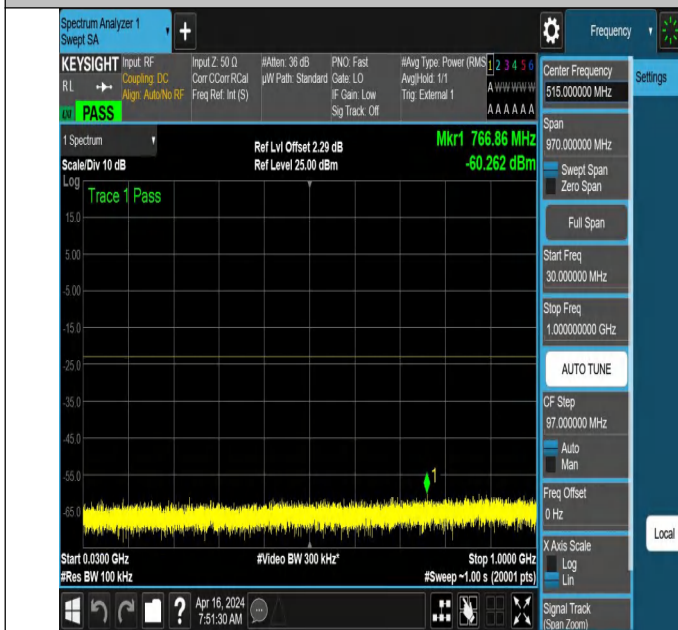
NTNV_N77-3700-3980_PC2_30_100_H_TID2_NS_01_0.009_0.1

5_#1



NTNV_N77-3700-3980_PC2_30_100_H_TID2_NS_01_0.15_30

#1



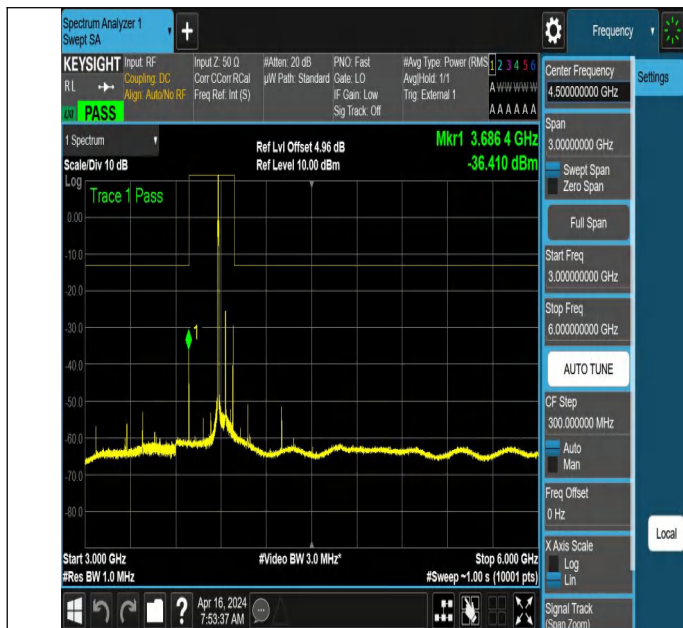
NTNV_N77-3700-3980_PC2_30_100_H_TID2_NS_01_30_1000

#1

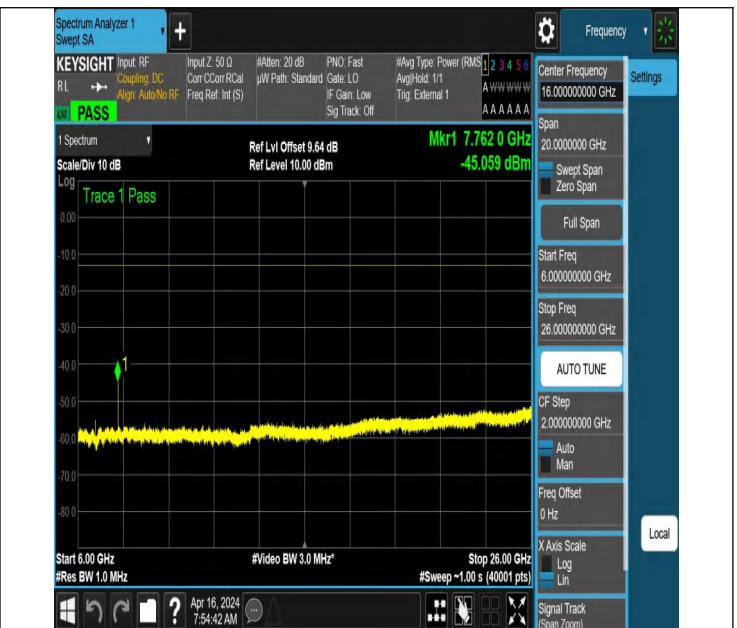


NTNV_N77-3700-3980_PC2_30_100_H_TID2_NS_01_1000_300

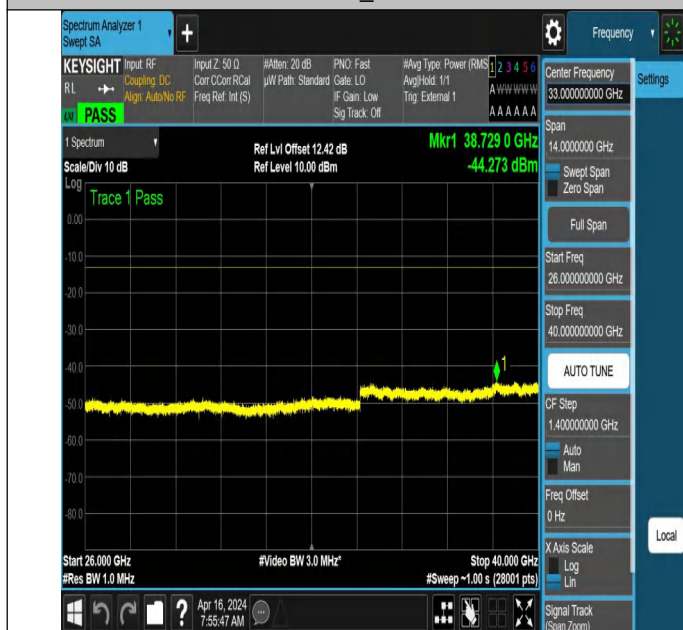
0_#1



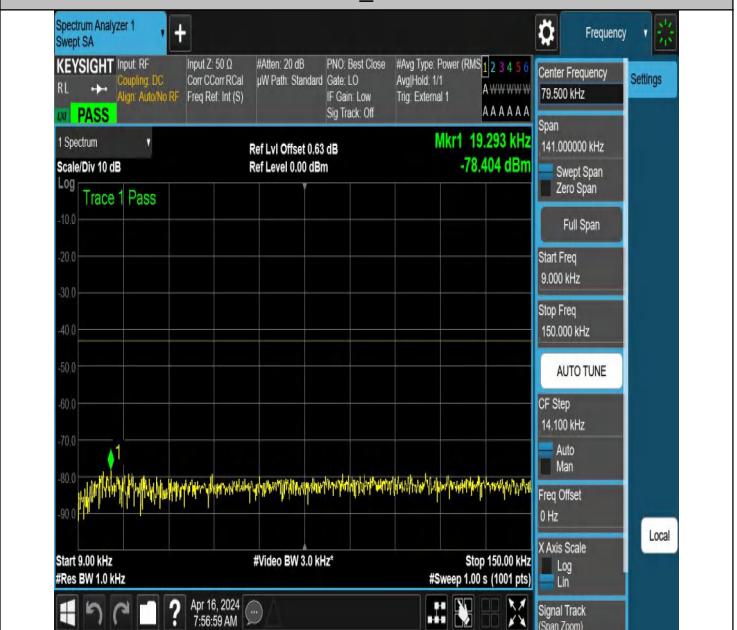
NTNV_N77-3700-3980_PC2_30_100_H_TID2_NS_01_3000_600



NTNV_N77-3700-3980_PC2_30_100_H_TID2_NS_01_6000_260
00 #1

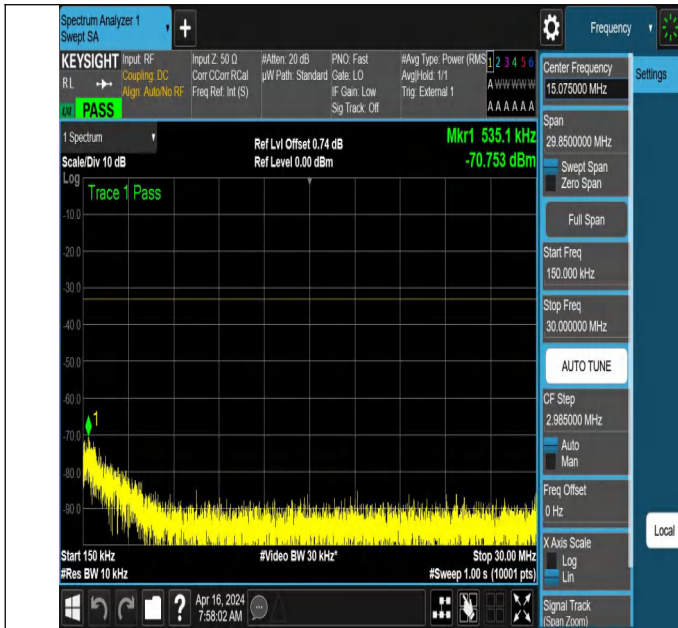


NTNV_N77-3700-3980_PC2_30_100_H_TID2_NS_01_26000_40
000 #1



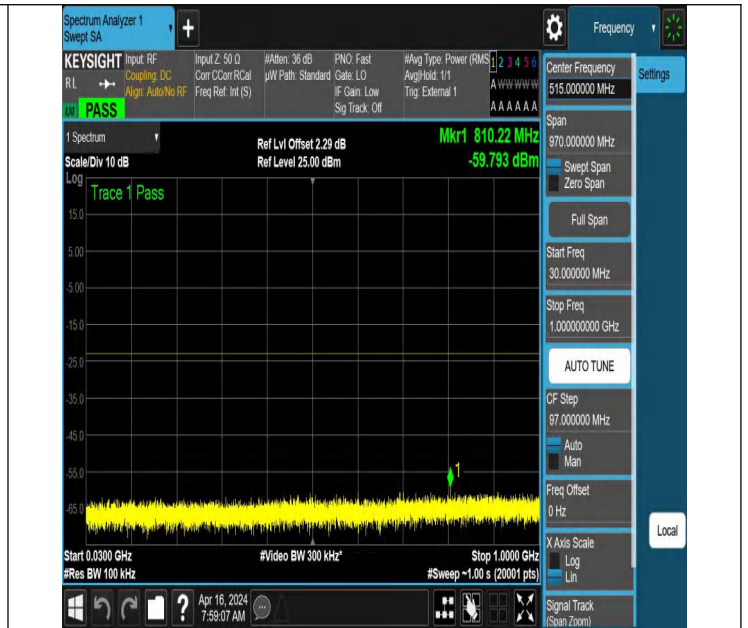
NTNV_N77-3700-3980_PC2_30_100_H_TID3_NS_01_0.009_0.1

5 #1



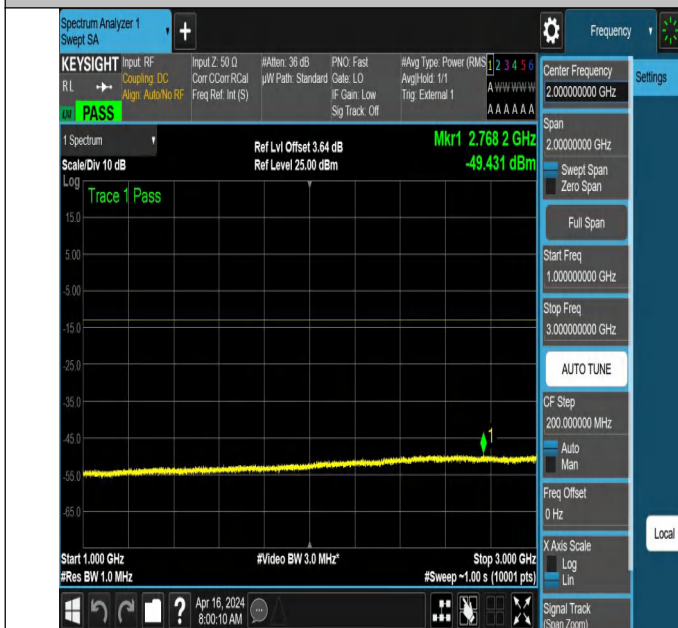
NTNV_N77-3700-3980_PC2_30_100_H_TID3_NS_01_0.15_30_

#1



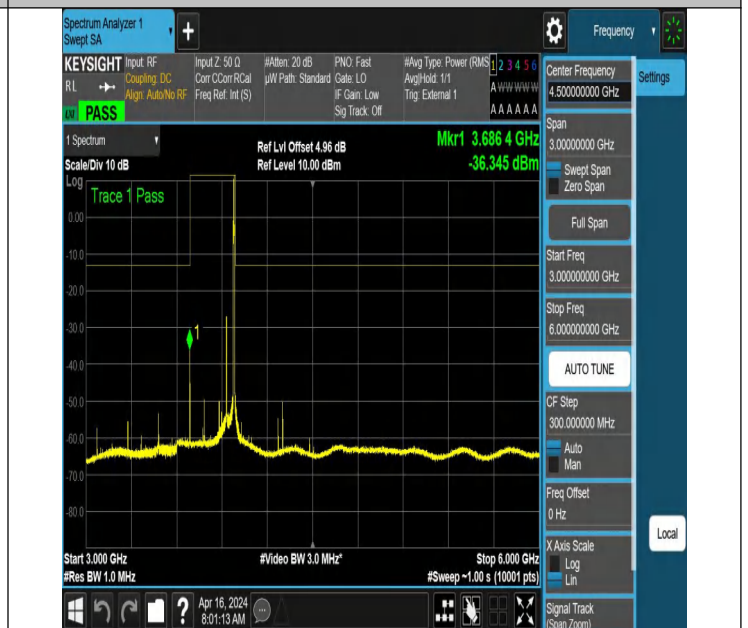
NTNV_N77-3700-3980_PC2_30_100_H_TID3_NS_01_30_1000_

#1



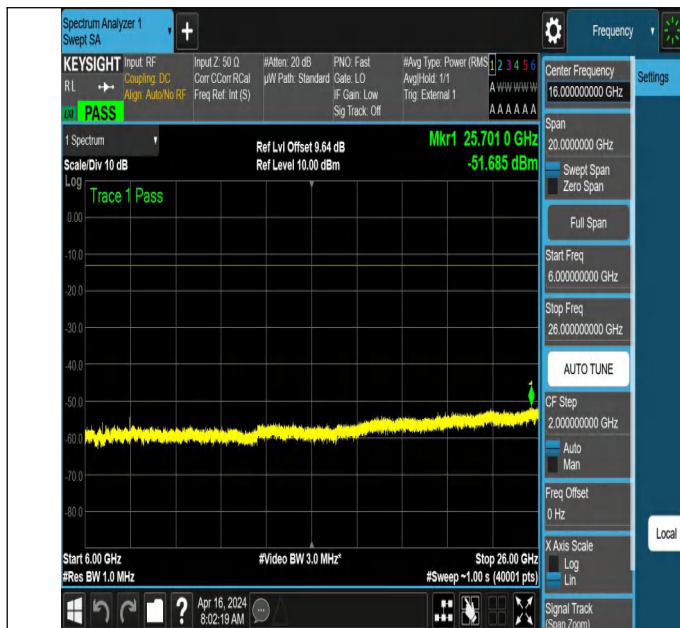
NTNV_N77-3700-3980_PC2_30_100_H_TID3_NS_01_1000_300

0_#1

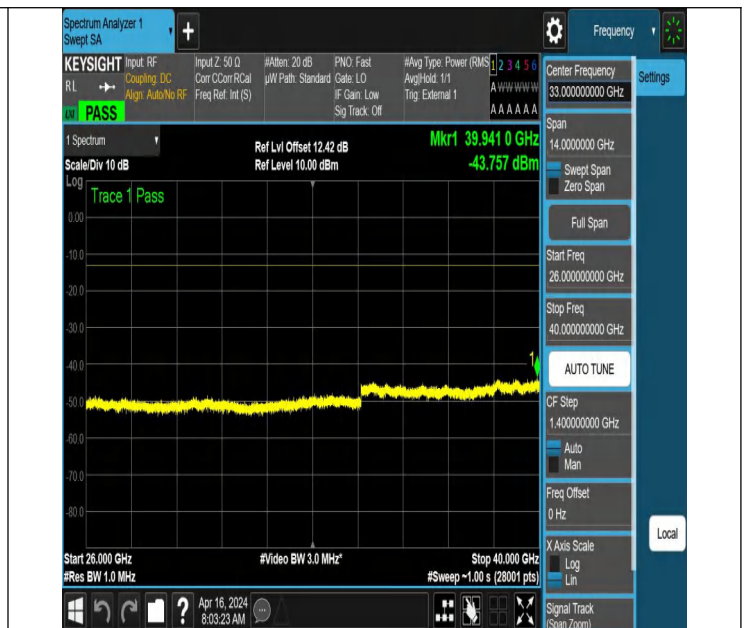


NTNV_N77-3700-3980_PC2_30_100_H_TID3_NS_01_3000_600

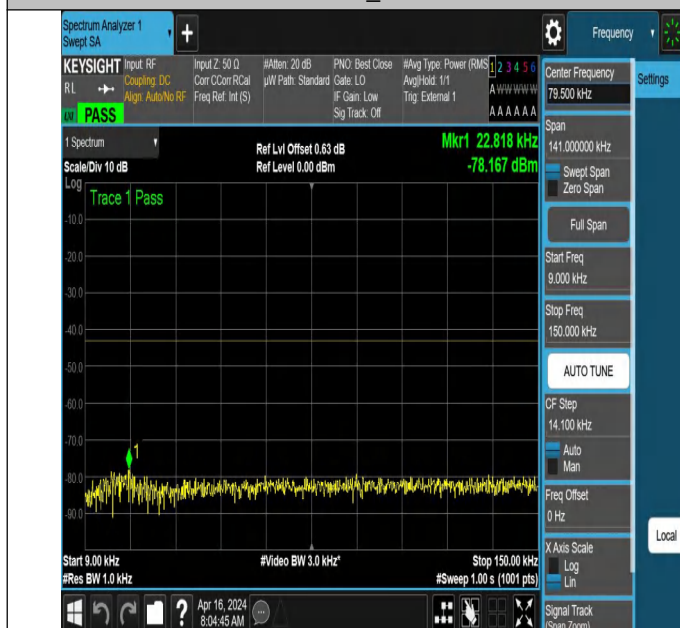
0_#1



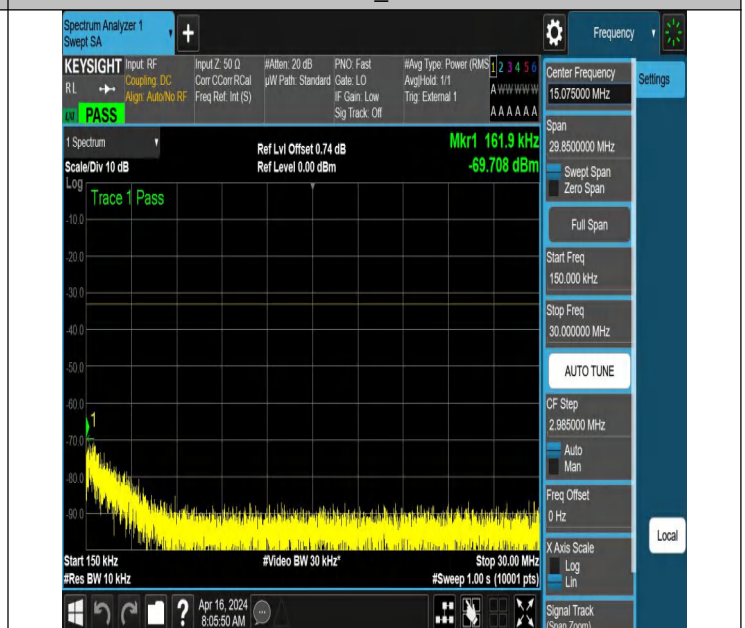
NTNV_N77-3700-3980_PC2_30_100_H_TID3_NS_01_6000_260



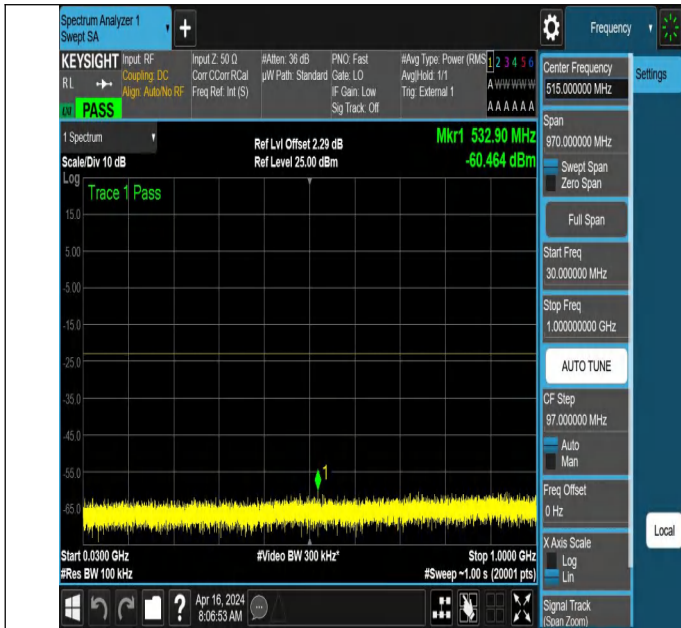
NTNV_N77-3700-3980_PC2_30_100_H_TID3_NS_01_26000_40
000 #1



NTNV_N77-3700-3980_PC2_30_100_H_TID5_NS_01_0.009_0.1

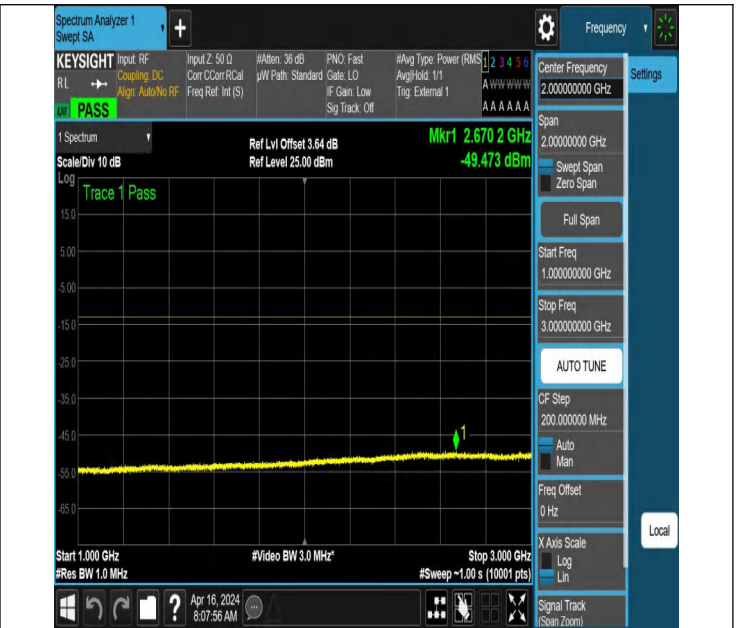


NTNV_N77-3700-3980_PC2_30_100_H_TID5_NS_01_0.15_30_#1



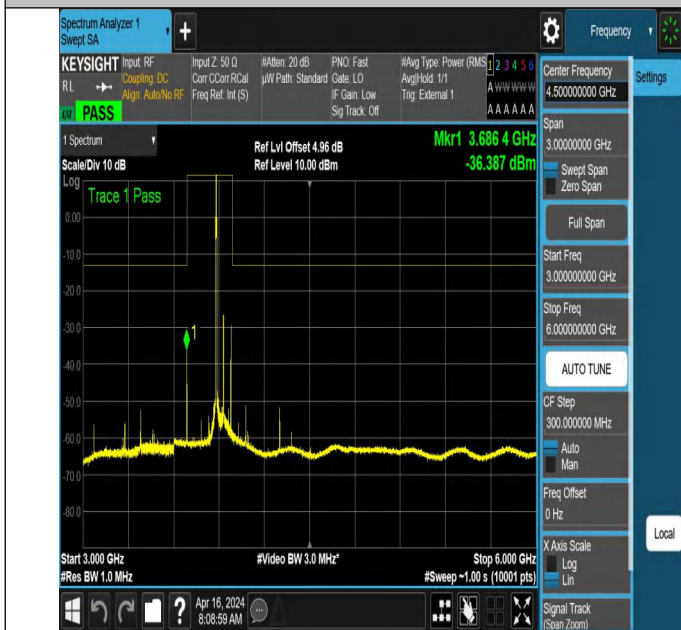
NTNV_N77-3700-3980_PC2_30_100_H_TID5_NS_01_30_1000_

#1



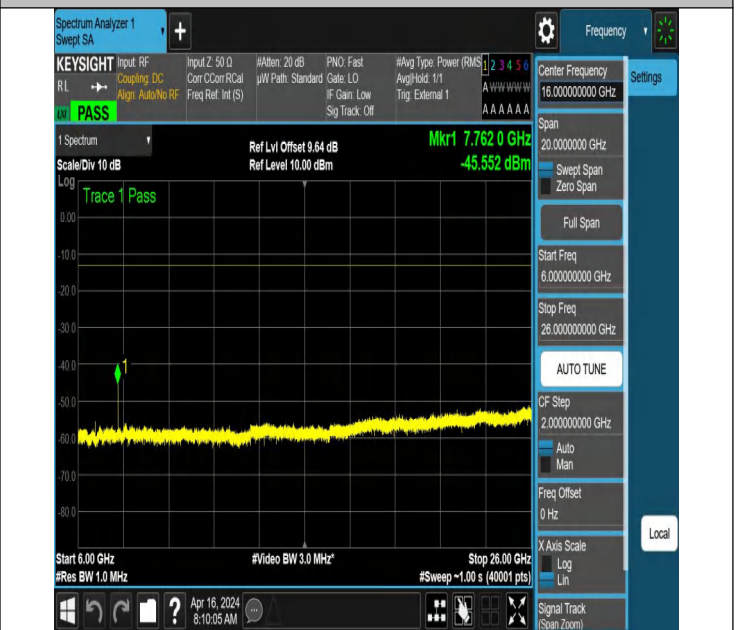
NTNV_N77-3700-3980_PC2_30_100_H_TID5_NS_01_1000_300

0_#1



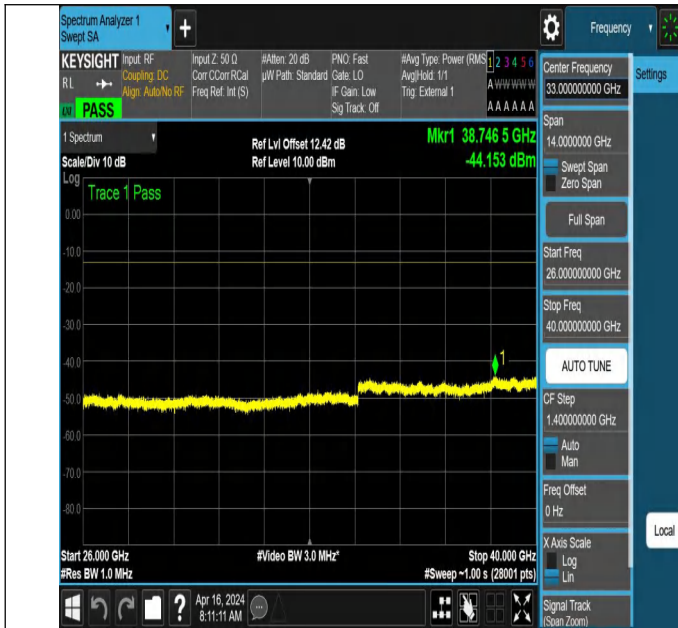
NTNV_N77-3700-3980_PC2_30_100_H_TID5_NS_01_3000_600

0_#1

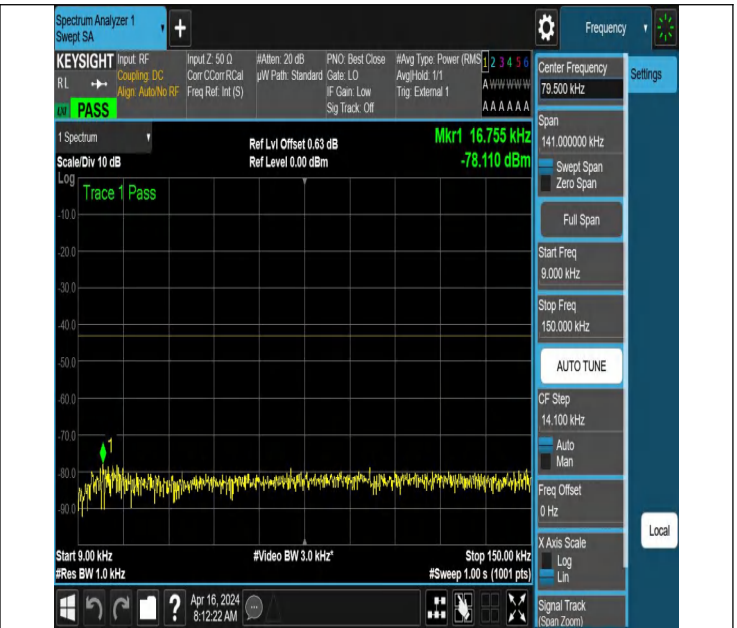


NTNV_N77-3700-3980_PC2_30_100_H_TID5_NS_01_6000_260

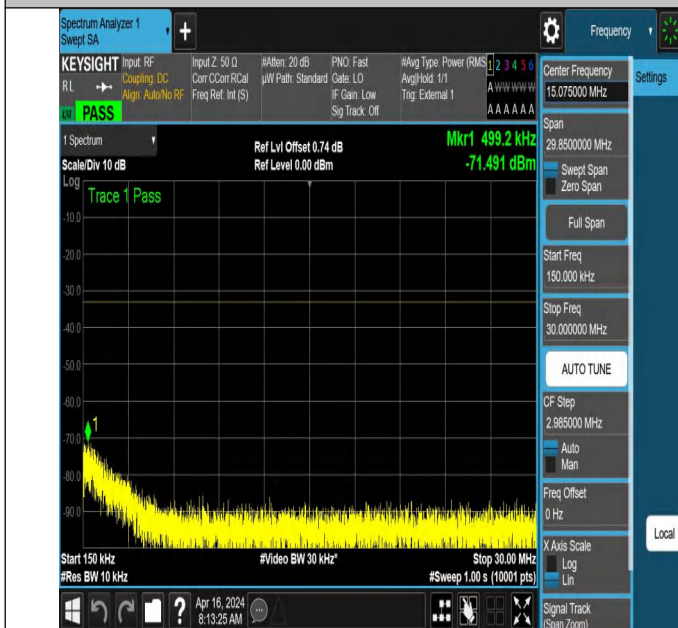
00_#1



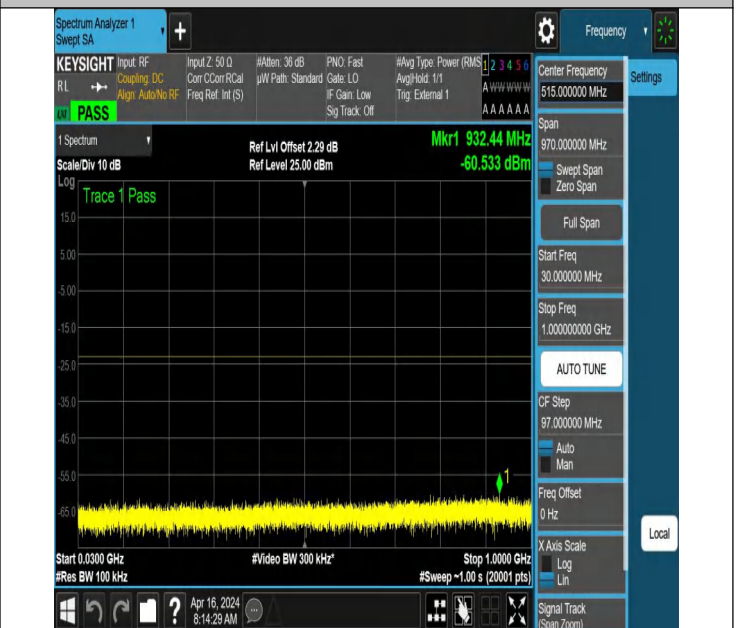
NTNV_N77-3700-3980_PC2_30_100_H_TID5_NS_01_26000_40000_#1



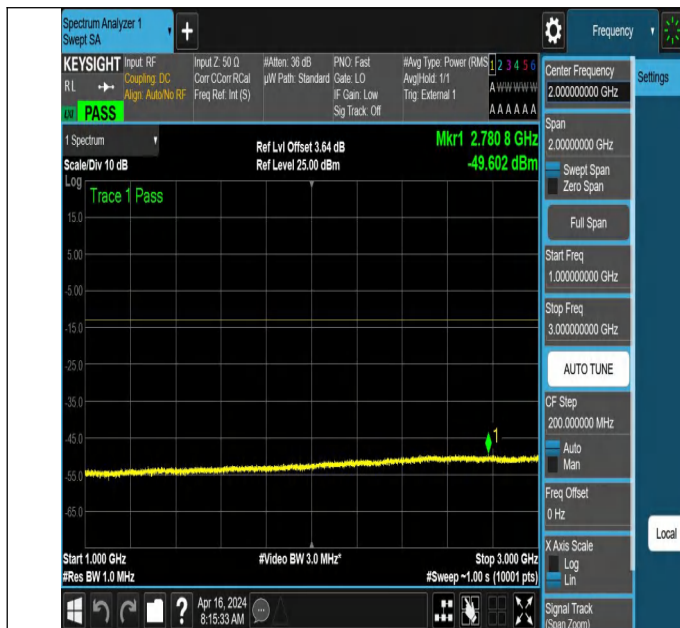
NTNV_N77-3700-3980_PC2_30_100_H_TID6_NS_01_0.009_0.15_#1



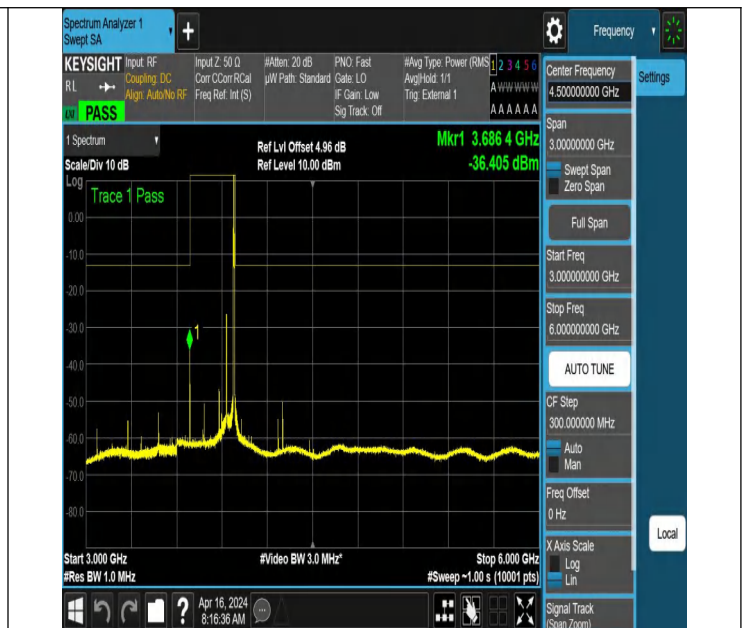
NTNV_N77-3700-3980_PC2_30_100_H_TID6_NS_01_0.15_30_#1



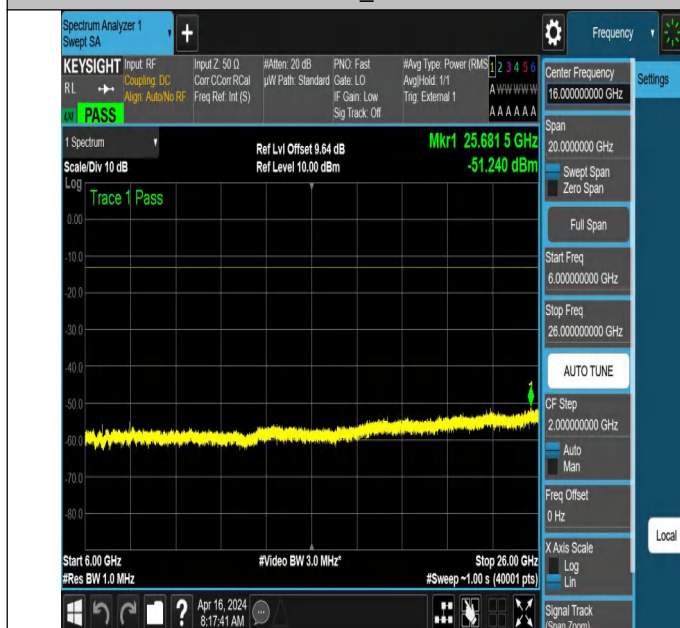
NTNV_N77-3700-3980_PC2_30_100_H_TID6_NS_01_30_1000_#1



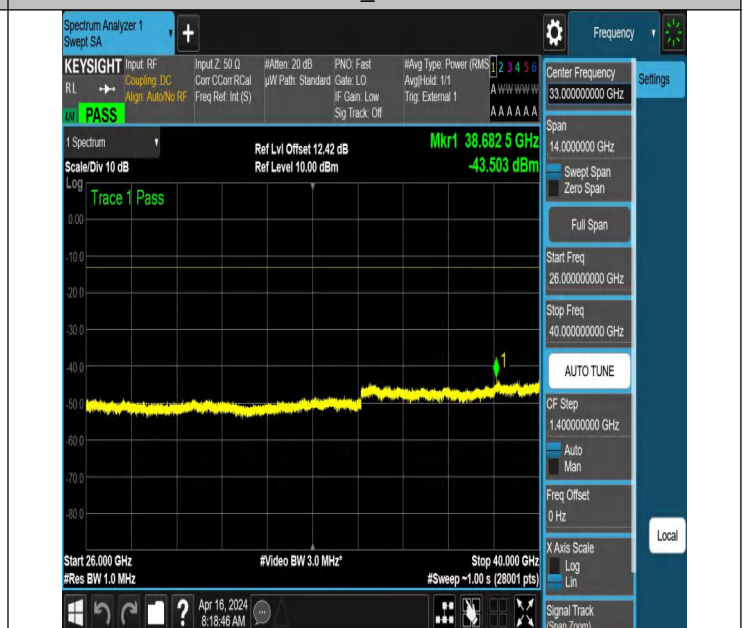
NTNV_N77-3700-3980_PC2_30_100_H_TID6_NS_01_1000_300



NTNV_N77-3700-3980_PC2_30_100_H_TID6_NS_01_3000_600



NTNV_N77-3700-3980_PC2_30_100_H_TID6_NS_01_6000_260



NTNV_N77-3700-3980_PC2_30_100_H_TID6_NS_01_26000_40
000 #1

Frequency Stability for SA

Test Result

Frequency Error VS. Voltage											
Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Voltage	Temperature	Deviation(Hz)	Deviation (ppm)	Limit (ppm)	Verdict
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	VL	NT	-17.940000	-0.004672	±2.5	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	VN	NT	-15.460000	-0.004026	±2.5	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	VH	NT	-9.220000	-0.002401	±2.5	PASS

Frequency Error VS. Temperature											
Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Voltage	Temperature	Deviation(Hz)	Deviation (ppm)	Limit (ppm)	Verdict
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	NV	-30	-3.230000	-0.000841	±2.5	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	NV	-20	-15.560000	-0.004052	±2.5	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	NV	-10	-14.100000	-0.003672	±2.5	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	NV	0	-8.200000	-0.002135	±2.5	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	NV	10	-18.350000	-0.004779	±2.5	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	NV	20	-13.880000	-0.003615	±2.5	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	NV	30	-21.580000	-0.005620	±2.5	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	NV	40	-20.680000	-0.005385	±2.5	PASS
N77-3700-3980	30	100	DFT-QPSK	M	Outer_Full	NV	50	-17.850000	-0.004648	±2.5	PASS

Other PA with Ant MAIN for NSA mode

Effective (Isotropic) Radiated Power Output Data for NSA

Test Result

Band	SC S	Bandwidth h	Modulation	Channel I	RB Config	Power (dBm)	EIRP (dBm)	Limit	Verdict
DC_26A_n77A-3700-3980	30	5+20	DFT-PI2BPSK	M+L	Outer_Full	24.99	27.58	30	PASS
DC_26A_n77A-3700-3980	30	5+20	DFT-PI2BPSK	M+L	Inner_Full	25.35	27.94	30	PASS
DC_26A_n77A-3700-3980	30	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.7	25.29	30	PASS
DC_26A_n77A-3700-3980	30	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Right	22.59	25.18	30	PASS
DC_26A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	24.5	27.09	30	PASS
DC_26A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Inner_Full	25.38	27.97	30	PASS
DC_26A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	22.61	25.2	30	PASS
DC_26A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	22.54	25.13	30	PASS
DC_26A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	23.6	26.19	30	PASS
DC_26A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Inner_Full	24.53	27.12	30	PASS
DC_26A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	21.43	24.02	30	PASS
DC_26A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	21.54	24.13	30	PASS
DC_26A_n77A-3700-3980	30	5+20	DFT-64QAM	M+L	Outer_Full	23.01	25.6	30	PASS
DC_26A_n77A-3700-3980	30	5+20	DFT-64QAM	M+L	Inner_Full	23.03	25.62	30	PASS
DC_26A_n77A-3700-3980	30	5+20	DFT-64QAM	M+L	Edge_1RB_Left	21.06	23.65	30	PASS

DC_26A_n77A-3700-398 0	30	5+20	DFT-64QAM	M+L	Edge_1RB_Righ t	20.98	23.57	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-256QAM	M+L	Outer_Full	21.05	23.64	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-256QAM	M+L	Inner_Full	21.06	23.65	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-256QAM	M+L	Edge_1RB_Left	21.13	23.72	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-256QAM	M+L	Edge_1RB_Righ t	20.93	23.52	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-PI2BPS K	M+M	Outer_Full	25.36	27.95	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-PI2BPS K	M+M	Inner_Full	25.71	28.3	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-PI2BPS K	M+M	Edge_1RB_Left	23.23	25.82	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-PI2BPS K	M+M	Edge_1RB_Righ t	23.43	26.02	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-QPSK	M+M	Outer_Full	24.86	27.45	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-QPSK	M+M	Inner_Full	25.78	28.37	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	22.74	25.33	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-QPSK	M+M	Edge_1RB_Righ t	22.92	25.51	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-16QAM	M+M	Outer_Full	23.91	26.5	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-16QAM	M+M	Inner_Full	24.84	27.43	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	21.69	24.28	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-16QAM	M+M	Edge_1RB_Righ t	21.84	24.43	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-64QAM	M+M	Outer_Full	23.45	26.04	30	PASS

DC_26A_n77A-3700-398 0	30	5+20	DFT-64QAM	M+M	Inner_Full	23.42	26.01	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-64QAM	M+M	Edge_1RB_Left	21.26	23.85	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-64QAM	M+M	Edge_1RB_Right	21.34	23.93	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-256QAM	M+M	Outer_Full	21.33	23.92	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-256QAM	M+M	Inner_Full	21.37	23.96	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-256QAM	M+M	Edge_1RB_Left	21.27	23.86	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-256QAM	M+M	Edge_1RB_Right	21.52	24.11	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-PI2BPS K	M+H	Outer_Full	24.23	26.82	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-PI2BPS K	M+H	Inner_Full	25.4	27.99	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-PI2BPS K	M+H	Edge_1RB_Left	23.2	25.79	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-PI2BPS K	M+H	Edge_1RB_Right	23.15	25.74	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-QPSK	M+H	Outer_Full	23.69	26.28	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-QPSK	M+H	Inner_Full	25.52	28.11	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	22.69	25.28	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	22.73	25.32	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-16QAM	M+H	Outer_Full	23.72	26.31	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-16QAM	M+H	Inner_Full	24.5	27.09	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	21.7	24.29	30	PASS

DC_26A_n77A-3700-398 0	30	5+20	DFT-16QAM	M+H	Edge_1RB_Righ t	21.54	24.13	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-64QAM	M+H	Outer_Full	23.24	25.83	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-64QAM	M+H	Inner_Full	23.25	25.84	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-64QAM	M+H	Edge_1RB_Left	21.05	23.64	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-64QAM	M+H	Edge_1RB_Righ t	20.96	23.55	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-256QAM	M+H	Outer_Full	21.18	23.77	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-256QAM	M+H	Inner_Full	21.08	23.67	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-256QAM	M+H	Edge_1RB_Left	21.3	23.89	30	PASS
DC_26A_n77A-3700-398 0	30	5+20	DFT-256QAM	M+H	Edge_1RB_Righ t	21.17	23.76	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-PI2BPS K	M+L	Outer_Full	25.14	27.73	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-PI2BPS K	M+L	Inner_Full	25.49	28.08	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-PI2BPS K	M+L	Edge_1RB_Left	22.54	25.13	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-PI2BPS K	M+L	Edge_1RB_Righ t	22.68	25.27	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-QPSK	M+L	Outer_Full	24.64	27.23	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-QPSK	M+L	Inner_Full	25.42	28.01	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	22.57	25.16	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-QPSK	M+L	Edge_1RB_Righ t	22.66	25.25	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-16QAM	M+L	Outer_Full	23.59	26.18	30	PASS

DC_26A_n77A-3700-398 0	30	5+30	DFT-16QAM	M+L	Inner_Full	24.56	27.15	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	21.47	24.06	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	21.57	24.16	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-64QAM	M+L	Outer_Full	23.16	25.75	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-64QAM	M+L	Inner_Full	23.11	25.7	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-64QAM	M+L	Edge_1RB_Left	21.12	23.71	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-64QAM	M+L	Edge_1RB_Right	21.17	23.76	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-256QAM	M+L	Outer_Full	21.14	23.73	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-256QAM	M+L	Inner_Full	21.18	23.77	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-256QAM	M+L	Edge_1RB_Left	21.15	23.74	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-256QAM	M+L	Edge_1RB_Right	21.18	23.77	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-PI2BPS K	M+M	Outer_Full	25.25	27.84	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-PI2BPS K	M+M	Inner_Full	25.72	28.31	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-PI2BPS K	M+M	Edge_1RB_Left	23.23	25.82	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-PI2BPS K	M+M	Edge_1RB_Right	23.45	26.04	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-QPSK	M+M	Outer_Full	24.8	27.39	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-QPSK	M+M	Inner_Full	25.67	28.26	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	22.67	25.26	30	PASS

DC_26A_n77A-3700-398 0	30	5+30	DFT-QPSK	M+M	Edge_1RB_Righ t	22.91	25.5	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-16QAM	M+M	Outer_Full	23.8	26.39	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-16QAM	M+M	Inner_Full	24.76	27.35	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	21.55	24.14	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-16QAM	M+M	Edge_1RB_Righ t	21.94	24.53	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-64QAM	M+M	Outer_Full	23.34	25.93	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-64QAM	M+M	Inner_Full	23.24	25.83	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-64QAM	M+M	Edge_1RB_Left	21.18	23.77	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-64QAM	M+M	Edge_1RB_Righ t	21.43	24.02	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-256QAM	M+M	Outer_Full	21.4	23.99	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-256QAM	M+M	Inner_Full	21.33	23.92	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-256QAM	M+M	Edge_1RB_Left	21.3	23.89	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-256QAM	M+M	Edge_1RB_Righ t	21.41	24	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-PI2BPS K	M+H	Outer_Full	24.13	26.72	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-PI2BPS K	M+H	Inner_Full	25.54	28.13	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-PI2BPS K	M+H	Edge_1RB_Left	23.32	25.91	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-PI2BPS K	M+H	Edge_1RB_Righ t	23.19	25.78	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-QPSK	M+H	Outer_Full	23.66	26.25	30	PASS

DC_26A_n77A-3700-398 0	30	5+30	DFT-QPSK	M+H	Inner_Full	25.51	28.1	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	22.77	25.36	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	22.7	25.29	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-16QAM	M+H	Outer_Full	23.6	26.19	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-16QAM	M+H	Inner_Full	24.63	27.22	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	21.77	24.36	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	21.74	24.33	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-64QAM	M+H	Outer_Full	23.35	25.94	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-64QAM	M+H	Inner_Full	23.11	25.7	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-64QAM	M+H	Edge_1RB_Left	21.14	23.73	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-64QAM	M+H	Edge_1RB_Right	20.97	23.56	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-256QAM	M+H	Outer_Full	21.11	23.7	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-256QAM	M+H	Inner_Full	21.21	23.8	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-256QAM	M+H	Edge_1RB_Left	21.38	23.97	30	PASS
DC_26A_n77A-3700-398 0	30	5+30	DFT-256QAM	M+H	Edge_1RB_Right	21.21	23.8	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-PI2BPS K	M+L	Outer_Full	25.02	27.61	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-PI2BPS K	M+L	Inner_Full	25.4	27.99	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-PI2BPS K	M+L	Edge_1RB_Left	22.6	25.19	30	PASS

DC_26A_n77A-3700-398 0	30	5+40	DFT-PI2BPS K	M+L	Edge_1RB_Righ t	22.72	25.31	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-QPSK	M+L	Outer_Full	24.54	27.13	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-QPSK	M+L	Inner_Full	25.38	27.97	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	22.53	25.12	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-QPSK	M+L	Edge_1RB_Righ t	22.79	25.38	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-16QAM	M+L	Outer_Full	23.52	26.11	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-16QAM	M+L	Inner_Full	24.53	27.12	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	21.57	24.16	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-16QAM	M+L	Edge_1RB_Righ t	21.67	24.26	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-64QAM	M+L	Outer_Full	22.99	25.58	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-64QAM	M+L	Inner_Full	23.02	25.61	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-64QAM	M+L	Edge_1RB_Left	21.08	23.67	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-64QAM	M+L	Edge_1RB_Righ t	21.15	23.74	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-256QAM	M+L	Outer_Full	21.1	23.69	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-256QAM	M+L	Inner_Full	21.12	23.71	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-256QAM	M+L	Edge_1RB_Left	21.11	23.7	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-256QAM	M+L	Edge_1RB_Righ t	21.05	23.64	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-PI2BPS K	M+M	Outer_Full	25.34	27.93	30	PASS

DC_26A_n77A-3700-398 0	30	5+40	DFT-PI2BPS K	M+M	Inner_Full	25.69	28.28	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-PI2BPS K	M+M	Edge_1RB_Left	23.32	25.91	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-PI2BPS K	M+M	Edge_1RB_Righ t	23.36	25.95	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-QPSK	M+M	Outer_Full	24.8	27.39	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-QPSK	M+M	Inner_Full	25.68	28.27	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	22.79	25.38	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-QPSK	M+M	Edge_1RB_Righ t	22.94	25.53	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-16QAM	M+M	Outer_Full	23.87	26.46	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-16QAM	M+M	Inner_Full	24.82	27.41	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	21.71	24.3	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-16QAM	M+M	Edge_1RB_Righ t	21.84	24.43	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-64QAM	M+M	Outer_Full	23.36	25.95	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-64QAM	M+M	Inner_Full	23.27	25.86	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-64QAM	M+M	Edge_1RB_Left	21.24	23.83	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-64QAM	M+M	Edge_1RB_Righ t	21.38	23.97	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-256QAM	M+M	Outer_Full	21.41	24	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-256QAM	M+M	Inner_Full	21.33	23.92	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-256QAM	M+M	Edge_1RB_Left	21.34	23.93	30	PASS

DC_26A_n77A-3700-398 0	30	5+40	DFT-256QAM	M+M	Edge_1RB_Righ t	21.5	24.09	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-PI2BPS K	M+H	Outer_Full	25.12	27.71	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-PI2BPS K	M+H	Inner_Full	25.54	28.13	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-PI2BPS K	M+H	Edge_1RB_Left	22.22	24.81	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-PI2BPS K	M+H	Edge_1RB_Righ t	22.12	24.71	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-QPSK	M+H	Outer_Full	24.63	27.22	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-QPSK	M+H	Inner_Full	25.52	28.11	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	22.15	24.74	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-QPSK	M+H	Edge_1RB_Righ t	22.12	24.71	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-16QAM	M+H	Outer_Full	23.6	26.19	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-16QAM	M+H	Inner_Full	24.68	27.27	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	22.18	24.77	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-16QAM	M+H	Edge_1RB_Righ t	22.08	24.67	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-64QAM	M+H	Outer_Full	23.22	25.81	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-64QAM	M+H	Inner_Full	23.18	25.77	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-64QAM	M+H	Edge_1RB_Left	22.19	24.78	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-64QAM	M+H	Edge_1RB_Righ t	22.21	24.8	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-256QAM	M+H	Outer_Full	21.18	23.77	30	PASS

DC_26A_n77A-3700-398 0	30	5+40	DFT-256QAM	M+H	Inner_Full	21.22	23.81	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-256QAM	M+H	Edge_1RB_Left	21.35	23.94	30	PASS
DC_26A_n77A-3700-398 0	30	5+40	DFT-256QAM	M+H	Edge_1RB_Right	21.15	23.74	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-PI2BPS K	M+L	Outer_Full	24.96	27.55	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-PI2BPS K	M+L	Inner_Full	25.28	27.87	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-PI2BPS K	M+L	Edge_1RB_Left	22.38	24.97	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-PI2BPS K	M+L	Edge_1RB_Right	22.45	25.04	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-QPSK	M+L	Outer_Full	24.35	26.94	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-QPSK	M+L	Inner_Full	25.24	27.83	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	22.36	24.95	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	22.42	25.01	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-16QAM	M+L	Outer_Full	23.37	25.96	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-16QAM	M+L	Inner_Full	24.36	26.95	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	21.37	23.96	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	21.33	23.92	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-64QAM	M+L	Outer_Full	22.96	25.55	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-64QAM	M+L	Inner_Full	22.91	25.5	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-64QAM	M+L	Edge_1RB_Left	20.92	23.51	30	PASS

DC_26A_n77A-3700-398 0	30	5+50	DFT-64QAM	M+L	Edge_1RB_Righ t	20.94	23.53	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-256QAM	M+L	Outer_Full	21	23.59	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-256QAM	M+L	Inner_Full	20.88	23.47	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-256QAM	M+L	Edge_1RB_Left	20.99	23.58	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-256QAM	M+L	Edge_1RB_Righ t	20.93	23.52	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-PI2BPS K	M+M	Outer_Full	25.29	27.88	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-PI2BPS K	M+M	Inner_Full	25.59	28.18	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-PI2BPS K	M+M	Edge_1RB_Left	23.08	25.67	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-PI2BPS K	M+M	Edge_1RB_Righ t	23.37	25.96	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-QPSK	M+M	Outer_Full	24.67	27.26	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-QPSK	M+M	Inner_Full	25.62	28.21	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	22.53	25.12	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-QPSK	M+M	Edge_1RB_Righ t	22.88	25.47	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-16QAM	M+M	Outer_Full	23.78	26.37	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-16QAM	M+M	Inner_Full	24.7	27.29	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	21.43	24.02	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-16QAM	M+M	Edge_1RB_Righ t	21.73	24.32	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-64QAM	M+M	Outer_Full	23.19	25.78	30	PASS

DC_26A_n77A-3700-398 0	30	5+50	DFT-64QAM	M+M	Inner_Full	23.24	25.83	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-64QAM	M+M	Edge_1RB_Left	21.06	23.65	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-64QAM	M+M	Edge_1RB_Right	21.35	23.94	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-256QAM	M+M	Outer_Full	21.3	23.89	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-256QAM	M+M	Inner_Full	21.2	23.79	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-256QAM	M+M	Edge_1RB_Left	20.99	23.58	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-256QAM	M+M	Edge_1RB_Right	21.52	24.11	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-PI2BPS K	M+H	Outer_Full	25.17	27.76	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-PI2BPS K	M+H	Inner_Full	25.59	28.18	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-PI2BPS K	M+H	Edge_1RB_Left	22.21	24.8	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-PI2BPS K	M+H	Edge_1RB_Right	22.16	24.75	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-QPSK	M+H	Outer_Full	24.73	27.32	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-QPSK	M+H	Inner_Full	25.6	28.19	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	22.19	24.78	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	22.19	24.78	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-16QAM	M+H	Outer_Full	23.72	26.31	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-16QAM	M+H	Inner_Full	24.7	27.29	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	22.13	24.72	30	PASS

DC_26A_n77A-3700-398 0	30	5+50	DFT-16QAM	M+H	Edge_1RB_Righ t	22.05	24.64	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-64QAM	M+H	Outer_Full	23.22	25.81	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-64QAM	M+H	Inner_Full	23.32	25.91	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-64QAM	M+H	Edge_1RB_Left	22.27	24.86	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-64QAM	M+H	Edge_1RB_Righ t	22.15	24.74	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-256QAM	M+H	Outer_Full	21.18	23.77	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-256QAM	M+H	Inner_Full	21.25	23.84	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-256QAM	M+H	Edge_1RB_Left	21.26	23.85	30	PASS
DC_26A_n77A-3700-398 0	30	5+50	DFT-256QAM	M+H	Edge_1RB_Righ t	21.14	23.73	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-PI2BPS K	M+L	Outer_Full	25.06	27.65	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-PI2BPS K	M+L	Inner_Full	25.49	28.08	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-PI2BPS K	M+L	Edge_1RB_Left	22.48	25.07	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-PI2BPS K	M+L	Edge_1RB_Righ t	22.59	25.18	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-QPSK	M+L	Outer_Full	24.6	27.19	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-QPSK	M+L	Inner_Full	25.48	28.07	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	22.46	25.05	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-QPSK	M+L	Edge_1RB_Righ t	22.52	25.11	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-16QAM	M+L	Outer_Full	23.54	26.13	30	PASS

DC_26A_n77A-3700-398 0	30	5+60	DFT-16QAM	M+L	Inner_Full	24.64	27.23	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	21.41	24	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	21.55	24.14	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-64QAM	M+L	Outer_Full	23.1	25.69	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-64QAM	M+L	Inner_Full	23.11	25.7	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-64QAM	M+L	Edge_1RB_Left	20.94	23.53	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-64QAM	M+L	Edge_1RB_Right	21.05	23.64	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-256QAM	M+L	Outer_Full	21.11	23.7	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-256QAM	M+L	Inner_Full	21.11	23.7	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-256QAM	M+L	Edge_1RB_Left	20.91	23.5	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-256QAM	M+L	Edge_1RB_Right	21.12	23.71	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-PI2BPS K	M+M	Outer_Full	25.3	27.89	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-PI2BPS K	M+M	Inner_Full	25.64	28.23	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-PI2BPS K	M+M	Edge_1RB_Left	23.05	25.64	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-PI2BPS K	M+M	Edge_1RB_Right	23.39	25.98	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-QPSK	M+M	Outer_Full	24.7	27.29	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-QPSK	M+M	Inner_Full	25.58	28.17	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	22.53	25.12	30	PASS

DC_26A_n77A-3700-398 0	30	5+60	DFT-QPSK	M+M	Edge_1RB_Righ t	22.88	25.47	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-16QAM	M+M	Outer_Full	23.7	26.29	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-16QAM	M+M	Inner_Full	24.71	27.3	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	21.38	23.97	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-16QAM	M+M	Edge_1RB_Righ t	21.78	24.37	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-64QAM	M+M	Outer_Full	23.24	25.83	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-64QAM	M+M	Inner_Full	23.23	25.82	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-64QAM	M+M	Edge_1RB_Left	21.04	23.63	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-64QAM	M+M	Edge_1RB_Righ t	21.34	23.93	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-256QAM	M+M	Outer_Full	21.25	23.84	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-256QAM	M+M	Inner_Full	21.35	23.94	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-256QAM	M+M	Edge_1RB_Left	21.12	23.71	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-256QAM	M+M	Edge_1RB_Righ t	21.53	24.12	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-PI2BPS K	M+H	Outer_Full	25.13	27.72	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-PI2BPS K	M+H	Inner_Full	25.57	28.16	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-PI2BPS K	M+H	Edge_1RB_Left	22.28	24.87	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-PI2BPS K	M+H	Edge_1RB_Righ t	22.13	24.72	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-QPSK	M+H	Outer_Full	24.63	27.22	30	PASS

DC_26A_n77A-3700-398 0	30	5+60	DFT-QPSK	M+H	Inner_Full	25.51	28.1	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	22.24	24.83	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	22.2	24.79	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-16QAM	M+H	Outer_Full	23.65	26.24	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-16QAM	M+H	Inner_Full	24.68	27.27	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	22.15	24.74	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	22.19	24.78	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-64QAM	M+H	Outer_Full	23.18	25.77	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-64QAM	M+H	Inner_Full	23.07	25.66	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-64QAM	M+H	Edge_1RB_Left	22.35	24.94	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-64QAM	M+H	Edge_1RB_Right	22.14	24.73	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-256QAM	M+H	Outer_Full	21.14	23.73	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-256QAM	M+H	Inner_Full	21.21	23.8	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-256QAM	M+H	Edge_1RB_Left	21.21	23.8	30	PASS
DC_26A_n77A-3700-398 0	30	5+60	DFT-256QAM	M+H	Edge_1RB_Right	21.15	23.74	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-PI2BPS K	M+L	Outer_Full	25.02	27.61	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-PI2BPS K	M+L	Inner_Full	25.41	28	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-PI2BPS K	M+L	Edge_1RB_Left	22.59	25.18	30	PASS

DC_26A_n77A-3700-398 0	30	5+80	DFT-PI2BPS K	M+L	Edge_1RB_Righ t	22.44	25.03	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-QPSK	M+L	Outer_Full	24.5	27.09	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-QPSK	M+L	Inner_Full	25.43	28.02	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	22.56	25.15	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-QPSK	M+L	Edge_1RB_Righ t	22.4	24.99	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-16QAM	M+L	Outer_Full	23.56	26.15	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-16QAM	M+L	Inner_Full	24.54	27.13	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	21.49	24.08	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-16QAM	M+L	Edge_1RB_Righ t	21.39	23.98	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-64QAM	M+L	Outer_Full	23.11	25.7	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-64QAM	M+L	Inner_Full	23.03	25.62	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-64QAM	M+L	Edge_1RB_Left	21.16	23.75	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-64QAM	M+L	Edge_1RB_Righ t	20.96	23.55	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-256QAM	M+L	Outer_Full	21.05	23.64	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-256QAM	M+L	Inner_Full	21.08	23.67	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-256QAM	M+L	Edge_1RB_Left	21.13	23.72	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-256QAM	M+L	Edge_1RB_Righ t	20.96	23.55	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-PI2BPS K	M+M	Outer_Full	25.41	28	30	PASS

DC_26A_n77A-3700-398 0	30	5+80	DFT-PI2BPS K	M+M	Inner_Full	25.61	28.2	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-PI2BPS K	M+M	Edge_1RB_Left	23.13	25.72	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-PI2BPS K	M+M	Edge_1RB_Right	23.37	25.96	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-QPSK	M+M	Outer_Full	24.69	27.28	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-QPSK	M+M	Inner_Full	25.62	28.21	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	22.61	25.2	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	22.9	25.49	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-16QAM	M+M	Outer_Full	23.71	26.3	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-16QAM	M+M	Inner_Full	24.69	27.28	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	21.63	24.22	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	21.92	24.51	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-64QAM	M+M	Outer_Full	23.27	25.86	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-64QAM	M+M	Inner_Full	23.18	25.77	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-64QAM	M+M	Edge_1RB_Left	21.19	23.78	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-64QAM	M+M	Edge_1RB_Right	21.47	24.06	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-256QAM	M+M	Outer_Full	21.27	23.86	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-256QAM	M+M	Inner_Full	21.24	23.83	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-256QAM	M+M	Edge_1RB_Left	21.31	23.9	30	PASS

DC_26A_n77A-3700-398 0	30	5+80	DFT-256QAM	M+M	Edge_1RB_Righ t	21.33	23.92	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-PI2BPS K	M+H	Outer_Full	25.16	27.75	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-PI2BPS K	M+H	Inner_Full	25.58	28.17	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-PI2BPS K	M+H	Edge_1RB_Left	23.17	25.76	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-PI2BPS K	M+H	Edge_1RB_Righ t	22.92	25.51	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-QPSK	M+H	Outer_Full	24.7	27.29	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-QPSK	M+H	Inner_Full	25.58	28.17	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	22.69	25.28	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-QPSK	M+H	Edge_1RB_Righ t	22.52	25.11	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-16QAM	M+H	Outer_Full	23.66	26.25	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-16QAM	M+H	Inner_Full	24.66	27.25	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	21.65	24.24	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-16QAM	M+H	Edge_1RB_Righ t	21.48	24.07	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-64QAM	M+H	Outer_Full	23.28	25.87	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-64QAM	M+H	Inner_Full	23.22	25.81	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-64QAM	M+H	Edge_1RB_Left	21.15	23.74	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-64QAM	M+H	Edge_1RB_Righ t	21.08	23.67	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-256QAM	M+H	Outer_Full	21.23	23.82	30	PASS

DC_26A_n77A-3700-398 0	30	5+80	DFT-256QAM	M+H	Inner_Full	21.15	23.74	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-256QAM	M+H	Edge_1RB_Left	21.31	23.9	30	PASS
DC_26A_n77A-3700-398 0	30	5+80	DFT-256QAM	M+H	Edge_1RB_Right	20.9	23.49	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-PI2BPS K	M+L	Outer_Full	25.01	27.6	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-PI2BPS K	M+L	Inner_Full	25.34	27.93	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-PI2BPS K	M+L	Edge_1RB_Left	22.63	25.22	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-PI2BPS K	M+L	Edge_1RB_Right	22.46	25.05	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-QPSK	M+L	Outer_Full	24.49	27.08	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-QPSK	M+L	Inner_Full	25.39	27.98	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	22.58	25.17	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	22.42	25.01	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-16QAM	M+L	Outer_Full	23.5	26.09	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-16QAM	M+L	Inner_Full	24.51	27.1	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	21.56	24.15	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	21.46	24.05	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-64QAM	M+L	Outer_Full	23.01	25.6	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-64QAM	M+L	Inner_Full	22.96	25.55	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-64QAM	M+L	Edge_1RB_Left	21.12	23.71	30	PASS

DC_26A_n77A-3700-398 0	30	5+90	DFT-64QAM	M+L	Edge_1RB_Righ t	20.96	23.55	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-256QAM	M+L	Outer_Full	21.05	23.64	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-256QAM	M+L	Inner_Full	21.06	23.65	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-256QAM	M+L	Edge_1RB_Left	21.17	23.76	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-256QAM	M+L	Edge_1RB_Righ t	21.02	23.61	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-PI2BPS K	M+M	Outer_Full	25.31	27.9	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-PI2BPS K	M+M	Inner_Full	25.6	28.19	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-PI2BPS K	M+M	Edge_1RB_Left	23.16	25.75	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-PI2BPS K	M+M	Edge_1RB_Righ t	23.39	25.98	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-QPSK	M+M	Outer_Full	24.65	27.24	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-QPSK	M+M	Inner_Full	25.6	28.19	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	22.72	25.31	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-QPSK	M+M	Edge_1RB_Righ t	22.81	25.4	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-16QAM	M+M	Outer_Full	23.74	26.33	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-16QAM	M+M	Inner_Full	24.73	27.32	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	21.68	24.27	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-16QAM	M+M	Edge_1RB_Righ t	21.6	24.19	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-64QAM	M+M	Outer_Full	23.26	25.85	30	PASS

DC_26A_n77A-3700-398 0	30	5+90	DFT-64QAM	M+M	Inner_Full	23.27	25.86	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-64QAM	M+M	Edge_1RB_Left	21.21	23.8	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-64QAM	M+M	Edge_1RB_Right	21.38	23.97	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-256QAM	M+M	Outer_Full	21.29	23.88	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-256QAM	M+M	Inner_Full	21.29	23.88	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-256QAM	M+M	Edge_1RB_Left	21.15	23.74	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-256QAM	M+M	Edge_1RB_Right	21.4	23.99	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-PI2BPS K	M+H	Outer_Full	25.24	27.83	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-PI2BPS K	M+H	Inner_Full	25.66	28.25	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-PI2BPS K	M+H	Edge_1RB_Left	23.37	25.96	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-PI2BPS K	M+H	Edge_1RB_Right	22.97	25.56	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-QPSK	M+H	Outer_Full	24.76	27.35	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-QPSK	M+H	Inner_Full	25.64	28.23	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	22.87	25.46	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	22.57	25.16	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-16QAM	M+H	Outer_Full	23.74	26.33	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-16QAM	M+H	Inner_Full	24.75	27.34	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	21.78	24.37	30	PASS

DC_26A_n77A-3700-398 0	30	5+90	DFT-16QAM	M+H	Edge_1RB_Righ t	21.49	24.08	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-64QAM	M+H	Outer_Full	23.21	25.8	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-64QAM	M+H	Inner_Full	23.26	25.85	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-64QAM	M+H	Edge_1RB_Left	21.37	23.96	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-64QAM	M+H	Edge_1RB_Righ t	21.14	23.73	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-256QAM	M+H	Outer_Full	21.26	23.85	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-256QAM	M+H	Inner_Full	21.36	23.95	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-256QAM	M+H	Edge_1RB_Left	21.36	23.95	30	PASS
DC_26A_n77A-3700-398 0	30	5+90	DFT-256QAM	M+H	Edge_1RB_Righ t	21.04	23.63	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-PI2BPS K	M+L	Outer_Full	25	27.59	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-PI2BPS K	M+L	Inner_Full	25.36	27.95	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-PI2BPS K	M+L	Edge_1RB_Left	22.57	25.16	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-PI2BPS K	M+L	Edge_1RB_Righ t	22.55	25.14	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-QPSK	M+L	Outer_Full	24.5	27.09	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-QPSK	M+L	Inner_Full	25.4	27.99	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	22.56	25.15	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-QPSK	M+L	Edge_1RB_Righ t	22.57	25.16	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-16QAM	M+L	Outer_Full	23.57	26.16	30	PASS

DC_26A_n77A-3700-398 0	30	5+100	DFT-16QAM	M+L	Inner_Full	24.58	27.17	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	21.54	24.13	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	21.4	23.99	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-64QAM	M+L	Outer_Full	23.08	25.67	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-64QAM	M+L	Inner_Full	23	25.59	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-64QAM	M+L	Edge_1RB_Left	21.11	23.7	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-64QAM	M+L	Edge_1RB_Right	21.02	23.61	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-256QAM	M+L	Outer_Full	21.07	23.66	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-256QAM	M+L	Inner_Full	21.2	23.79	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-256QAM	M+L	Edge_1RB_Left	21.07	23.66	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-256QAM	M+L	Edge_1RB_Right	21.07	23.66	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-PI2BPS K	M+M	Outer_Full	25.31	27.9	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-PI2BPS K	M+M	Inner_Full	25.66	28.25	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-PI2BPS K	M+M	Edge_1RB_Left	23.03	25.62	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-PI2BPS K	M+M	Edge_1RB_Right	23.38	25.97	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-QPSK	M+M	Outer_Full	24.71	27.3	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-QPSK	M+M	Inner_Full	25.61	28.2	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	22.5	25.09	30	PASS

DC_26A_n77A-3700-398 0	30	5+100	DFT-QPSK	M+M	Edge_1RB_Righ t	22.94	25.53	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-16QAM	M+M	Outer_Full	23.71	26.3	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-16QAM	M+M	Inner_Full	24.69	27.28	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	21.58	24.17	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-16QAM	M+M	Edge_1RB_Righ t	21.88	24.47	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-64QAM	M+M	Outer_Full	23.18	25.77	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-64QAM	M+M	Inner_Full	23.22	25.81	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-64QAM	M+M	Edge_1RB_Left	21.16	23.75	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-64QAM	M+M	Edge_1RB_Righ t	21.39	23.98	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-256QAM	M+M	Outer_Full	21.21	23.8	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-256QAM	M+M	Inner_Full	21.28	23.87	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-256QAM	M+M	Edge_1RB_Left	21.1	23.69	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-256QAM	M+M	Edge_1RB_Righ t	21.47	24.06	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-PI2BPS K	M+H	Outer_Full	25.33	27.92	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-PI2BPS K	M+H	Inner_Full	25.63	28.22	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-PI2BPS K	M+H	Edge_1RB_Left	23.33	25.92	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-PI2BPS K	M+H	Edge_1RB_Righ t	23.1	25.69	30	PASS
DC_26A_n77A-3700-398 0	30	5+100	DFT-QPSK	M+H	Outer_Full	24.63	27.22	30	PASS