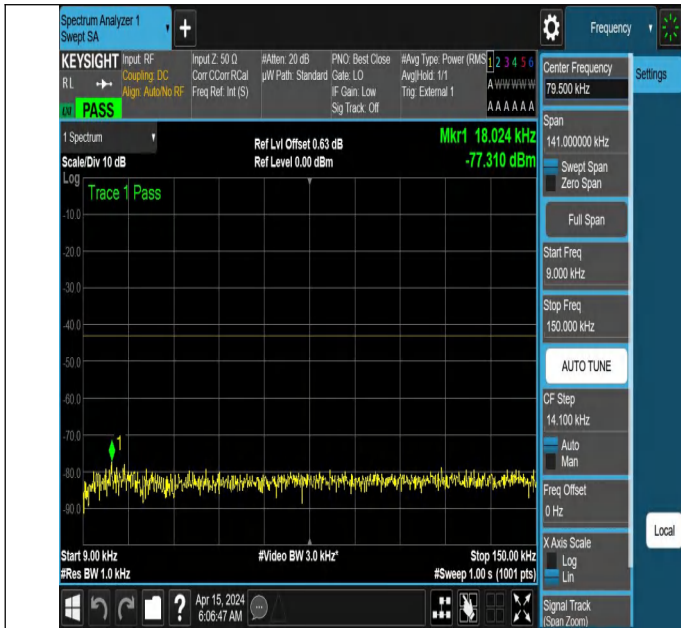
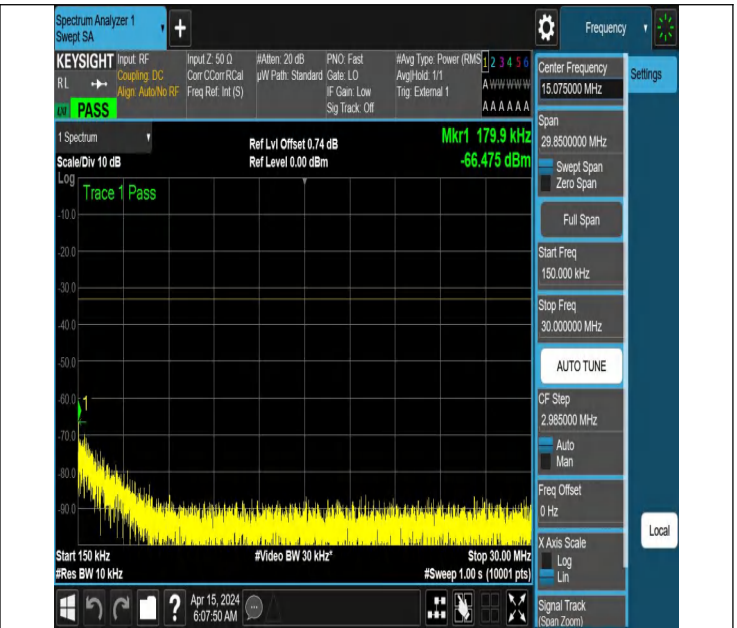




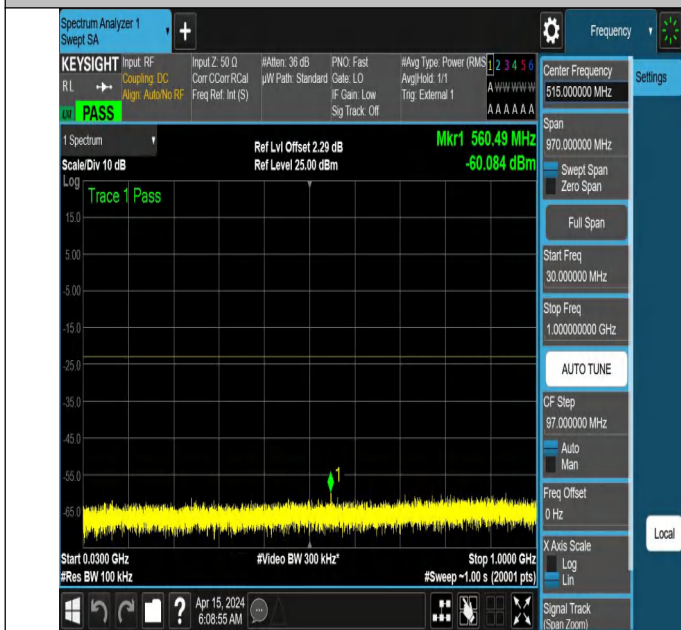
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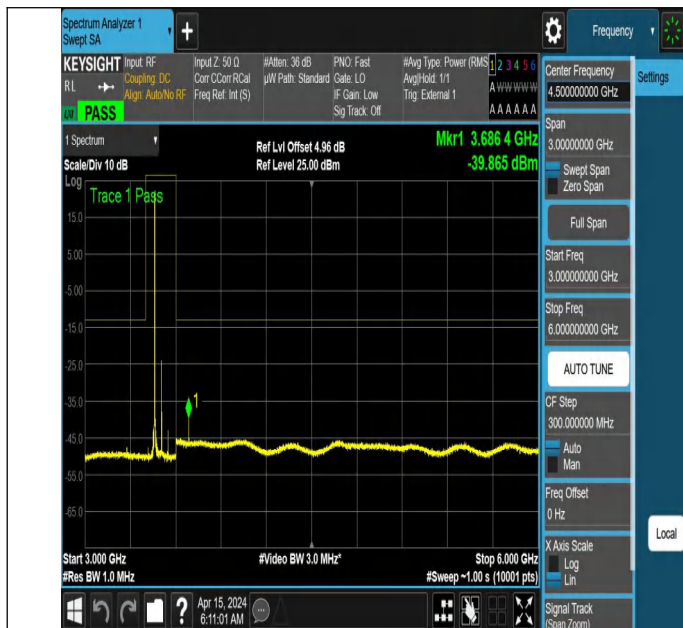
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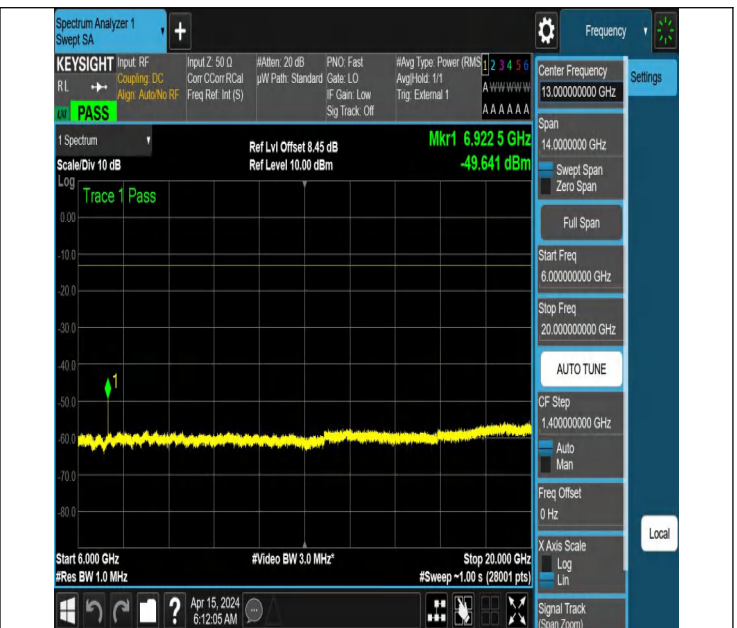
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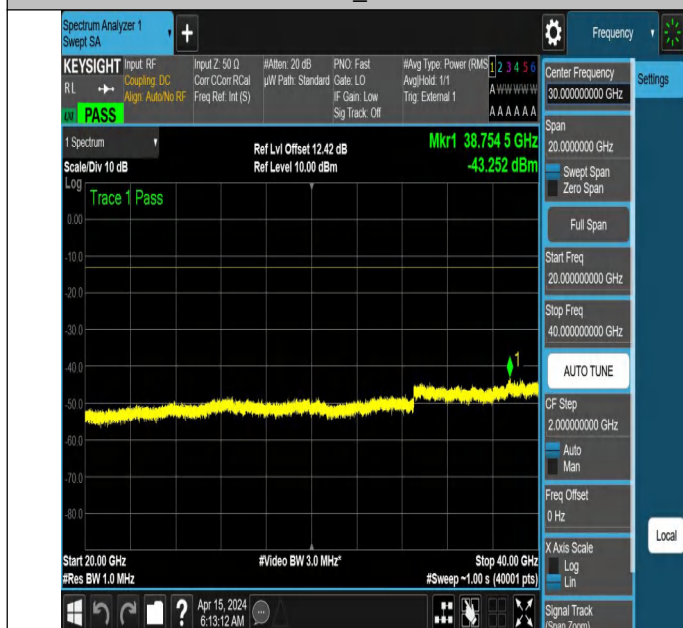
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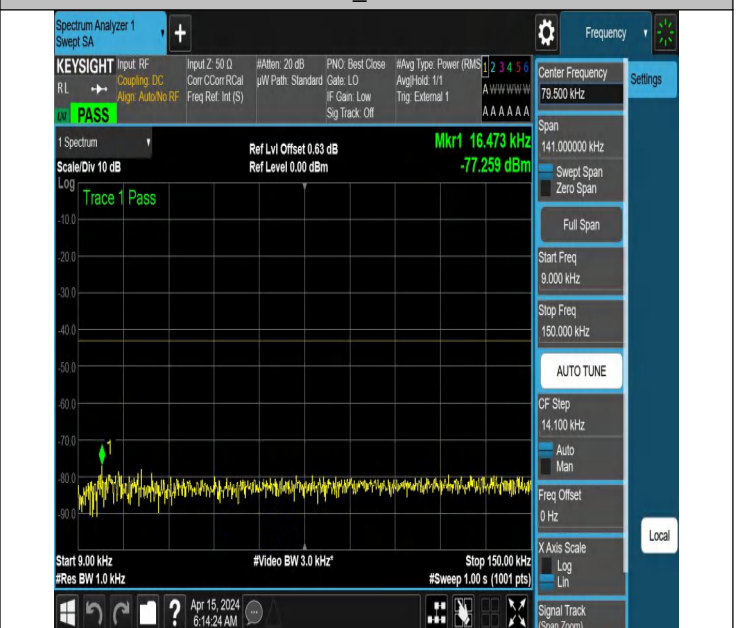
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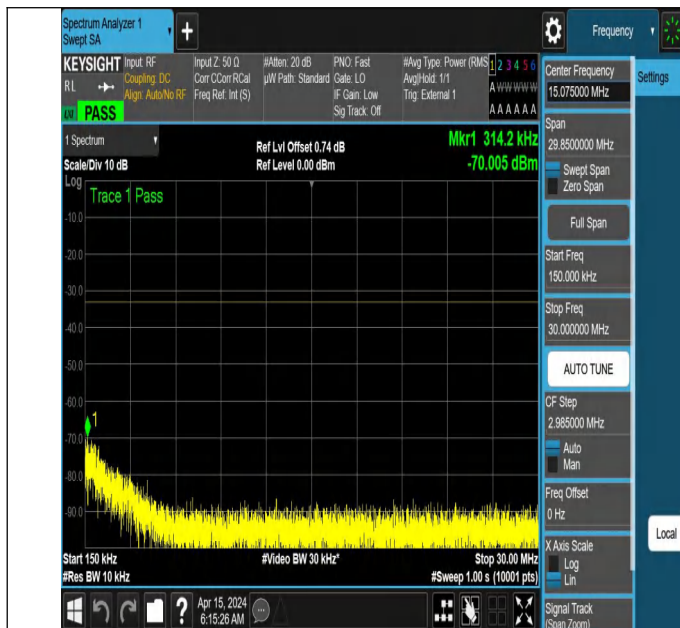
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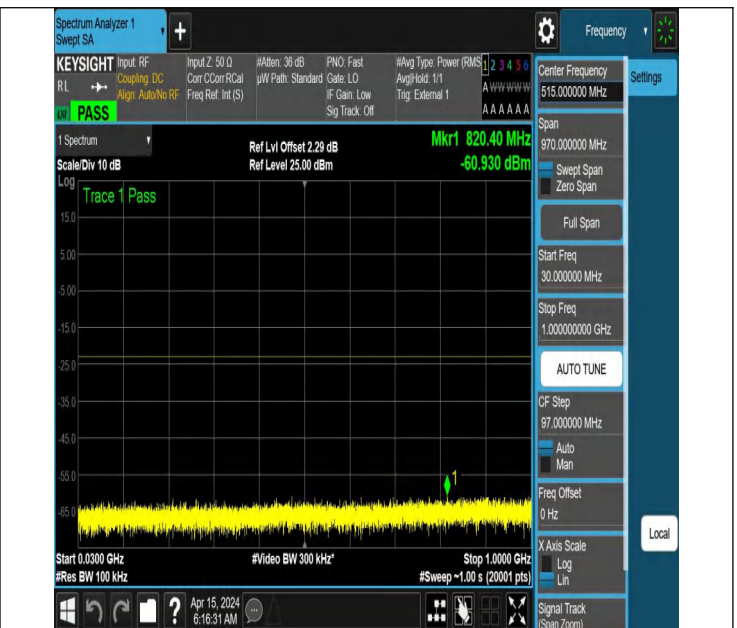
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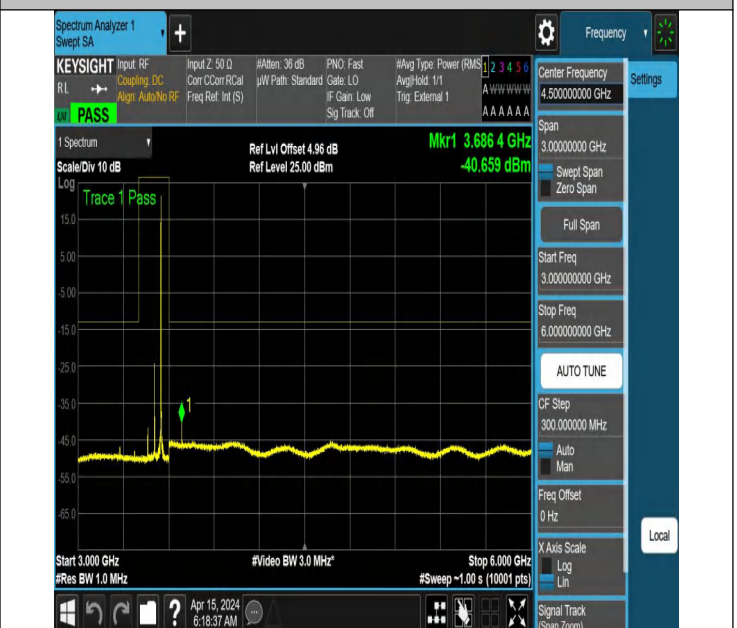
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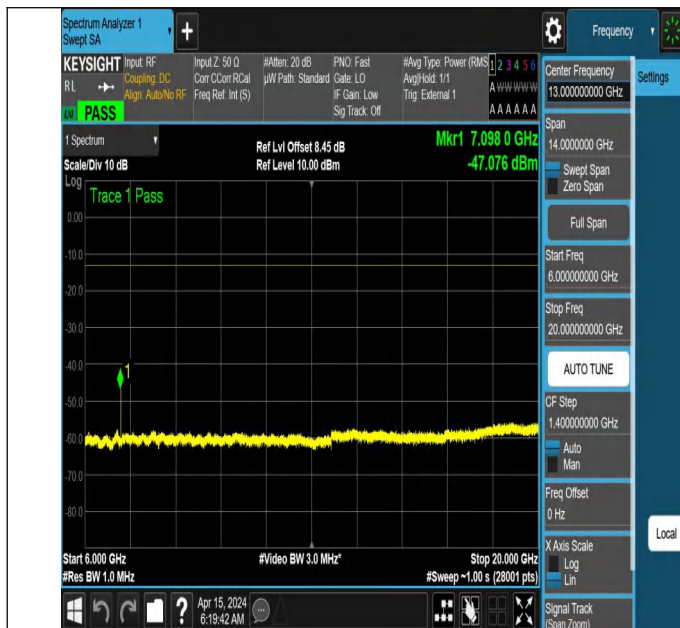
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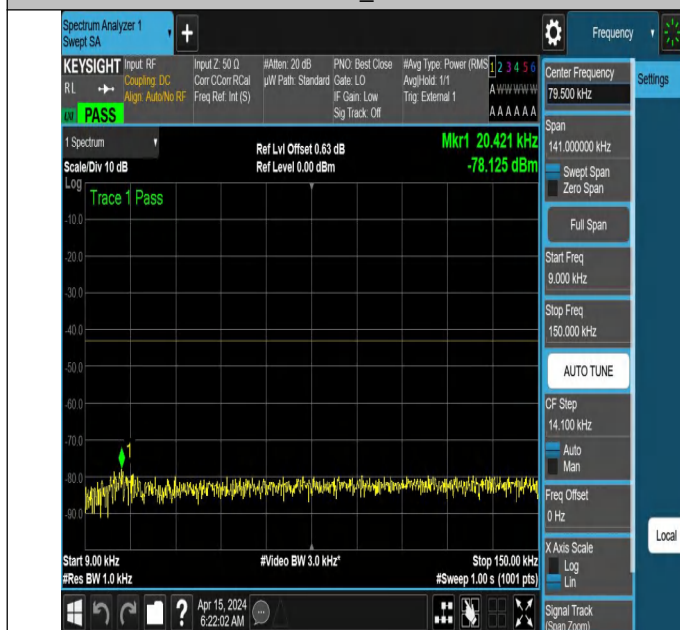
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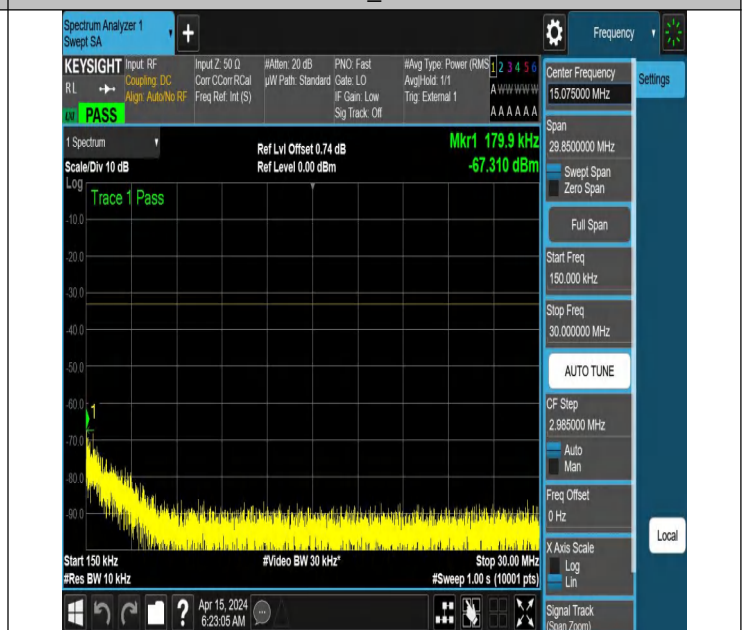
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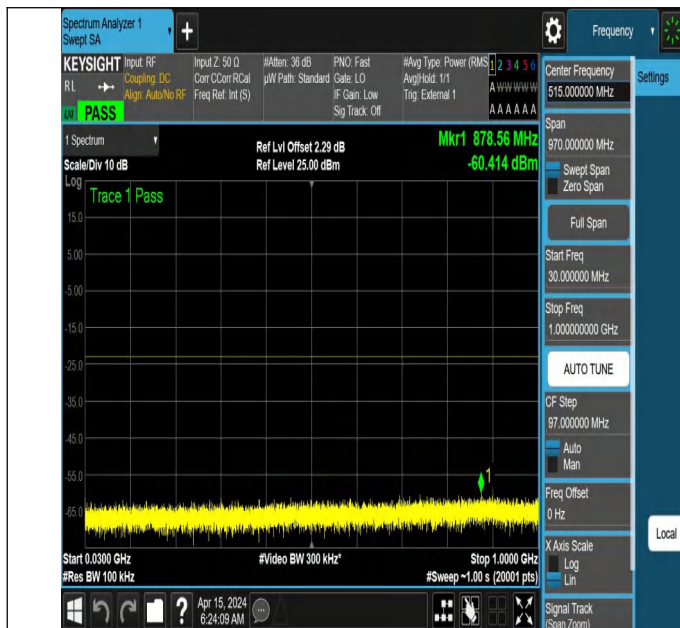
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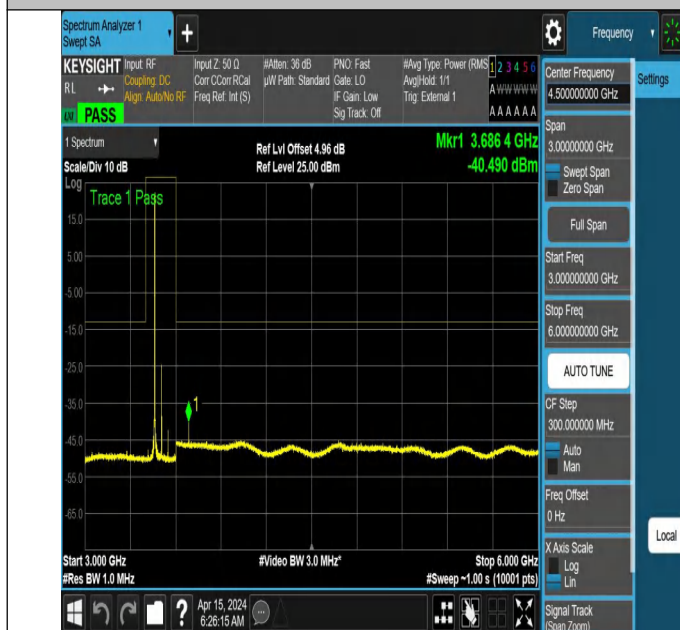
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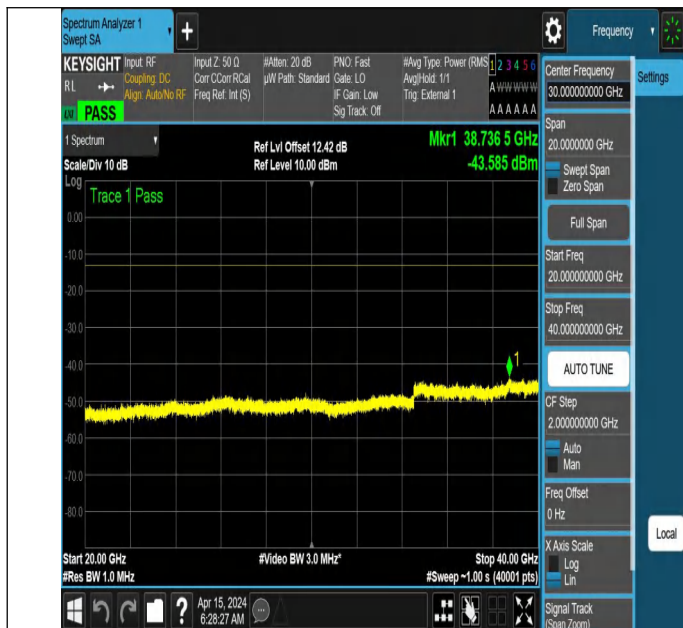
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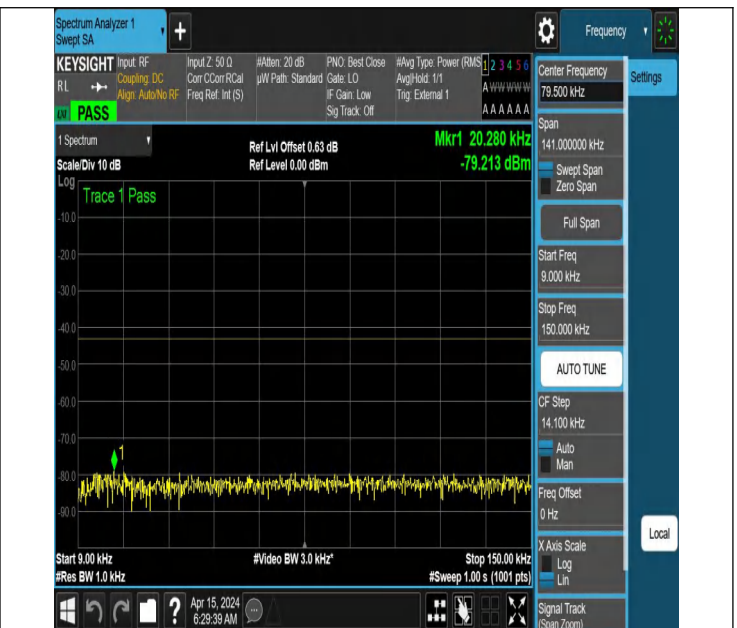


NTNV N77-3450-3550 PC2 30 90 H TID5 NS 01 6000 2000

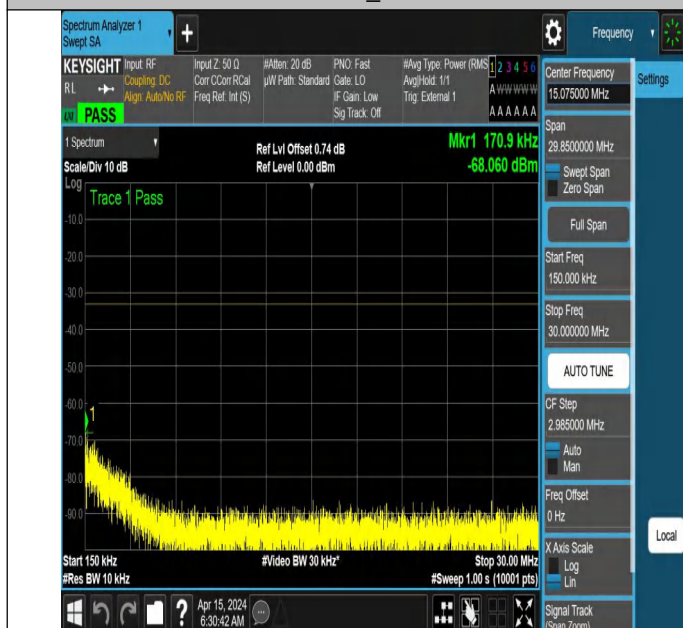
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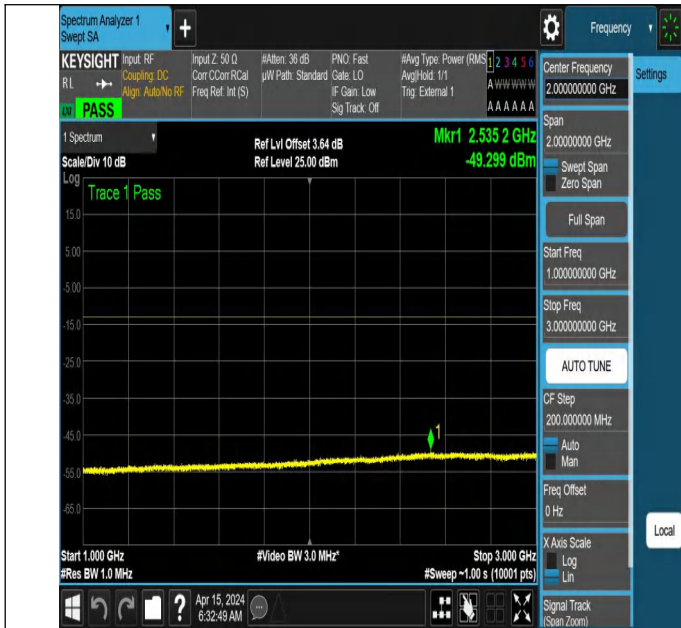
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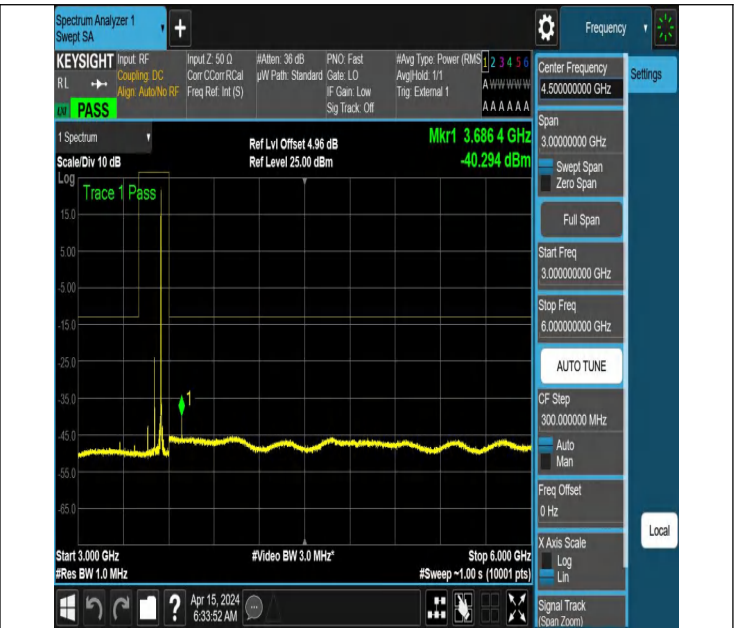


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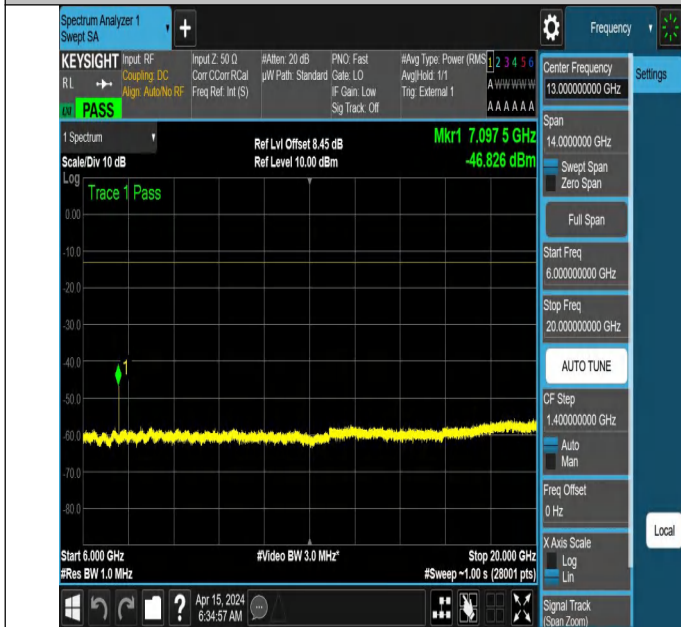
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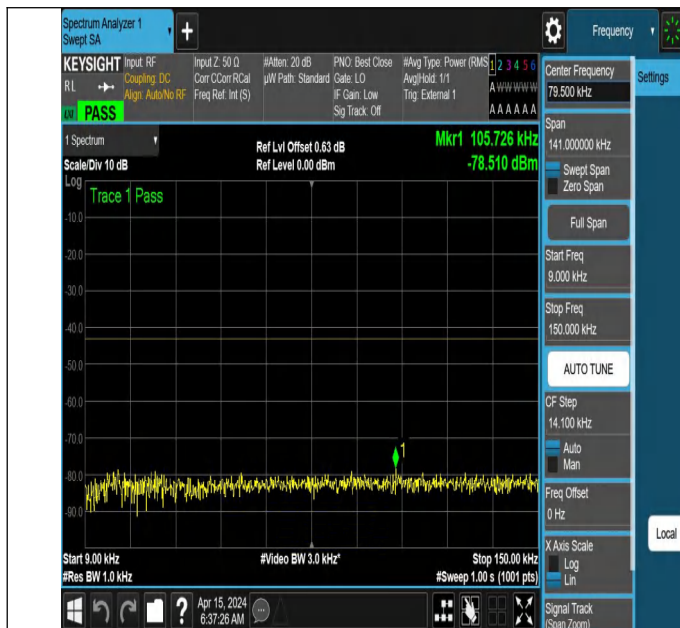
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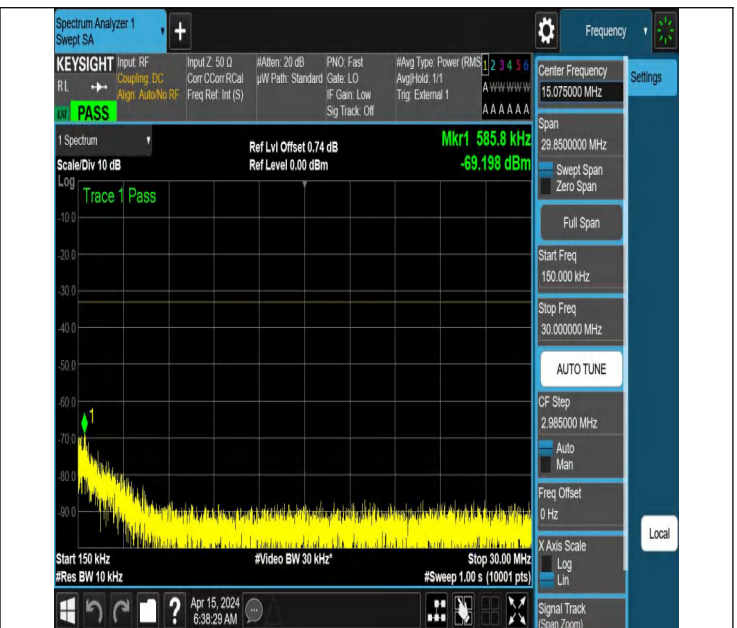
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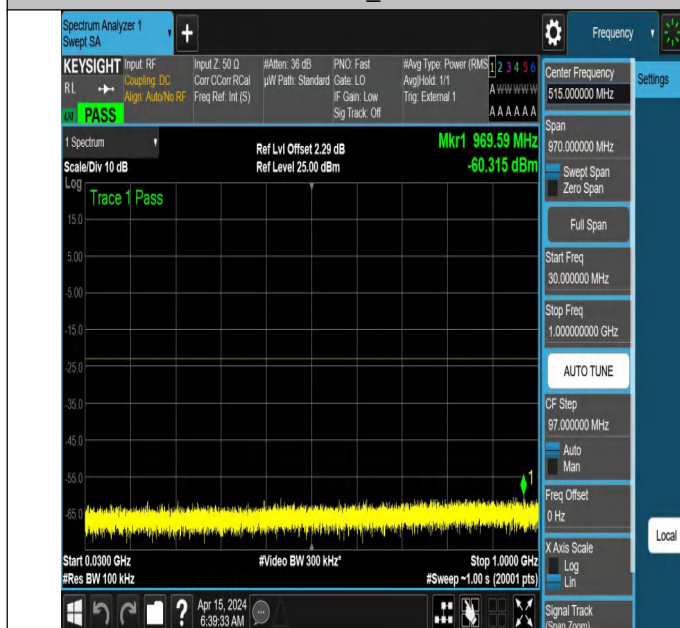
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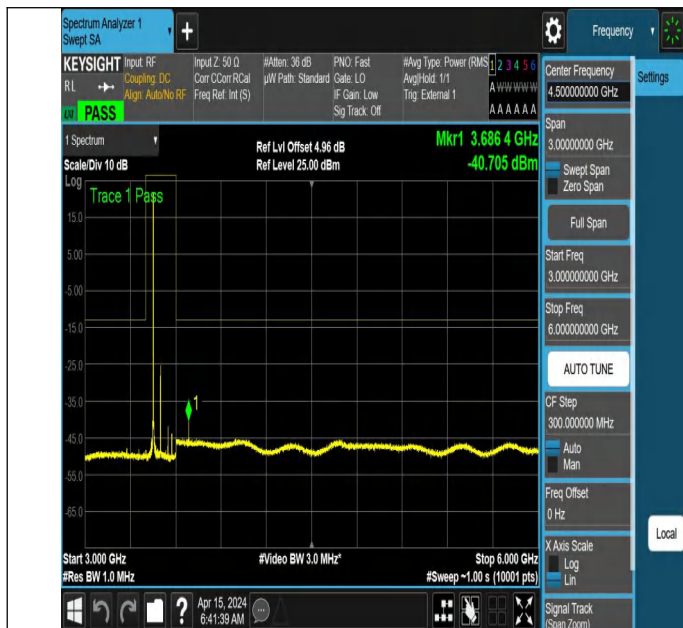
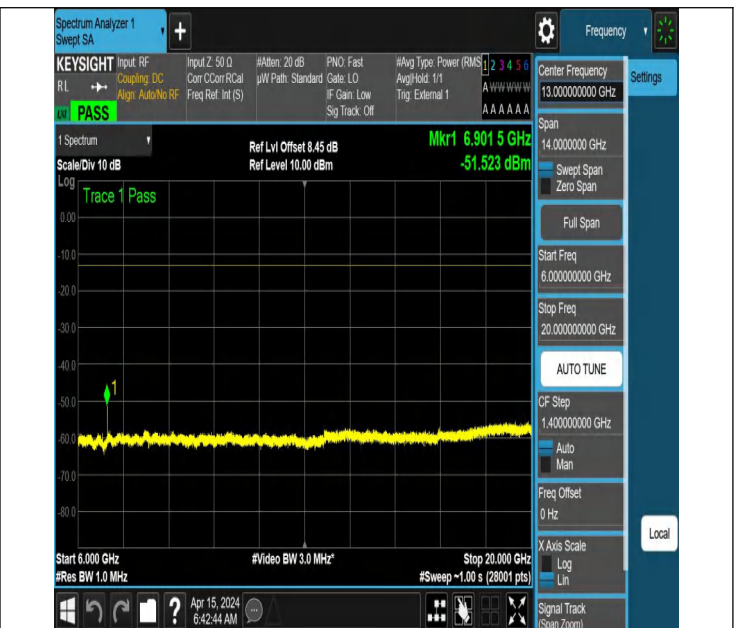
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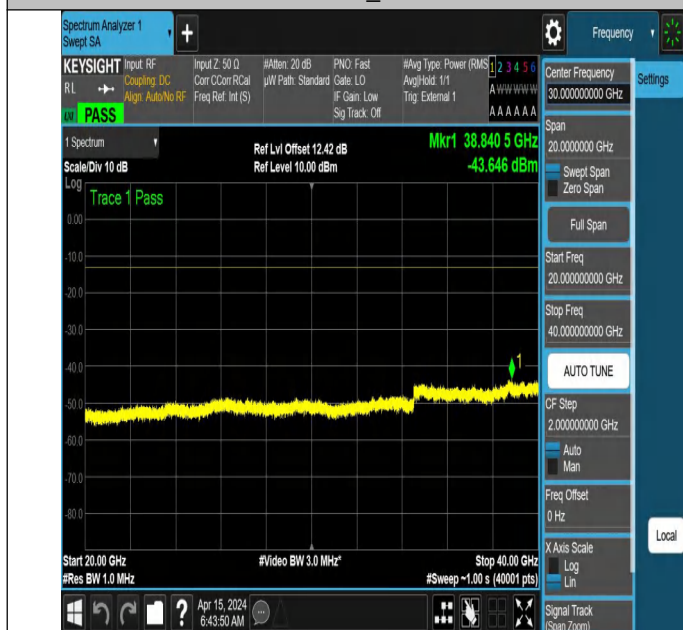
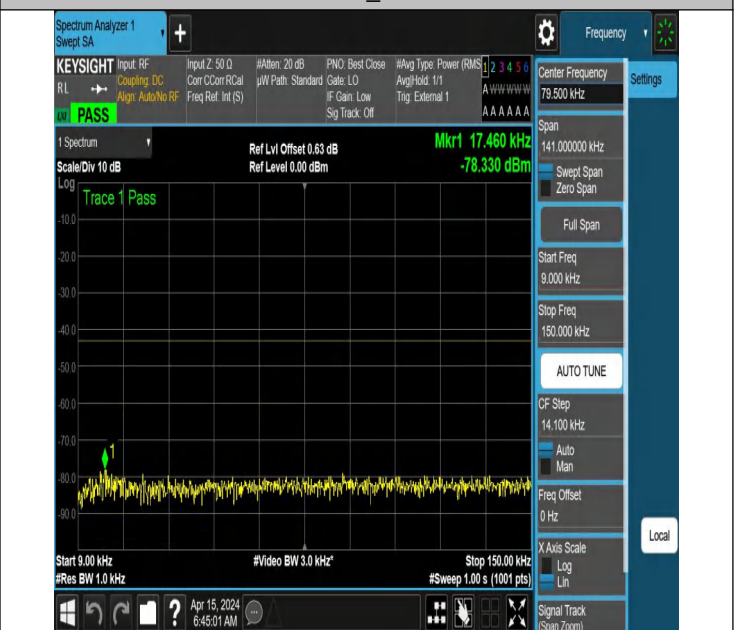


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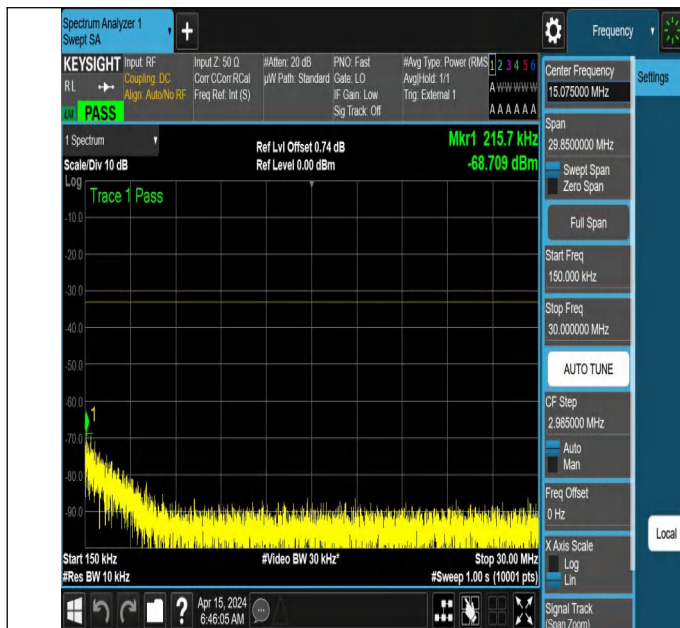
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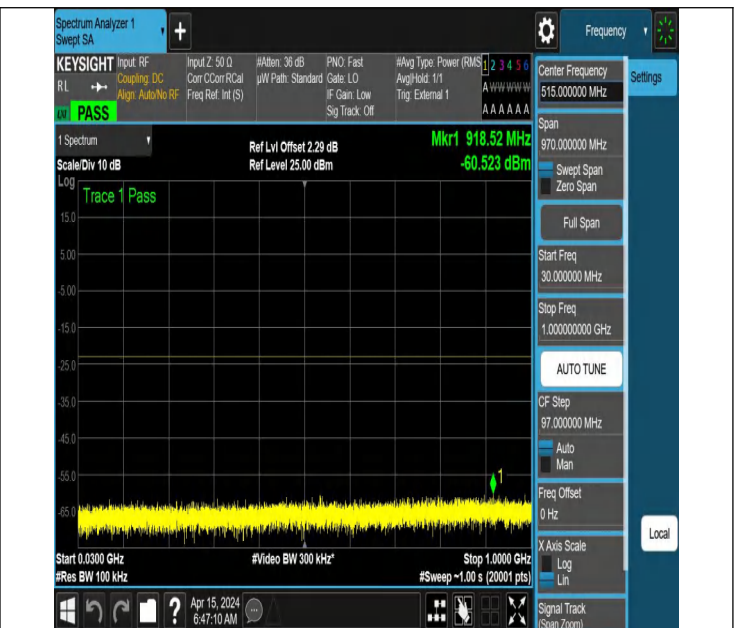
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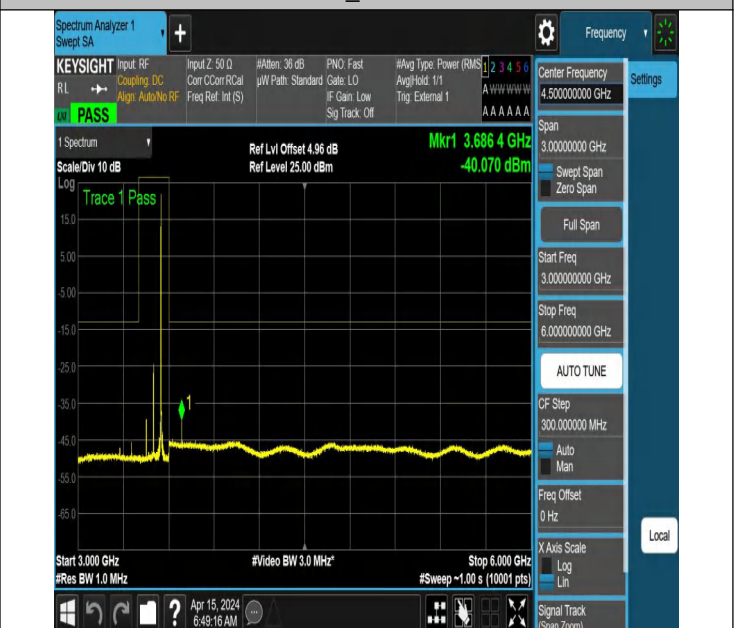
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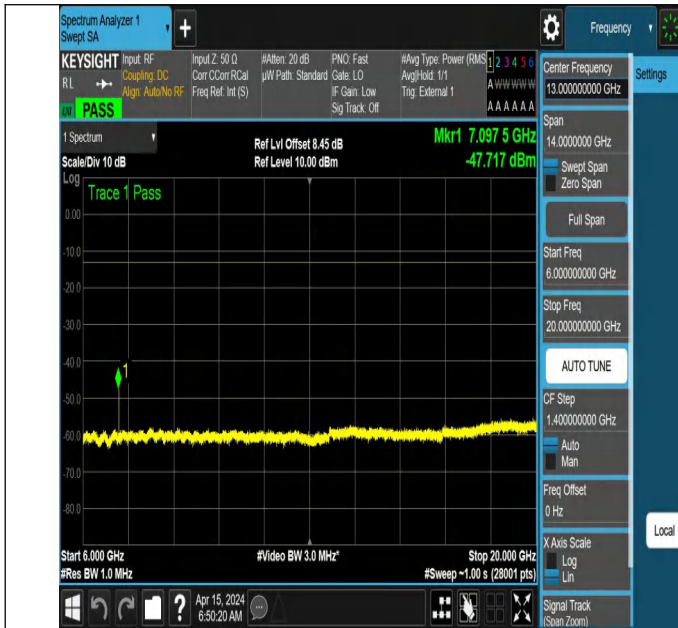
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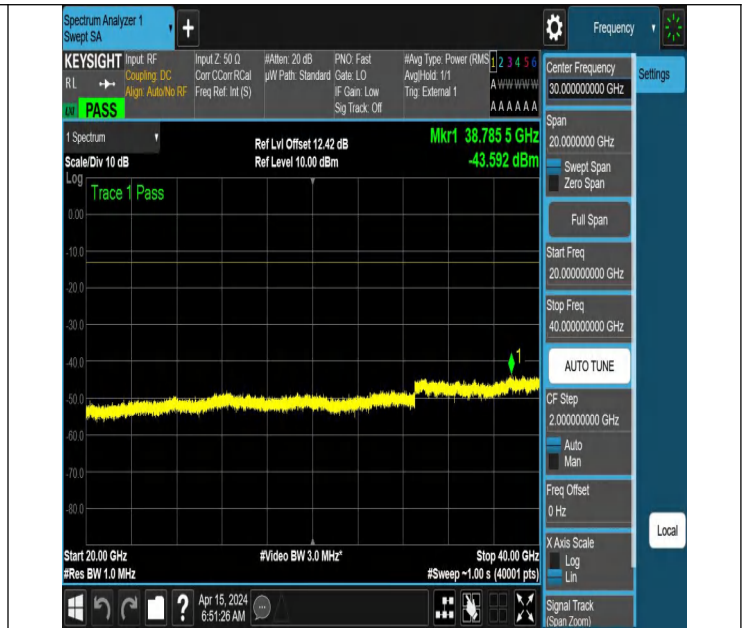
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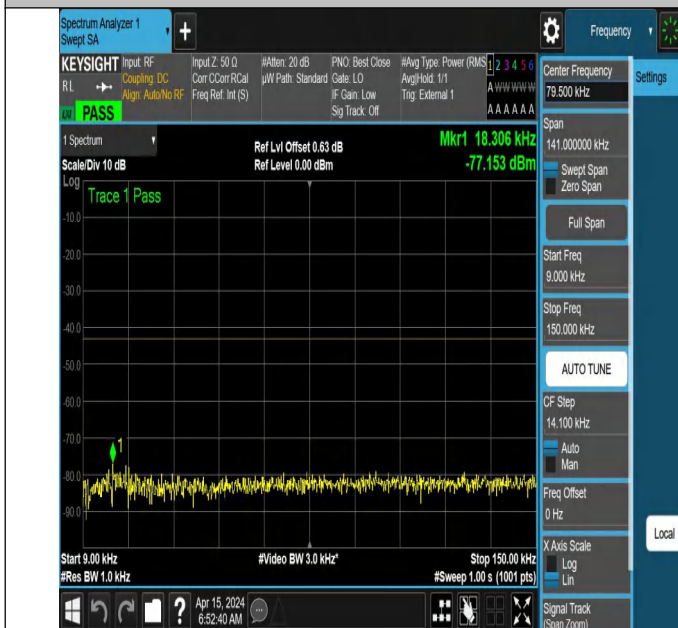
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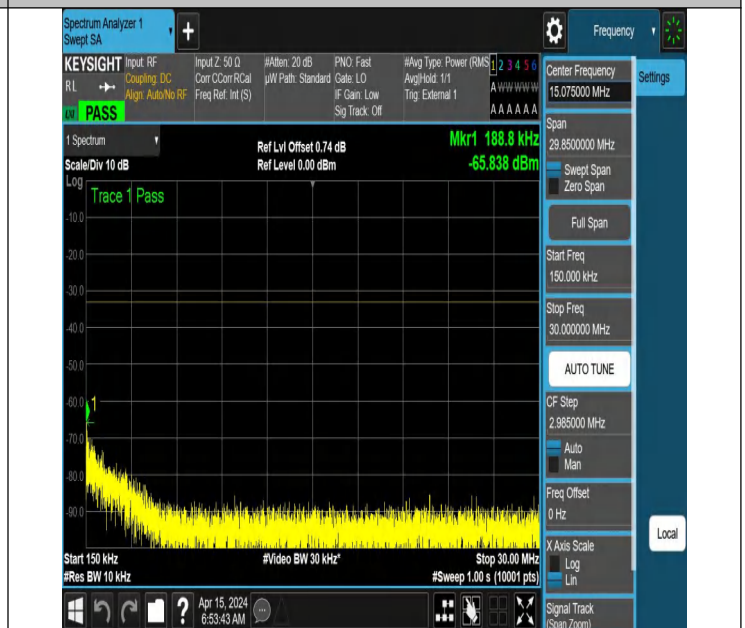
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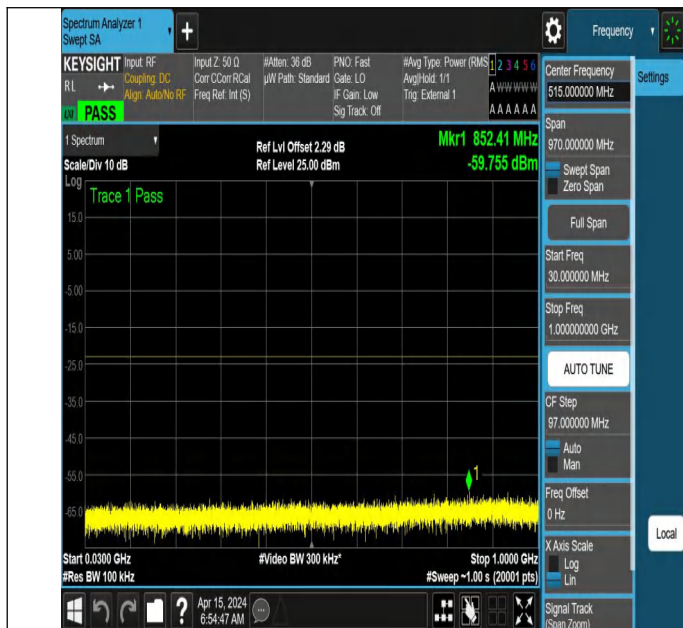
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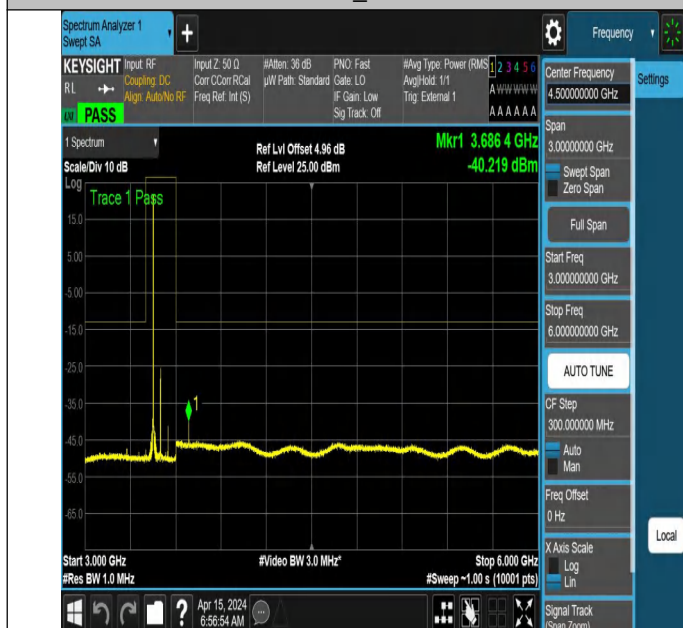
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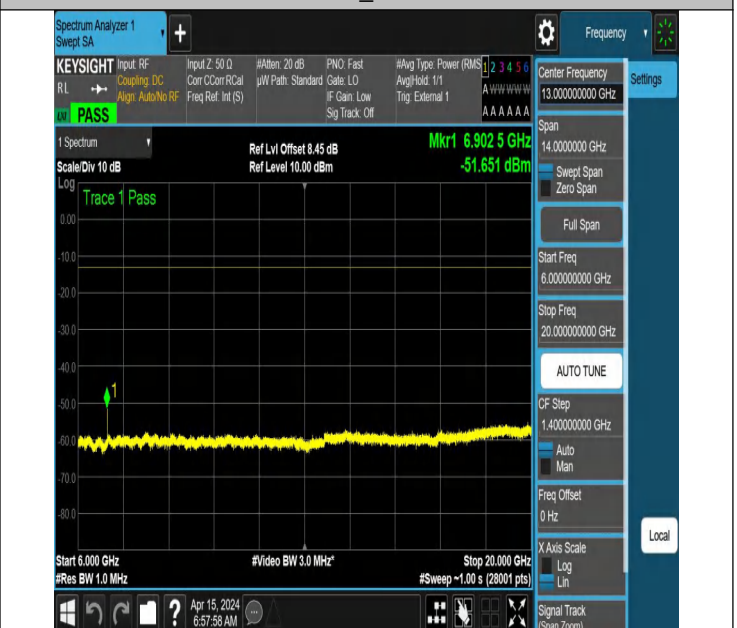
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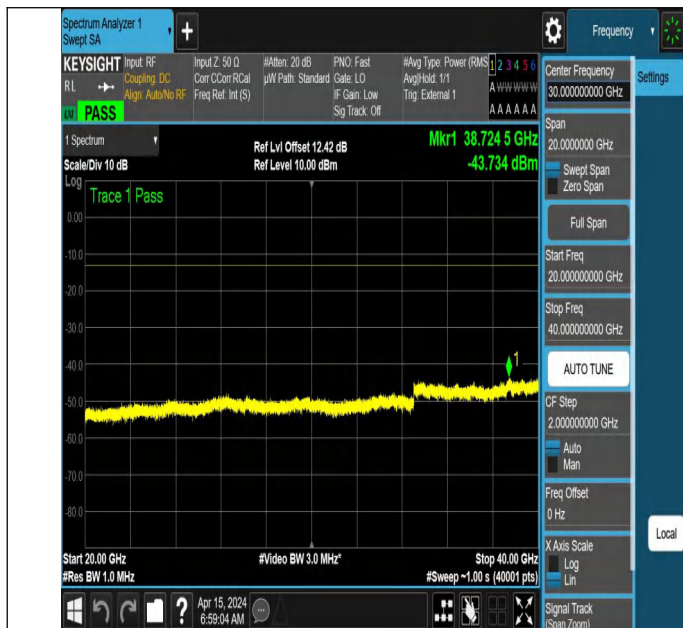
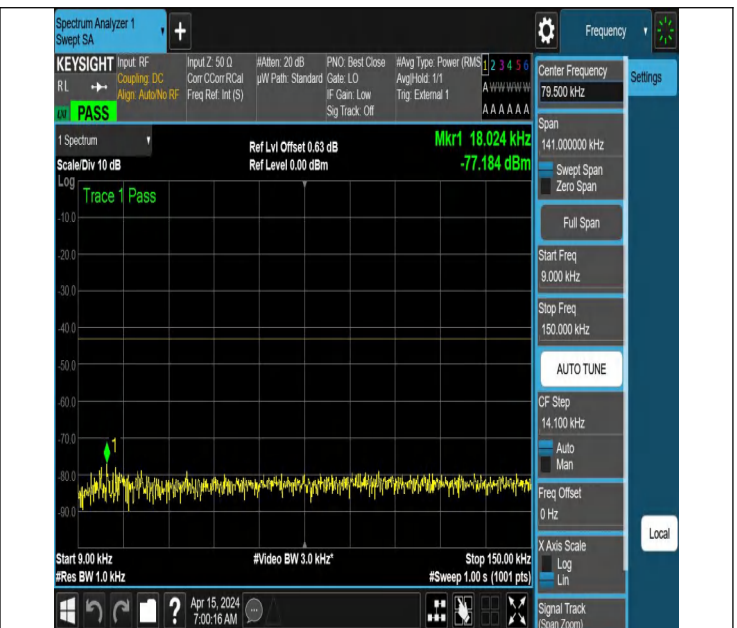
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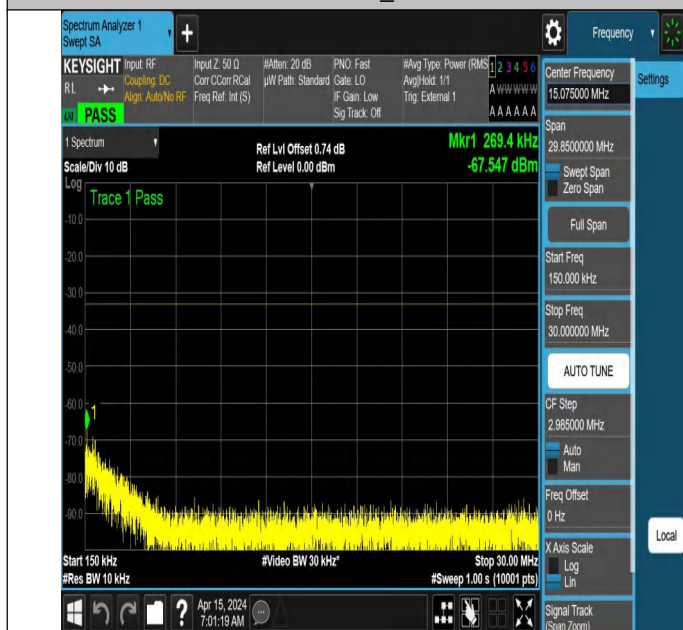
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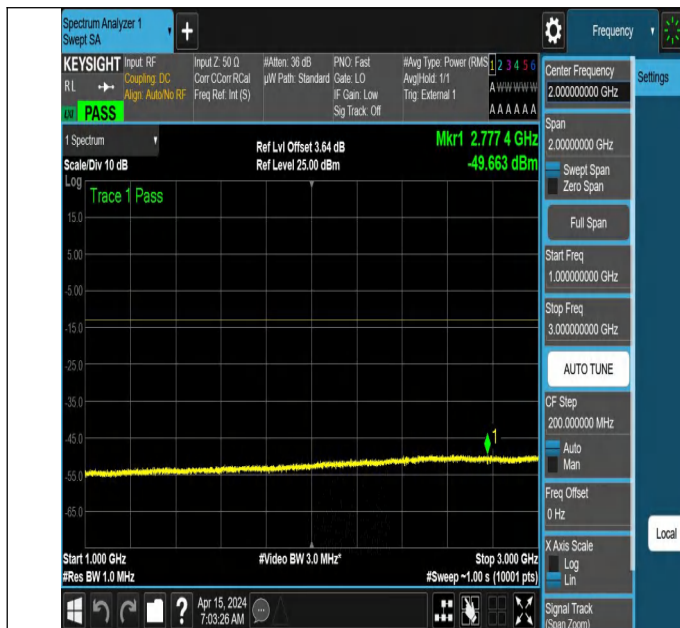
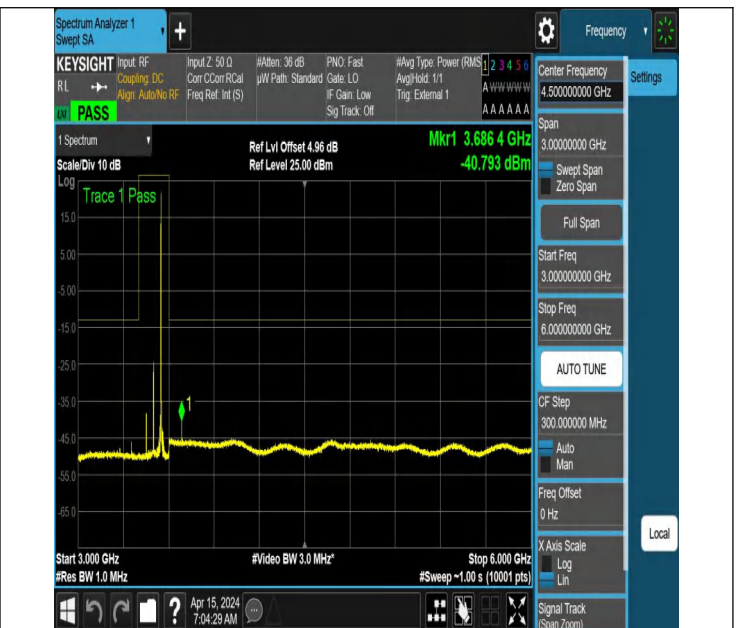
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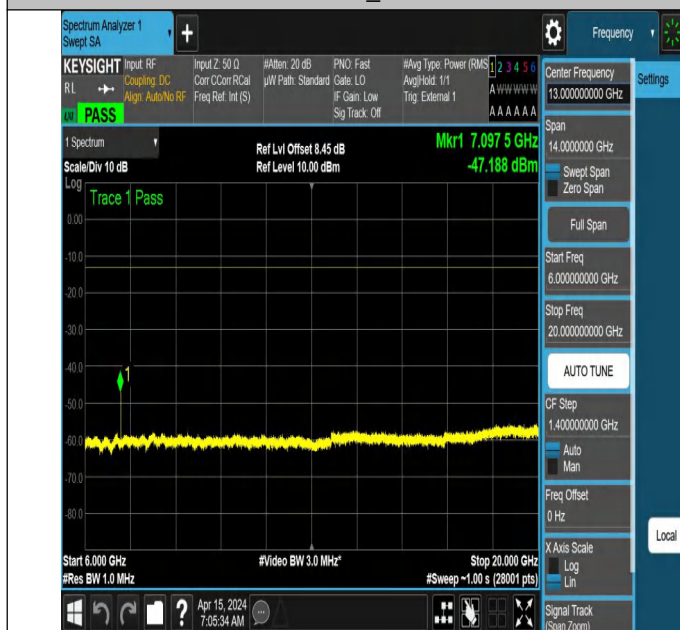
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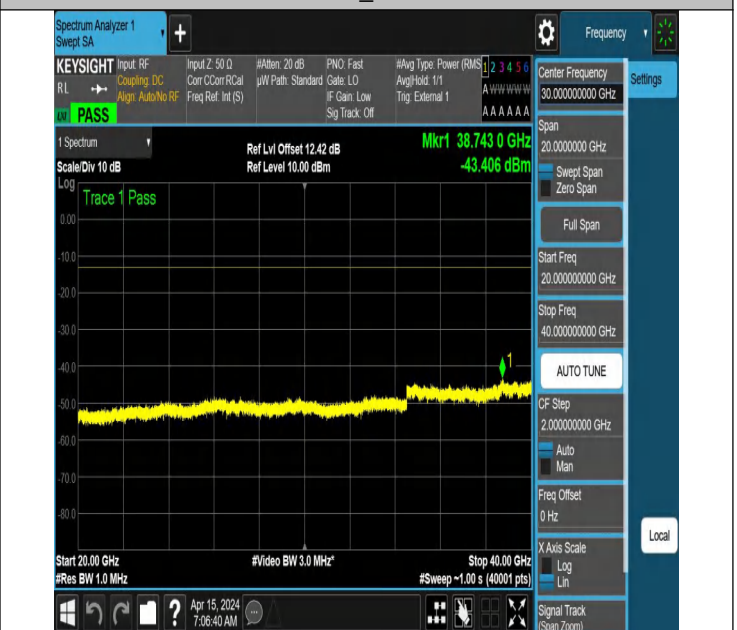
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NTNV_N77-3450-3550_PC2_30_100_M_TID6_NS_01_6000_2000_#1



NTNV_N77-3450-3550_PC2_30_100_M_TID6_NS_01_20000_4
0000_#1

Frequency Stability for SA

Test Result

Frequency Error VS. Voltage											
Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Voltage	Temperature	Deviation(Hz)	Deviation (ppm)	Limit (ppm)	Verdict
N77-3450-3550	30	100	DFT-QPSK	M	Outer_Full	VL	NT	-23.280000	-0.006651	±2.5	PASS
N77-3450-3550	30	100	DFT-QPSK	M	Outer_Full	VN	NT	-29.230000	-0.008351	±2.5	PASS
N77-3450-3550	30	100	DFT-QPSK	M	Outer_Full	VH	NT	-32.460000	-0.009274	±2.5	PASS

Frequency Error VS. Temperature											
Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Voltage	Temperature	Deviation(Hz)	Deviation (ppm)	Limit (ppm)	Verdict
N77-3450-3550	30	100	DFT-QPSK	M	Outer_Full	NV	-30	-30.590000	-0.008740	±2.5	PASS
N77-3450-3550	30	100	DFT-QPSK	M	Outer_Full	NV	-20	-28.760000	-0.008217	±2.5	PASS
N77-3450-3550	30	100	DFT-QPSK	M	Outer_Full	NV	-10	-34.790000	-0.009940	±2.5	PASS
N77-3450-3550	30	100	DFT-QPSK	M	Outer_Full	NV	0	-33.710000	-0.009631	±2.5	PASS
N77-3450-3550	30	100	DFT-QPSK	M	Outer_Full	NV	10	-32.210000	-0.009203	±2.5	PASS
N77-3450-3550	30	100	DFT-QPSK	M	Outer_Full	NV	20	-29.440000	-0.008411	±2.5	PASS
N77-3450-3550	30	100	DFT-QPSK	M	Outer_Full	NV	30	-39.640000	-0.011326	±2.5	PASS
N77-3450-3550	30	100	DFT-QPSK	M	Outer_Full	NV	40	-33.220000	-0.009491	±2.5	PASS
N77-3450-3550	30	100	DFT-QPSK	M	Outer_Full	NV	50	-29.050000	-0.008300	±2.5	PASS

Other PA with Ant MAIN for NSA mode

Effective (Isotropic) Radiated Power Output Data for NSA

Test Result

Band	SC S	Bandwidth h	Modulation	Channel I	RB Config	Power (dBm)	EIRP (dBm)	Limit	Verdict
DC_26A_n77A-3450-3550	30	5+20	DFT-PI2BPSK	M+L	Outer_Full	25.07	26.68	30	PASS
DC_26A_n77A-3450-3550	30	5+20	DFT-PI2BPSK	M+L	Inner_Full	25.45	27.06	30	PASS
DC_26A_n77A-3450-3550	30	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Left	22.79	24.4	30	PASS
DC_26A_n77A-3450-3550	30	5+20	DFT-PI2BPSK	M+L	Edge_1RB_Right	22.58	24.19	30	PASS
DC_26A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	23.73	25.34	30	PASS
DC_26A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Inner_Full	25.49	27.1	30	PASS
DC_26A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	22.79	24.4	30	PASS
DC_26A_n77A-3450-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	22.65	24.26	30	PASS
DC_26A_n77A-3450-3550	30	5+20	DFT-16QAM	M+L	Outer_Full	23.47	25.08	30	PASS
DC_26A_n77A-3450-3550	30	5+20	DFT-16QAM	M+L	Inner_Full	24.66	26.27	30	PASS
DC_26A_n77A-3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	21.68	23.29	30	PASS
DC_26A_n77A-3450-3550	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	21.55	23.16	30	PASS
DC_26A_n77A-3450-3550	30	5+20	DFT-64QAM	M+L	Outer_Full	23.19	24.8	30	PASS
DC_26A_n77A-3450-3550	30	5+20	DFT-64QAM	M+L	Inner_Full	23.16	24.77	30	PASS
DC_26A_n77A-3450-3550	30	5+20	DFT-64QAM	M+L	Edge_1RB_Left	21.42	23.03	30	PASS

DC_26A_n77A-3450-355 0	30	5+20	DFT-64QAM	M+L	Edge_1RB_Righ t	21.13	22.74	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-256QAM	M+L	Outer_Full	21.12	22.73	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-256QAM	M+L	Inner_Full	21.23	22.84	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-256QAM	M+L	Edge_1RB_Left	21.36	22.97	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-256QAM	M+L	Edge_1RB_Righ t	21.32	22.93	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-PI2BPS K	M+M	Outer_Full	24.91	26.52	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-PI2BPS K	M+M	Inner_Full	25.39	27	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-PI2BPS K	M+M	Edge_1RB_Left	22.55	24.16	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-PI2BPS K	M+M	Edge_1RB_Righ t	22.33	23.94	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-QPSK	M+M	Outer_Full	23.47	25.08	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-QPSK	M+M	Inner_Full	25.34	26.95	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	22.69	24.3	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-QPSK	M+M	Edge_1RB_Righ t	22.36	23.97	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-16QAM	M+M	Outer_Full	23.44	25.05	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-16QAM	M+M	Inner_Full	24.41	26.02	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	21.48	23.09	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-16QAM	M+M	Edge_1RB_Righ t	21.21	22.82	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-64QAM	M+M	Outer_Full	22.9	24.51	30	PASS

DC_26A_n77A-3450-355 0	30	5+20	DFT-64QAM	M+M	Inner_Full	22.97	24.58	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-64QAM	M+M	Edge_1RB_Left	21.08	22.69	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-64QAM	M+M	Edge_1RB_Right	20.79	22.4	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-256QAM	M+M	Outer_Full	20.97	22.58	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-256QAM	M+M	Inner_Full	21.06	22.67	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-256QAM	M+M	Edge_1RB_Left	21.24	22.85	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-256QAM	M+M	Edge_1RB_Right	20.92	22.53	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-PI2BPS K	M+H	Outer_Full	24.94	26.55	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-PI2BPS K	M+H	Inner_Full	25.41	27.02	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-PI2BPS K	M+H	Edge_1RB_Left	22.6	24.21	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-PI2BPS K	M+H	Edge_1RB_Right	22.45	24.06	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-QPSK	M+H	Outer_Full	23.58	25.19	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-QPSK	M+H	Inner_Full	25.35	26.96	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	22.67	24.28	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	22.49	24.1	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-16QAM	M+H	Outer_Full	23.5	25.11	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-16QAM	M+H	Inner_Full	24.51	26.12	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	21.72	23.33	30	PASS

DC_26A_n77A-3450-355 0	30	5+20	DFT-16QAM	M+H	Edge_1RB_Righ t	21.43	23.04	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-64QAM	M+H	Outer_Full	23.07	24.68	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-64QAM	M+H	Inner_Full	23.01	24.62	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-64QAM	M+H	Edge_1RB_Left	21	22.61	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-64QAM	M+H	Edge_1RB_Righ t	21.12	22.73	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-256QAM	M+H	Outer_Full	21.17	22.78	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-256QAM	M+H	Inner_Full	21.12	22.73	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-256QAM	M+H	Edge_1RB_Left	21.08	22.69	30	PASS
DC_26A_n77A-3450-355 0	30	5+20	DFT-256QAM	M+H	Edge_1RB_Righ t	20.98	22.59	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-PI2BPS K	M+L	Outer_Full	25.05	26.66	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-PI2BPS K	M+L	Inner_Full	25.39	27	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-PI2BPS K	M+L	Edge_1RB_Left	22.7	24.31	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-PI2BPS K	M+L	Edge_1RB_Righ t	22.53	24.14	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-QPSK	M+L	Outer_Full	23.6	25.21	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-QPSK	M+L	Inner_Full	25.43	27.04	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	22.79	24.4	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-QPSK	M+L	Edge_1RB_Righ t	22.44	24.05	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-16QAM	M+L	Outer_Full	23.57	25.18	30	PASS

DC_26A_n77A-3450-355 0	30	5+30	DFT-16QAM	M+L	Inner_Full	24.65	26.26	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	21.62	23.23	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	21.59	23.2	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-64QAM	M+L	Outer_Full	23.14	24.75	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-64QAM	M+L	Inner_Full	23.04	24.65	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-64QAM	M+L	Edge_1RB_Left	21.23	22.84	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-64QAM	M+L	Edge_1RB_Right	21.01	22.62	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-256QAM	M+L	Outer_Full	21.07	22.68	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-256QAM	M+L	Inner_Full	21.19	22.8	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-256QAM	M+L	Edge_1RB_Left	21.19	22.8	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-256QAM	M+L	Edge_1RB_Right	20.91	22.52	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-PI2BPS K	M+M	Outer_Full	25.03	26.64	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-PI2BPS K	M+M	Inner_Full	25.33	26.94	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-PI2BPS K	M+M	Edge_1RB_Left	22.5	24.11	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-PI2BPS K	M+M	Edge_1RB_Right	22.47	24.08	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-QPSK	M+M	Outer_Full	23.52	25.13	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-QPSK	M+M	Inner_Full	25.39	27	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	22.56	24.17	30	PASS

DC_26A_n77A-3450-355 0	30	5+30	DFT-QPSK	M+M	Edge_1RB_Righ t	22.55	24.16	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-16QAM	M+M	Outer_Full	23.5	25.11	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-16QAM	M+M	Inner_Full	24.44	26.05	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	21.4	23.01	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-16QAM	M+M	Edge_1RB_Righ t	21.32	22.93	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-64QAM	M+M	Outer_Full	22.99	24.6	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-64QAM	M+M	Inner_Full	22.97	24.58	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-64QAM	M+M	Edge_1RB_Left	21	22.61	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-64QAM	M+M	Edge_1RB_Righ t	20.9	22.51	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-256QAM	M+M	Outer_Full	20.98	22.59	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-256QAM	M+M	Inner_Full	20.97	22.58	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-256QAM	M+M	Edge_1RB_Left	20.94	22.55	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-256QAM	M+M	Edge_1RB_Righ t	21.15	22.76	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-PI2BPS K	M+H	Outer_Full	25.12	26.73	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-PI2BPS K	M+H	Inner_Full	25.46	27.07	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-PI2BPS K	M+H	Edge_1RB_Left	22.56	24.17	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-PI2BPS K	M+H	Edge_1RB_Righ t	22.67	24.28	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-QPSK	M+H	Outer_Full	23.69	25.3	30	PASS

DC_26A_n77A-3450-355 0	30	5+30	DFT-QPSK	M+H	Inner_Full	25.49	27.1	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	22.53	24.14	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	22.55	24.16	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-16QAM	M+H	Outer_Full	23.56	25.17	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-16QAM	M+H	Inner_Full	24.61	26.22	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	21.45	23.06	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	21.53	23.14	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-64QAM	M+H	Outer_Full	23.08	24.69	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-64QAM	M+H	Inner_Full	23.21	24.82	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-64QAM	M+H	Edge_1RB_Left	20.84	22.45	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-64QAM	M+H	Edge_1RB_Right	21.16	22.77	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-256QAM	M+H	Outer_Full	21.18	22.79	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-256QAM	M+H	Inner_Full	21.24	22.85	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-256QAM	M+H	Edge_1RB_Left	21.02	22.63	30	PASS
DC_26A_n77A-3450-355 0	30	5+30	DFT-256QAM	M+H	Edge_1RB_Right	21.08	22.69	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-PI2BPS K	M+L	Outer_Full	25.11	26.72	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-PI2BPS K	M+L	Inner_Full	25.38	26.99	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-PI2BPS K	M+L	Edge_1RB_Left	22.74	24.35	30	PASS

DC_26A_n77A-3450-355 0	30	5+40	DFT-PI2BPS K	M+L	Edge_1RB_Righ t	22.71	24.32	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-QPSK	M+L	Outer_Full	23.56	25.17	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-QPSK	M+L	Inner_Full	25.4	27.01	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	22.7	24.31	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-QPSK	M+L	Edge_1RB_Righ t	22.71	24.32	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-16QAM	M+L	Outer_Full	23.57	25.18	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-16QAM	M+L	Inner_Full	24.5	26.11	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	21.55	23.16	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-16QAM	M+L	Edge_1RB_Righ t	21.57	23.18	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-64QAM	M+L	Outer_Full	23.18	24.79	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-64QAM	M+L	Inner_Full	22.99	24.6	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-64QAM	M+L	Edge_1RB_Left	21.34	22.95	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-64QAM	M+L	Edge_1RB_Righ t	21.25	22.86	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-256QAM	M+L	Outer_Full	21.14	22.75	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-256QAM	M+L	Inner_Full	21.01	22.62	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-256QAM	M+L	Edge_1RB_Left	21.26	22.87	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-256QAM	M+L	Edge_1RB_Righ t	21.14	22.75	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-PI2BPS K	M+M	Outer_Full	25.03	26.64	30	PASS

DC_26A_n77A-3450-355 0	30	5+40	DFT-PI2BPS K	M+M	Inner_Full	25.27	26.88	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-PI2BPS K	M+M	Edge_1RB_Left	22.4	24.01	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-PI2BPS K	M+M	Edge_1RB_Righ t	22.63	24.24	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-QPSK	M+M	Outer_Full	23.46	25.07	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-QPSK	M+M	Inner_Full	25.37	26.98	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	22.37	23.98	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-QPSK	M+M	Edge_1RB_Righ t	22.6	24.21	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-16QAM	M+M	Outer_Full	23.44	25.05	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-16QAM	M+M	Inner_Full	24.41	26.02	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	21.37	22.98	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-16QAM	M+M	Edge_1RB_Righ t	21.57	23.18	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-64QAM	M+M	Outer_Full	23	24.61	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-64QAM	M+M	Inner_Full	22.97	24.58	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-64QAM	M+M	Edge_1RB_Left	21.16	22.77	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-64QAM	M+M	Edge_1RB_Righ t	21.22	22.83	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-256QAM	M+M	Outer_Full	21.01	22.62	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-256QAM	M+M	Inner_Full	21.07	22.68	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-256QAM	M+M	Edge_1RB_Left	20.91	22.52	30	PASS

DC_26A_n77A-3450-355 0	30	5+40	DFT-256QAM	M+M	Edge_1RB_Righ t	21.12	22.73	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-PI2BPS K	M+H	Outer_Full	25.02	26.63	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-PI2BPS K	M+H	Inner_Full	25.48	27.09	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-PI2BPS K	M+H	Edge_1RB_Left	22.45	24.06	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-PI2BPS K	M+H	Edge_1RB_Righ t	22.49	24.1	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-QPSK	M+H	Outer_Full	23.5	25.11	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-QPSK	M+H	Inner_Full	25.37	26.98	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	22.43	24.04	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-QPSK	M+H	Edge_1RB_Righ t	22.54	24.15	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-16QAM	M+H	Outer_Full	23.57	25.18	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-16QAM	M+H	Inner_Full	24.5	26.11	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	21.3	22.91	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-16QAM	M+H	Edge_1RB_Righ t	21.48	23.09	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-64QAM	M+H	Outer_Full	23.06	24.67	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-64QAM	M+H	Inner_Full	23.01	24.62	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-64QAM	M+H	Edge_1RB_Left	20.9	22.51	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-64QAM	M+H	Edge_1RB_Righ t	21	22.61	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-256QAM	M+H	Outer_Full	20.97	22.58	30	PASS

DC_26A_n77A-3450-355 0	30	5+40	DFT-256QAM	M+H	Inner_Full	21.1	22.71	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-256QAM	M+H	Edge_1RB_Left	20.95	22.56	30	PASS
DC_26A_n77A-3450-355 0	30	5+40	DFT-256QAM	M+H	Edge_1RB_Right	21.11	22.72	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-PI2BPS K	M+L	Outer_Full	24.97	26.58	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-PI2BPS K	M+L	Inner_Full	25.41	27.02	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-PI2BPS K	M+L	Edge_1RB_Left	22.64	24.25	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-PI2BPS K	M+L	Edge_1RB_Right	22.56	24.17	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-QPSK	M+L	Outer_Full	23.56	25.17	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-QPSK	M+L	Inner_Full	25.32	26.93	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	22.68	24.29	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	22.4	24.01	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-16QAM	M+L	Outer_Full	23.43	25.04	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-16QAM	M+L	Inner_Full	24.42	26.03	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	21.64	23.25	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	21.38	22.99	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-64QAM	M+L	Outer_Full	23.03	24.64	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-64QAM	M+L	Inner_Full	22.95	24.56	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-64QAM	M+L	Edge_1RB_Left	21.13	22.74	30	PASS

DC_26A_n77A-3450-355 0	30	5+50	DFT-64QAM	M+L	Edge_1RB_Righ t	20.96	22.57	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-256QAM	M+L	Outer_Full	20.91	22.52	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-256QAM	M+L	Inner_Full	20.83	22.44	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-256QAM	M+L	Edge_1RB_Left	21.13	22.74	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-256QAM	M+L	Edge_1RB_Righ t	21	22.61	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-PI2BPS K	M+M	Outer_Full	24.84	26.45	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-PI2BPS K	M+M	Inner_Full	25.24	26.85	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-PI2BPS K	M+M	Edge_1RB_Left	22.28	23.89	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-PI2BPS K	M+M	Edge_1RB_Righ t	22.5	24.11	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-QPSK	M+M	Outer_Full	23.38	24.99	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-QPSK	M+M	Inner_Full	25.26	26.87	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	22.26	23.87	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-QPSK	M+M	Edge_1RB_Righ t	22.41	24.02	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-16QAM	M+M	Outer_Full	23.38	24.99	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-16QAM	M+M	Inner_Full	24.36	25.97	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	21.09	22.7	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-16QAM	M+M	Edge_1RB_Righ t	21.48	23.09	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-64QAM	M+M	Outer_Full	22.87	24.48	30	PASS

DC_26A_n77A-3450-355 0	30	5+50	DFT-64QAM	M+M	Inner_Full	22.94	24.55	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-64QAM	M+M	Edge_1RB_Left	20.65	22.26	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-64QAM	M+M	Edge_1RB_Right	21.12	22.73	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-256QAM	M+M	Outer_Full	20.83	22.44	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-256QAM	M+M	Inner_Full	20.91	22.52	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-256QAM	M+M	Edge_1RB_Left	20.66	22.27	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-256QAM	M+M	Edge_1RB_Right	20.94	22.55	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-PI2BPS K	M+H	Outer_Full	24.84	26.45	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-PI2BPS K	M+H	Inner_Full	25.33	26.94	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-PI2BPS K	M+H	Edge_1RB_Left	22.27	23.88	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-PI2BPS K	M+H	Edge_1RB_Right	22.3	23.91	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-QPSK	M+H	Outer_Full	23.39	25	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-QPSK	M+H	Inner_Full	25.29	26.9	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	22.34	23.95	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	22.23	23.84	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-16QAM	M+H	Outer_Full	23.37	24.98	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-16QAM	M+H	Inner_Full	24.33	25.94	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	21.26	22.87	30	PASS

DC_26A_n77A-3450-355 0	30	5+50	DFT-16QAM	M+H	Edge_1RB_Righ t	21.19	22.8	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-64QAM	M+H	Outer_Full	22.82	24.43	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-64QAM	M+H	Inner_Full	22.98	24.59	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-64QAM	M+H	Edge_1RB_Left	20.82	22.43	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-64QAM	M+H	Edge_1RB_Righ t	21.06	22.67	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-256QAM	M+H	Outer_Full	20.94	22.55	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-256QAM	M+H	Inner_Full	20.88	22.49	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-256QAM	M+H	Edge_1RB_Left	20.85	22.46	30	PASS
DC_26A_n77A-3450-355 0	30	5+50	DFT-256QAM	M+H	Edge_1RB_Righ t	20.73	22.34	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-PI2BPS K	M+L	Outer_Full	25.01	26.62	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-PI2BPS K	M+L	Inner_Full	25.32	26.93	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-PI2BPS K	M+L	Edge_1RB_Left	22.62	24.23	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-PI2BPS K	M+L	Edge_1RB_Righ t	22.33	23.94	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-QPSK	M+L	Outer_Full	23.52	25.13	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-QPSK	M+L	Inner_Full	25.37	26.98	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	22.74	24.35	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-QPSK	M+L	Edge_1RB_Righ t	22.35	23.96	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-16QAM	M+L	Outer_Full	23.46	25.07	30	PASS

DC_26A_n77A-3450-355 0	30	5+60	DFT-16QAM	M+L	Inner_Full	24.45	26.06	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	21.56	23.17	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	21.37	22.98	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-64QAM	M+L	Outer_Full	23.03	24.64	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-64QAM	M+L	Inner_Full	23	24.61	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-64QAM	M+L	Edge_1RB_Left	21.19	22.8	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-64QAM	M+L	Edge_1RB_Right	20.72	22.33	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-256QAM	M+L	Outer_Full	21.06	22.67	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-256QAM	M+L	Inner_Full	21.01	22.62	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-256QAM	M+L	Edge_1RB_Left	21.16	22.77	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-256QAM	M+L	Edge_1RB_Right	20.87	22.48	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-PI2BPS K	M+M	Outer_Full	24.99	26.6	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-PI2BPS K	M+M	Inner_Full	25.35	26.96	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-PI2BPS K	M+M	Edge_1RB_Left	22.31	23.92	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-PI2BPS K	M+M	Edge_1RB_Right	22.65	24.26	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-QPSK	M+M	Outer_Full	23.47	25.08	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-QPSK	M+M	Inner_Full	25.32	26.93	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	22.32	23.93	30	PASS

DC_26A_n77A-3450-355 0	30	5+60	DFT-QPSK	M+M	Edge_1RB_Righ t	22.57	24.18	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-16QAM	M+M	Outer_Full	23.46	25.07	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-16QAM	M+M	Inner_Full	24.5	26.11	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	21.28	22.89	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-16QAM	M+M	Edge_1RB_Righ t	21.61	23.22	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-64QAM	M+M	Outer_Full	23.02	24.63	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-64QAM	M+M	Inner_Full	22.97	24.58	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-64QAM	M+M	Edge_1RB_Left	21.14	22.75	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-64QAM	M+M	Edge_1RB_Righ t	21.02	22.63	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-256QAM	M+M	Outer_Full	21.07	22.68	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-256QAM	M+M	Inner_Full	21.03	22.64	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-256QAM	M+M	Edge_1RB_Left	20.88	22.49	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-256QAM	M+M	Edge_1RB_Righ t	21.08	22.69	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-PI2BPS K	M+H	Outer_Full	25.05	26.66	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-PI2BPS K	M+H	Inner_Full	25.42	27.03	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-PI2BPS K	M+H	Edge_1RB_Left	22.43	24.04	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-PI2BPS K	M+H	Edge_1RB_Righ t	22.46	24.07	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-QPSK	M+H	Outer_Full	23.52	25.13	30	PASS

DC_26A_n77A-3450-355 0	30	5+60	DFT-QPSK	M+H	Inner_Full	25.42	27.03	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	22.54	24.15	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	22.44	24.05	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-16QAM	M+H	Outer_Full	23.57	25.18	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-16QAM	M+H	Inner_Full	24.57	26.18	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	21.37	22.98	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	21.45	23.06	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-64QAM	M+H	Outer_Full	23.03	24.64	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-64QAM	M+H	Inner_Full	23.04	24.65	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-64QAM	M+H	Edge_1RB_Left	21.16	22.77	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-64QAM	M+H	Edge_1RB_Right	20.97	22.58	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-256QAM	M+H	Outer_Full	21.08	22.69	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-256QAM	M+H	Inner_Full	21.05	22.66	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-256QAM	M+H	Edge_1RB_Left	21.1	22.71	30	PASS
DC_26A_n77A-3450-355 0	30	5+60	DFT-256QAM	M+H	Edge_1RB_Right	21	22.61	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-PI2BPS K	M+L	Outer_Full	25.01	26.62	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-PI2BPS K	M+L	Inner_Full	25.37	26.98	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-PI2BPS K	M+L	Edge_1RB_Left	22.69	24.3	30	PASS

DC_26A_n77A-3450-355 0	30	5+80	DFT-PI2BPS K	M+L	Edge_1RB_Righ t	22.62	24.23	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-QPSK	M+L	Outer_Full	23.59	25.2	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-QPSK	M+L	Inner_Full	25.41	27.02	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	22.74	24.35	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-QPSK	M+L	Edge_1RB_Righ t	22.48	24.09	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-16QAM	M+L	Outer_Full	23.62	25.23	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-16QAM	M+L	Inner_Full	24.53	26.14	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	21.77	23.38	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-16QAM	M+L	Edge_1RB_Righ t	21.55	23.16	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-64QAM	M+L	Outer_Full	23.06	24.67	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-64QAM	M+L	Inner_Full	23.06	24.67	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-64QAM	M+L	Edge_1RB_Left	21.38	22.99	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-64QAM	M+L	Edge_1RB_Righ t	20.99	22.6	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-256QAM	M+L	Outer_Full	21.06	22.67	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-256QAM	M+L	Inner_Full	21.01	22.62	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-256QAM	M+L	Edge_1RB_Left	21.24	22.85	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-256QAM	M+L	Edge_1RB_Righ t	21.15	22.76	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-PI2BPS K	M+M	Outer_Full	24.98	26.59	30	PASS

DC_26A_n77A-3450-355 0	30	5+80	DFT-PI2BPS K	M+M	Inner_Full	25.48	27.09	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-PI2BPS K	M+M	Edge_1RB_Left	22.54	24.15	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-PI2BPS K	M+M	Edge_1RB_Right	22.43	24.04	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-QPSK	M+M	Outer_Full	23.47	25.08	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-QPSK	M+M	Inner_Full	25.3	26.91	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	22.58	24.19	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	22.48	24.09	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-16QAM	M+M	Outer_Full	23.53	25.14	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-16QAM	M+M	Inner_Full	24.45	26.06	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	21.48	23.09	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	21.36	22.97	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-64QAM	M+M	Outer_Full	22.98	24.59	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-64QAM	M+M	Inner_Full	23	24.61	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-64QAM	M+M	Edge_1RB_Left	21.19	22.8	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-64QAM	M+M	Edge_1RB_Right	20.97	22.58	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-256QAM	M+M	Outer_Full	20.99	22.6	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-256QAM	M+M	Inner_Full	21.03	22.64	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-256QAM	M+M	Edge_1RB_Left	21.17	22.78	30	PASS

DC_26A_n77A-3450-355 0	30	5+80	DFT-256QAM	M+M	Edge_1RB_Righ t	21.02	22.63	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-PI2BPS K	M+H	Outer_Full	24.94	26.55	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-PI2BPS K	M+H	Inner_Full	25.32	26.93	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-PI2BPS K	M+H	Edge_1RB_Left	22.43	24.04	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-PI2BPS K	M+H	Edge_1RB_Righ t	22.47	24.08	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-QPSK	M+H	Outer_Full	23.51	25.12	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-QPSK	M+H	Inner_Full	25.36	26.97	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	22.48	24.09	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-QPSK	M+H	Edge_1RB_Righ t	22.49	24.1	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-16QAM	M+H	Outer_Full	23.5	25.11	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-16QAM	M+H	Inner_Full	24.46	26.07	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	21.32	22.93	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-16QAM	M+H	Edge_1RB_Righ t	21.29	22.9	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-64QAM	M+H	Outer_Full	22.95	24.56	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-64QAM	M+H	Inner_Full	23.03	24.64	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-64QAM	M+H	Edge_1RB_Left	20.83	22.44	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-64QAM	M+H	Edge_1RB_Righ t	20.99	22.6	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-256QAM	M+H	Outer_Full	21.06	22.67	30	PASS

DC_26A_n77A-3450-355 0	30	5+80	DFT-256QAM	M+H	Inner_Full	21.09	22.7	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-256QAM	M+H	Edge_1RB_Left	21.07	22.68	30	PASS
DC_26A_n77A-3450-355 0	30	5+80	DFT-256QAM	M+H	Edge_1RB_Right	20.86	22.47	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-PI2BPS K	M+L	Outer_Full	25	26.61	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-PI2BPS K	M+L	Inner_Full	25.33	26.94	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-PI2BPS K	M+L	Edge_1RB_Left	22.66	24.27	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-PI2BPS K	M+L	Edge_1RB_Right	22.49	24.1	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-QPSK	M+L	Outer_Full	23.46	25.07	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-QPSK	M+L	Inner_Full	25.31	26.92	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	22.82	24.43	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	22.42	24.03	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-16QAM	M+L	Outer_Full	23.44	25.05	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-16QAM	M+L	Inner_Full	24.44	26.05	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	21.72	23.33	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	21.48	23.09	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-64QAM	M+L	Outer_Full	23.04	24.65	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-64QAM	M+L	Inner_Full	22.99	24.6	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-64QAM	M+L	Edge_1RB_Left	21.34	22.95	30	PASS

DC_26A_n77A-3450-355 0	30	5+90	DFT-64QAM	M+L	Edge_1RB_Righ t	20.94	22.55	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-256QAM	M+L	Outer_Full	21.03	22.64	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-256QAM	M+L	Inner_Full	21.05	22.66	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-256QAM	M+L	Edge_1RB_Left	21.29	22.9	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-256QAM	M+L	Edge_1RB_Righ t	20.88	22.49	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-PI2BPS K	M+M	Outer_Full	25.02	26.63	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-PI2BPS K	M+M	Inner_Full	25.37	26.98	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-PI2BPS K	M+M	Edge_1RB_Left	22.73	24.34	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-PI2BPS K	M+M	Edge_1RB_Righ t	22.43	24.04	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-QPSK	M+M	Outer_Full	23.52	25.13	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-QPSK	M+M	Inner_Full	25.36	26.97	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	22.58	24.19	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-QPSK	M+M	Edge_1RB_Righ t	22.39	24	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-16QAM	M+M	Outer_Full	23.55	25.16	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-16QAM	M+M	Inner_Full	24.4	26.01	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	21.6	23.21	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-16QAM	M+M	Edge_1RB_Righ t	21.33	22.94	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-64QAM	M+M	Outer_Full	23.04	24.65	30	PASS

DC_26A_n77A-3450-355 0	30	5+90	DFT-64QAM	M+M	Inner_Full	23	24.61	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-64QAM	M+M	Edge_1RB_Left	21.17	22.78	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-64QAM	M+M	Edge_1RB_Right	20.97	22.58	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-256QAM	M+M	Outer_Full	20.97	22.58	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-256QAM	M+M	Inner_Full	21.05	22.66	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-256QAM	M+M	Edge_1RB_Left	21.09	22.7	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-256QAM	M+M	Edge_1RB_Right	20.94	22.55	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-PI2BPS K	M+H	Outer_Full	24.94	26.55	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-PI2BPS K	M+H	Inner_Full	25.41	27.02	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-PI2BPS K	M+H	Edge_1RB_Left	22.62	24.23	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-PI2BPS K	M+H	Edge_1RB_Right	22.52	24.13	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-QPSK	M+H	Outer_Full	23.51	25.12	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-QPSK	M+H	Inner_Full	25.48	27.09	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	22.57	24.18	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	22.43	24.04	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-16QAM	M+H	Outer_Full	23.54	25.15	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-16QAM	M+H	Inner_Full	24.53	26.14	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	21.45	23.06	30	PASS

DC_26A_n77A-3450-355 0	30	5+90	DFT-16QAM	M+H	Edge_1RB_Righ t	21.5	23.11	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-64QAM	M+H	Outer_Full	23.1	24.71	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-64QAM	M+H	Inner_Full	23.05	24.66	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-64QAM	M+H	Edge_1RB_Left	21.03	22.64	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-64QAM	M+H	Edge_1RB_Righ t	20.96	22.57	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-256QAM	M+H	Outer_Full	21.1	22.71	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-256QAM	M+H	Inner_Full	21.11	22.72	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-256QAM	M+H	Edge_1RB_Left	21.06	22.67	30	PASS
DC_26A_n77A-3450-355 0	30	5+90	DFT-256QAM	M+H	Edge_1RB_Righ t	21.24	22.85	30	PASS
DC_26A_n77A-3450-355 0	30	5+100	DFT-PI2BPS K	M+M	Outer_Full	24.98	26.59	30	PASS
DC_26A_n77A-3450-355 0	30	5+100	DFT-PI2BPS K	M+M	Inner_Full	25.37	26.98	30	PASS
DC_26A_n77A-3450-355 0	30	5+100	DFT-PI2BPS K	M+M	Edge_1RB_Left	22.66	24.27	30	PASS
DC_26A_n77A-3450-355 0	30	5+100	DFT-PI2BPS K	M+M	Edge_1RB_Righ t	22.49	24.1	30	PASS
DC_26A_n77A-3450-355 0	30	5+100	DFT-QPSK	M+M	Outer_Full	23.46	25.07	30	PASS
DC_26A_n77A-3450-355 0	30	5+100	DFT-QPSK	M+M	Inner_Full	25.37	26.98	30	PASS
DC_26A_n77A-3450-355 0	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	22.71	24.32	30	PASS
DC_26A_n77A-3450-355 0	30	5+100	DFT-QPSK	M+M	Edge_1RB_Righ t	22.46	24.07	30	PASS
DC_26A_n77A-3450-355 0	30	5+100	DFT-16QAM	M+M	Outer_Full	23.58	25.19	30	PASS

DC_26A_n77A-3450-355 0	30	5+100	DFT-16QAM	M+M	Inner_Full	24.53	26.14	30	PASS
DC_26A_n77A-3450-355 0	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	21.52	23.13	30	PASS
DC_26A_n77A-3450-355 0	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	21.41	23.02	30	PASS
DC_26A_n77A-3450-355 0	30	5+100	DFT-64QAM	M+M	Outer_Full	23.03	24.64	30	PASS
DC_26A_n77A-3450-355 0	30	5+100	DFT-64QAM	M+M	Inner_Full	23.01	24.62	30	PASS
DC_26A_n77A-3450-355 0	30	5+100	DFT-64QAM	M+M	Edge_1RB_Left	21.25	22.86	30	PASS
DC_26A_n77A-3450-355 0	30	5+100	DFT-64QAM	M+M	Edge_1RB_Right	20.94	22.55	30	PASS
DC_26A_n77A-3450-355 0	30	5+100	DFT-256QAM	M+M	Outer_Full	21.05	22.66	30	PASS
DC_26A_n77A-3450-355 0	30	5+100	DFT-256QAM	M+M	Inner_Full	20.97	22.58	30	PASS
DC_26A_n77A-3450-355 0	30	5+100	DFT-256QAM	M+M	Edge_1RB_Left	21.15	22.76	30	PASS
DC_26A_n77A-3450-355 0	30	5+100	DFT-256QAM	M+M	Edge_1RB_Right	20.89	22.5	30	PASS