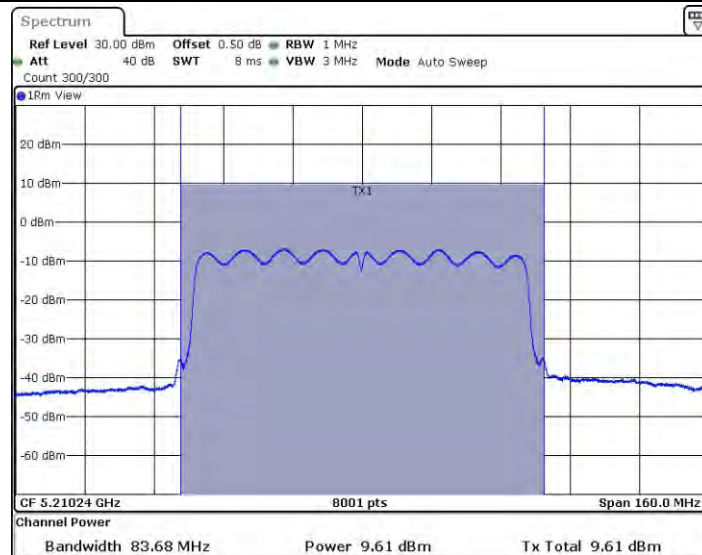
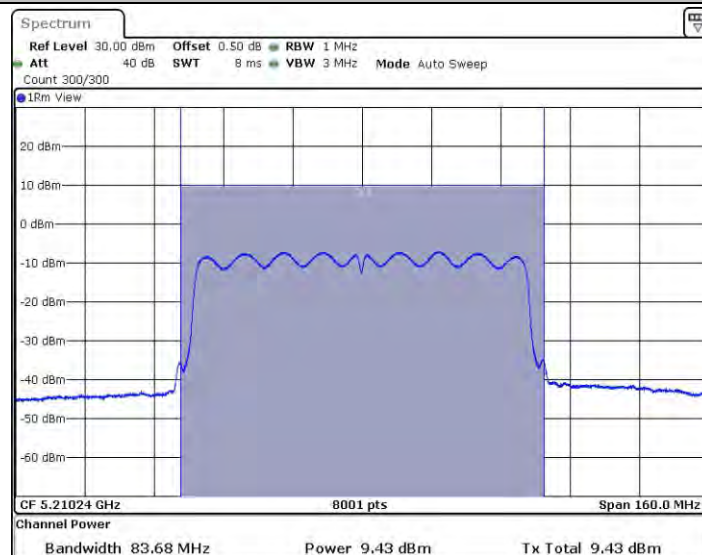
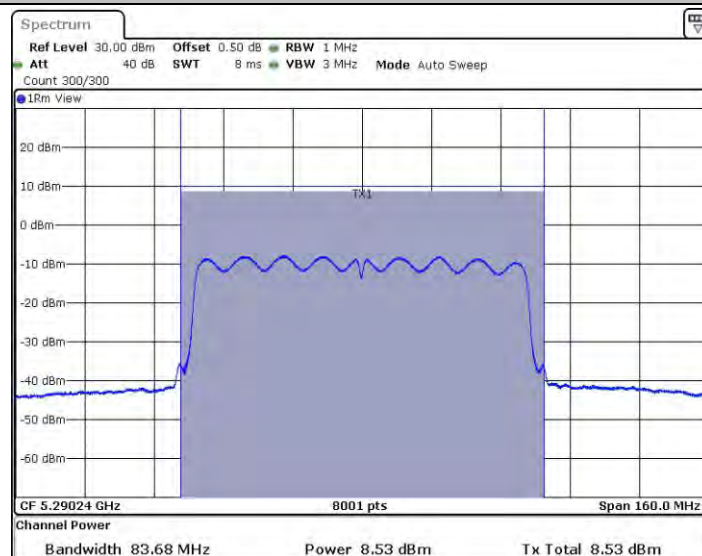




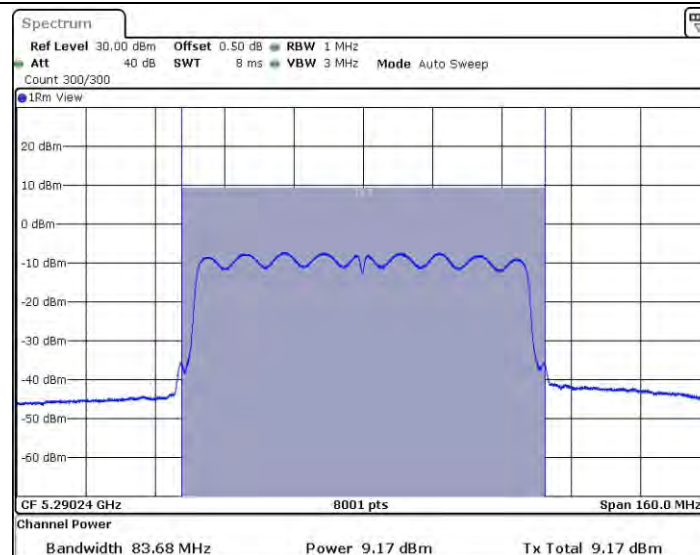
11AC80SISO_Ant1_5210



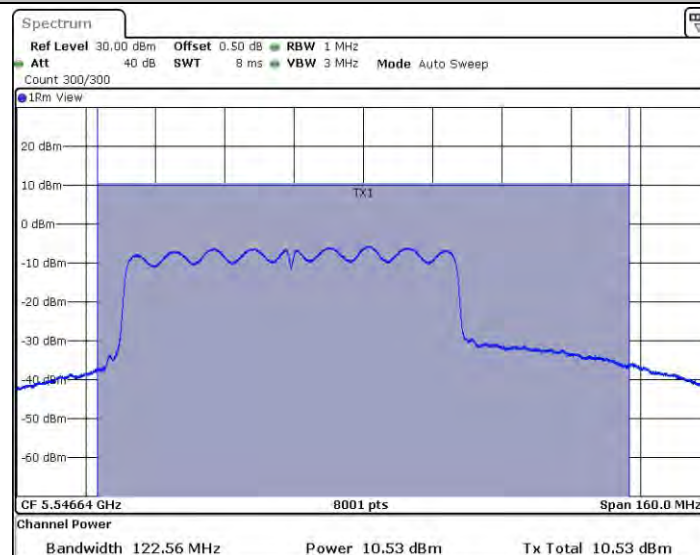
11AC80SISO_Ant2_5210



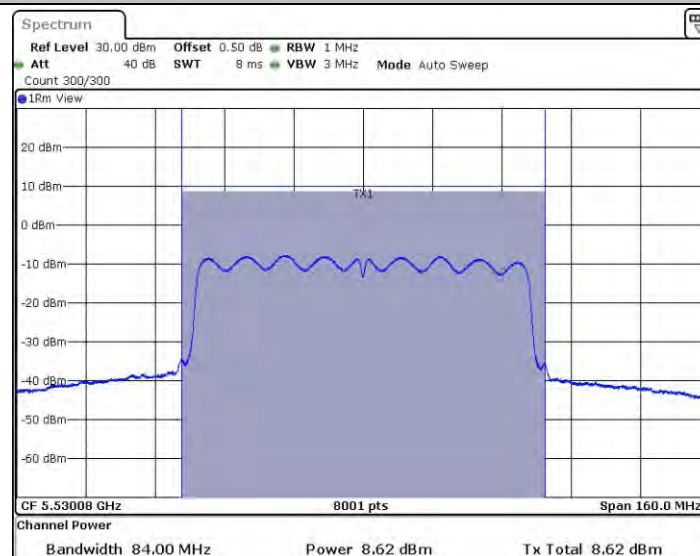
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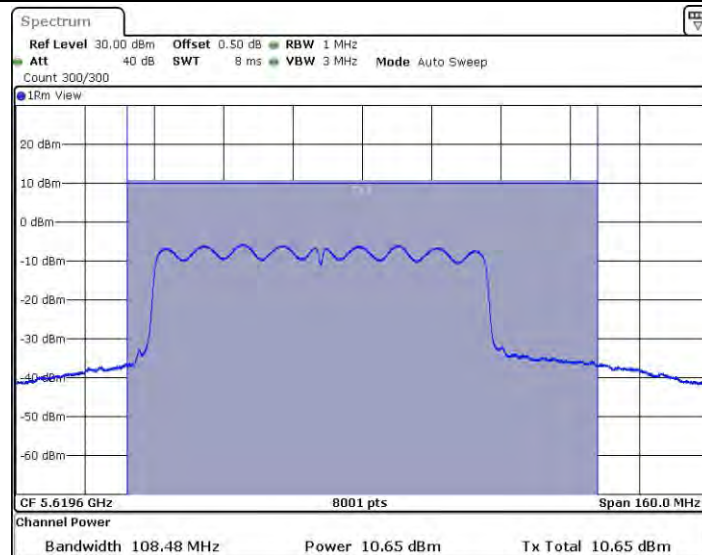
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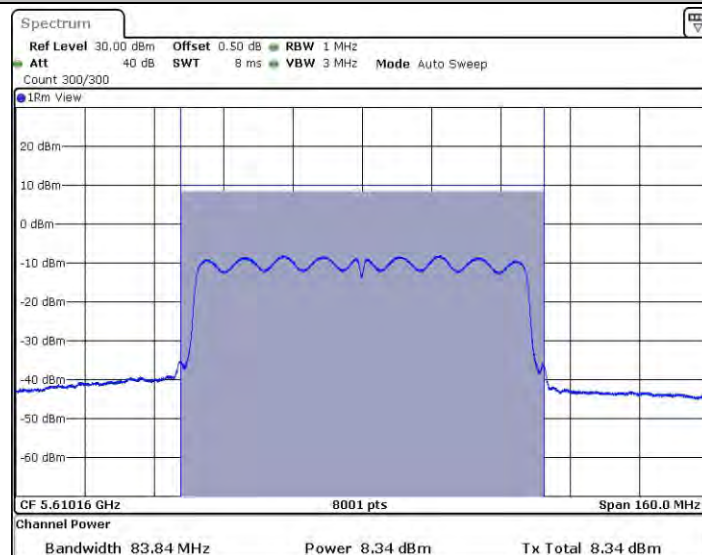
11AC80SISO_Ant1_5530



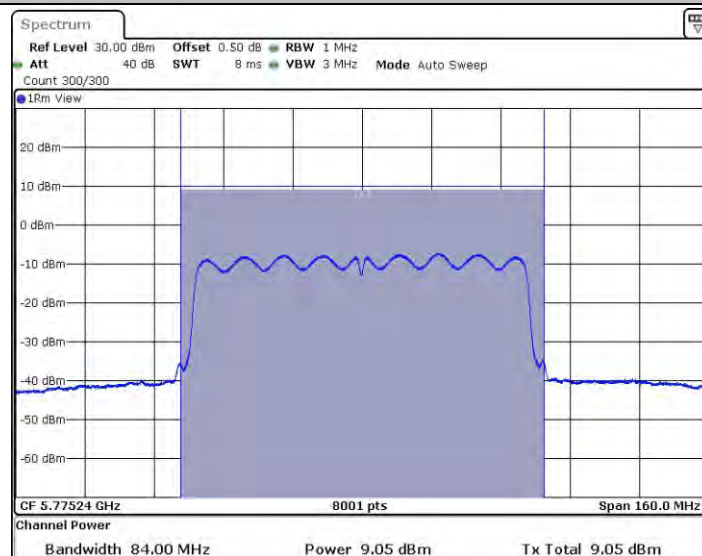
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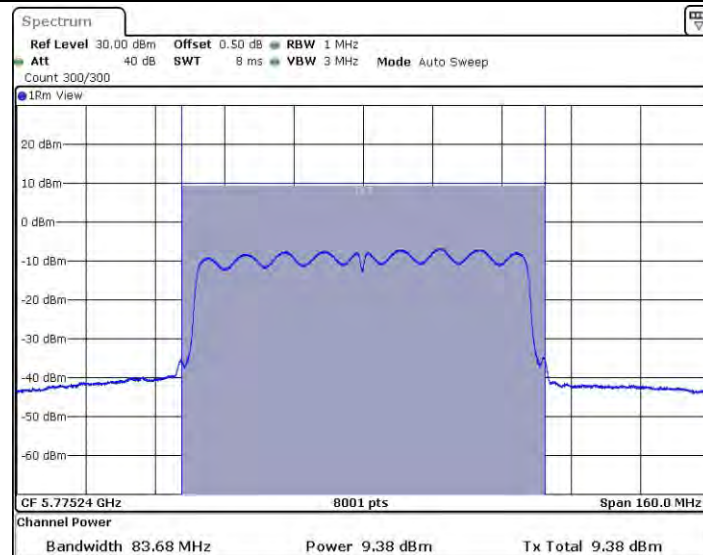
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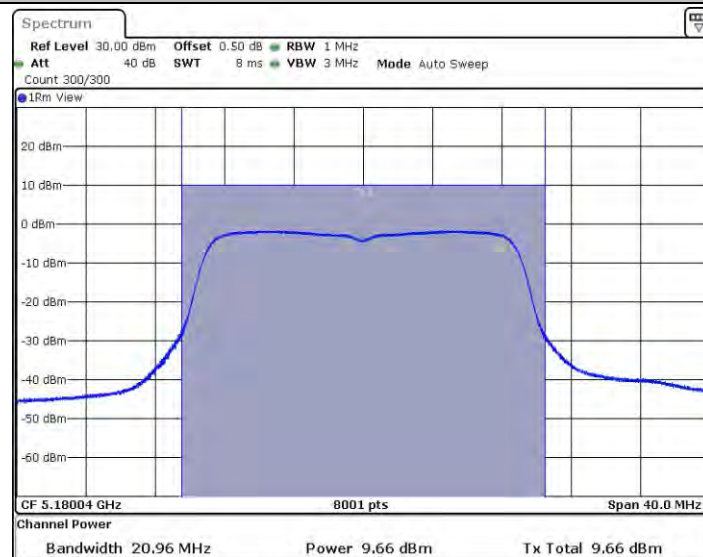
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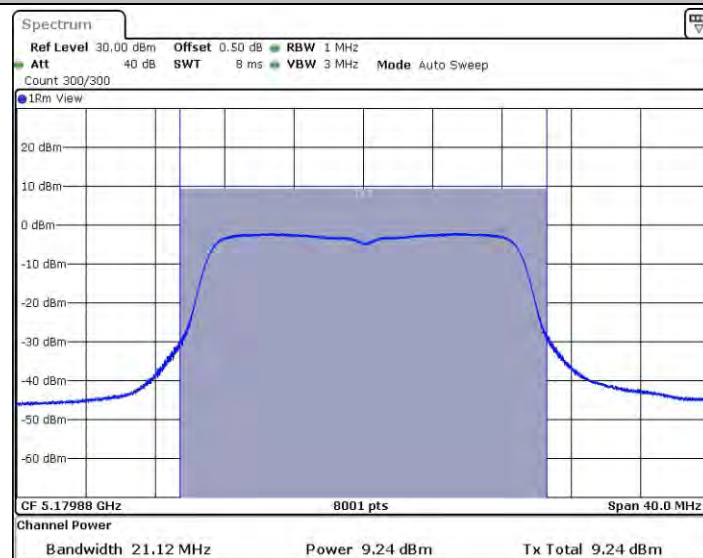
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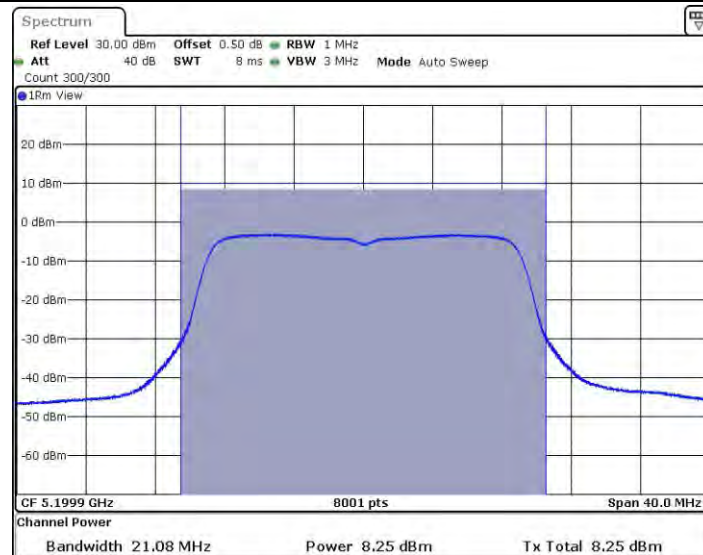
11AC80SISO_Ant2_5775



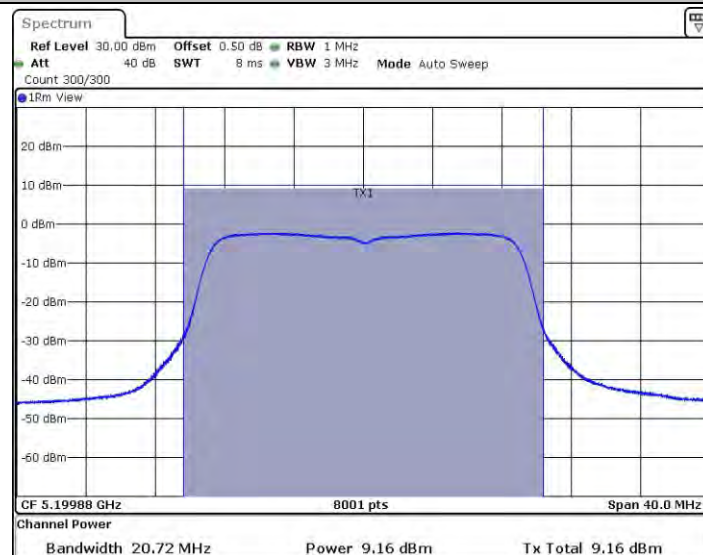
11N20MIMO_Ant1_5180



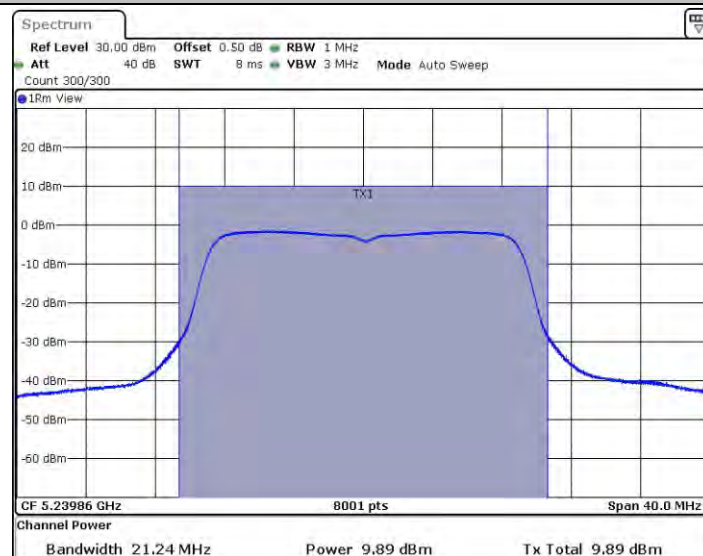
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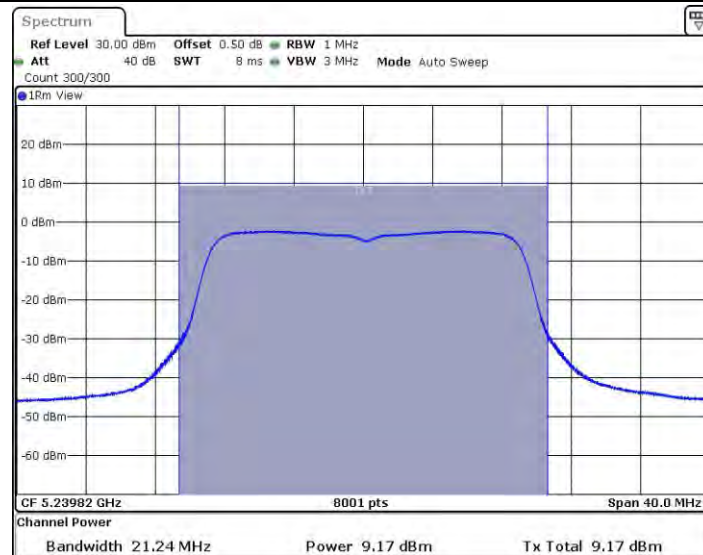
11N20MIMO_Ant1_5200



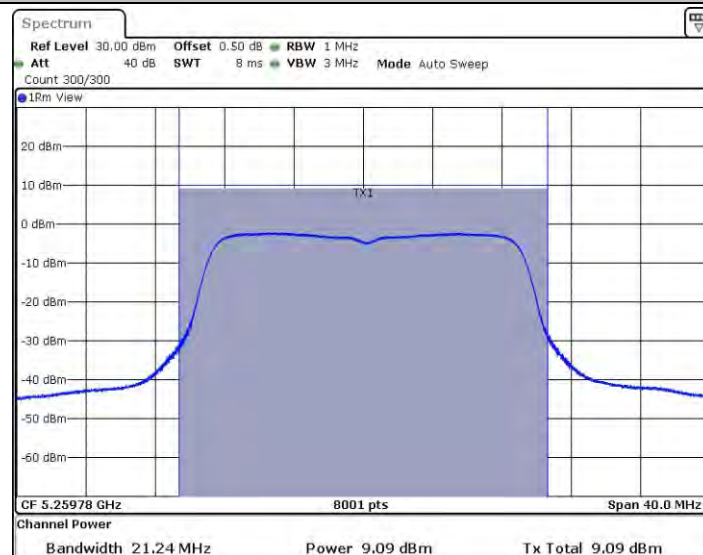
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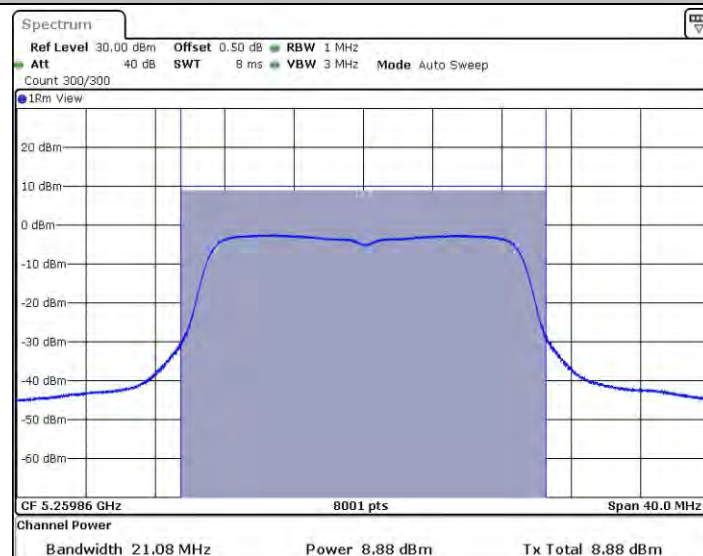
11N20MIMO_Ant1_5240



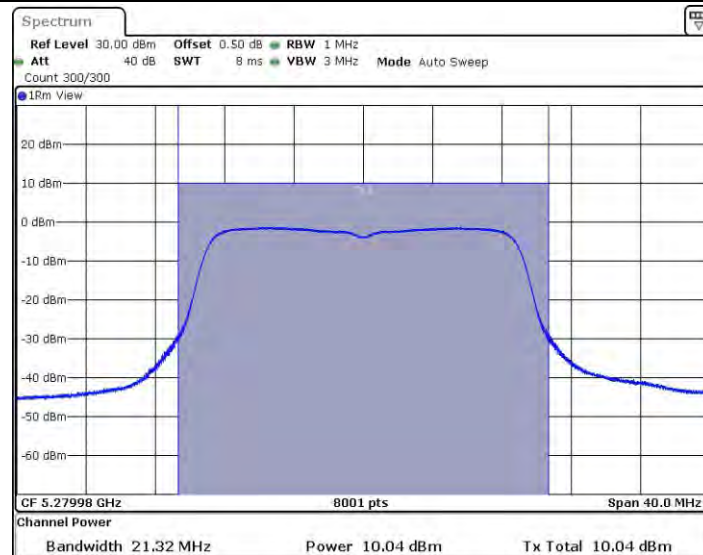
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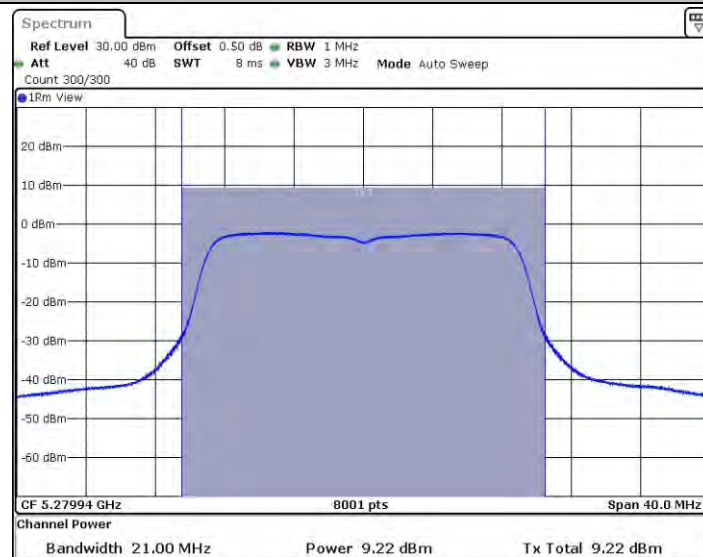
11N20MIMO_Ant1_5260



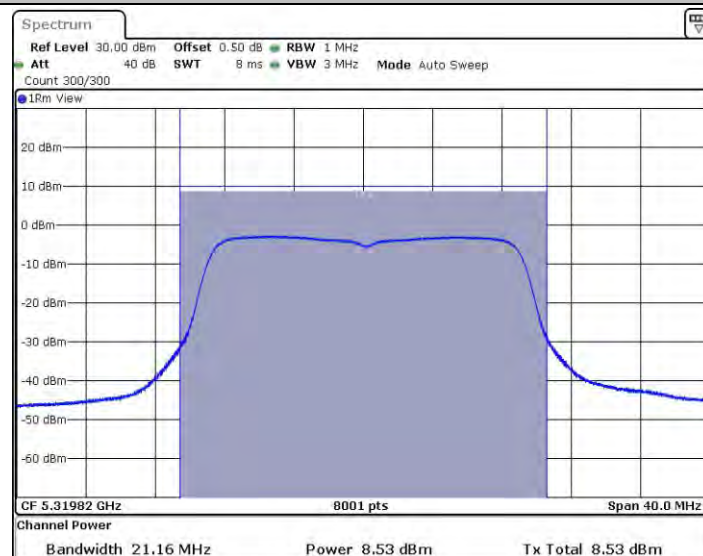
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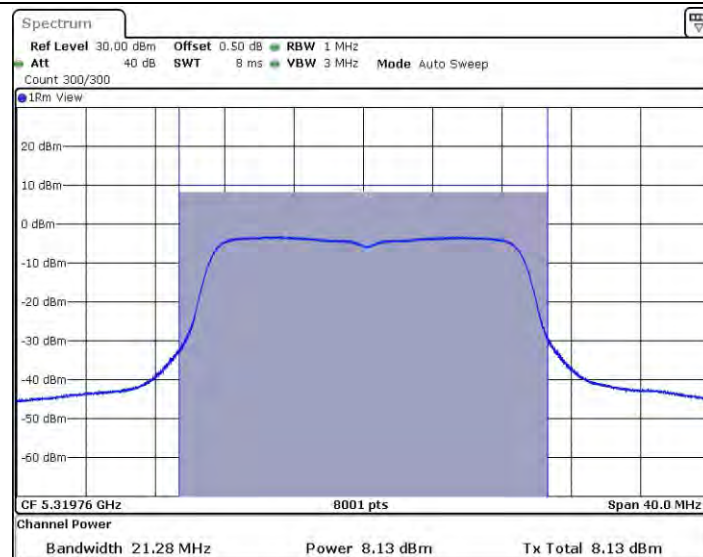
11N20MIMO_Ant1_5280



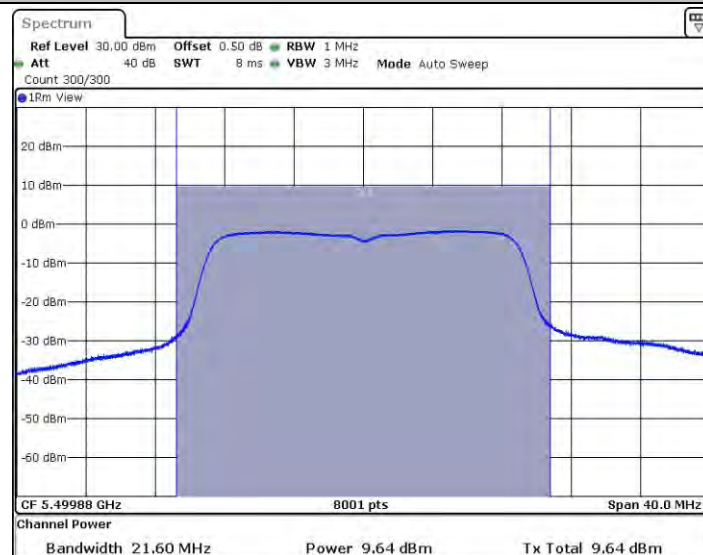
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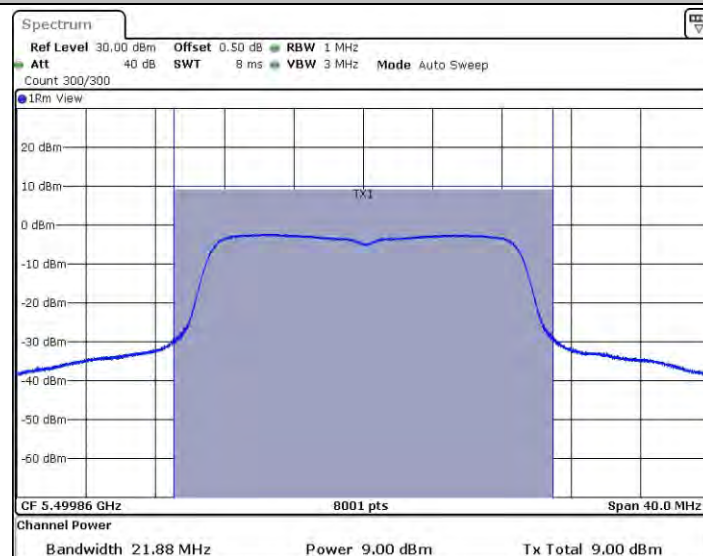
11N20MIMO_Ant1_5320



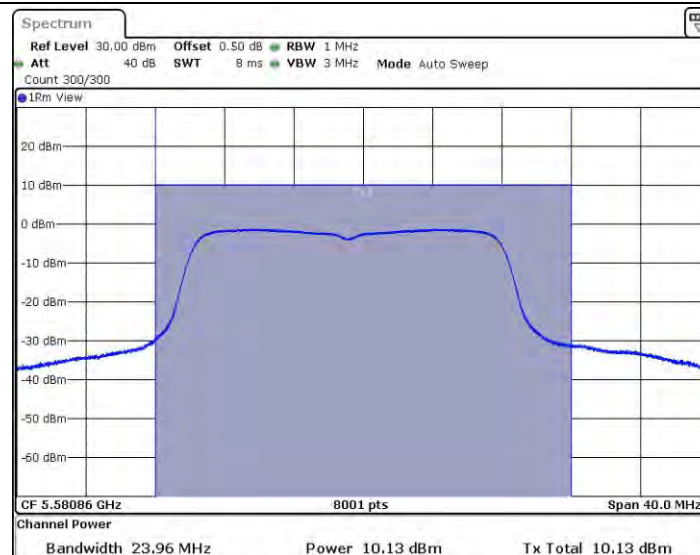
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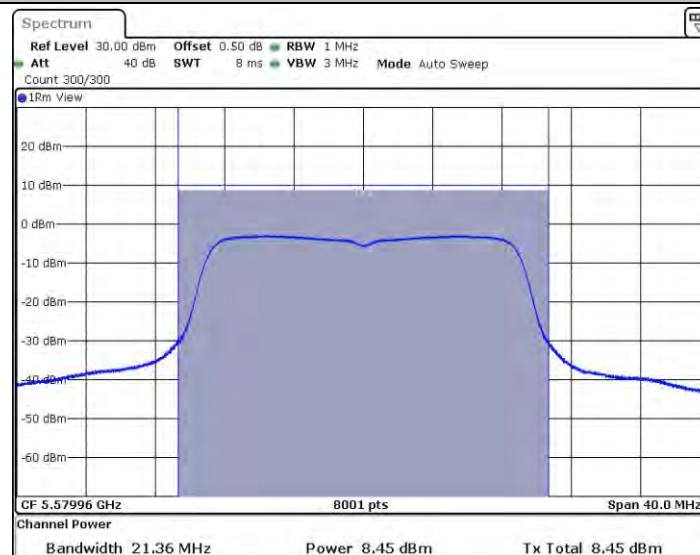
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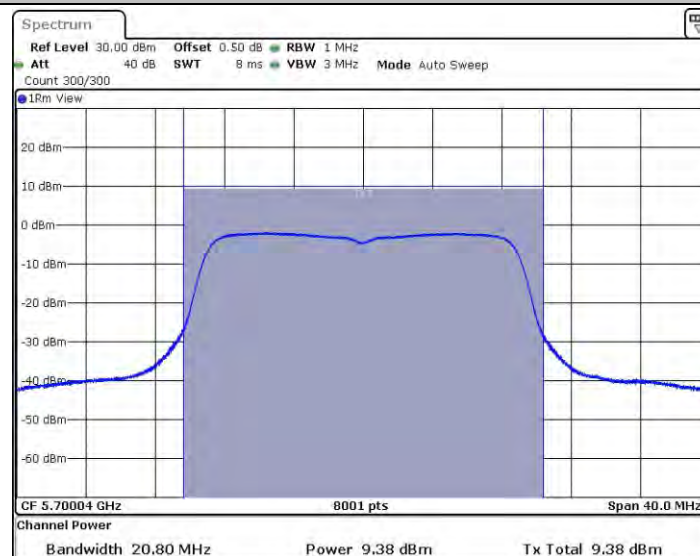
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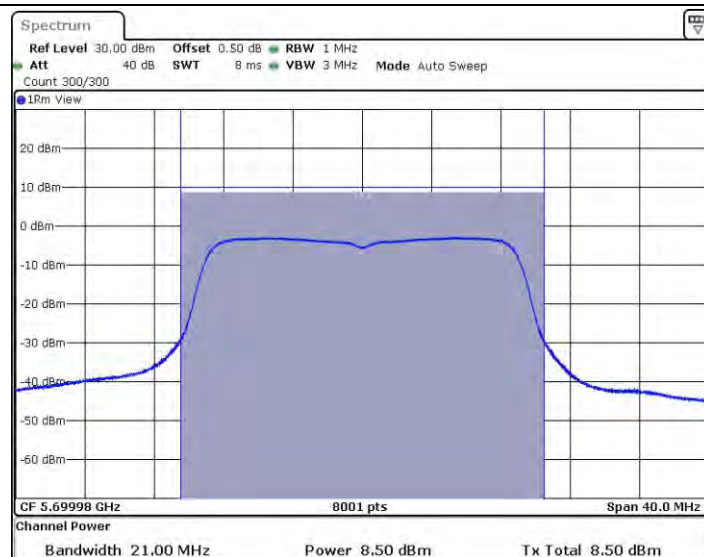
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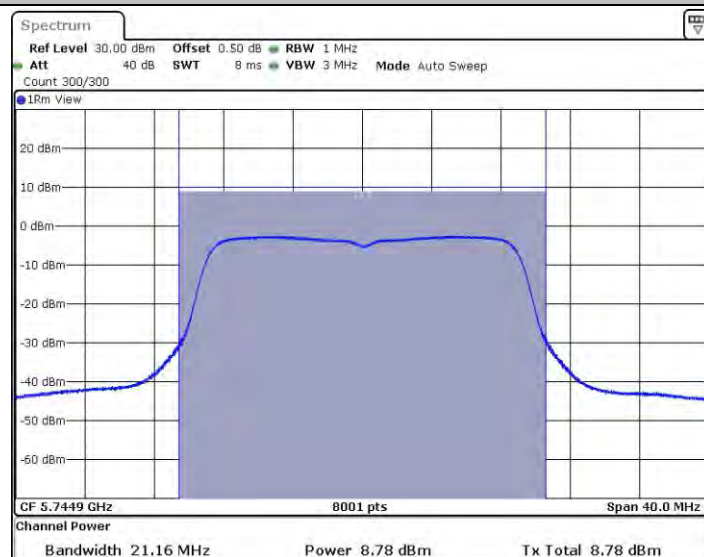
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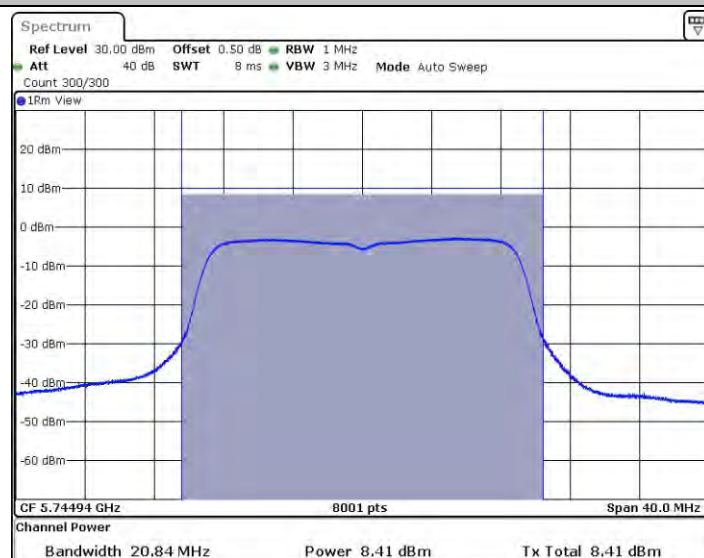
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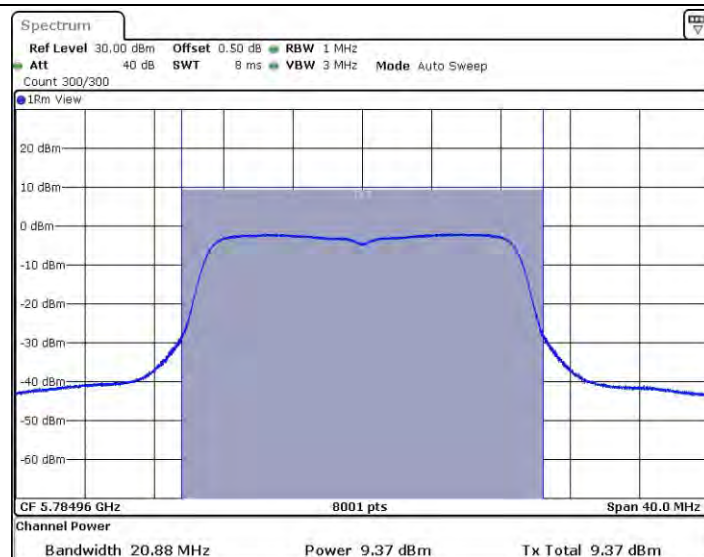
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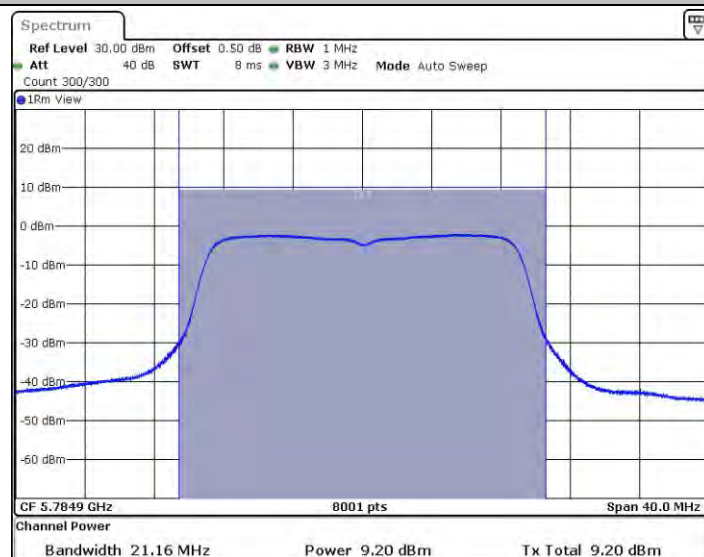
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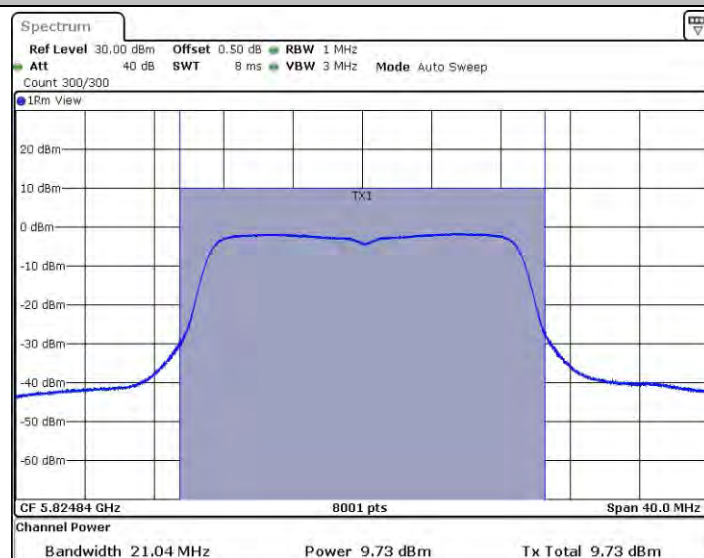
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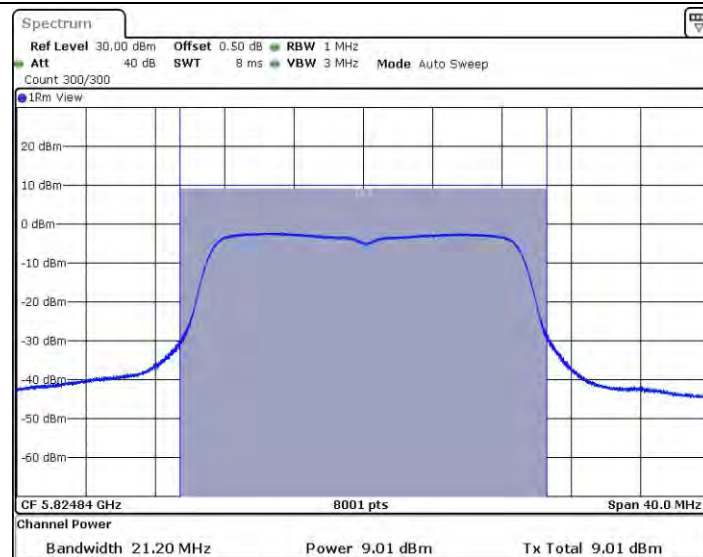
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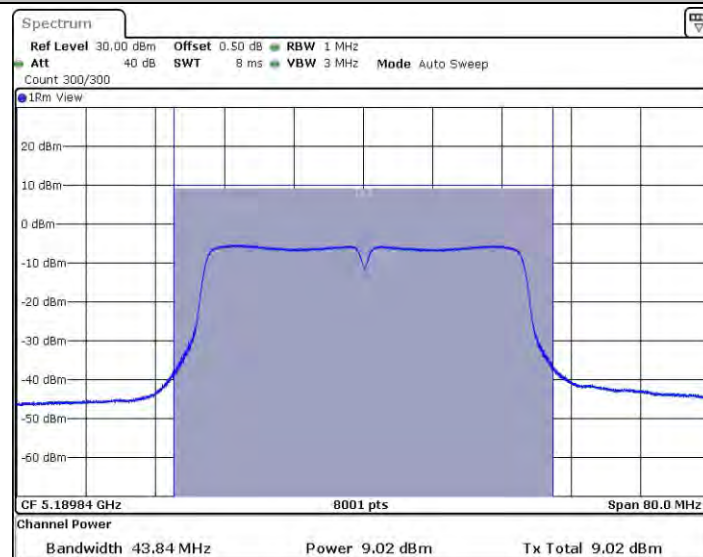
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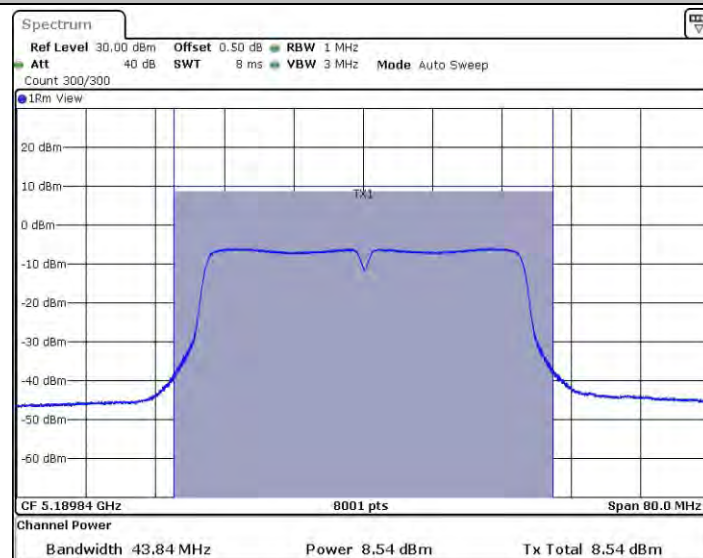
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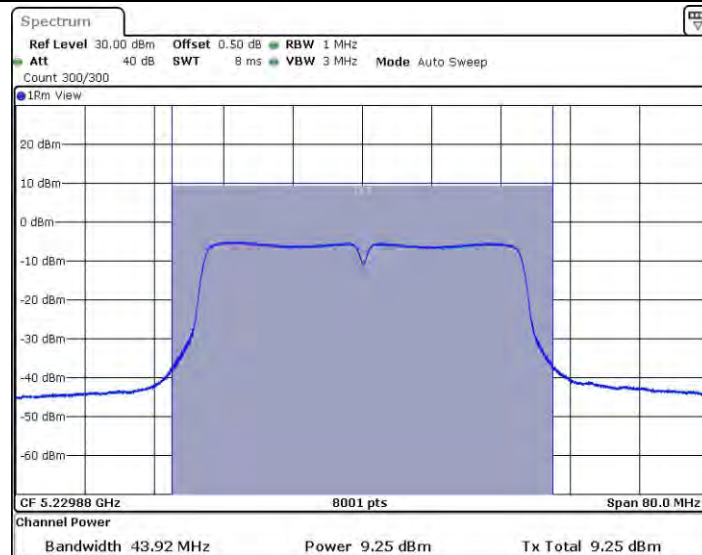
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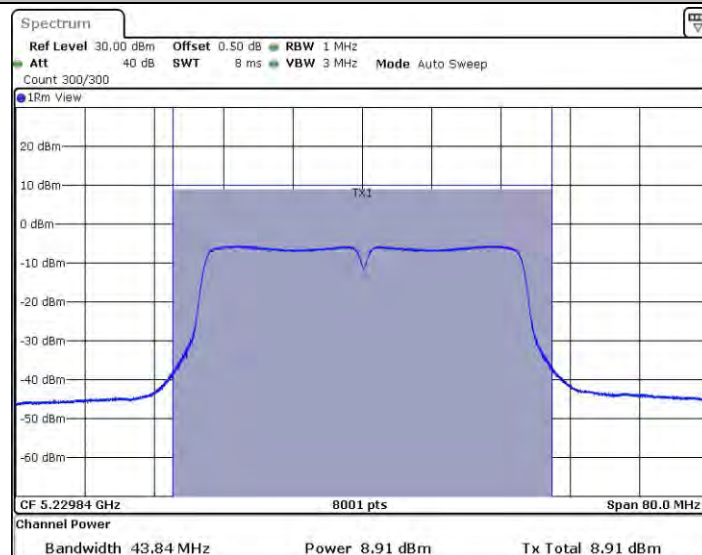
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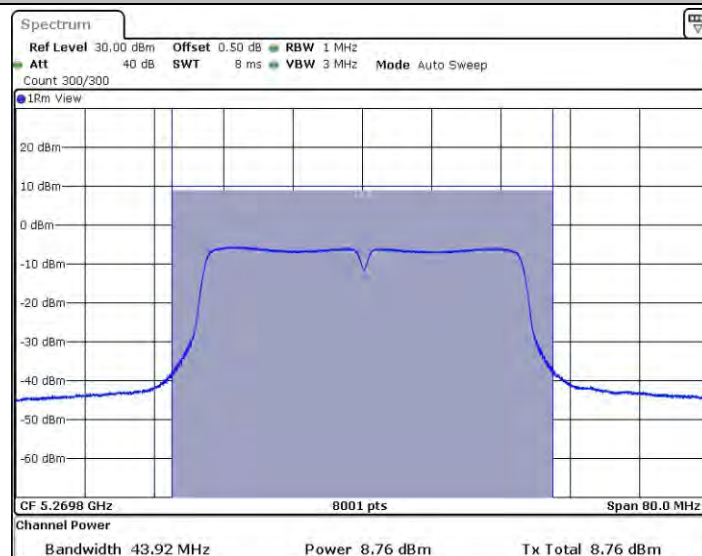
11N40MIMO_Ant2_5190



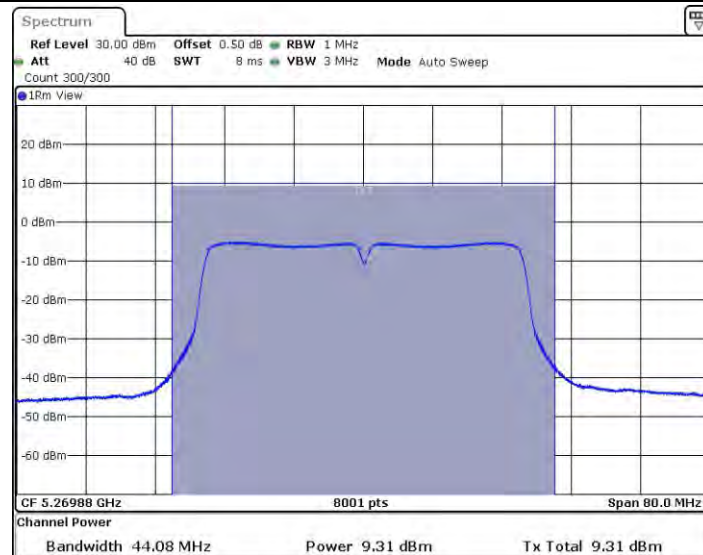
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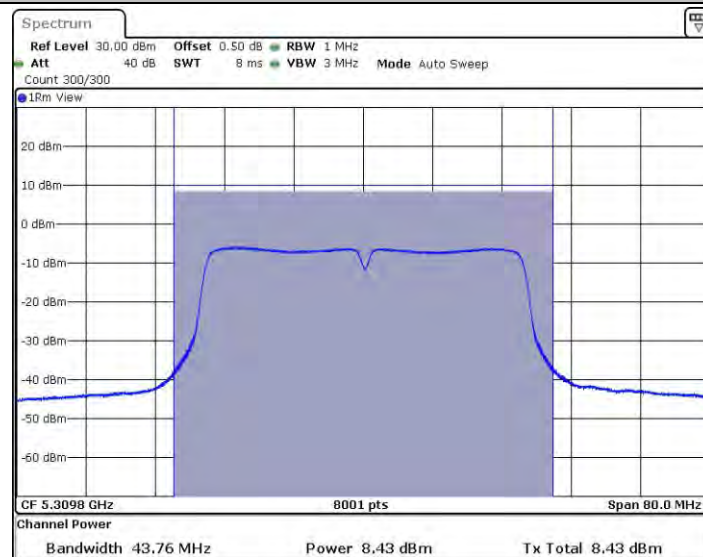
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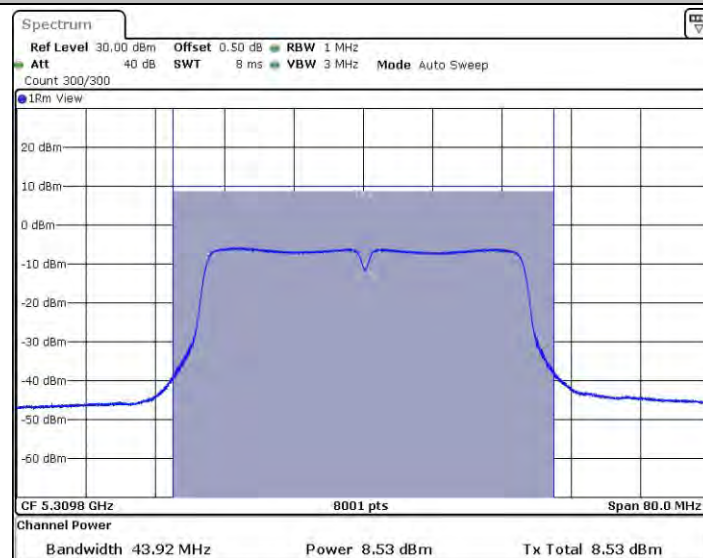
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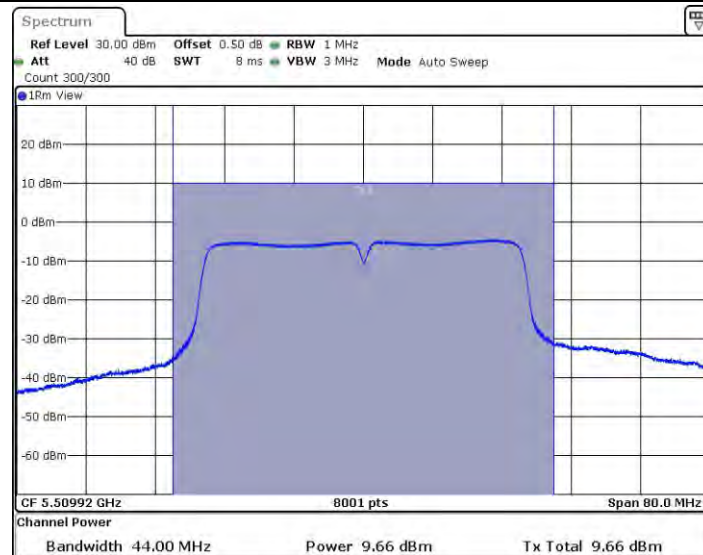
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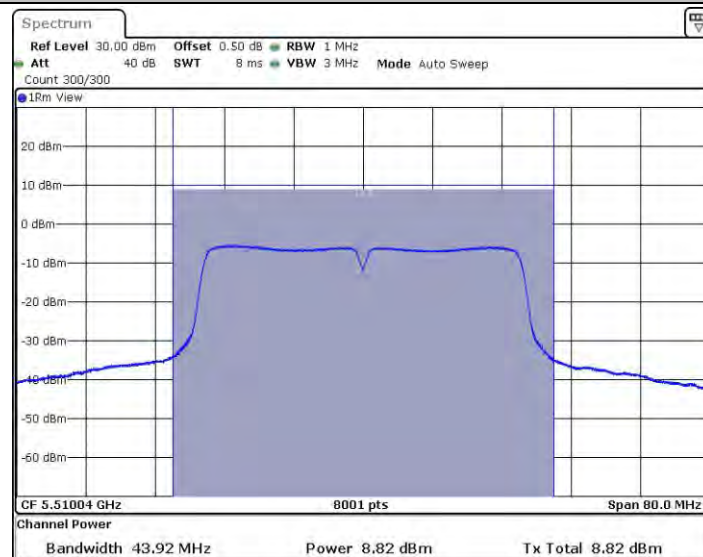
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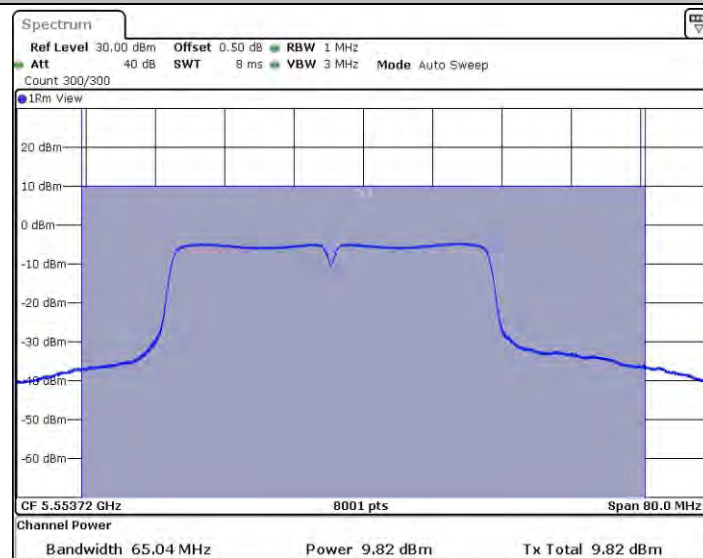
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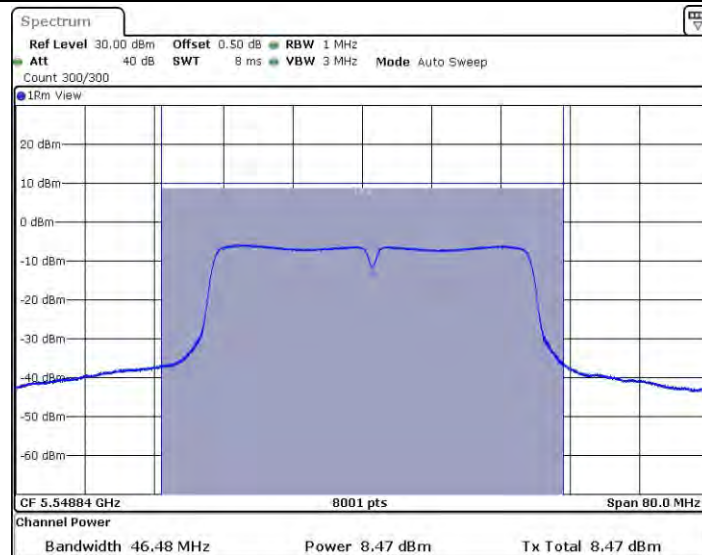
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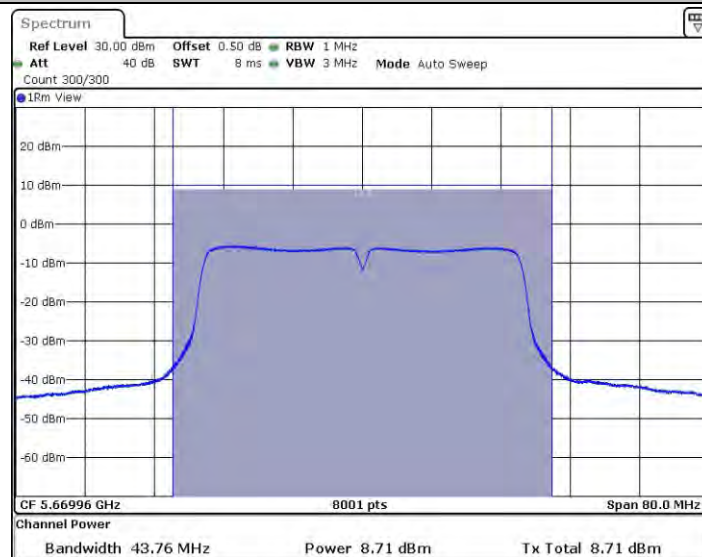
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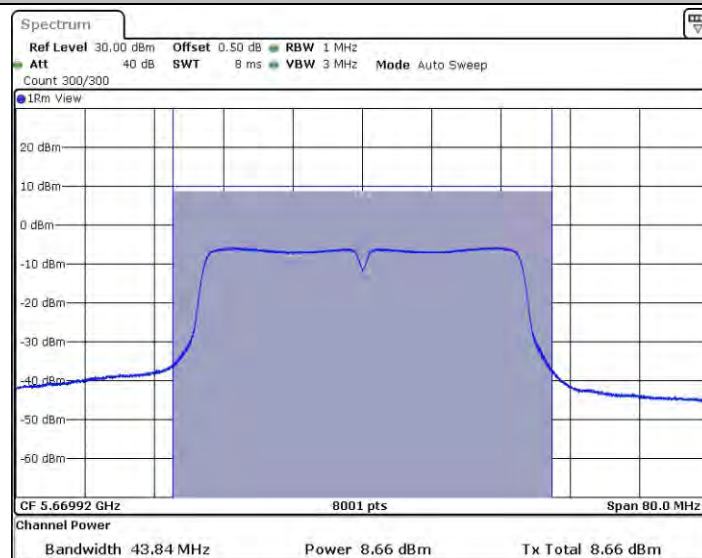
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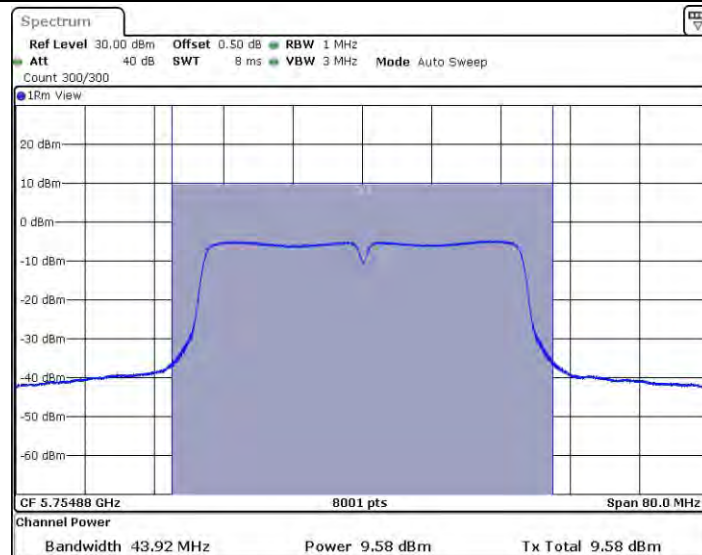
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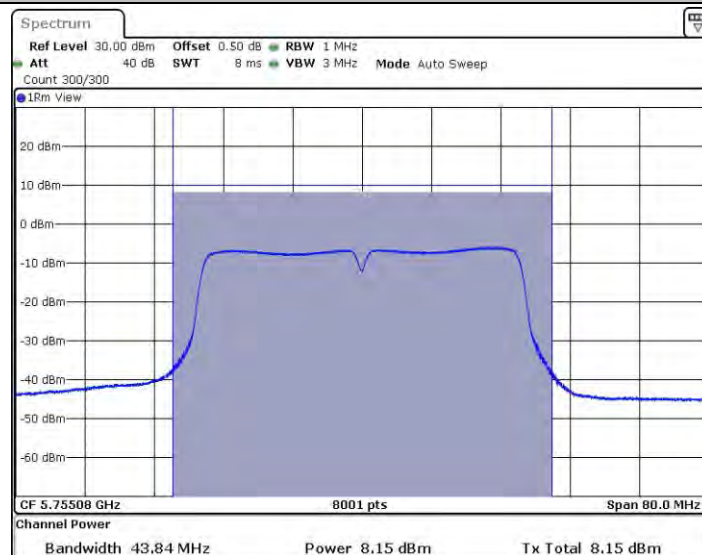
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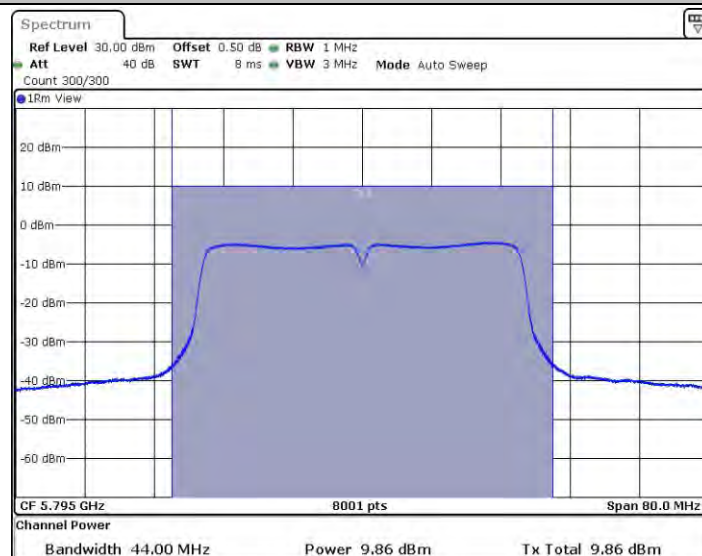
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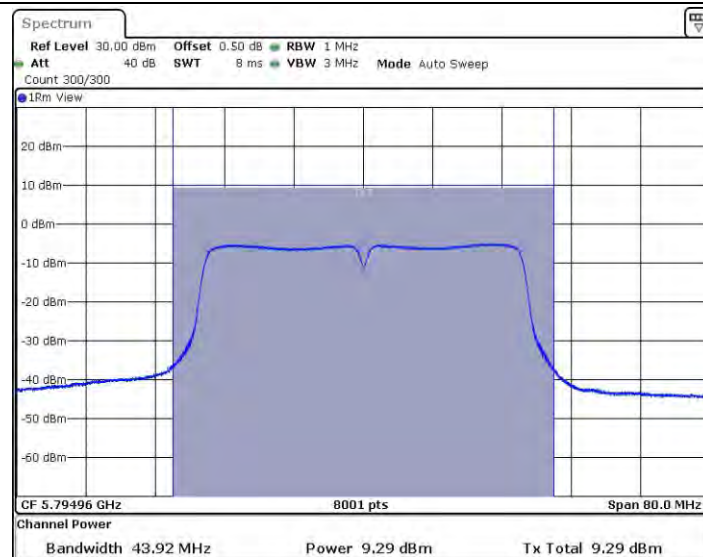
11N40MIMO_Ant1_5755



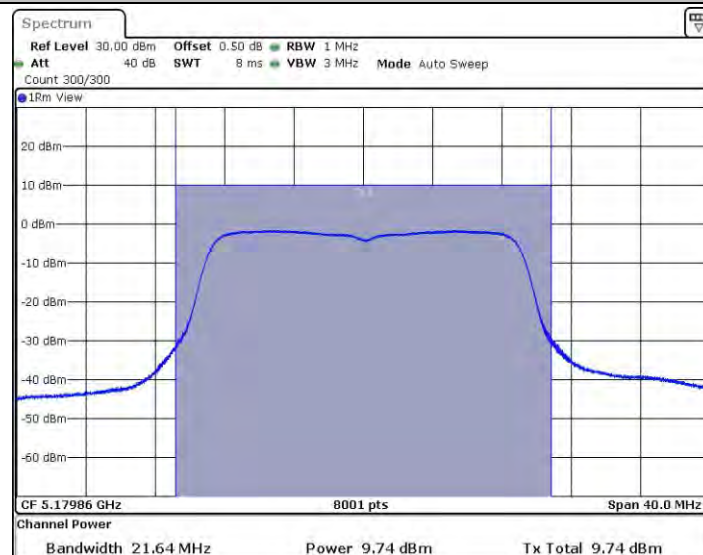
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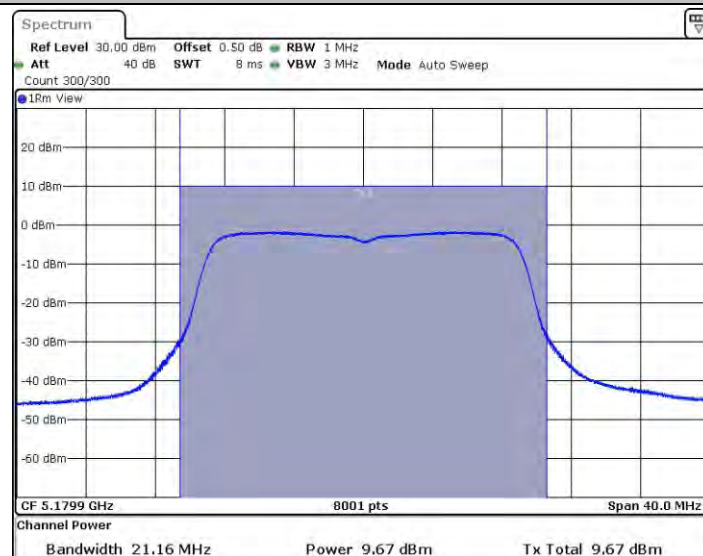
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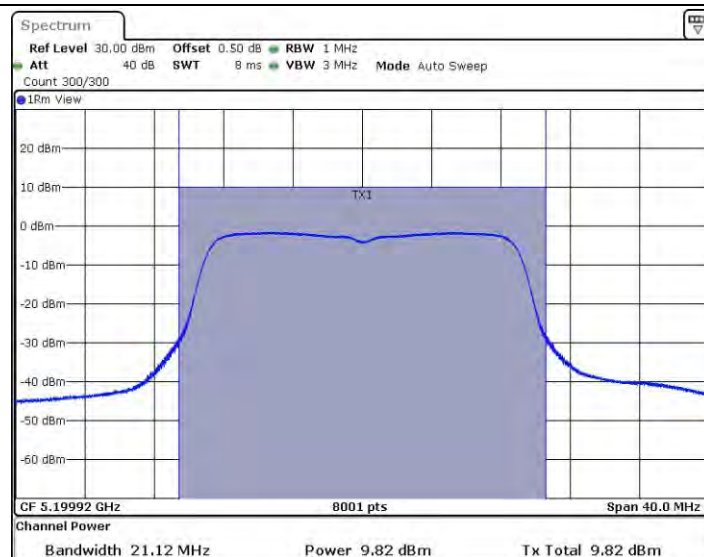
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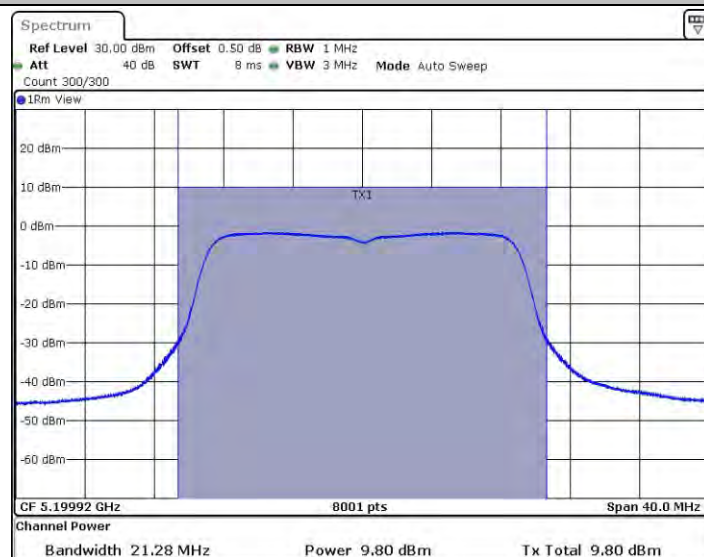
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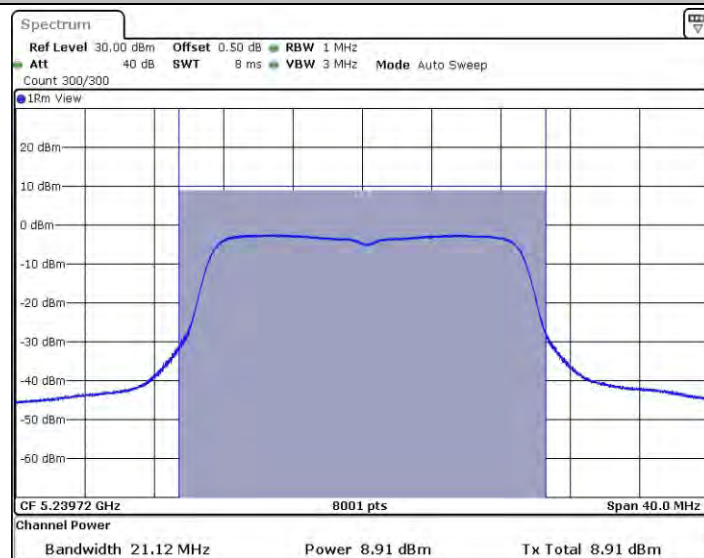
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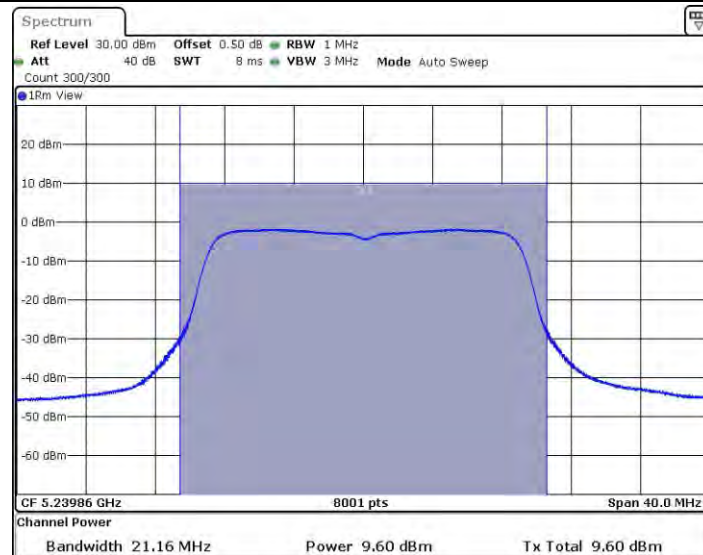
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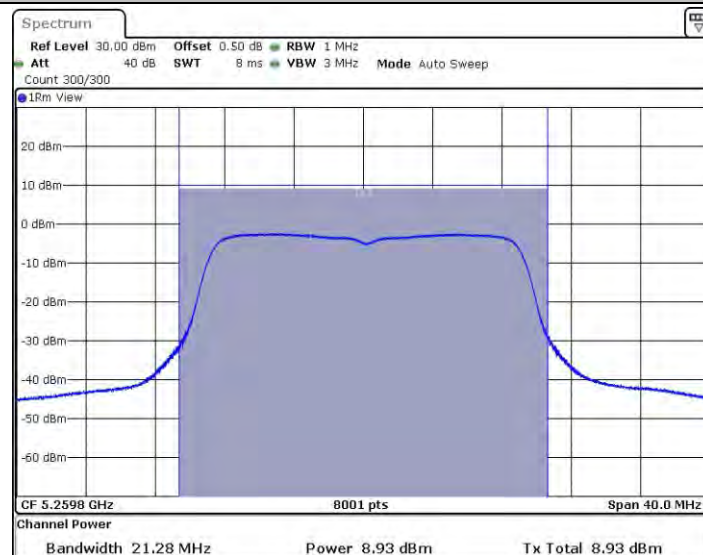
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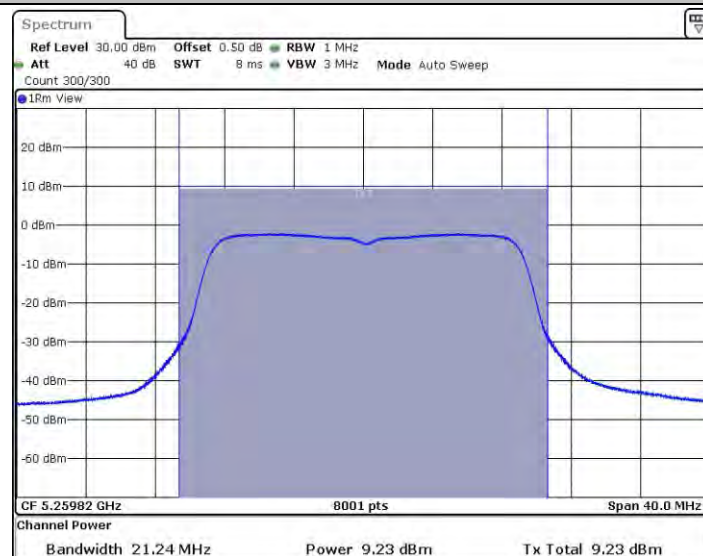
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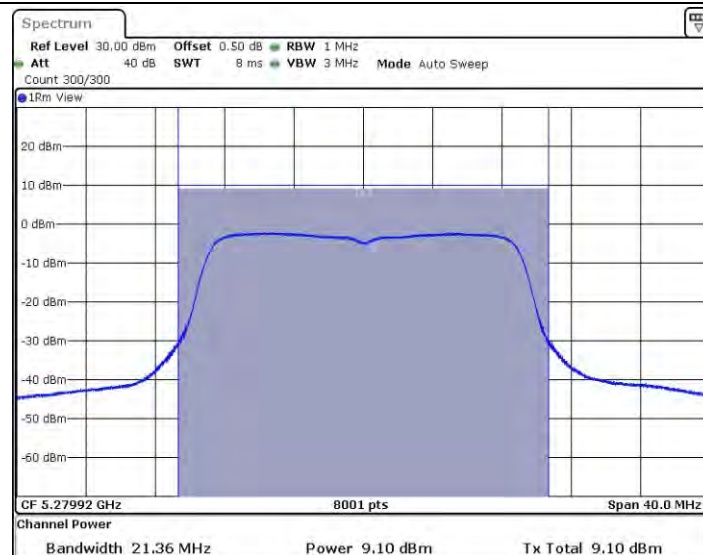
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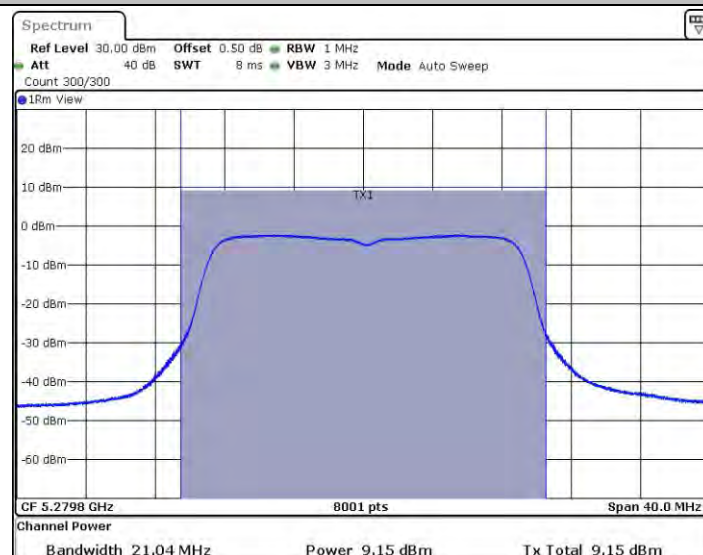
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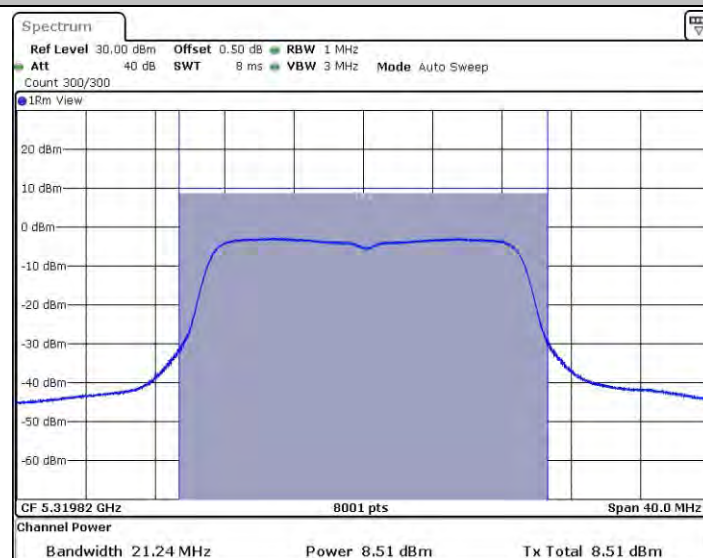
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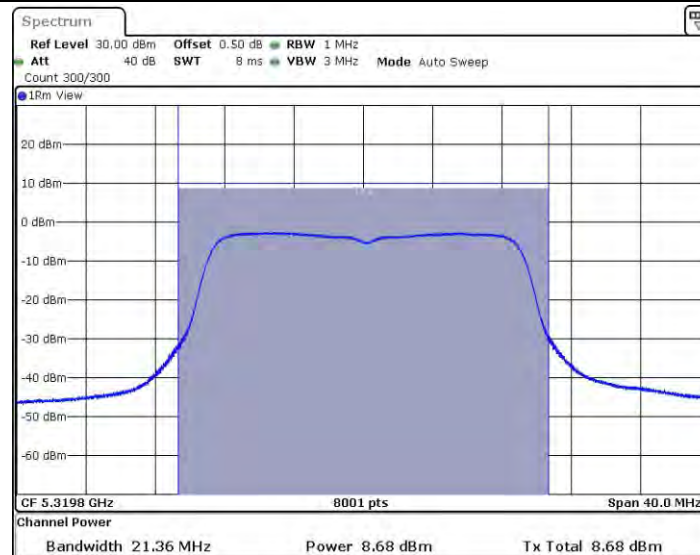
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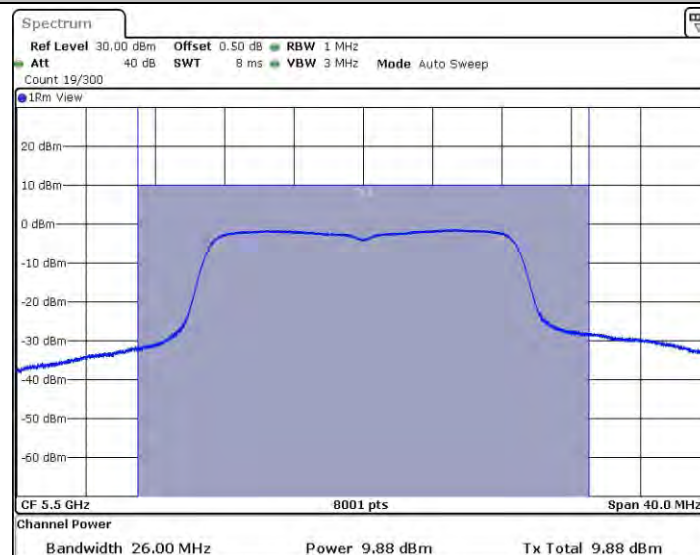
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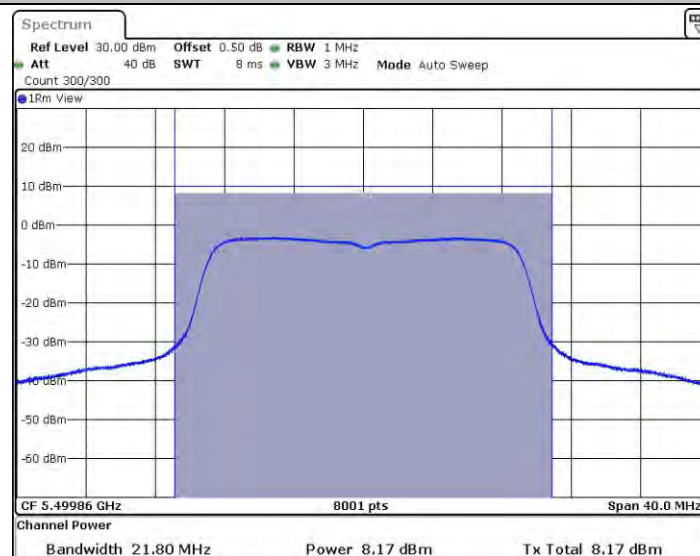
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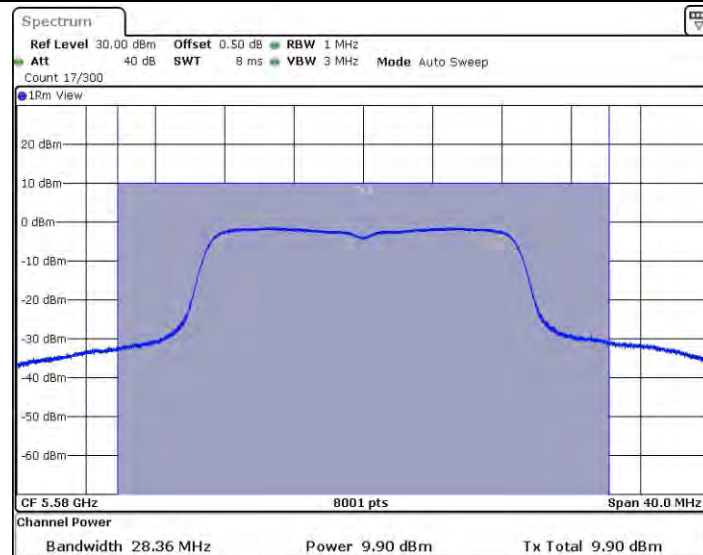
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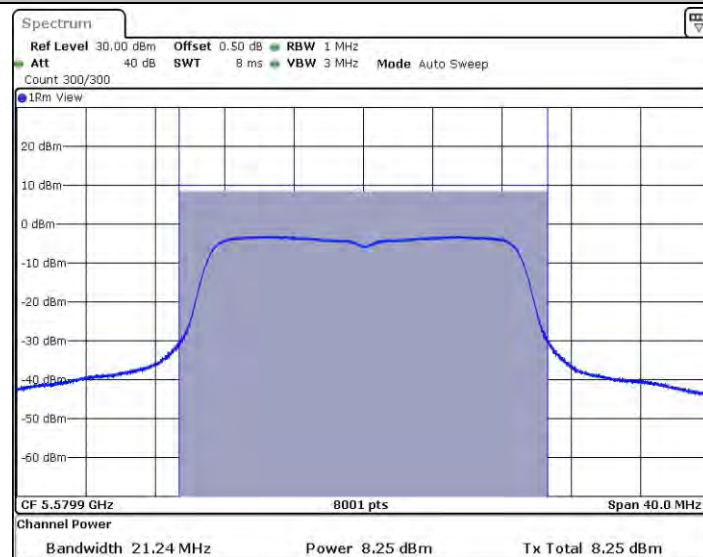
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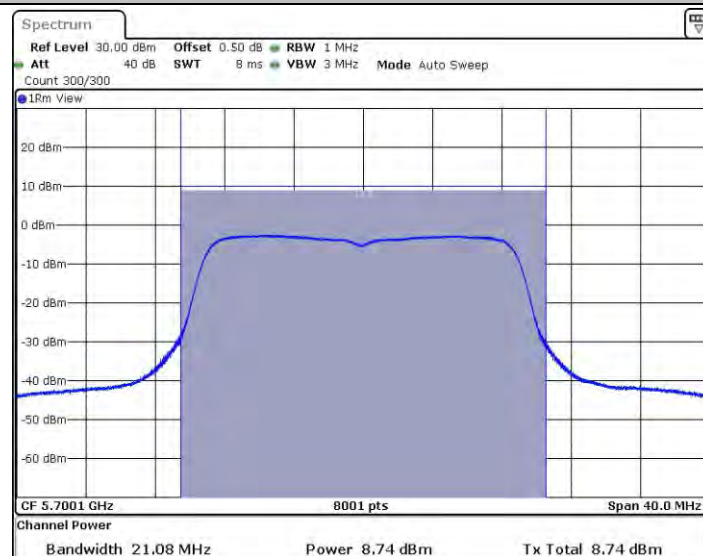
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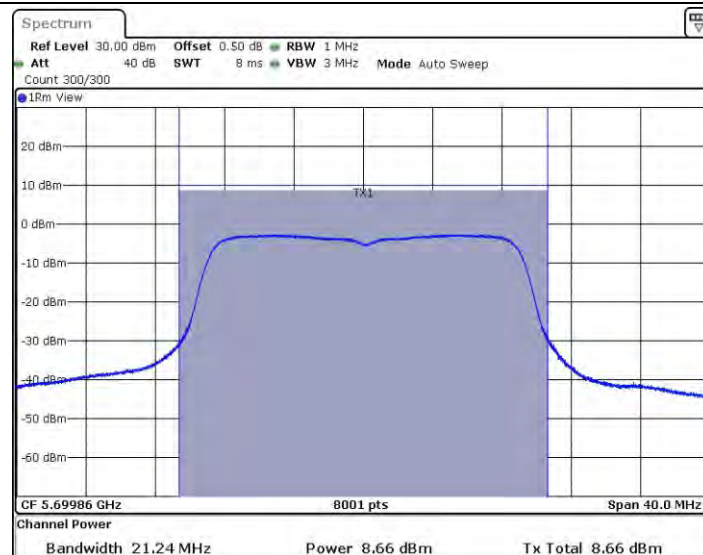
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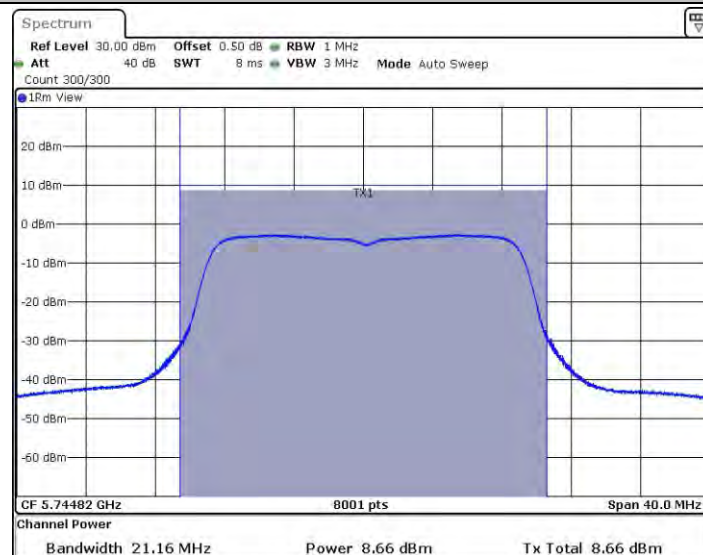
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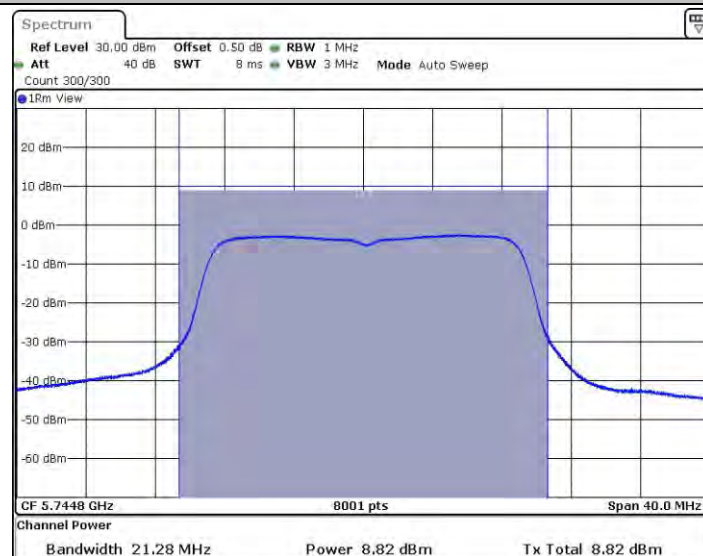
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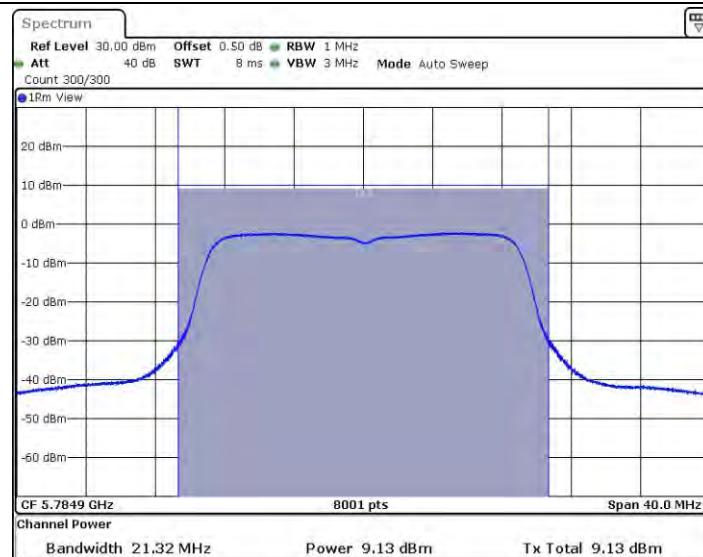
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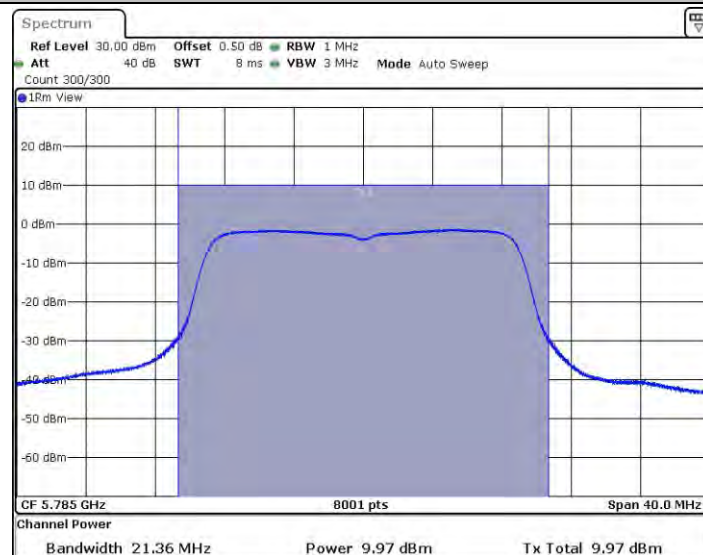
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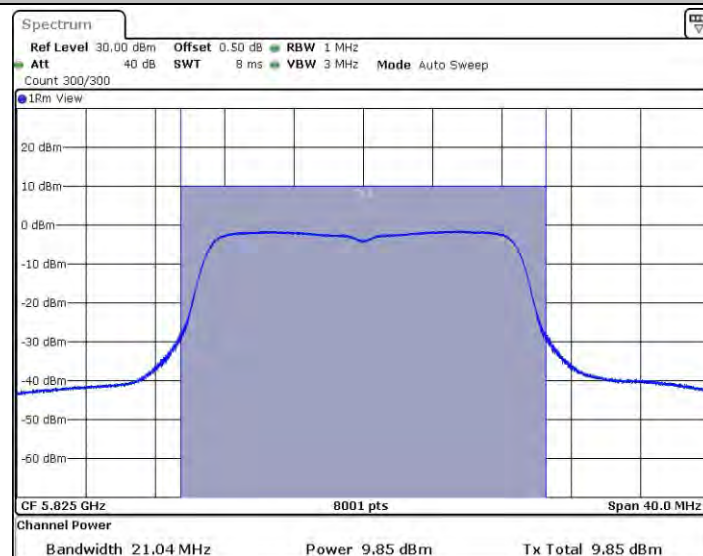
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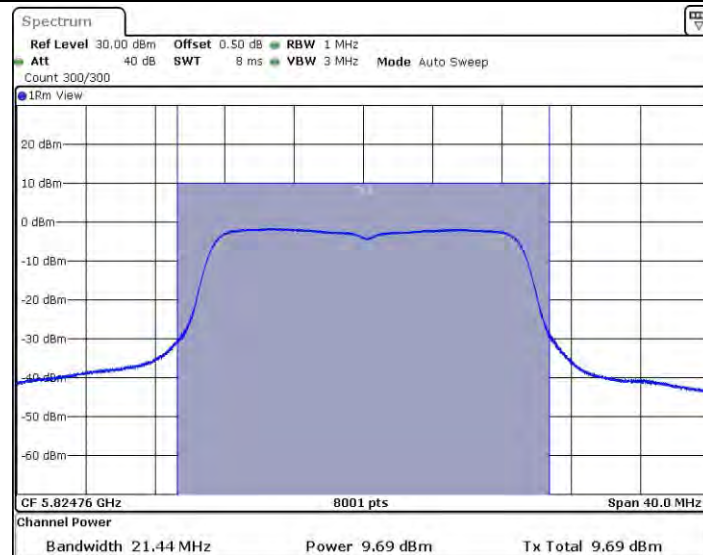
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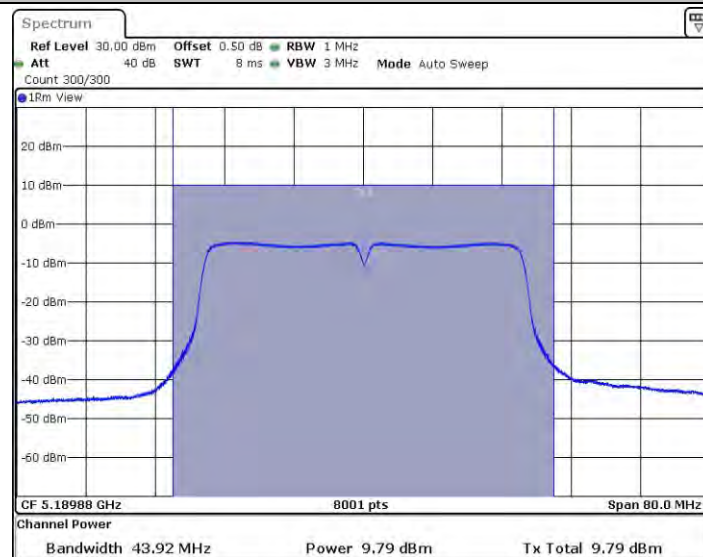
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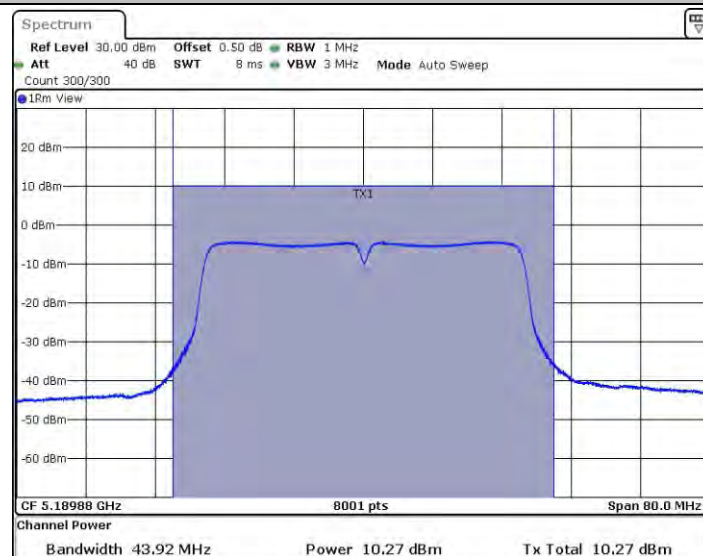
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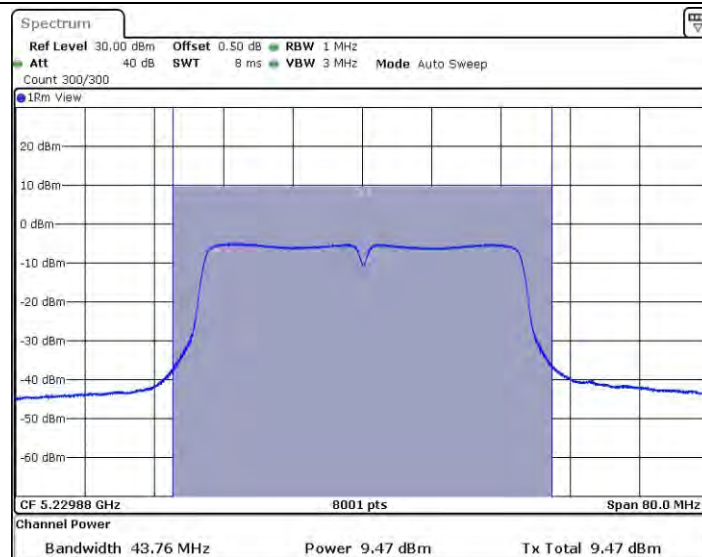
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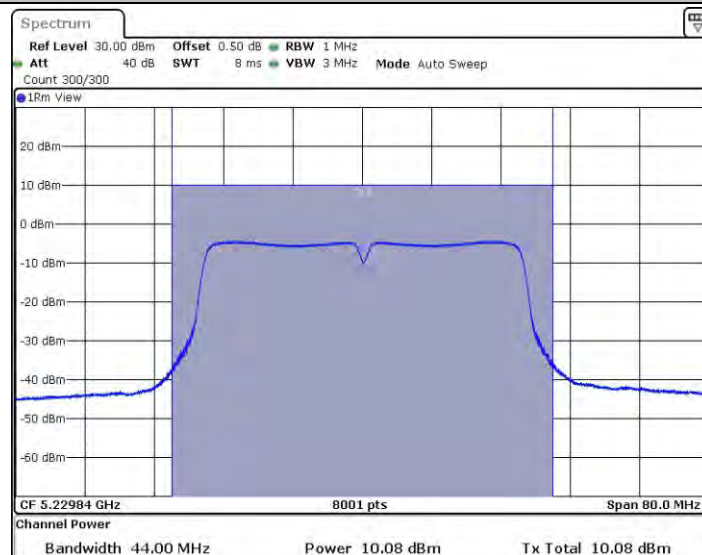
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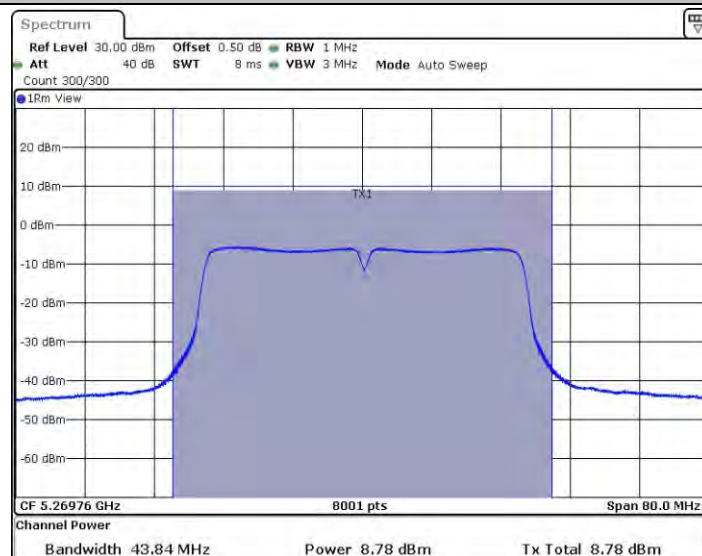
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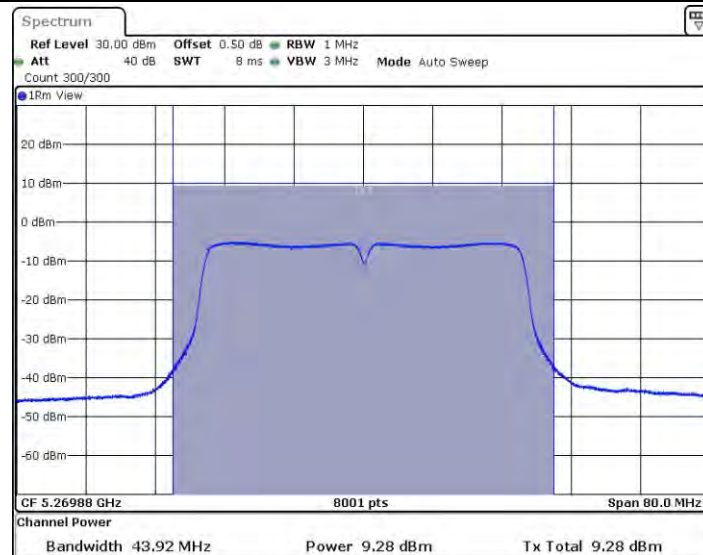
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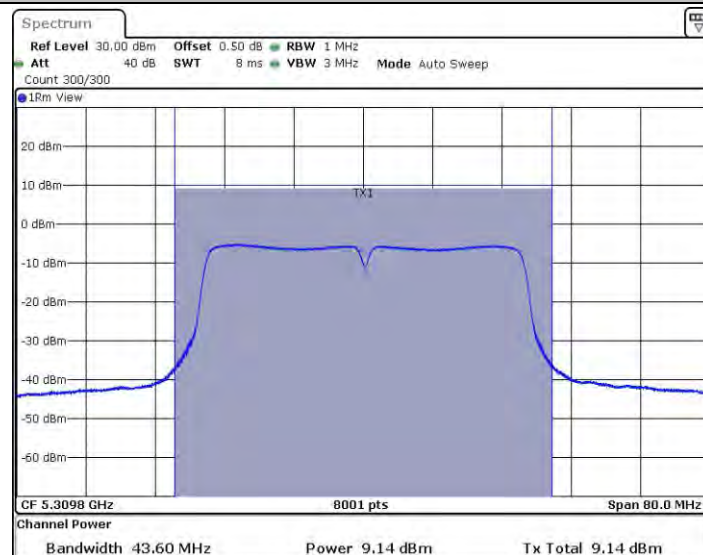
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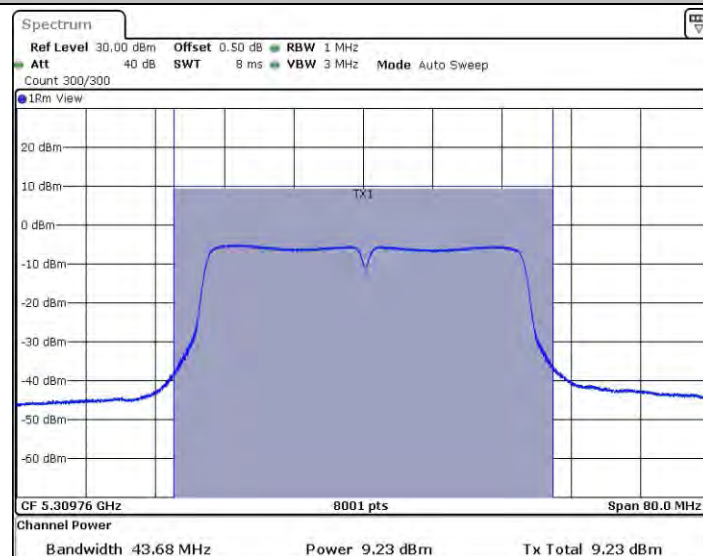
11AC40MIMO_Ant1_5270



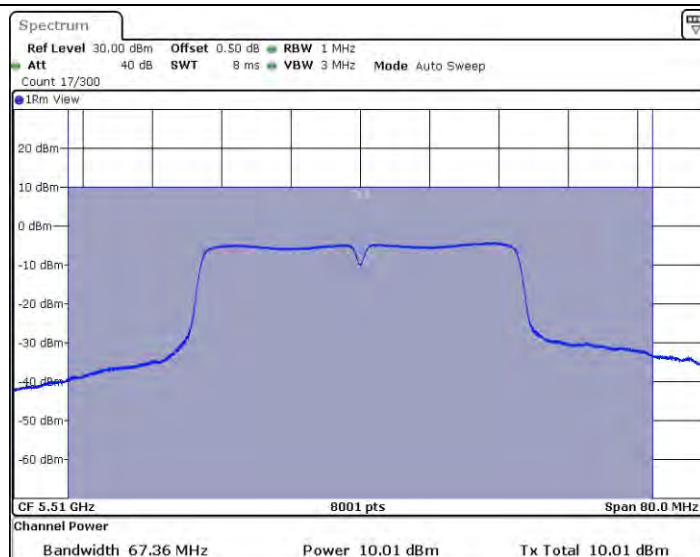
11AC40MIMO_Ant2_5270



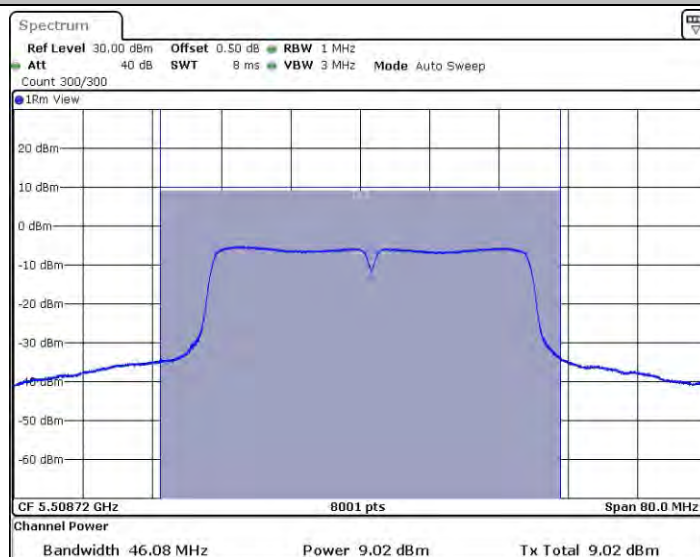
11AC40MIMO_Ant1_5310



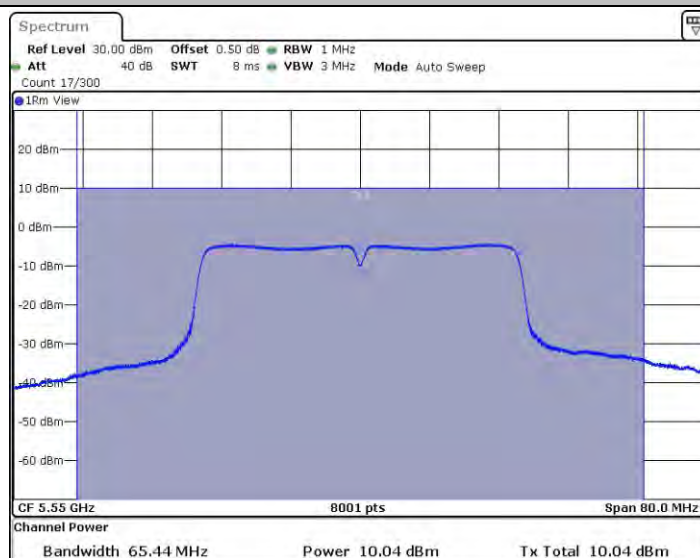
11AC40MIMO_Ant2_5310



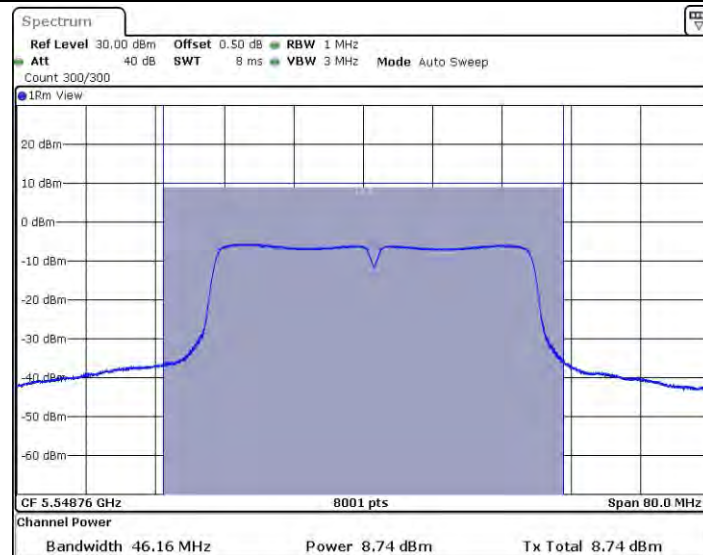
11AC40MIMO_Ant1_5510



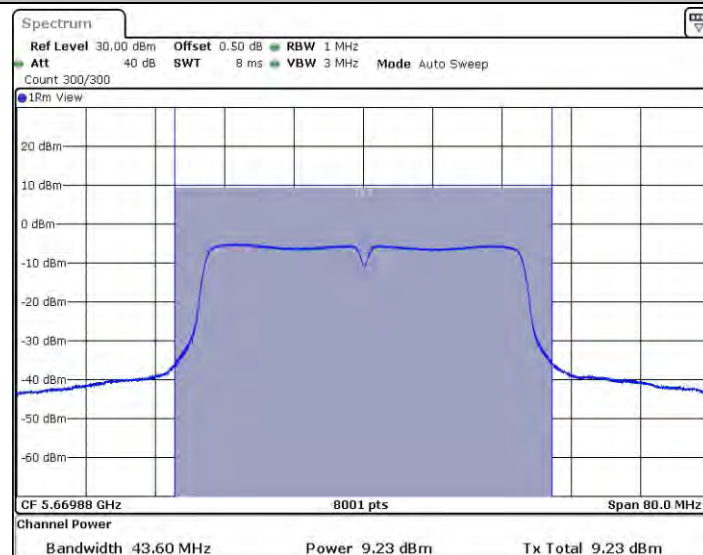
11AC40MIMO_Ant2_5510



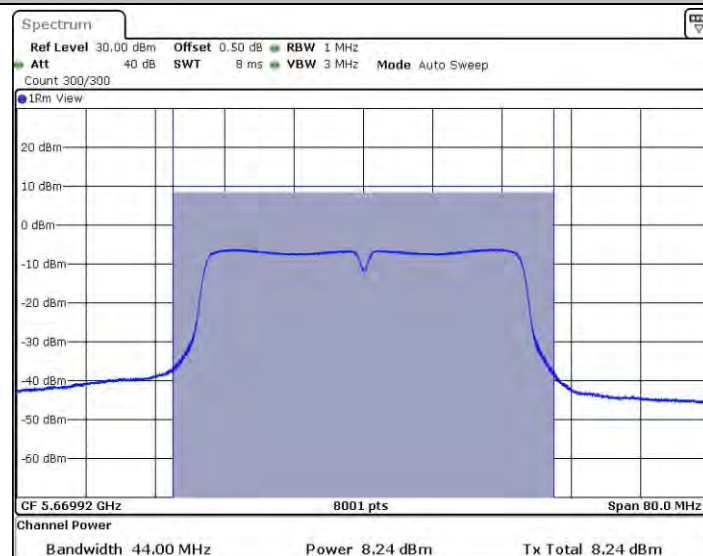
11AC40MIMO_Ant1_5550



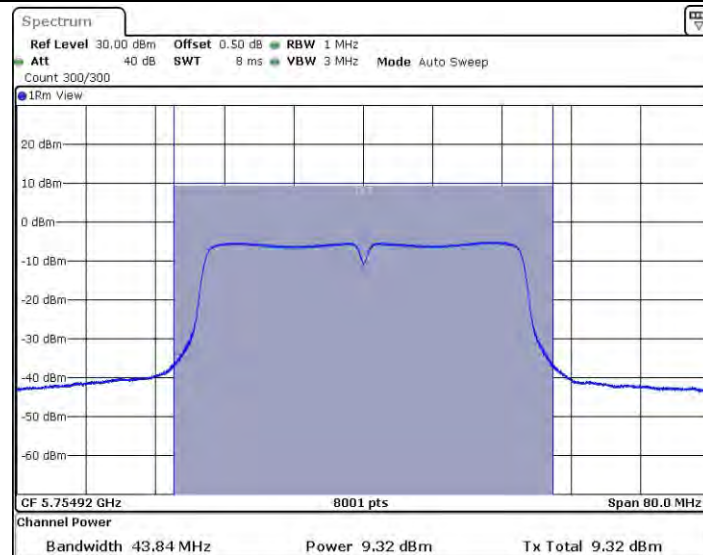
11AC40MIMO_Ant2_5550



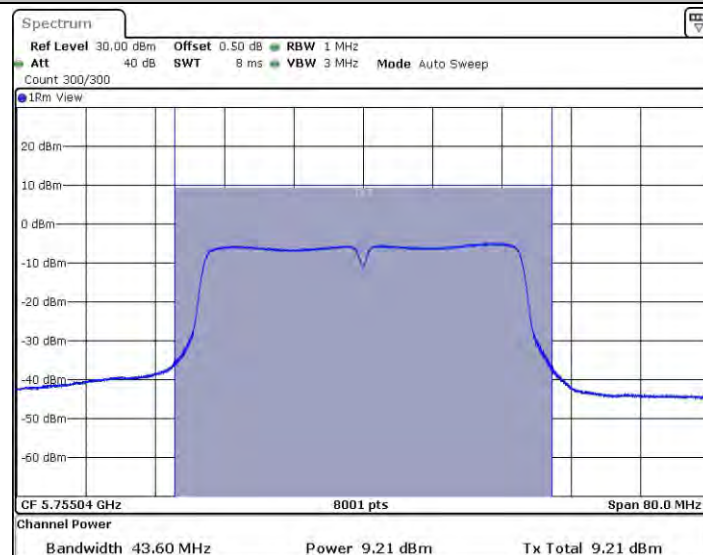
11AC40MIMO_Ant1_5670



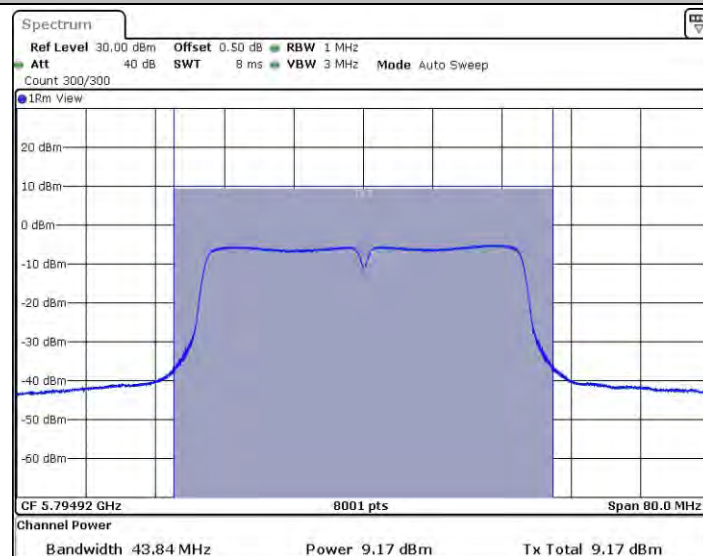
11AC40MIMO_Ant2_5670



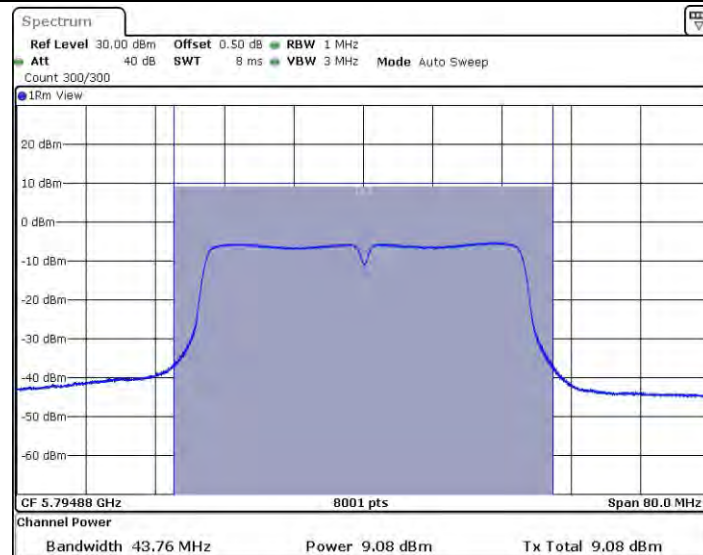
11AC40MIMO_Ant1_5755



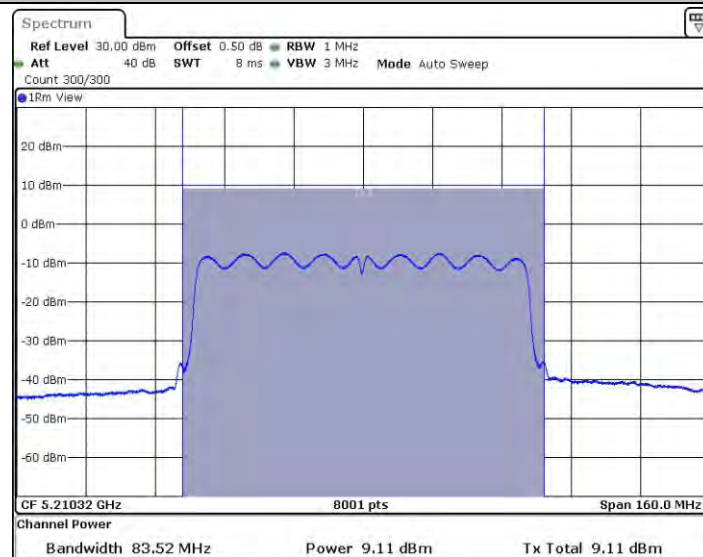
11AC40MIMO_Ant2_5755



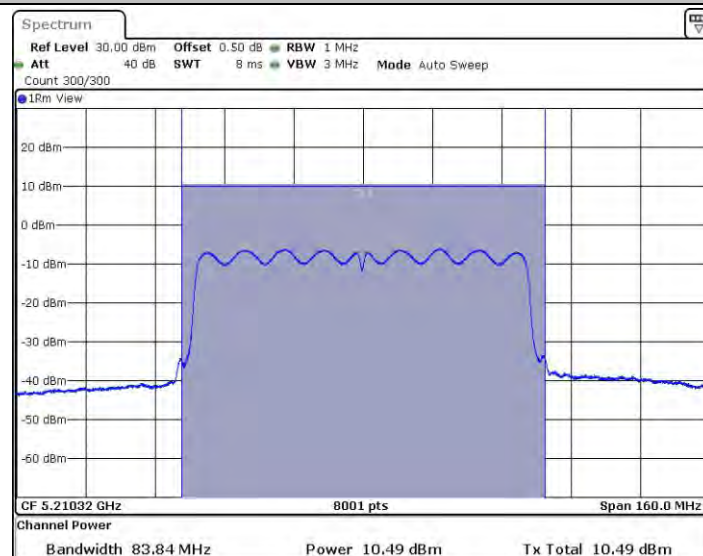
11AC40MIMO_Ant1_5795



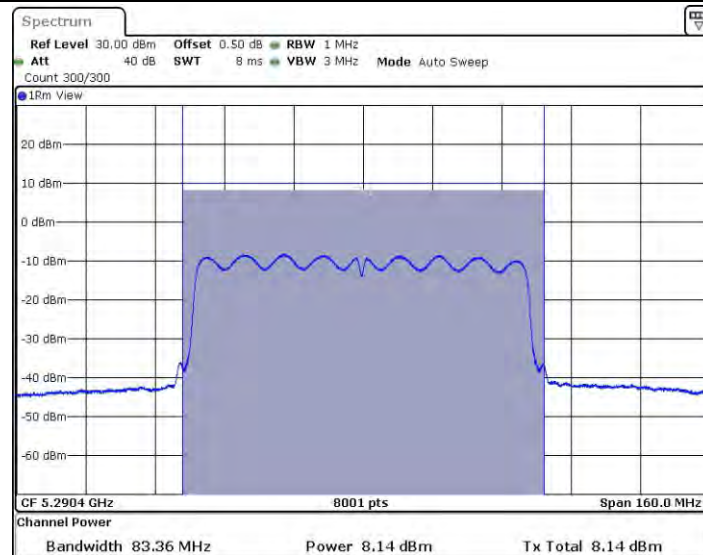
11AC40MIMO_Ant2_5795



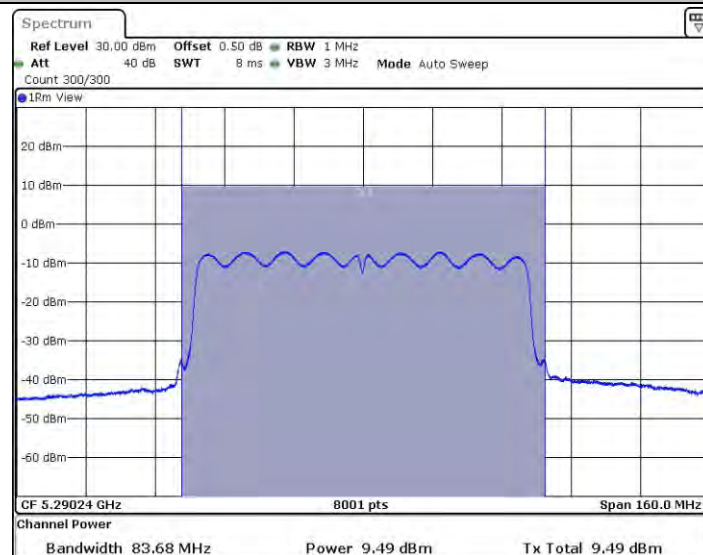
11AC80MIMO_Ant1_5210



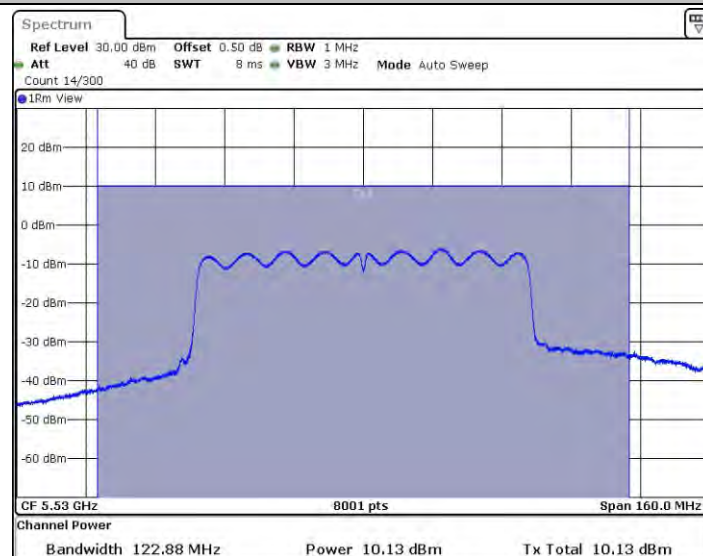
11AC80MIMO_Ant2_5210



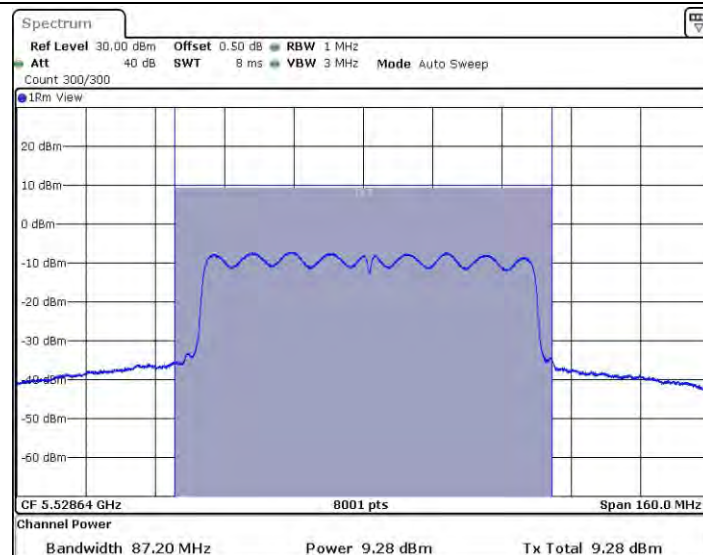
11AC80MIMO_Ant1_5290



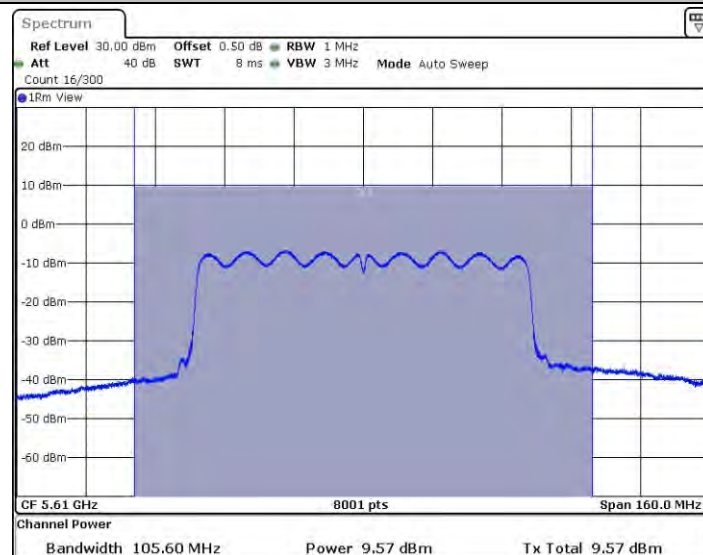
11AC80MIMO_Ant2_5290



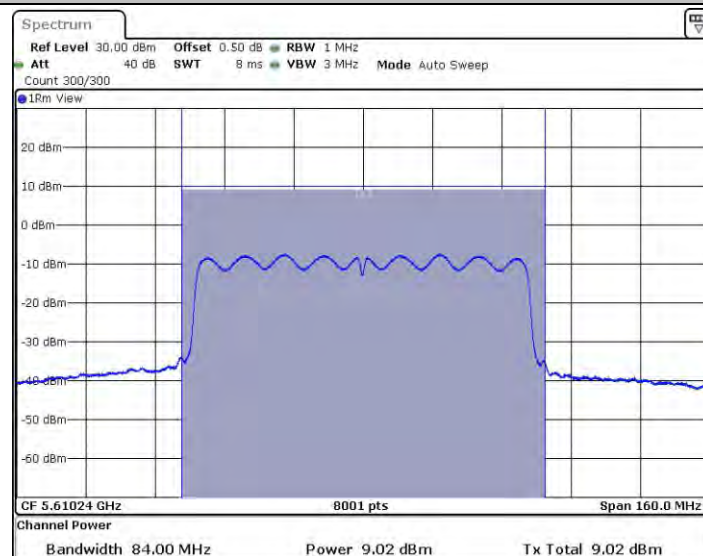
11AC80MIMO_Ant1_5530



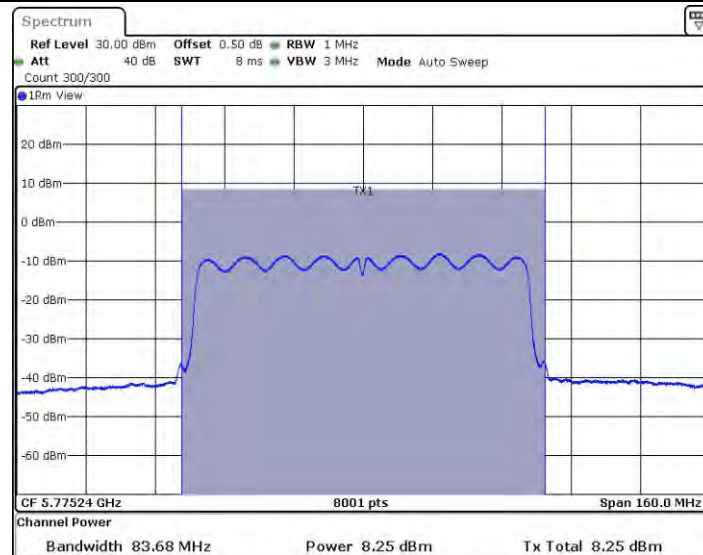
11AC80MIMO_Ant2_5530



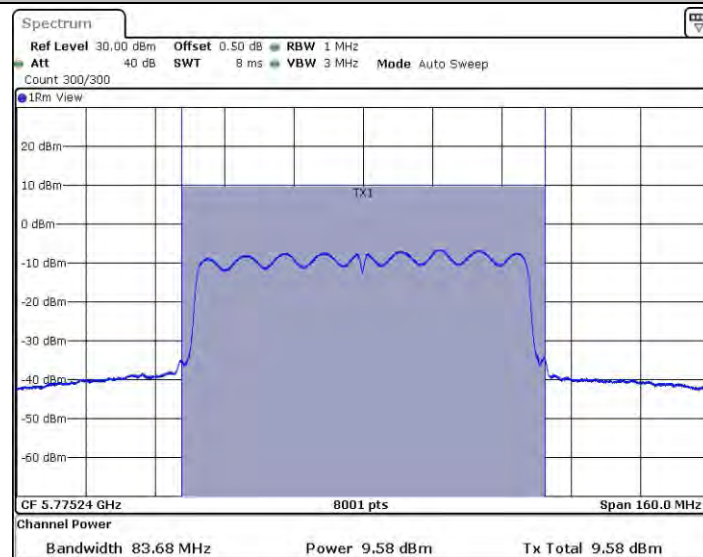
11AC80MIMO_Ant1_5610



11AC80MIMO_Ant2_5610



11AC80MIMO_Ant1_5775



11AC80MIMO_Ant2_5775

Appendix C: Maximum power spectral density

Test Result

TestMode	Antenna	Channel	Result	Limit	Verdict
11A	Ant1	5180	5.8	<=11	PASS
	Ant2	5180	2.49	<=11	PASS
	Ant1	5200	6.25	<=11	PASS
	Ant2	5200	2.66	<=11	PASS
	Ant1	5240	6.15	<=11	PASS
	Ant2	5240	2.48	<=11	PASS
	Ant1	5260	5.97	<=11	PASS
	Ant2	5260	2.29	<=11	PASS
	Ant1	5280	6.13	<=11	PASS
	Ant2	5280	2.42	<=11	PASS
	Ant1	5320	5.52	<=11	PASS
	Ant2	5320	1.87	<=11	PASS
	Ant1	5500	7.33	<=11	PASS
	Ant2	5500	1.09	<=11	PASS
	Ant1	5580	6.05	<=11	PASS
	Ant2	5580	2.94	<=11	PASS
	Ant1	5700	6.5	<=11	PASS
	Ant2	5700	0.86	<=11	PASS
	Ant1	5745	5.27	<=30	PASS
	Ant2	5745	1.35	<=30	PASS
	Ant1	5785	5.78	<=30	PASS
	Ant2	5785	2.25	<=30	PASS
	Ant1	5825	5.65	<=30	PASS
	Ant2	5825	1.77	<=30	PASS
11N20SISO	Ant1	5180	7.21	<=11	PASS
	Ant2	5180	3.41	<=11	PASS
	Ant1	5200	6.95	<=11	PASS
	Ant2	5200	3.31	<=11	PASS
	Ant1	5240	6.44	<=11	PASS
	Ant2	5240	3.4	<=11	PASS
	Ant1	5260	6.26	<=11	PASS
	Ant2	5260	3.21	<=11	PASS
	Ant1	5280	5.63	<=11	PASS
	Ant2	5280	3.02	<=11	PASS
	Ant1	5320	5.36	<=11	PASS
	Ant2	5320	2.67	<=11	PASS
	Ant1	5500	3.3	<=11	PASS
	Ant2	5500	2.5	<=11	PASS
	Ant1	5580	4.01	<=11	PASS
	Ant2	5580	2.53	<=11	PASS

	Ant1	5700	2.96	<=11	PASS
	Ant2	5700	2.42	<=11	PASS
	Ant1	5745	2.83	<=30	PASS
	Ant2	5745	0.99	<=30	PASS
	Ant1	5785	2.94	<=30	PASS
	Ant2	5785	2.26	<=30	PASS
	Ant1	5825	4.25	<=30	PASS
	Ant2	5825	1.91	<=30	PASS
11N40SISO	Ant1	5190	1.15	<=11	PASS
	Ant2	5190	-0.69	<=11	PASS
	Ant1	5230	0.58	<=11	PASS
	Ant2	5230	-0.41	<=11	PASS
	Ant1	5270	0.98	<=11	PASS
	Ant2	5270	-0.44	<=11	PASS
	Ant1	5310	0.44	<=11	PASS
	Ant2	5310	-0.68	<=11	PASS
	Ant1	5510	0.76	<=11	PASS
	Ant2	5510	-0.61	<=11	PASS
	Ant1	5550	1.54	<=11	PASS
	Ant2	5550	-1	<=11	PASS
	Ant1	5670	0.96	<=11	PASS
	Ant2	5670	-0.71	<=11	PASS
	Ant1	5755	-1.75	<=30	PASS
	Ant2	5755	-1.71	<=30	PASS
	Ant1	5795	-1.12	<=30	PASS
	Ant2	5795	-1.64	<=30	PASS
11AC20SISO	Ant1	5180	3.58	<=11	PASS
	Ant2	5180	3	<=11	PASS
	Ant1	5200	3.46	<=11	PASS
	Ant2	5200	3.54	<=11	PASS
	Ant1	5240	3.16	<=11	PASS
	Ant2	5240	3.35	<=11	PASS
	Ant1	5260	2.89	<=11	PASS
	Ant2	5260	3.19	<=11	PASS
	Ant1	5280	3.3	<=11	PASS
	Ant2	5280	3.5	<=11	PASS
	Ant1	5320	2.76	<=11	PASS
	Ant2	5320	2.79	<=11	PASS
	Ant1	5500	3.76	<=11	PASS
	Ant2	5500	2.19	<=11	PASS
	Ant1	5580	4.77	<=11	PASS
	Ant2	5580	1.86	<=11	PASS
	Ant1	5700	3.7	<=11	PASS
	Ant2	5700	1.79	<=11	PASS
	Ant1	5745	1.56	<=30	PASS

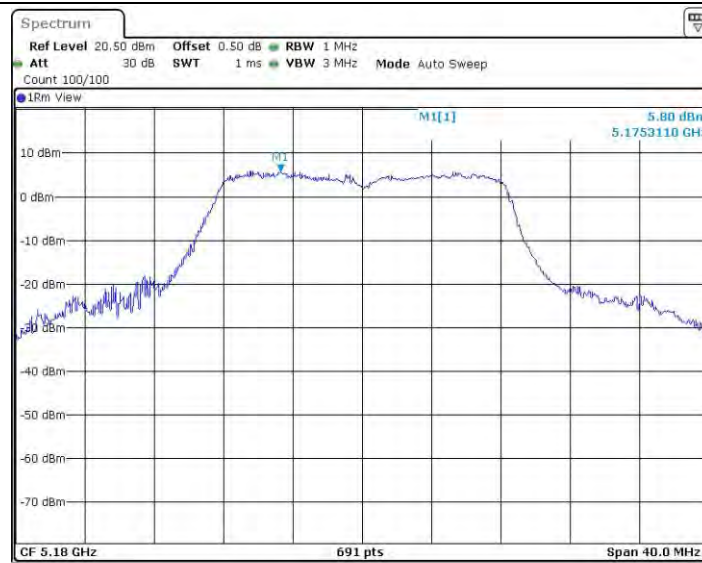
	Ant2	5745	0.59	<=30	PASS
	Ant1	5785	1.71	<=30	PASS
	Ant2	5785	1.58	<=30	PASS
	Ant1	5825	1.62	<=30	PASS
	Ant2	5825	1.53	<=30	PASS
11AC40SISO	Ant1	5190	0.38	<=11	PASS
	Ant2	5190	0.28	<=11	PASS
	Ant1	5230	-0.2	<=11	PASS
	Ant2	5230	0.1	<=11	PASS
	Ant1	5270	-0.36	<=11	PASS
	Ant2	5270	0.01	<=11	PASS
	Ant1	5310	-0.81	<=11	PASS
	Ant2	5310	-0.51	<=11	PASS
	Ant1	5510	1.25	<=11	PASS
	Ant2	5510	-0.04	<=11	PASS
	Ant1	5550	1.78	<=11	PASS
	Ant2	5550	-0.76	<=11	PASS
	Ant1	5670	1.16	<=11	PASS
	Ant2	5670	-1.08	<=11	PASS
	Ant1	5755	-1.44	<=30	PASS
	Ant2	5755	-2.02	<=30	PASS
	Ant1	5795	-0.94	<=30	PASS
	Ant2	5795	-1.08	<=30	PASS
11AC80SISO	Ant1	5210	-2.42	<=11	PASS
	Ant2	5210	-2.4	<=11	PASS
	Ant1	5290	-3.36	<=11	PASS
	Ant2	5290	-2.5	<=11	PASS
	Ant1	5530	-0.95	<=11	PASS
	Ant2	5530	-2.96	<=11	PASS
	Ant1	5610	-1.06	<=11	PASS
	Ant2	5610	-3.16	<=11	PASS
	Ant1	5775	-4.03	<=30	PASS
	Ant2	5775	-3.38	<=30	PASS
11N20MIMO	Ant1	5180	2.78	<=11	PASS
	Ant2	5180	2.54	<=11	PASS
	total	5180	5.67	<=11	PASS
	Ant1	5200	1.77	<=11	PASS
	Ant2	5200	2.76	<=11	PASS
	total	5200	5.30	<=11	PASS
	Ant1	5240	3.35	<=11	PASS
	Ant2	5240	2.81	<=11	PASS
	total	5240	6.10	<=11	PASS
	Ant1	5260	2.79	<=11	PASS
	Ant2	5260	2.46	<=11	PASS
	total	5260	5.64	<=11	PASS

	Ant1	5280	3.5	<=11	PASS
	Ant2	5280	2.81	<=11	PASS
	total	5280	6.18	<=11	PASS
	Ant1	5320	2.1	<=11	PASS
	Ant2	5320	1.68	<=11	PASS
	total	5320	4.91	<=11	PASS
	Ant1	5500	3.27	<=11	PASS
	Ant2	5500	2.34	<=11	PASS
	total	5500	5.84	<=11	PASS
	Ant1	5580	3.65	<=11	PASS
	Ant2	5580	2	<=11	PASS
	total	5580	5.91	<=11	PASS
	Ant1	5700	3.11	<=11	PASS
	Ant2	5700	2.17	<=11	PASS
	total	5700	5.68	<=11	PASS
	Ant1	5745	1	<=30	PASS
	Ant2	5745	0.87	<=30	PASS
	total	5745	3.95	<=30	PASS
	Ant1	5785	1.51	<=30	PASS
	Ant2	5785	1.46	<=30	PASS
	total	5785	4.50	<=30	PASS
	Ant1	5825	1.8	<=30	PASS
	Ant2	5825	1.69	<=30	PASS
	total	5825	4.76	<=30	PASS
11N40MIMO	Ant1	5190	-0.9	<=11	PASS
	Ant2	5190	-0.86	<=11	PASS
	total	5190	2.13	<=11	PASS
	Ant1	5230	-0.38	<=11	PASS
	Ant2	5230	-0.94	<=11	PASS
	total	5230	2.36	<=11	PASS
	Ant1	5270	-0.86	<=11	PASS
	Ant2	5270	-0.37	<=11	PASS
	total	5270	2.40	<=11	PASS
	Ant1	5310	-1.26	<=11	PASS
	Ant2	5310	-1.02	<=11	PASS
	total	5310	1.87	<=11	PASS
	Ant1	5510	0.05	<=11	PASS
	Ant2	5510	-0.59	<=11	PASS
	total	5510	2.75	<=11	PASS
	Ant1	5550	0.03	<=11	PASS
	Ant2	5550	-0.86	<=11	PASS
	total	5550	2.62	<=11	PASS
	Ant1	5670	-0.57	<=11	PASS
	Ant2	5670	-0.78	<=11	PASS
	total	5670	2.34	<=11	PASS

	Ant1	5755	-1.29	<=30	PASS
	Ant2	5755	-2.71	<=30	PASS
	total	5755	1.07	<=30	PASS
	Ant1	5795	-1.18	<=30	PASS
	Ant2	5795	-1.44	<=30	PASS
	total	5795	1.70	<=30	PASS
11AC20MIMO	Ant1	5180	3.13	<=11	PASS
	Ant2	5180	3.17	<=11	PASS
	total	5180	6.16	<=11	PASS
	Ant1	5200	3.31	<=11	PASS
	Ant2	5200	3.21	<=11	PASS
	total	5200	6.27	<=11	PASS
	Ant1	5240	2.56	<=11	PASS
	Ant2	5240	3.12	<=11	PASS
	total	5240	5.86	<=11	PASS
	Ant1	5260	2.63	<=11	PASS
	Ant2	5260	2.7	<=11	PASS
	total	5260	5.68	<=11	PASS
	Ant1	5280	2.71	<=11	PASS
	Ant2	5280	2.79	<=11	PASS
	total	5280	5.76	<=11	PASS
	Ant1	5320	2.28	<=11	PASS
	Ant2	5320	2.33	<=11	PASS
	total	5320	5.32	<=11	PASS
	Ant1	5500	3.51	<=11	PASS
	Ant2	5500	1.67	<=11	PASS
	total	5500	5.70	<=11	PASS
	Ant1	5580	4.04	<=11	PASS
	Ant2	5580	1.95	<=11	PASS
	total	5580	6.13	<=11	PASS
	Ant1	5700	2.49	<=11	PASS
	Ant2	5700	2.59	<=11	PASS
	total	5700	5.55	<=11	PASS
	Ant1	5745	1.3	<=30	PASS
	Ant2	5745	1.21	<=30	PASS
	total	5745	4.27	<=30	PASS
	Ant1	5785	1.62	<=30	PASS
	Ant2	5785	2.22	<=30	PASS
	total	5785	4.94	<=30	PASS
	Ant1	5825	2.4	<=30	PASS
	Ant2	5825	2.45	<=30	PASS
	total	5825	5.44	<=30	PASS
11AC40MIMO	Ant1	5190	-0.06	<=11	PASS
	Ant2	5190	0.55	<=11	PASS
	total	5190	3.27	<=11	PASS

	Ant1	5230	-0.3	<=11	PASS
	Ant2	5230	0.15	<=11	PASS
	total	5230	2.94	<=11	PASS
	Ant1	5270	-0.8	<=11	PASS
	Ant2	5270	-0.41	<=11	PASS
	total	5270	2.41	<=11	PASS
	Ant1	5310	-0.67	<=11	PASS
	Ant2	5310	-0.57	<=11	PASS
	total	5310	2.39	<=11	PASS
	Ant1	5510	0.41	<=11	PASS
	Ant2	5510	-0.64	<=11	PASS
	total	5510	2.93	<=11	PASS
	Ant1	5550	0.54	<=11	PASS
	Ant2	5550	-0.82	<=11	PASS
	total	5550	2.92	<=11	PASS
	Ant1	5670	-0.18	<=11	PASS
	Ant2	5670	-1.23	<=11	PASS
	total	5670	2.34	<=11	PASS
	Ant1	5755	-1.71	<=30	PASS
	Ant2	5755	-1.94	<=30	PASS
	total	5755	1.19	<=30	PASS
	Ant1	5795	-1.42	<=30	PASS
	Ant2	5795	-1.56	<=30	PASS
	total	5795	1.52	<=30	PASS
11AC80MIMO	Ant1	5210	-2.92	<=11	PASS
	Ant2	5210	-1.1	<=11	PASS
	total	5210	1.09	<=11	PASS
	Ant1	5290	-3.88	<=11	PASS
	Ant2	5290	-2.64	<=11	PASS
	total	5290	-0.21	<=11	PASS
	Ant1	5530	-1.3	<=11	PASS
	Ant2	5530	-2.55	<=11	PASS
	total	5530	1.13	<=11	PASS
	Ant1	5610	-1.98	<=11	PASS
	Ant2	5610	-2.59	<=11	PASS
	total	5610	0.74	<=11	PASS
	Ant1	5775	-4.91	<=30	PASS
	Ant2	5775	-3.15	<=30	PASS
	total	5775	-0.93	<=30	PASS

Test Graphs



11A_Ant1_5180



11A_Ant2_5180



11A_Ant1_5200



11A_Ant2_5200



11A_Ant1_5240



11A_Ant2_5240



11A_Ant1_5260



11A_Ant2_5260



11A_Ant1_5280



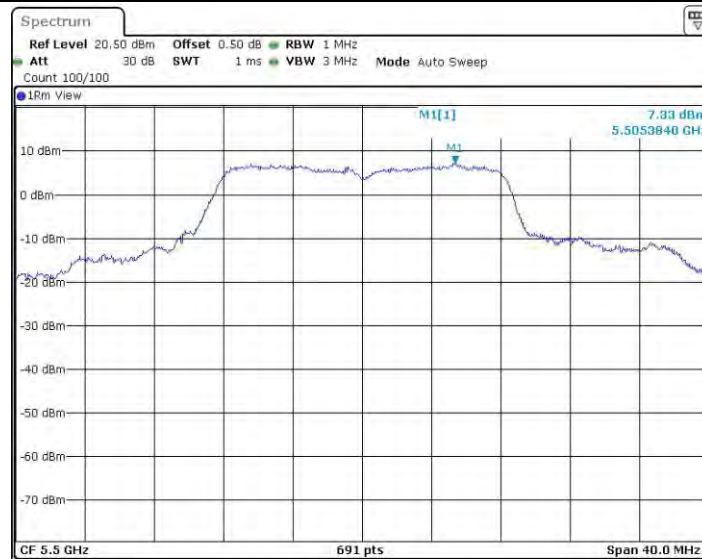
11A_Ant2_5280



11A_Ant1_5320



11A_Ant2_5320



11A_Ant1_5500



11A_Ant2_5500



11A_Ant1_5580



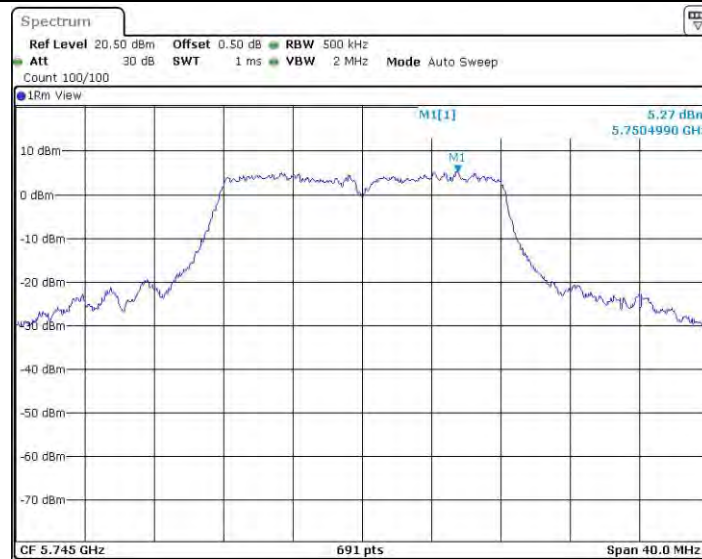
11A_Ant2_5580



11A_Ant1_5700



11A_Ant2_5700



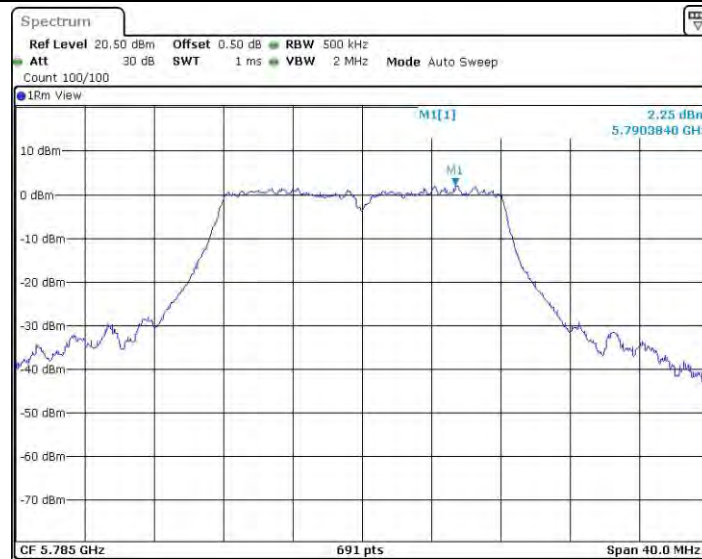
11A_Ant1_5745



11A_Ant2_5745



11A_Ant1_5785



11A_Ant2_5785



11A_Ant1_5825



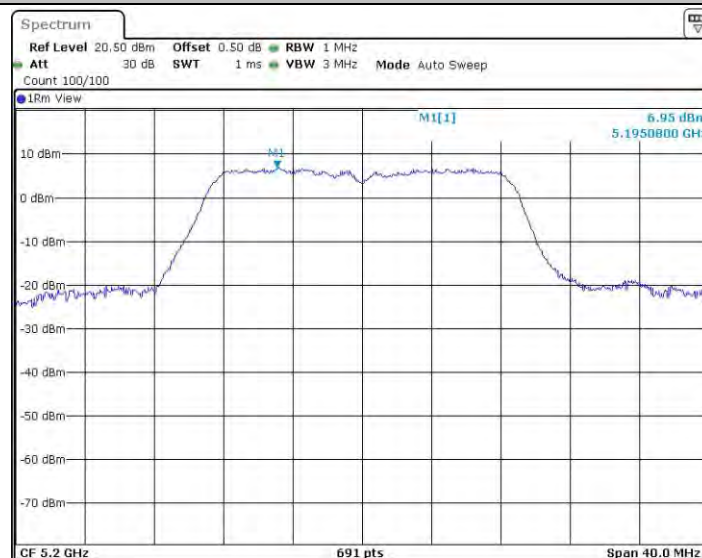
11A_Ant2_5825



11N20SISO_Ant1_5180



11N20SISO_Ant2_5180



11N20SISO_Ant1_5200



11N20SISO_Ant2_5200



11N20SISO_Ant1_5240



11N20SISO_Ant2_5240



11N20SISO_Ant1_5260



11N20SISO_Ant2_5260



11N20SISO_Ant1_5280



11N20SISO_Ant2_5280



11N20SISO_Ant1_5320



11N20SISO_Ant2_5320



11N20SISO_Ant1_5500



11N20SISO_Ant2_5500



11N20SISO_Ant1_5580



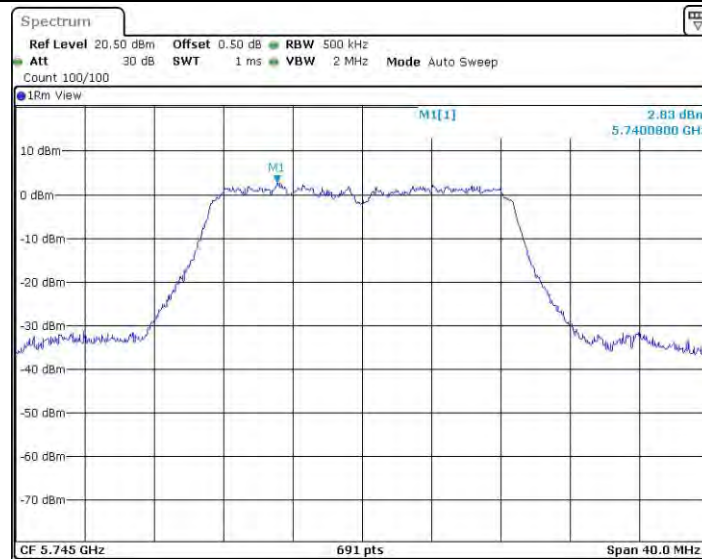
11N20SISO_Ant2_5580



11N20SISO_Ant1_5700



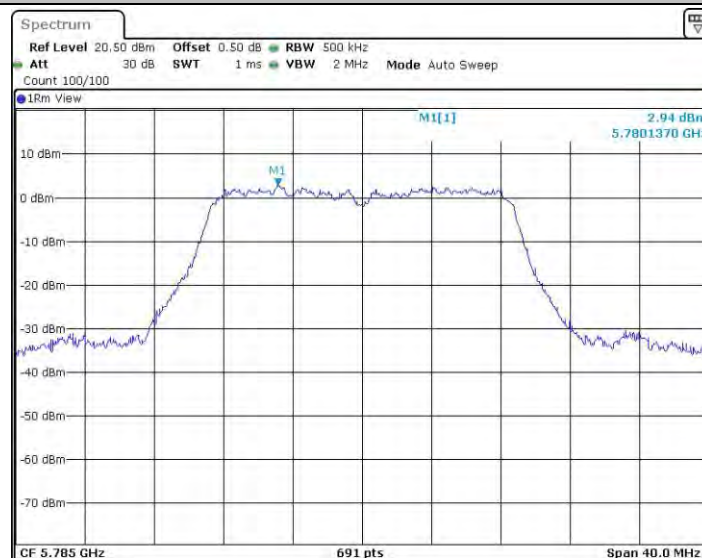
11N20SISO_Ant2_5700



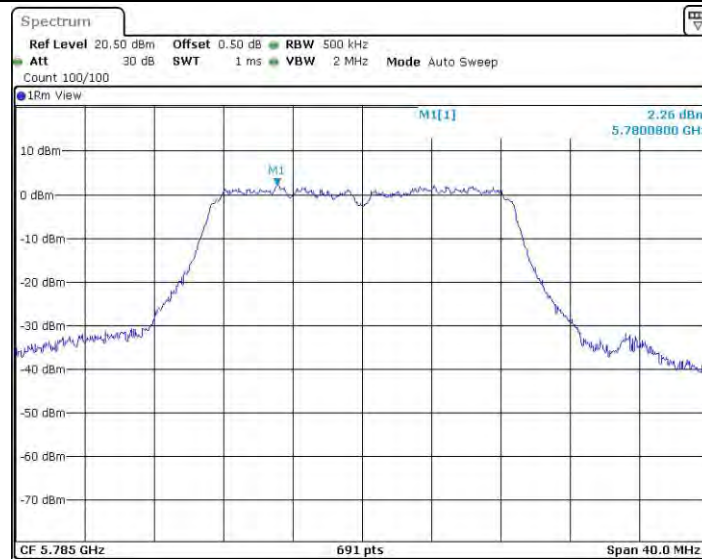
11N20SISO_Ant1_5745



11N20SISO_Ant2_5745



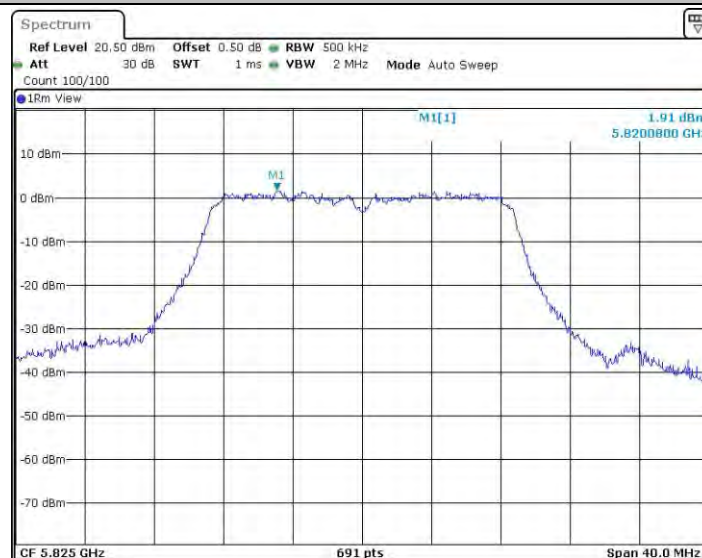
11N20SISO_Ant1_5785



11N20SISO_Ant2_5785



11N20SISO_Ant1_5825



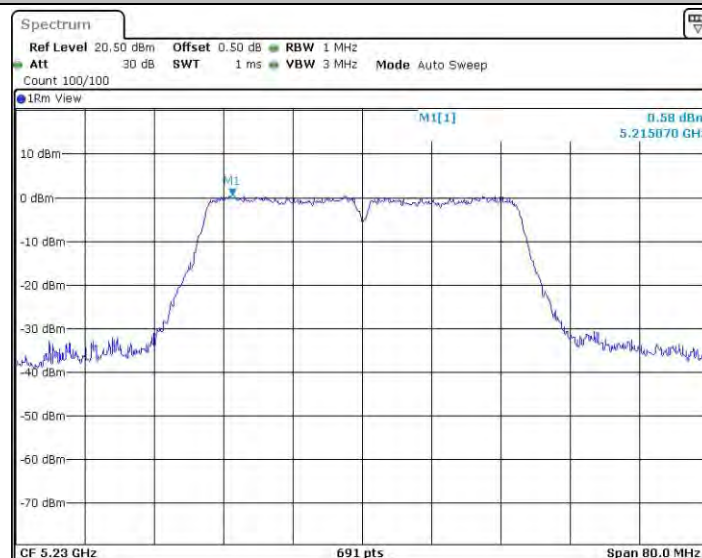
11N20SISO_Ant2_5825



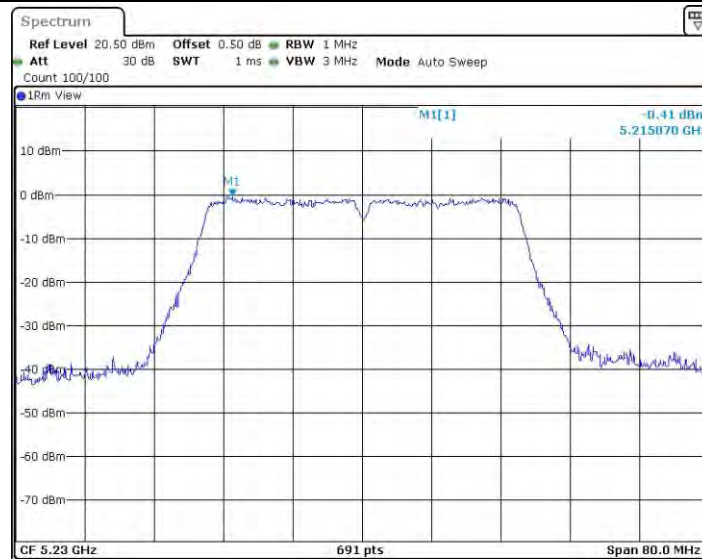
11N40SISO_Ant1_5190



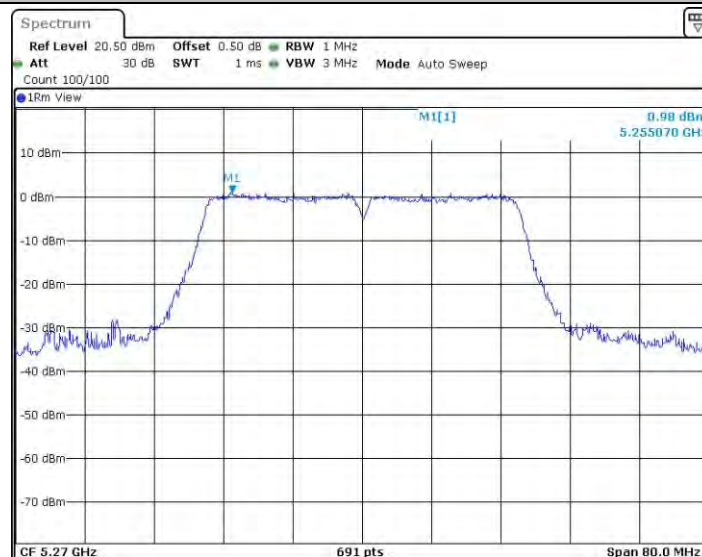
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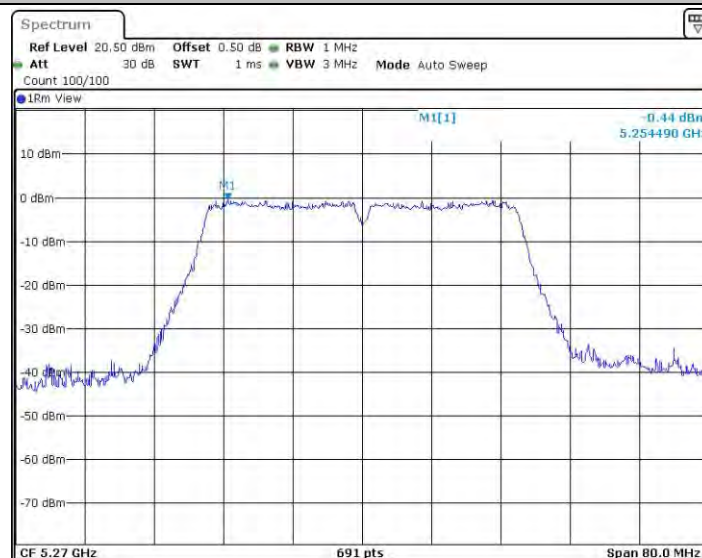
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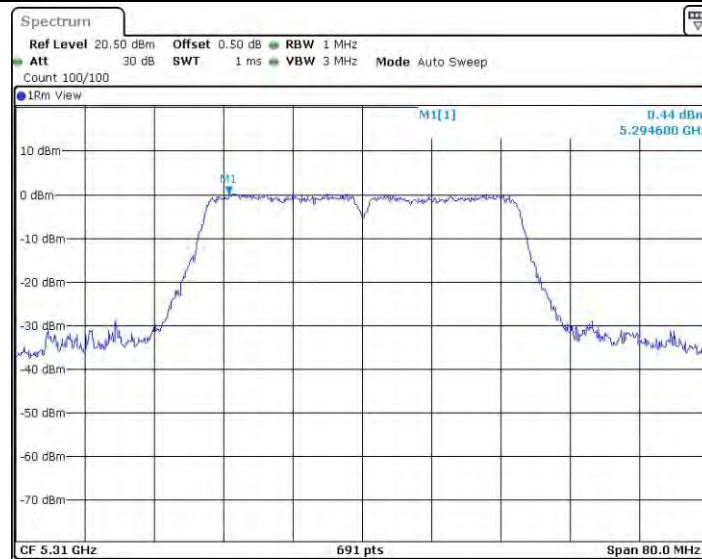
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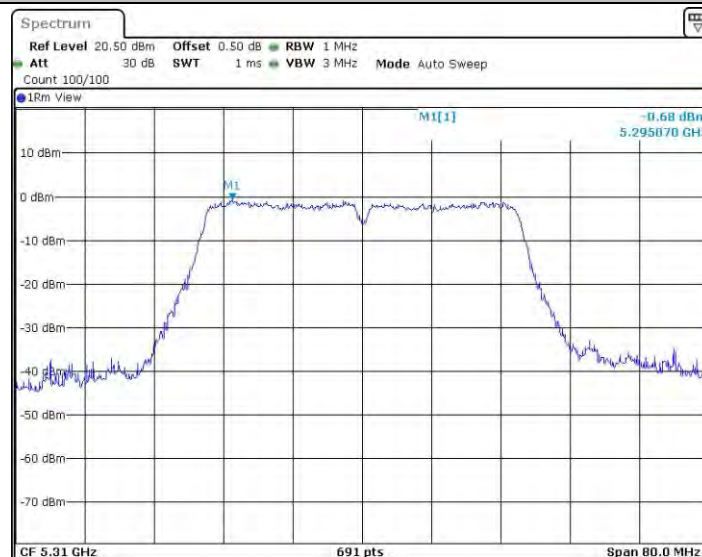
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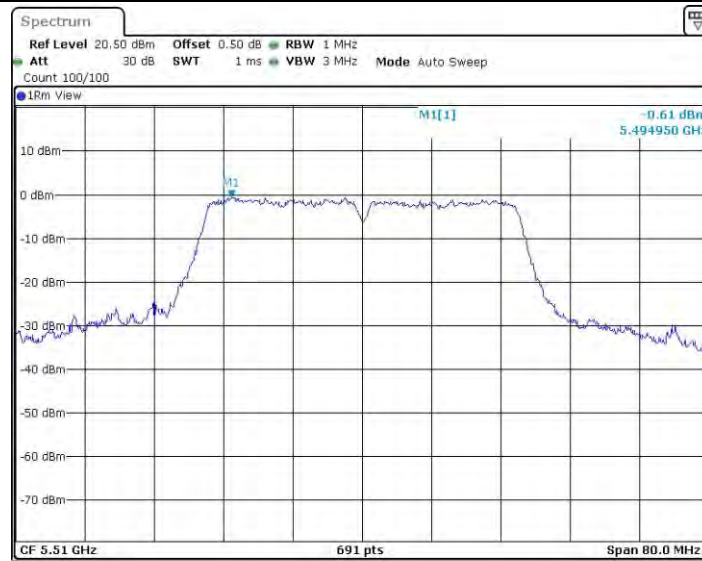
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11N40SISO_Ant2_5310



11N40SISO_Ant1_5510



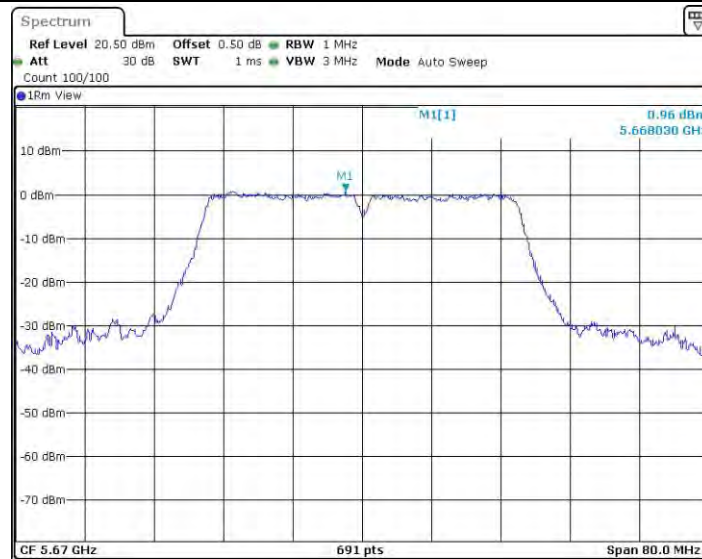
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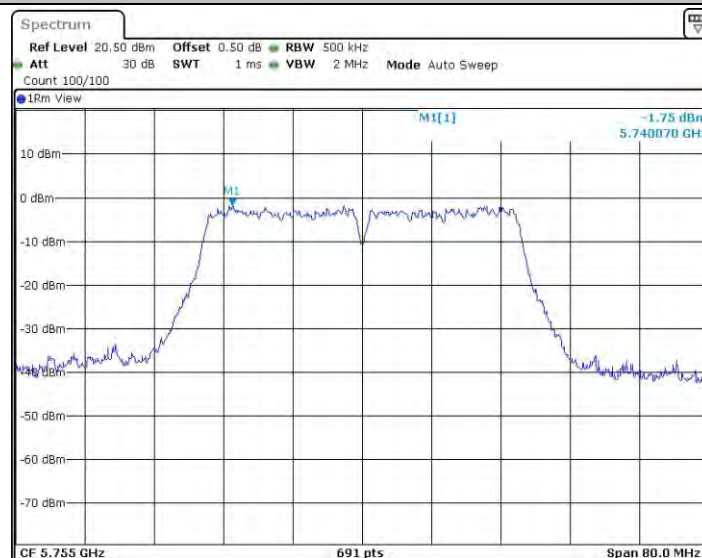
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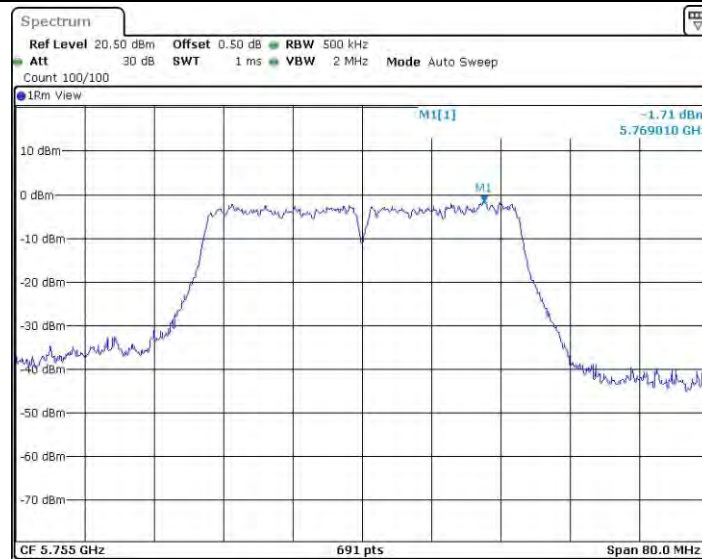
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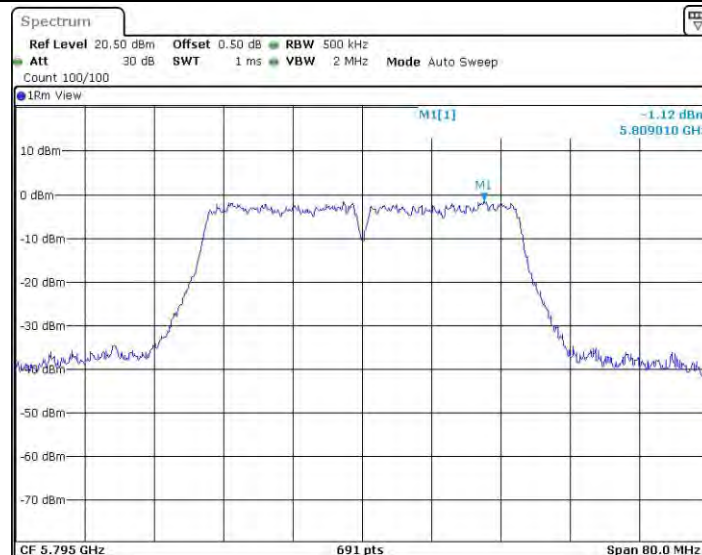
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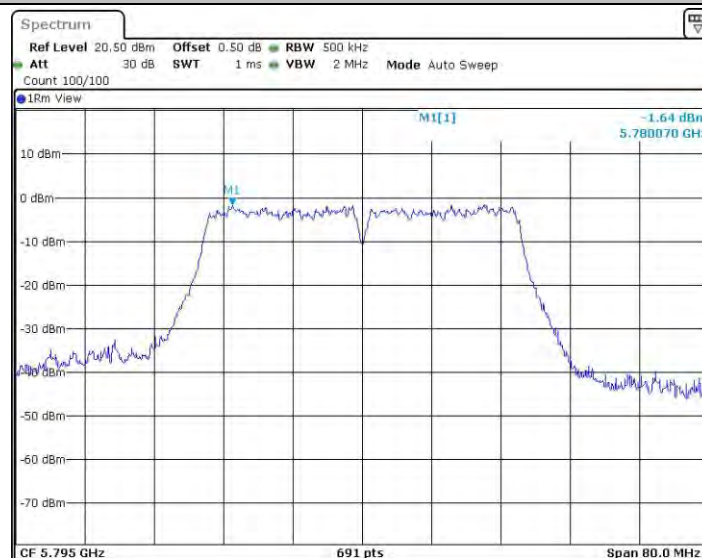
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11N40SISO_Ant2_5755



11N40SISO_Ant1_5795



11N40SISO_Ant2_5795



11AC20SISO_Ant1_5180



11AC20SISO_Ant2_5180



11AC20SISO_Ant1_5200



11AC20SISO_Ant2_5200



11AC20SISO_Ant1_5240



11AC20SISO_Ant2_5240



11AC20SISO_Ant1_5260



11AC20SISO_Ant2_5260



11AC20SISO_Ant1_5280



11AC20SISO_Ant2_5280



11AC20SISO_Ant1_5320



11AC20SISO_Ant2_5320



11AC20SISO_Ant1_5500



11AC20SISO_Ant2_5500



11AC20SISO_Ant1_5580



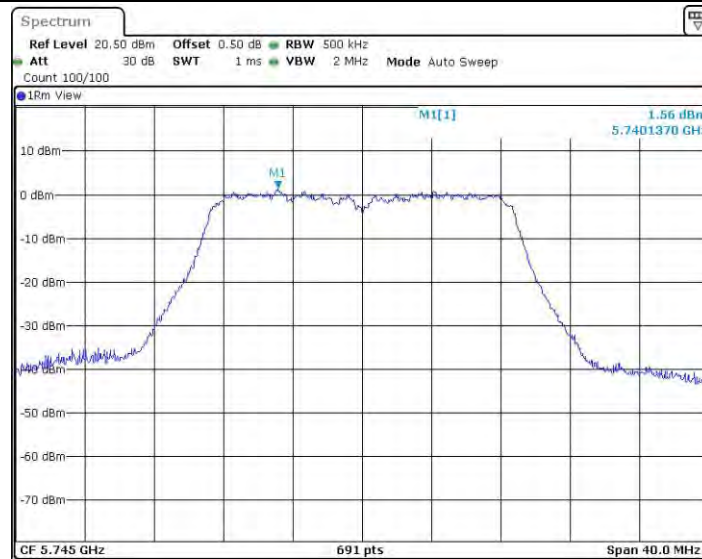
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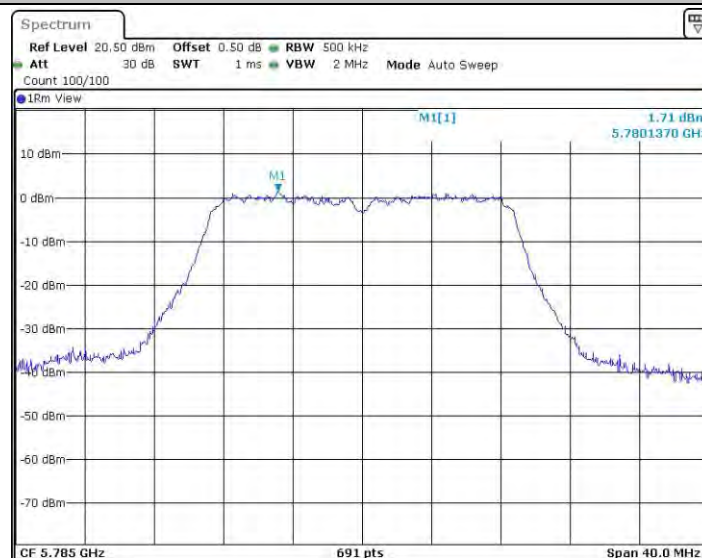
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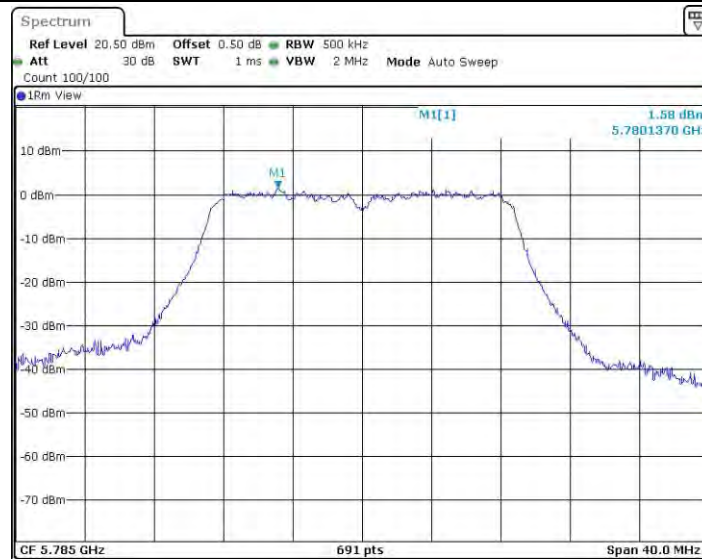
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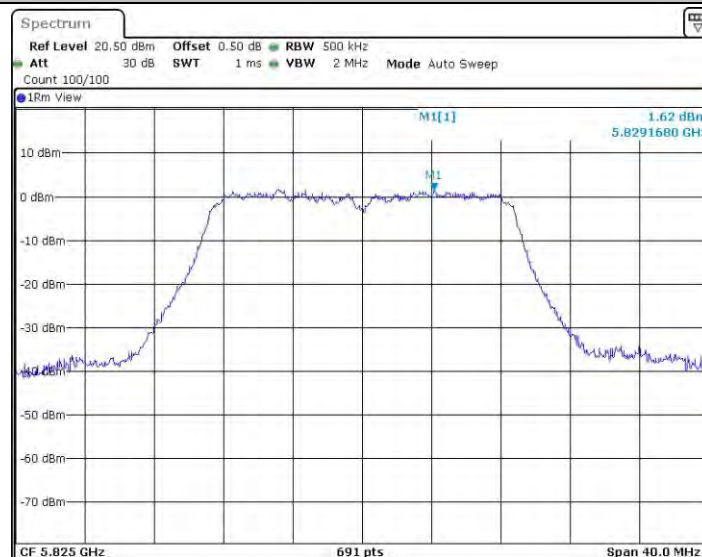
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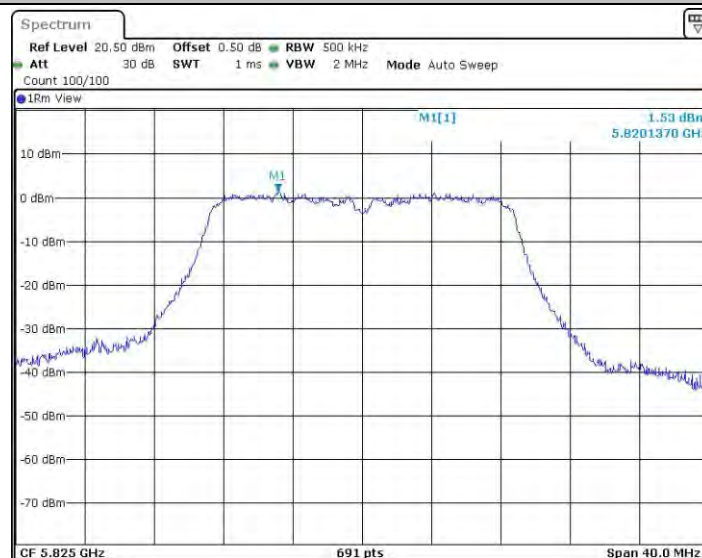
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11AC20SISO_Ant2_5785



11AC20SISO_Ant1_5825



11AC20SISO_Ant2_5825