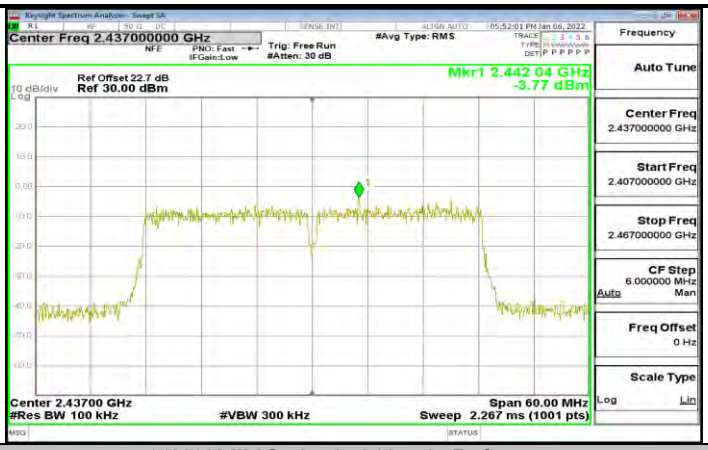
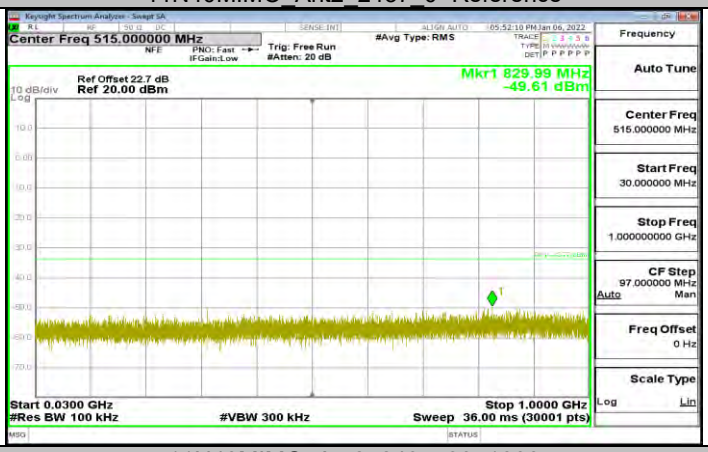
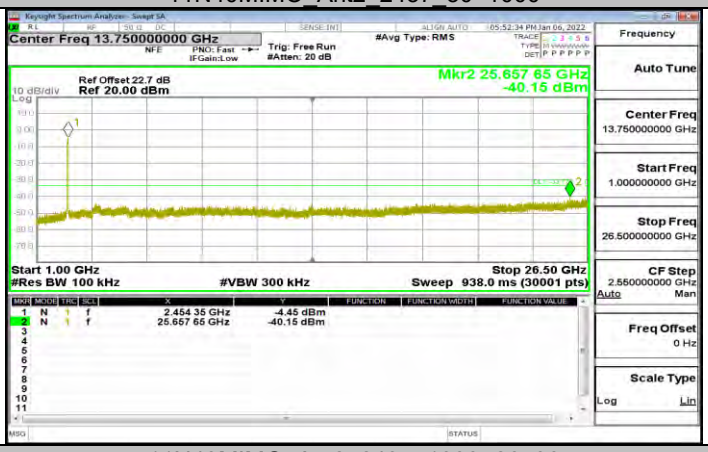


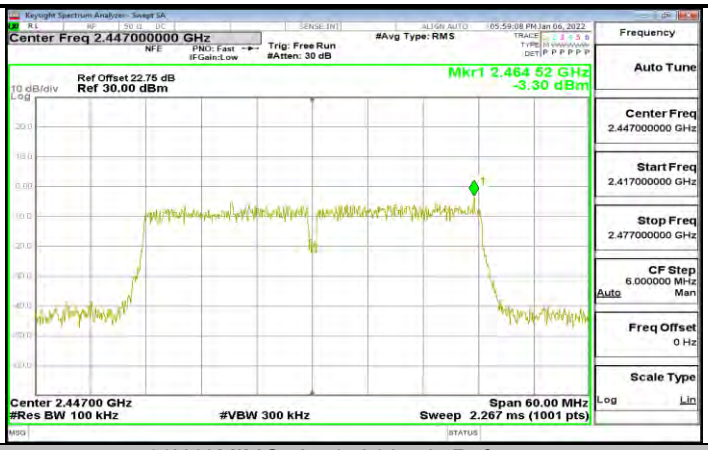
11N40MIMO_Ant2_2437_0~Reference



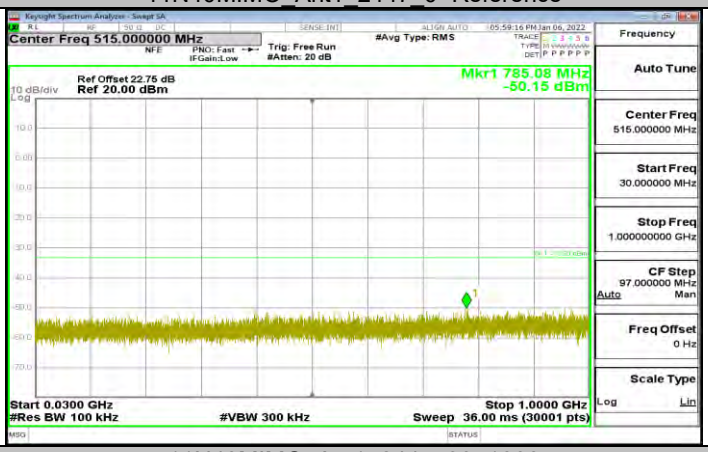
11N40MIMO_Ant2_2437_30~1000



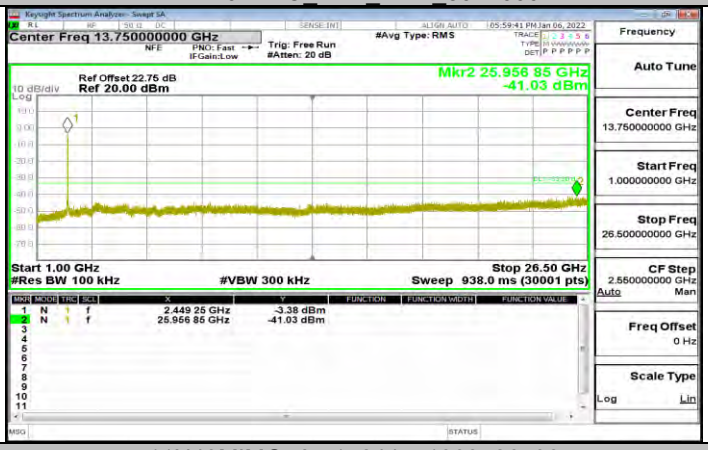
11N40MIMO_Ant2_2437_1000~26500



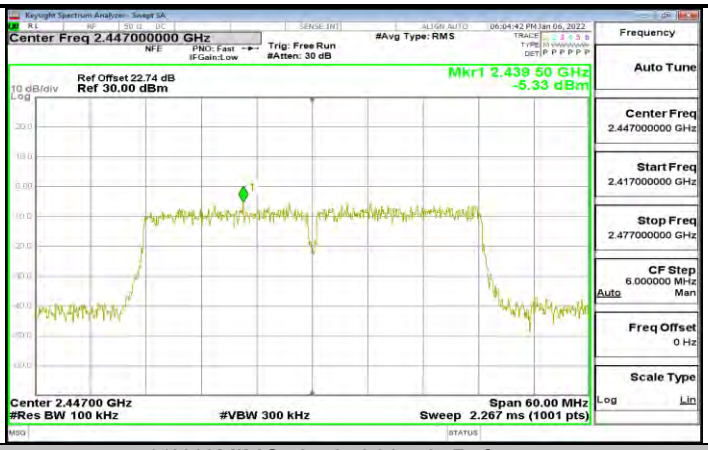
11N40MIMO_Ant1_2447_0~Reference



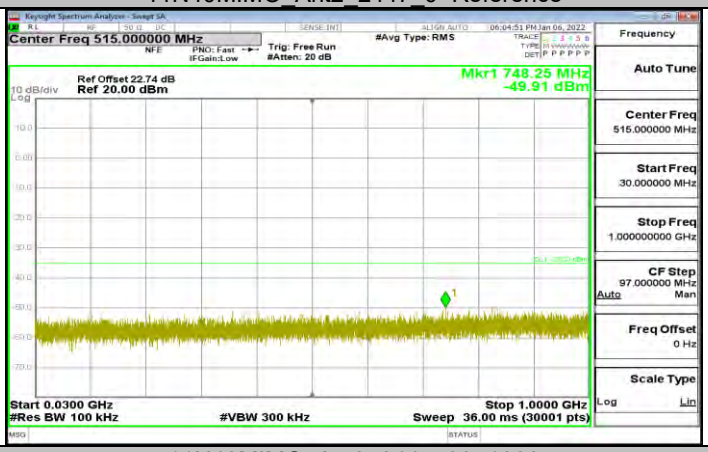
11N40MIMO_Ant1_2447_30~1000



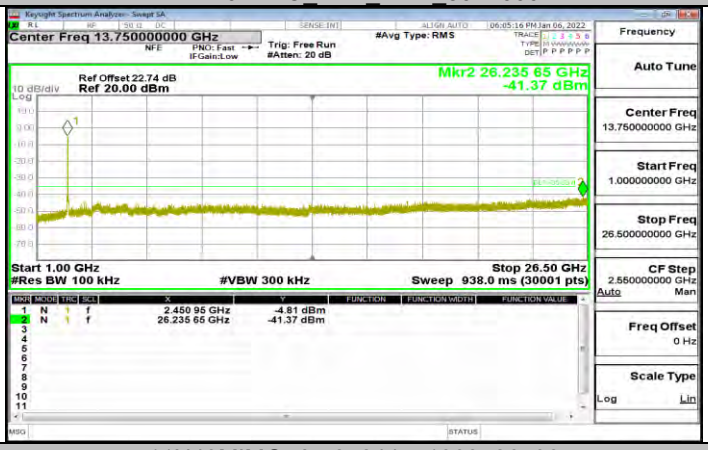
11N40MIMO_Ant1_2447_1000~26500



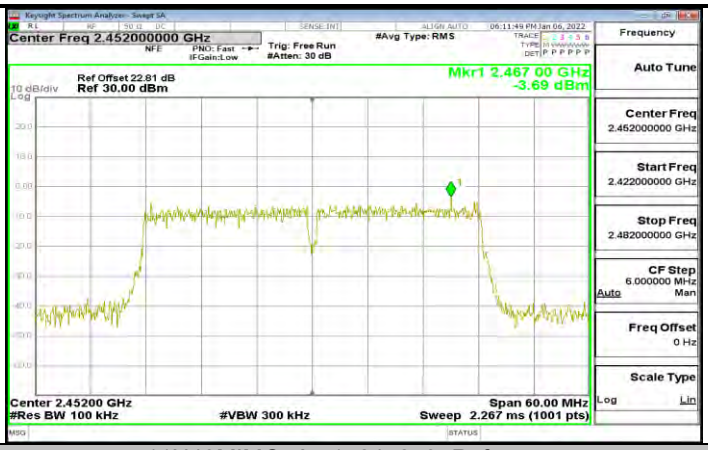
11N40MIMO_Ant2_2447_0~Reference



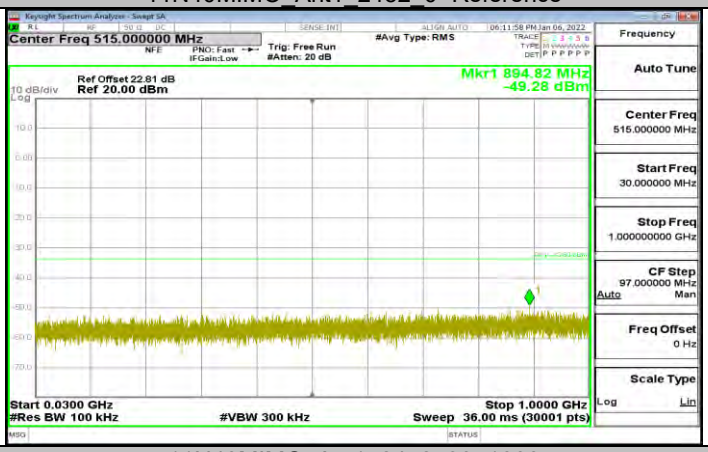
11N40MIMO_Ant2_2447_30~1000



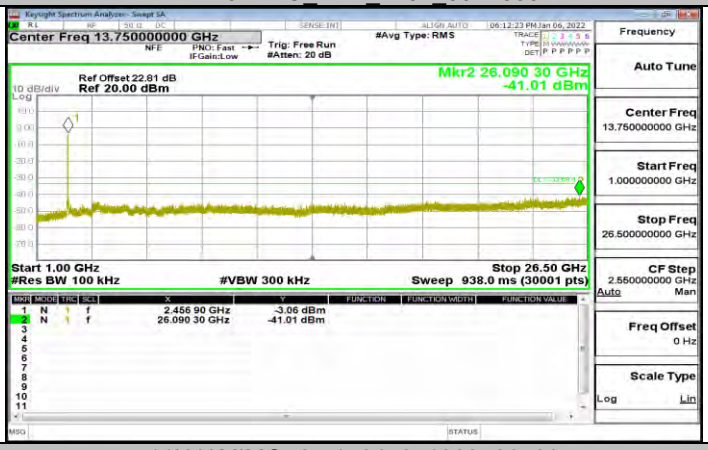
11N40MIMO_Ant2_2447_1000~26500



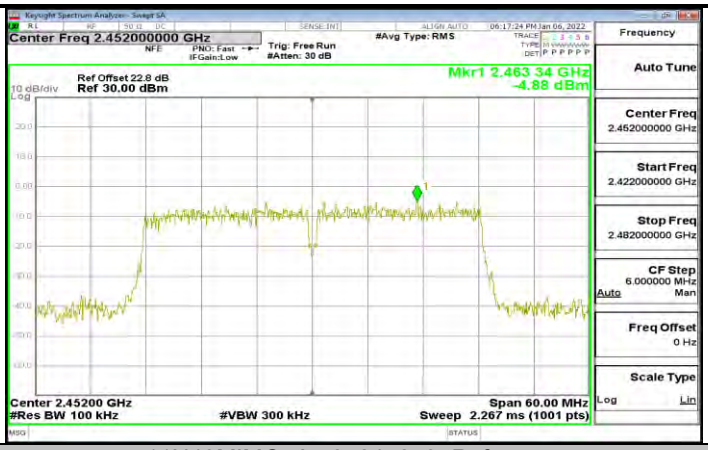
11N40MIMO_Ant1_2452_0~Reference



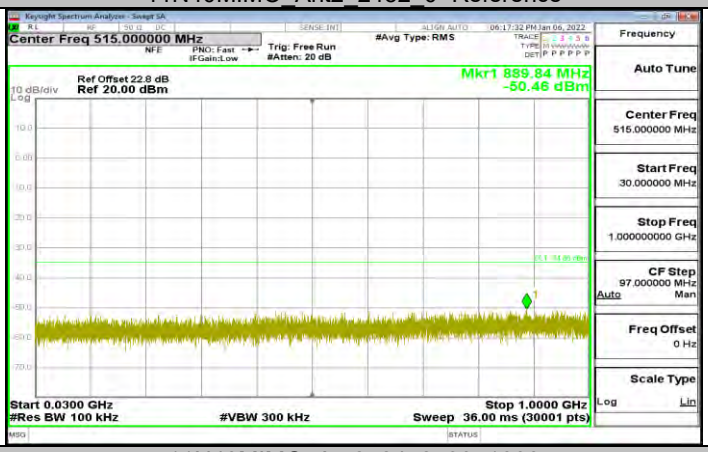
11N40MIMO_Ant1_2452_30~1000



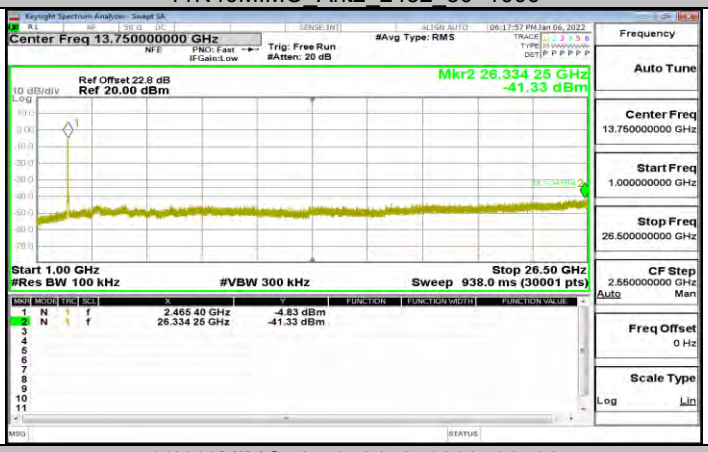
11N40MIMO_Ant1_2452_1000~26500



11N40MIMO_Ant2_2452_0~Reference

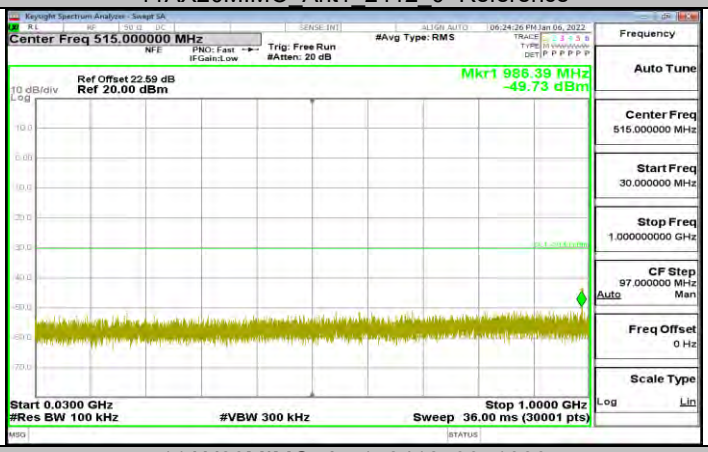


11N40MIMO_Ant2_2452_30~1000

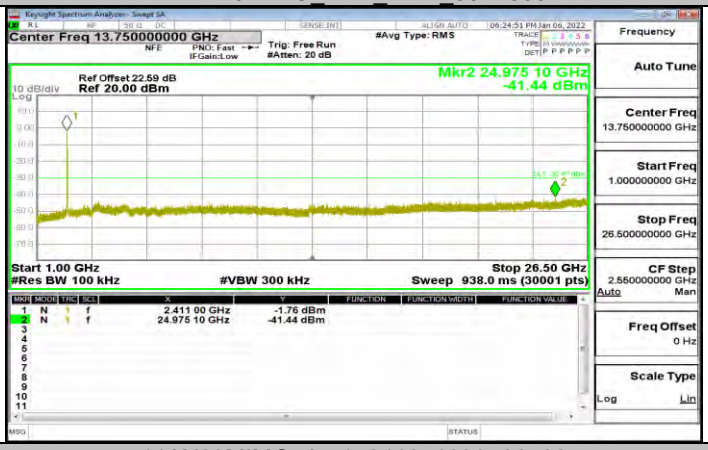


11N40MIMO_Ant2_2452_1000~26500

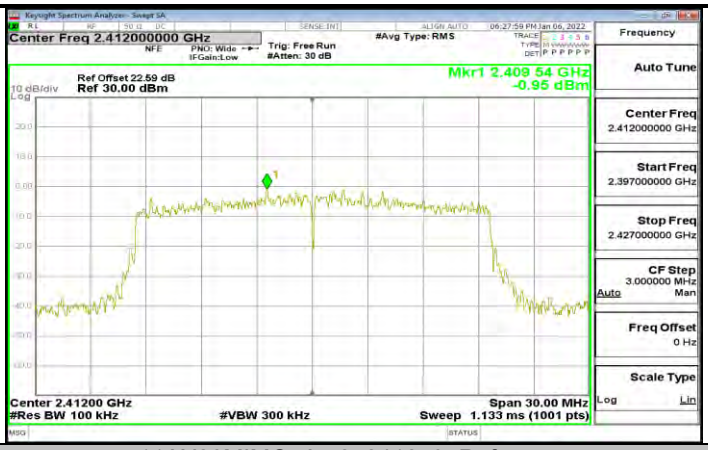
11AX20MIMO_Ant1_2412_0~Reference



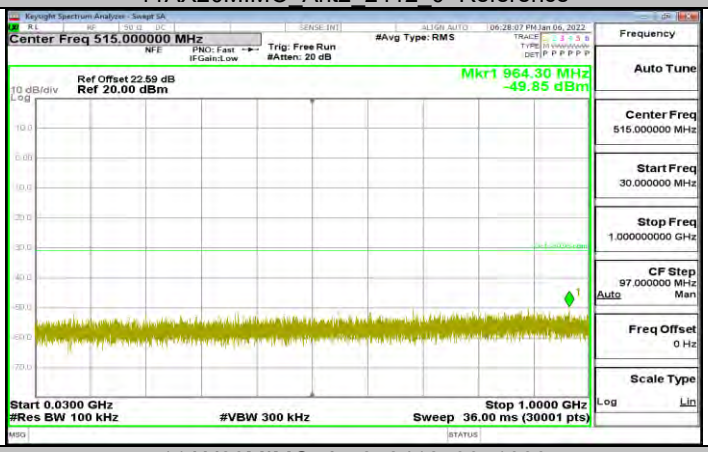
11AX20MIMO_Ant1_2412_30~1000



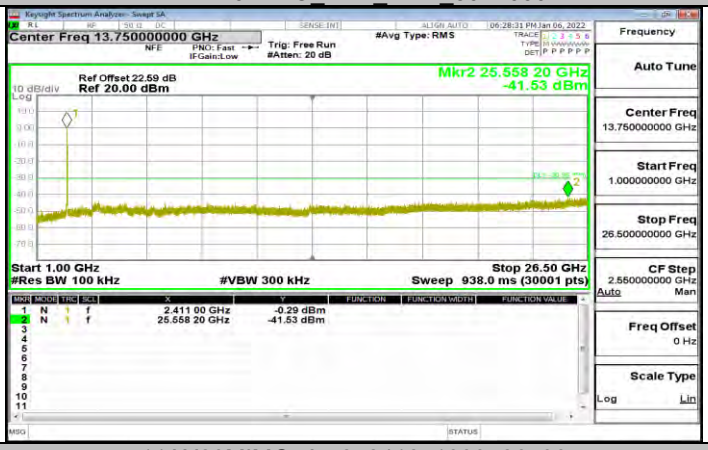
11AX20MIMO_Ant1_2412_1000~26500



11AX20MIMO_Ant2_2412_0~Reference

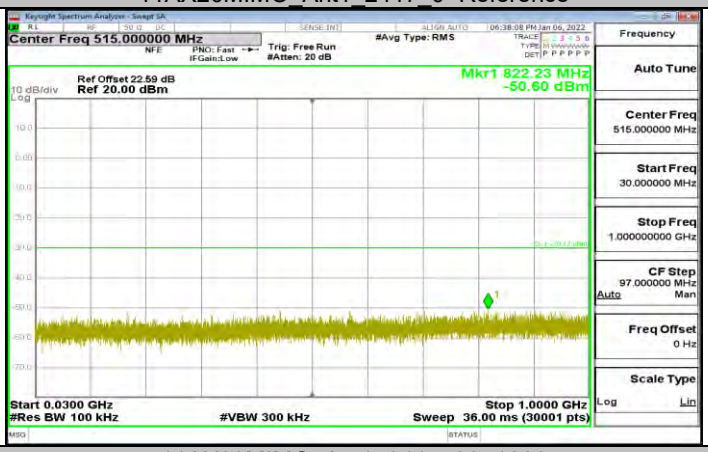


11AX20MIMO_Ant2_2412_30~1000

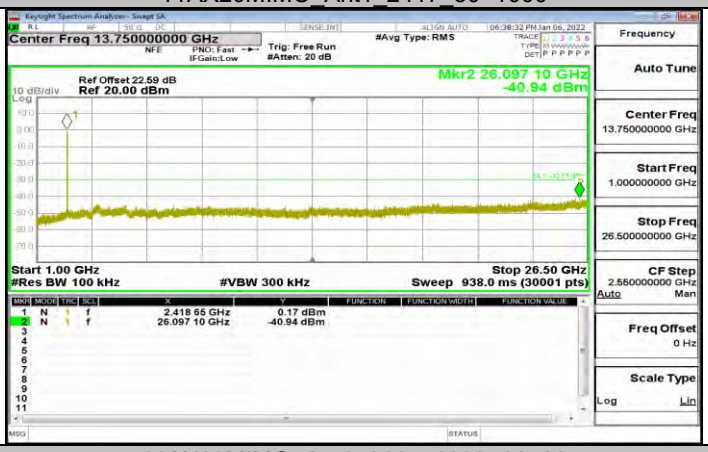


11AX20MIMO_Ant2_2412_1000~26500

11AX20MIMO_Ant1_2417_0~Reference

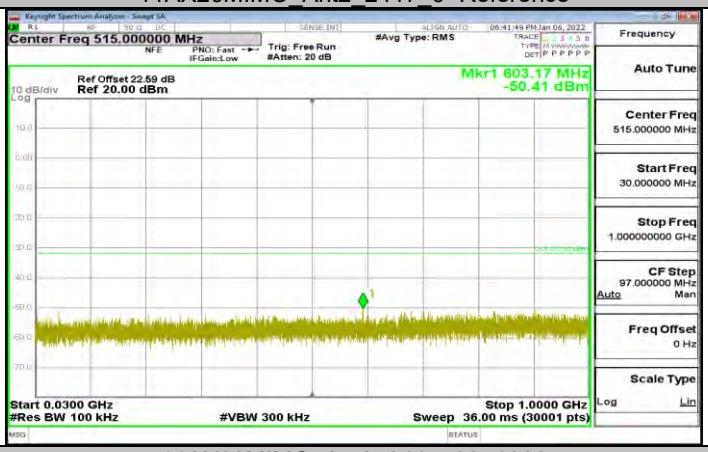


11AX20MIMO_Ant1_2417_30~1000

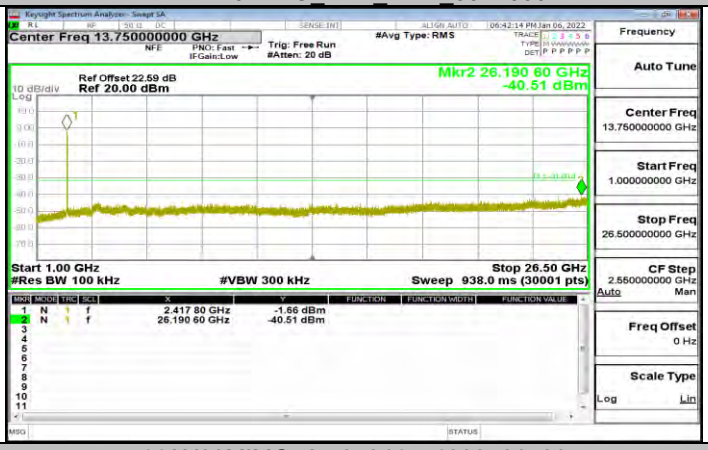


11AX20MIMO_Ant1_2417_1000~26500

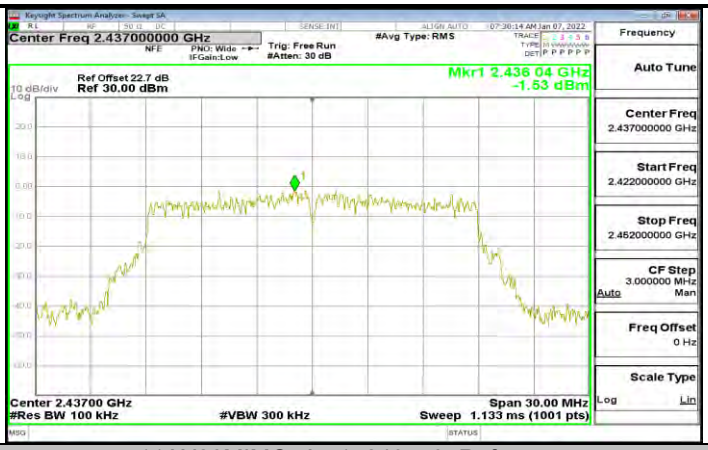
11AX20MIMO_Ant2_2417_0~Reference



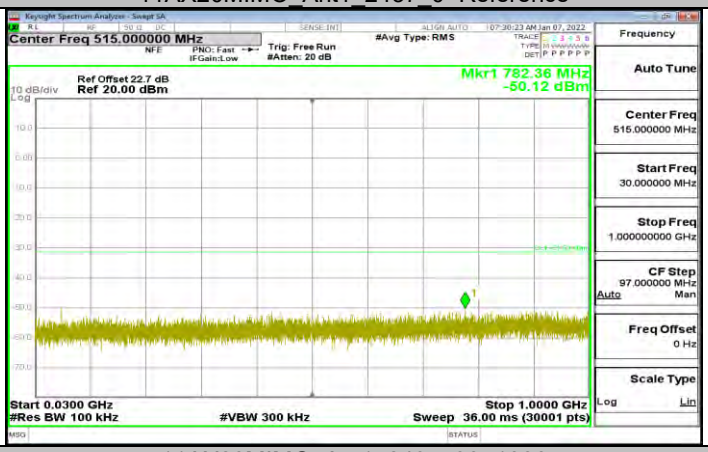
11AX20MIMO_Ant2_2417_30~1000



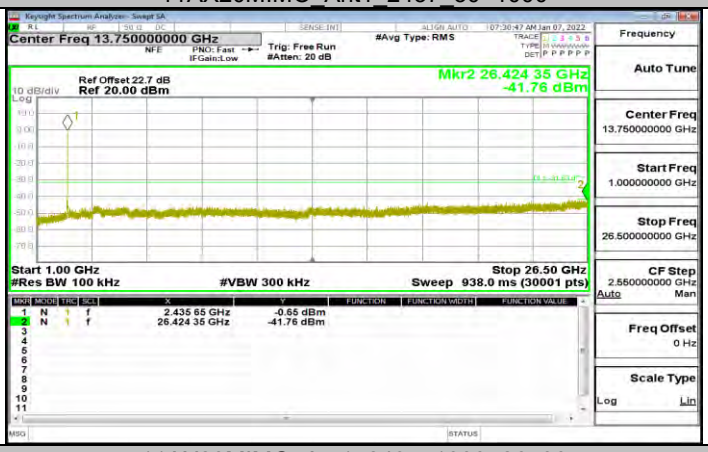
11AX20MIMO_Ant2_2417_1000~26500



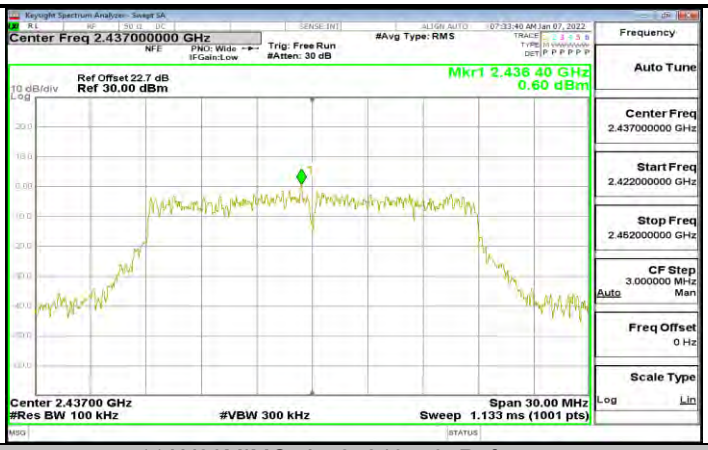
11AX20MIMO_Ant1_2437_0~Reference



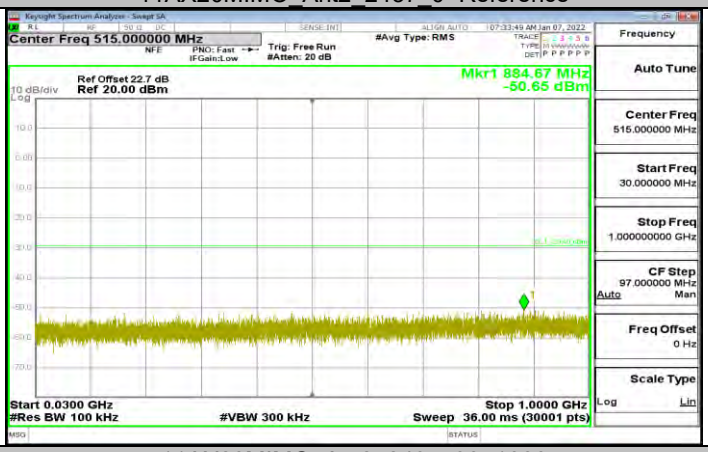
11AX20MIMO_Ant1_2437_30~1000



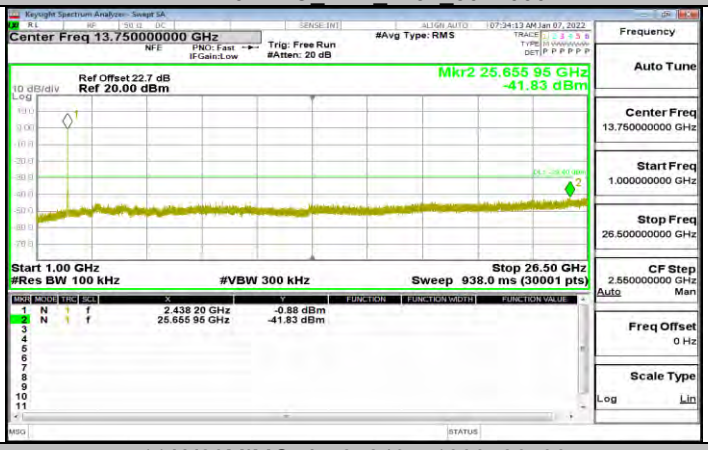
11AX20MIMO_Ant1_2437_1000~26500



11AX20MIMO_Ant2_2437_0~Reference

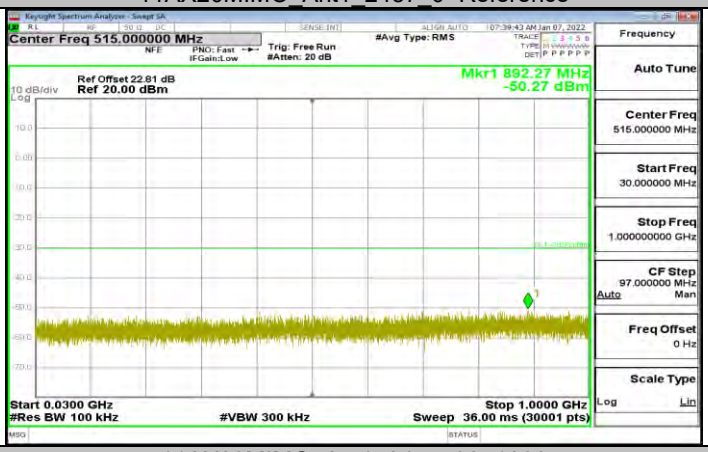


11AX20MIMO_Ant2_2437_30~1000

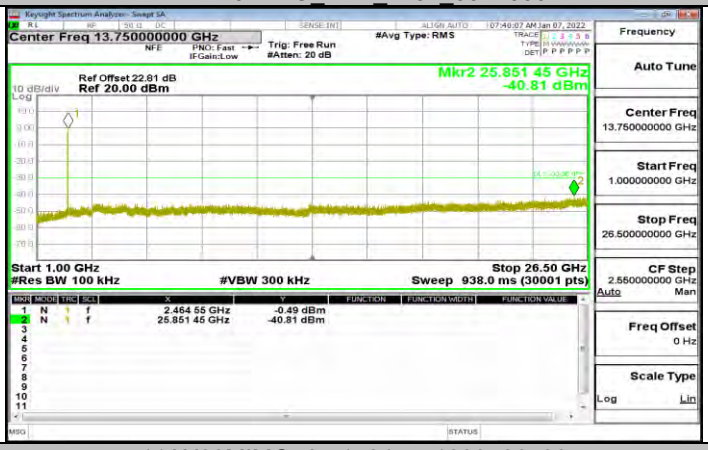


11AX20MIMO_Ant2_2437_1000~26500

11AX20MIMO_Ant1_2457_0~Reference

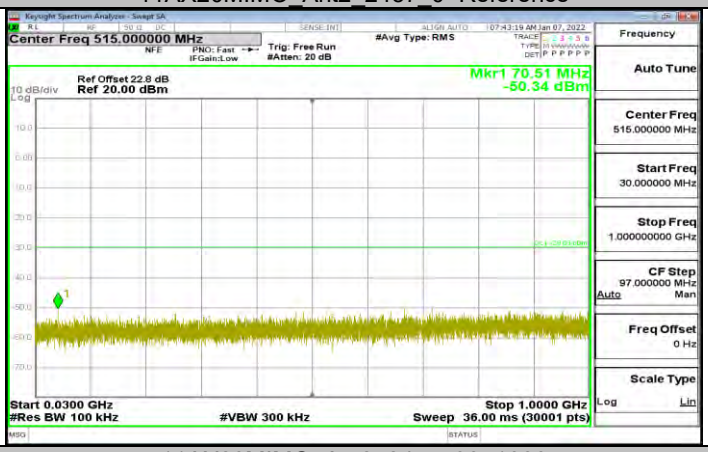


11AX20MIMO_Ant1_2457_30~1000

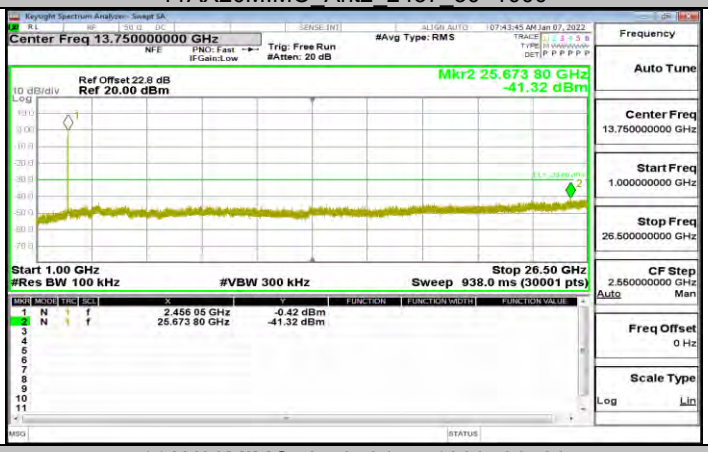


11AX20MIMO_Ant1_2457_1000~26500

11AX20MIMO_Ant2_2457_0~Reference

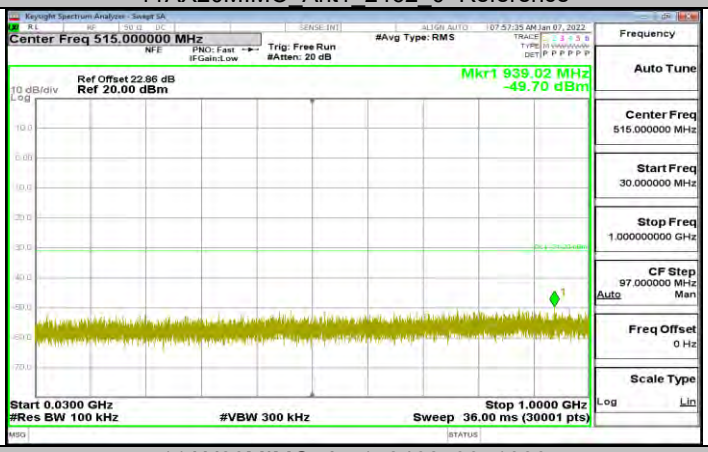


11AX20MIMO_Ant2_2457_30~1000

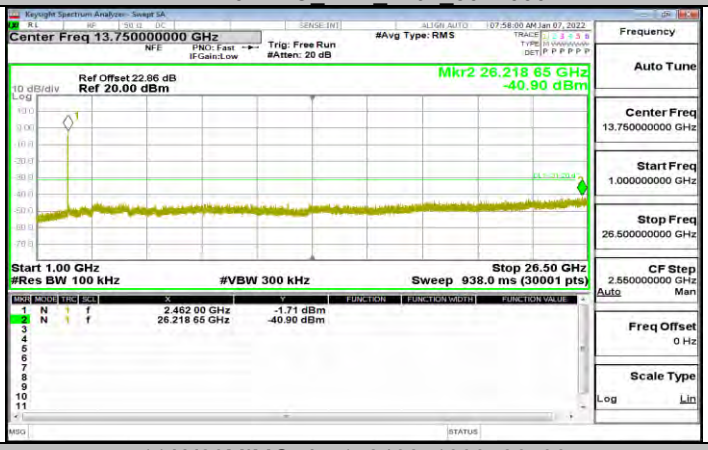


11AX20MIMO_Ant2_2457_1000~26500

11AX20MIMO_Ant1_2462_0~Reference

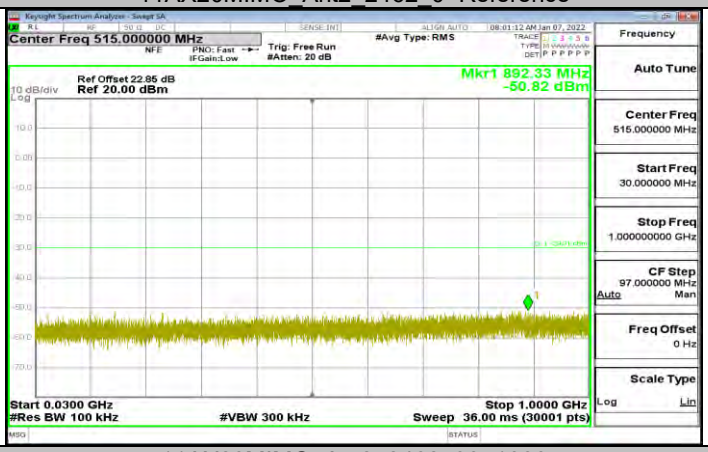


11AX20MIMO_Ant1_2462_30~1000

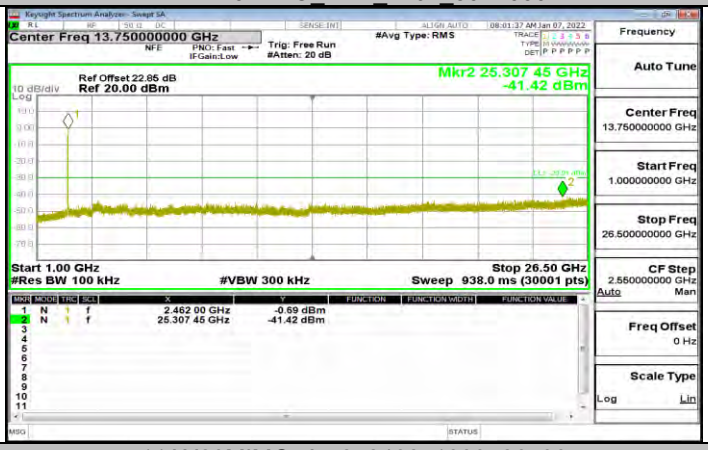


11AX20MIMO_Ant1_2462_1000~26500

11AX20MIMO_Ant2_2462_0~Reference

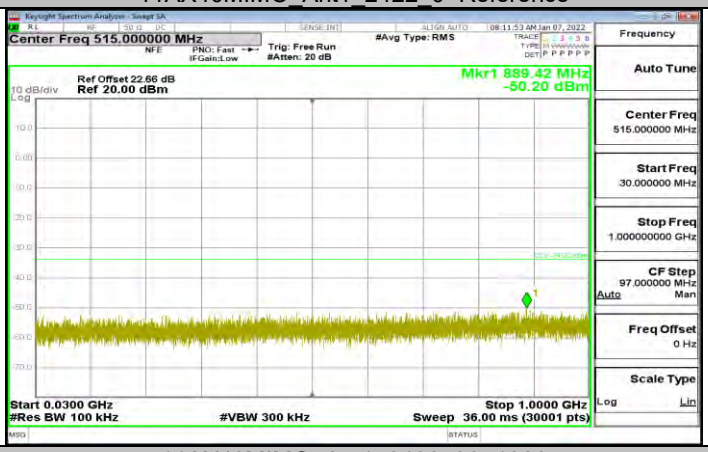


11AX20MIMO_Ant2_2462_30~1000

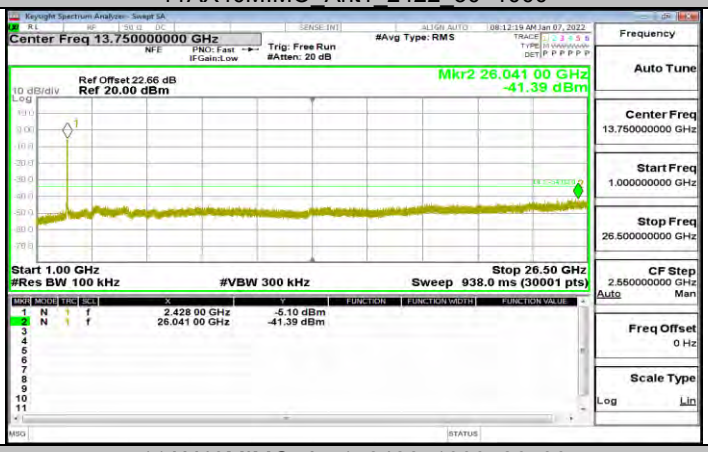


11AX20MIMO_Ant2_2462_1000~26500

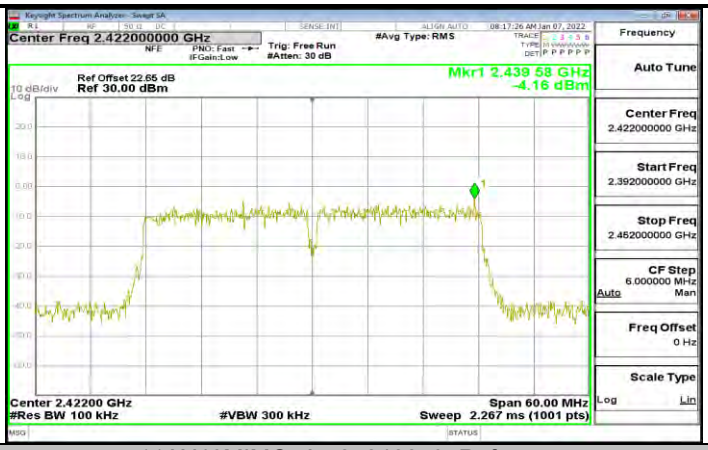
11AX40MIMO_Ant1_2422_0~Reference



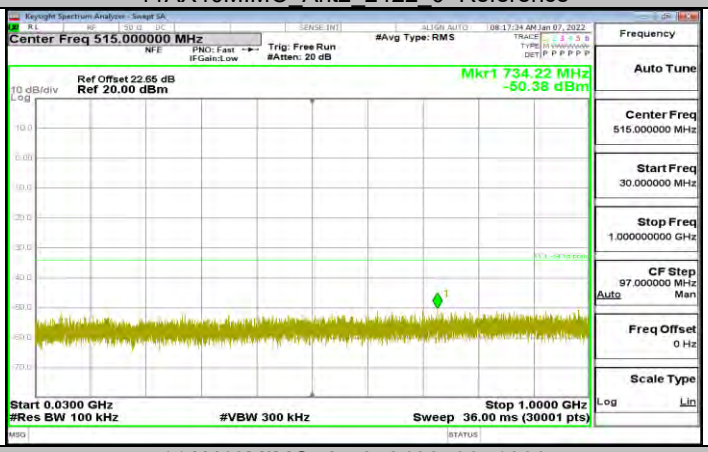
11AX40MIMO_Ant1_2422_30~1000



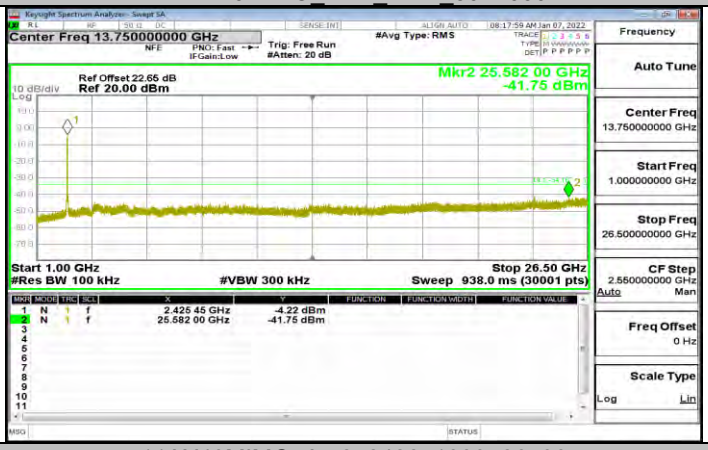
11AX40MIMO_Ant1_2422_1000~26500



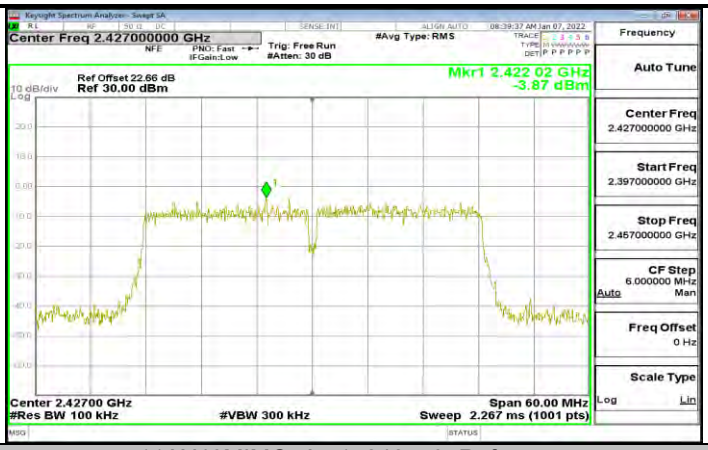
11AX40MIMO_Ant2_2422_0~Reference



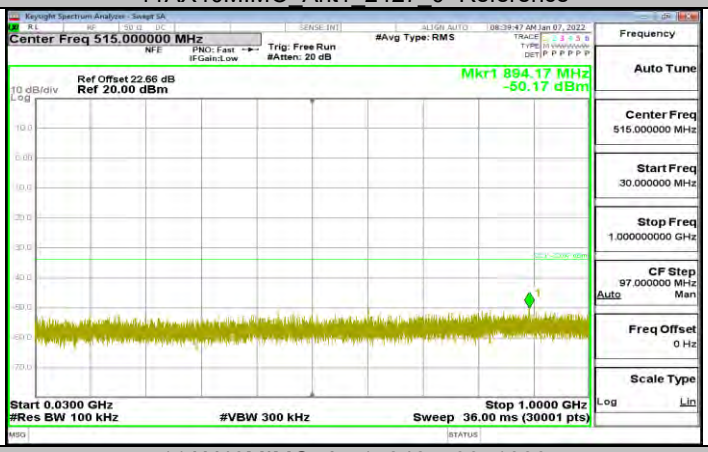
11AX40MIMO_Ant2_2422_30~1000



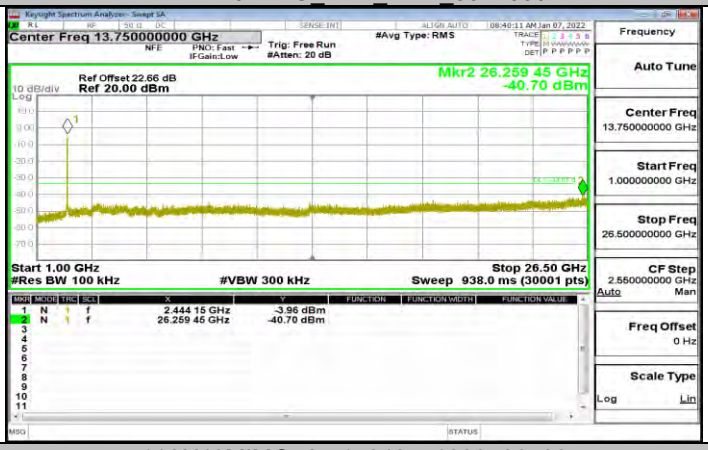
11AX40MIMO_Ant2_2422_1000~26500



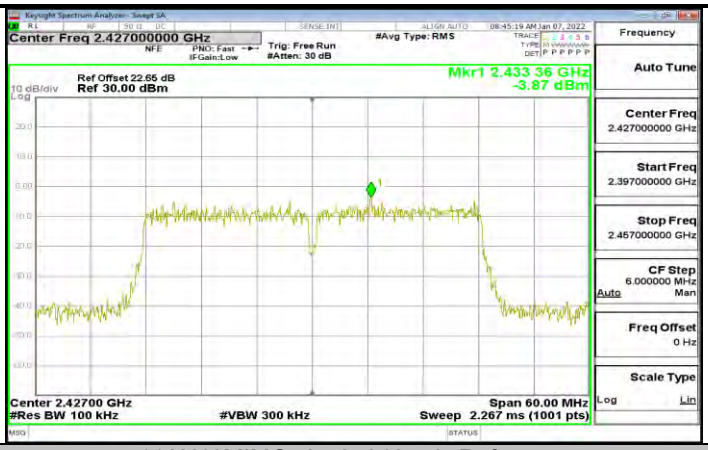
11AX40MIMO_Ant1_2427_0~Reference



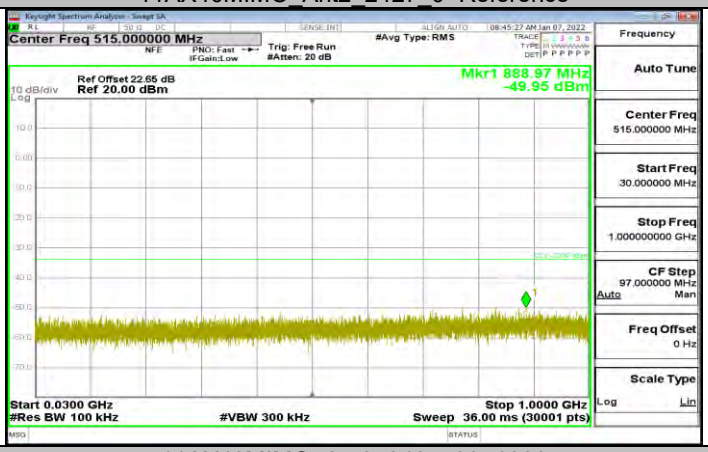
11AX40MIMO_Ant1_2427_30~1000



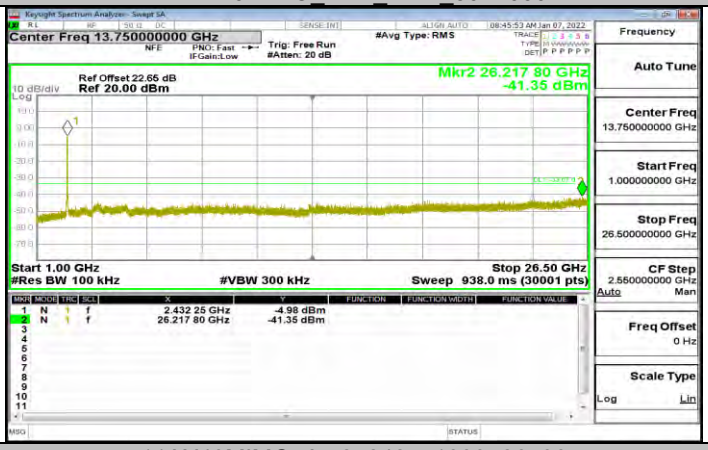
11AX40MIMO_Ant1_2427_1000~26500



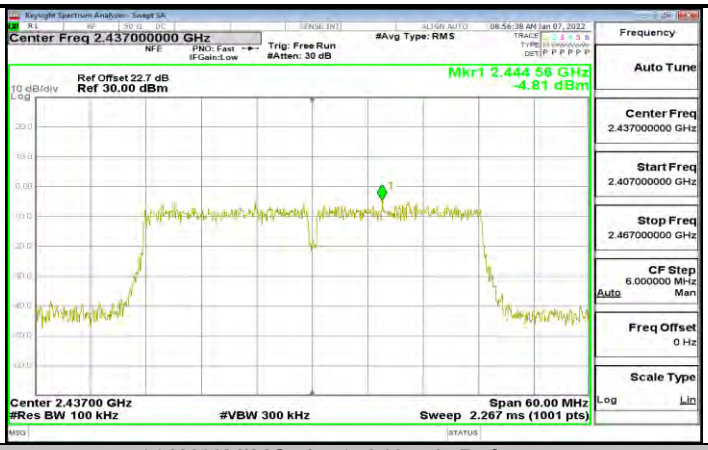
11AX40MIMO_Ant2_2427_0~Reference



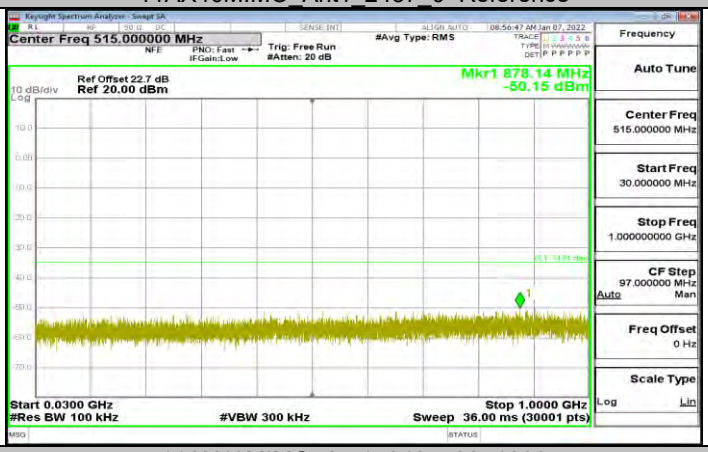
11AX40MIMO_Ant2_2427_30~1000



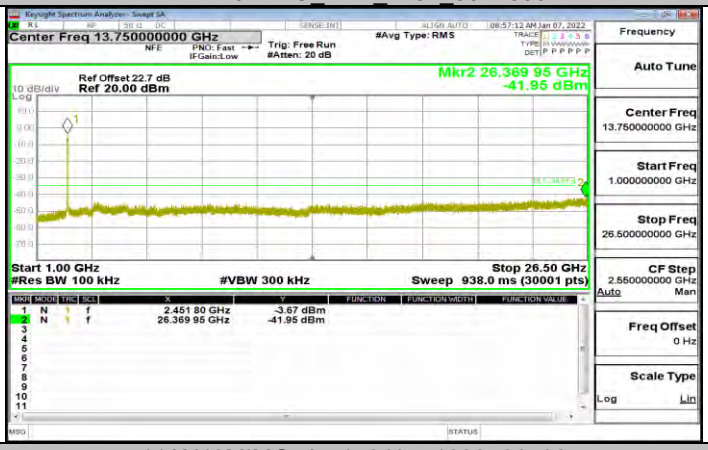
11AX40MIMO_Ant2_2427_1000~26500



11AX40MIMO_Ant1_2437_0~Reference

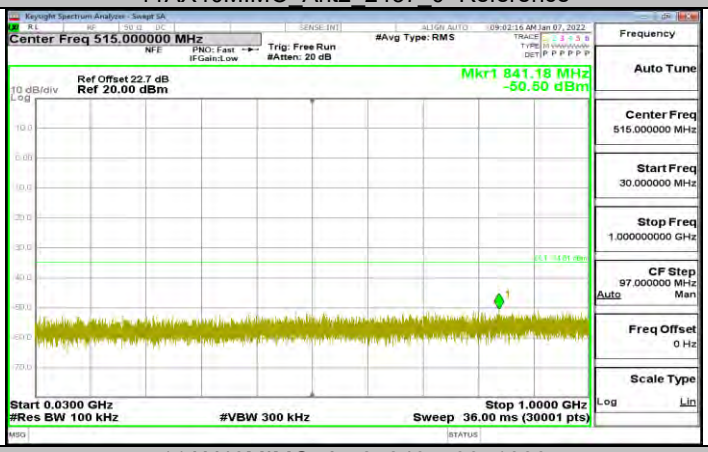


11AX40MIMO_Ant1_2437_30~1000

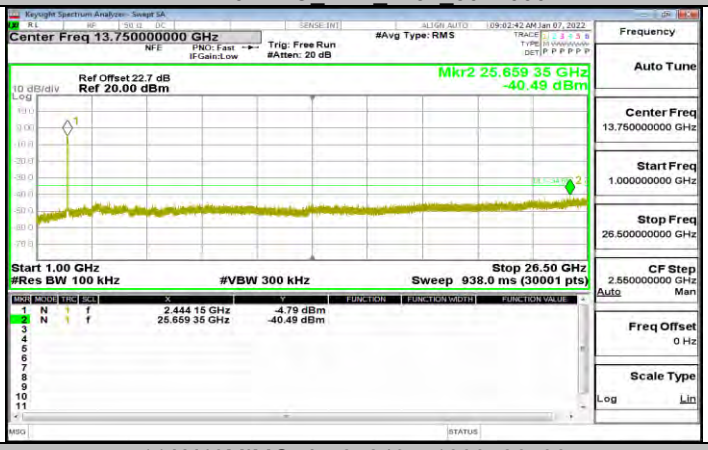


11AX40MIMO_Ant1_2437_1000~26500

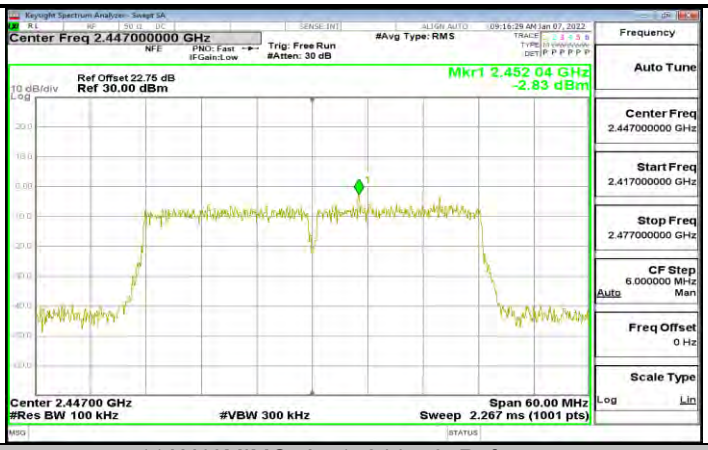
11AX40MIMO_Ant2_2437_0~Reference



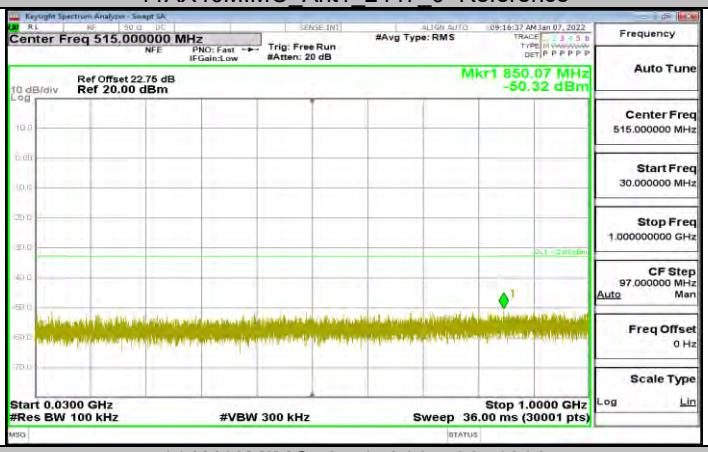
11AX40MIMO_Ant2_2437_30~1000



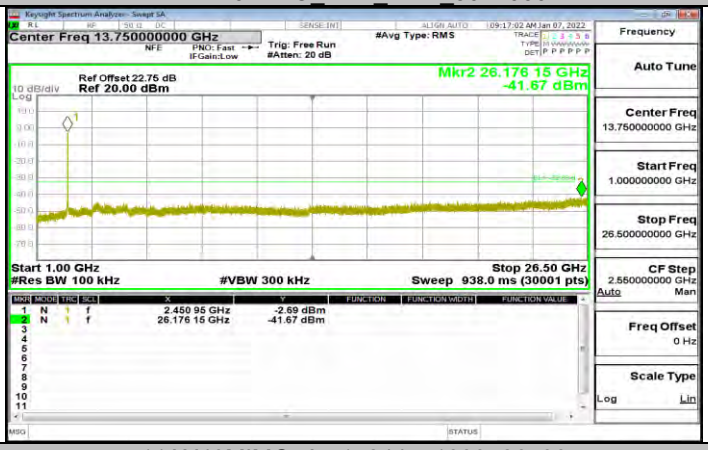
11AX40MIMO_Ant2_2437_1000~26500



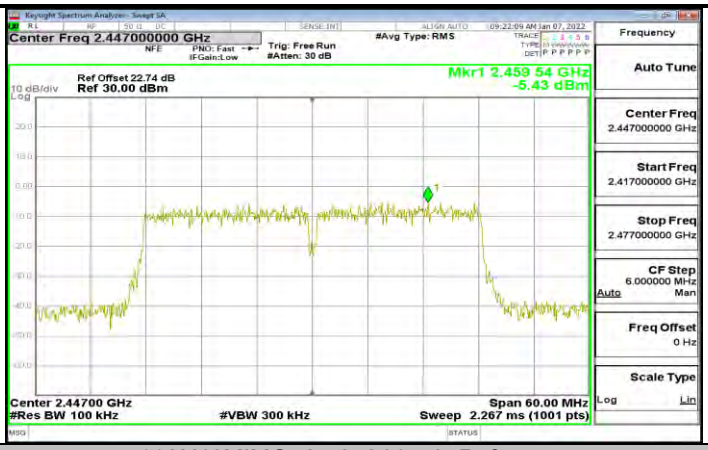
11AX40MIMO_Ant1_2447_0~Reference



11AX40MIMO_Ant1_2447_30~1000



11AX40MIMO_Ant1_2447_1000~26500



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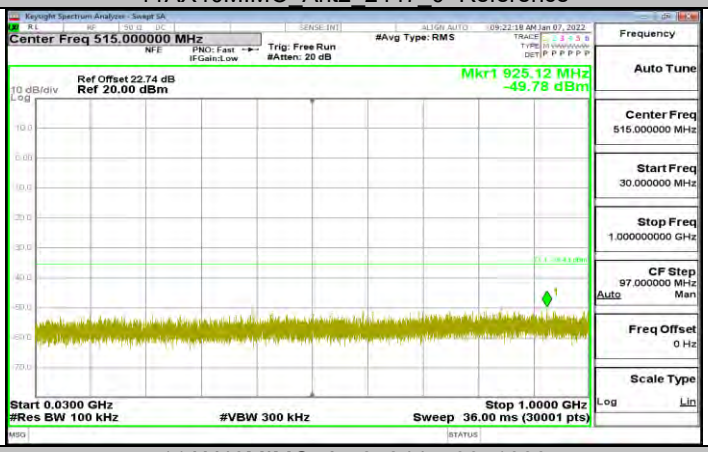

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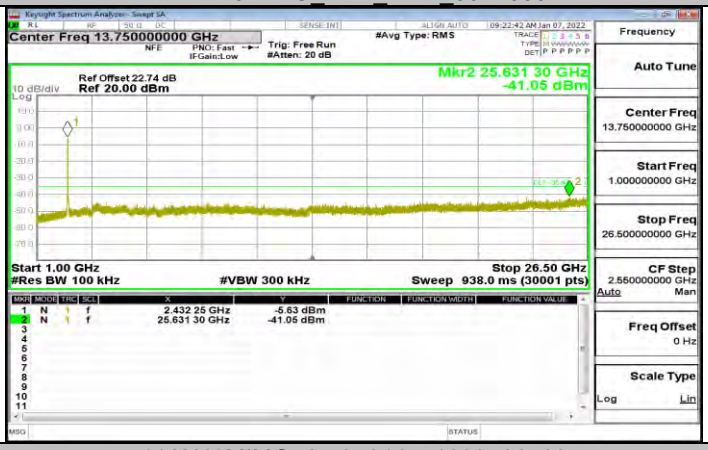

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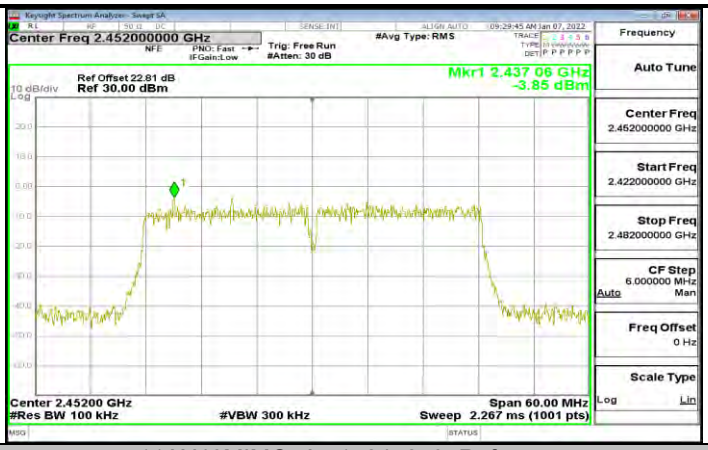
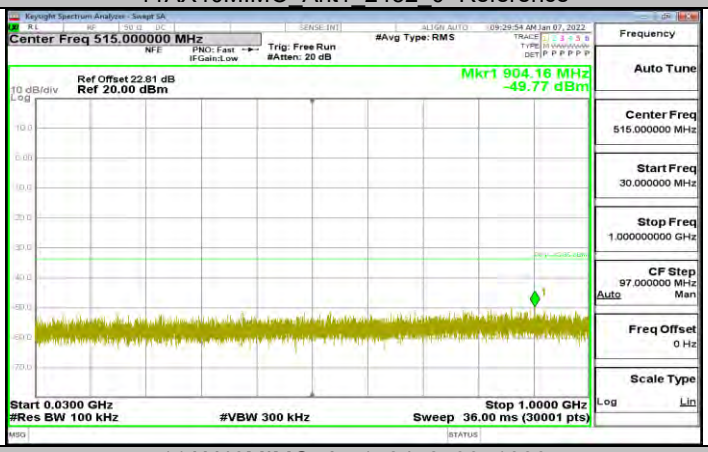


11AX40MIMO_Ant1_2452_0~Reference



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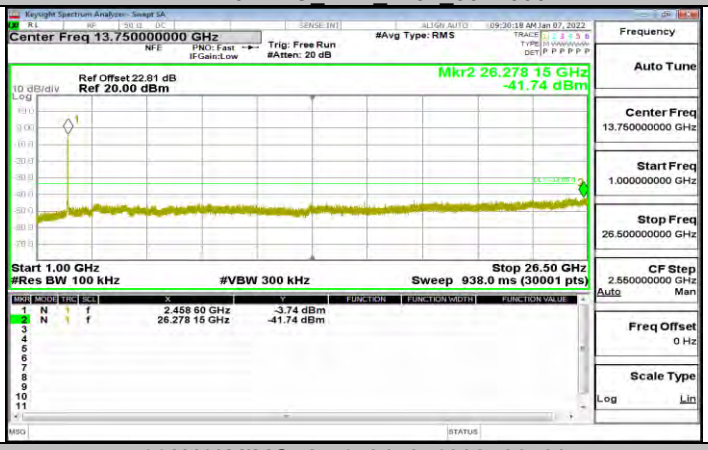

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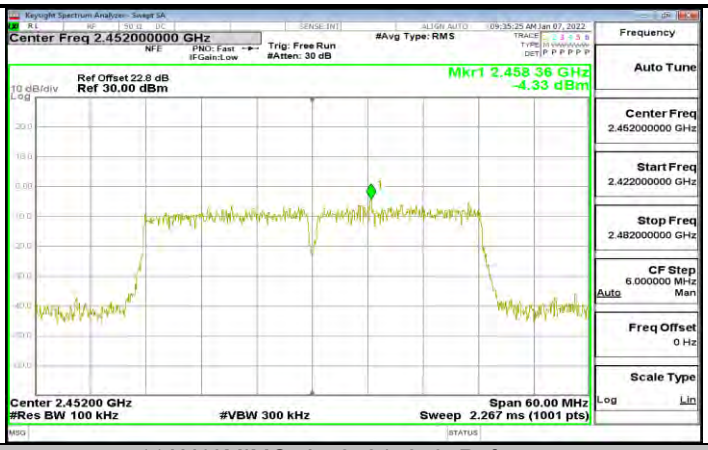

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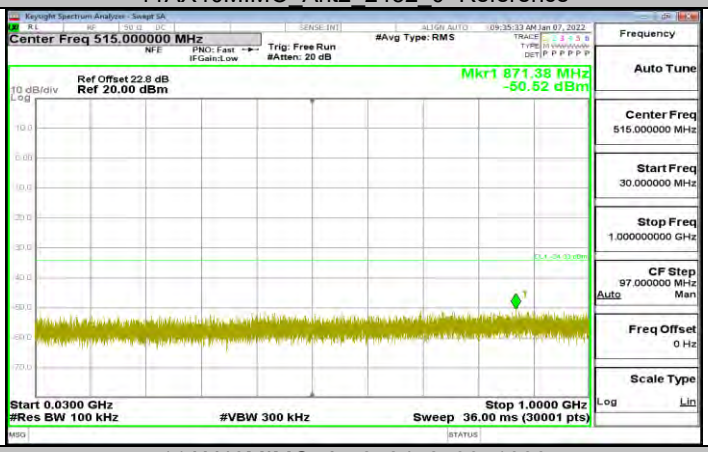

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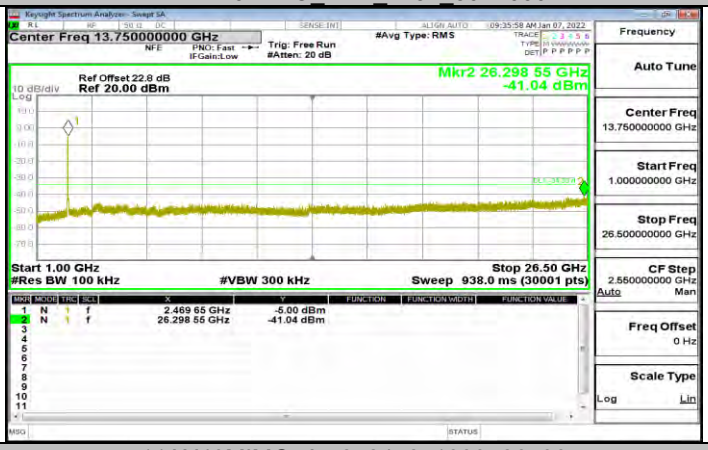

11AX40MIMO_Ant2_2452_1000~26500

11.7. Appendix G: Duty Cycle

11.7.1. Test Result

Test Mode	On Time (msec)	Period (msec)	Duty Cycle x (Linear)	Duty Cycle (%)	Duty Cycle Correction Factor (dB)	1/T Minimum VBW (kHz)	Final setting For VBW (kHz)
11B	16.79	16.81	0.9988	99.88	0.01	0.06	0.01
11G	2.79	2.81	0.9929	99.29	0.03	0.36	0.01
11N20MIMO	1.32	1.34	0.9851	98.51	0.07	0.76	0.01
11N40MIMO	0.66	0.68	0.9706	97.06	0.13	1.52	2
11AX20MIMO	1.04	1.05	0.9905	99.05	0.04	0.96	0.01
11AX40MIMO	0.55	0.57	0.9649	96.49	0.16	1.82	2

Note:
Duty Cycle Correction Factor=10log (1/x).
Where: x is Duty Cycle (Linear)
Where: T is On Time
If that calculated VBW is not available on the analyzer then the next higher value should be used.
If the EUT is configured to transmit with duty cycle ≥ 98%, set VBW ≤ RBW/100 (i.e., 10 kHz) but not less than 10 Hz.

11.7.1. Test Graphs

11B_Ant2_2437

11G_Ant2_2437

11N20MIMO_Ant2_2437

11N40MIMO_Ant2_2437

11AX20MIMO_Ant1_2412

11AX40MIMO_Ant1_2422

END OF REPORT