

Report No.: WSCT-ANAB-R&E240700030A-Wi-Fi2

ax40	5670	0.61	11	Pass
ax40	5755	-1.49	30	Pass
ax40	5795	-1.24	30	Pass
ax80	5210	-1.39	11	Pass
ax80	5290	-2.36	11	Pass
ax80	5530	-1.68	11	Pass
ax80	5610	-1.99	11	Pass
ax80	5775	-5.23	30	Pass
ax160	5250	-7.33	11	Pass
ax160	5570	-6.84	11	Pass

AUX Ant2

Mode	Frequency (MHz)	Total PSD (dBm)	Limit (dBm)	Verdict
a	5180	1.39	11	Pass
a	5240	0.6	11	Pass
a	5260	0.98	11	Pass
a	5320	1.72	11	Pass
a	5500	-0.46	11	Pass
a	5700	-0.27	11	Pass
a	5745	-3.19	30	Pass
a	5825	-3.21	30	Pass
n20	5180	-0.41	11	Pass
n20	5240	-0.11	11	Pass
n20	5260	0.23	11	Pass
n20	5320	0.58	11	Pass
n20	5500	-1.35	11	Pass
n20	5700	-1.31	11	Pass
n20	5745	-4.72	30	Pass
n20	5825	-4.02	30	Pass
n40	5190	-3.06	11	Pass
n40	5230	-2.3	11	Pass
n40	5270	-1.99	11	Pass
n40	5310	-1.76	11	Pass
n40	5510	-3.01	11	Pass
n40	5670	-2.07	11	Pass
n40	5755	-6.54	30	Pass
n40	5795	0.69	30	Pass
ac20	5180	-0.65	11	Pass
ac20	5240	0	11	Pass
ac20	5260	0.89	11	Pass
ac20	5320	0.13	11	Pass
ac20	5500	-1.02	11	Pass
ac20	5700	-0.83	11	Pass
ac20	5745	-4.33	30	Pass
ac20	5825	-3.7	30	Pass
ac40	5190	-1.97	11	Pass
ac40	5230	-2.69	11	Pass
ac40	5270	-2	11	Pass
ac40	5310	-2.35	11	Pass
ac40	5510	-3.95	11	Pass
ac40	5670	-2.36	11	Pass
ac40	5755	-6.79	30	Pass
ac40	5795	-6.03	30	Pass
ac80	5210	0.6	11	Pass
ac80	5290	1.86	11	Pass
ac80	5530	-2.67	11	Pass
ac80	5610	1.66	11	Pass
ac80	5775	-6.16	30	Pass
ax20	5180	-0.82	11	Pass
ax20	5240	0.15	11	Pass
ax20	5260	0.51	11	Pass
ax20	5320	0.09	11	Pass
ax20	5500	-1.15	11	Pass
ax20	5700	-0.32	11	Pass
ax20	5745	-4.4	30	Pass
ax20	5825	-4.03	30	Pass
ax40	5190	-1.39	11	Pass
ax40	5230	-1.29	11	Pass
ax40	5270	-0.44	11	Pass
ax40	5310	-1.72	11	Pass
ax40	5510	-3.17	11	Pass
ax40	5670	-2.17	11	Pass

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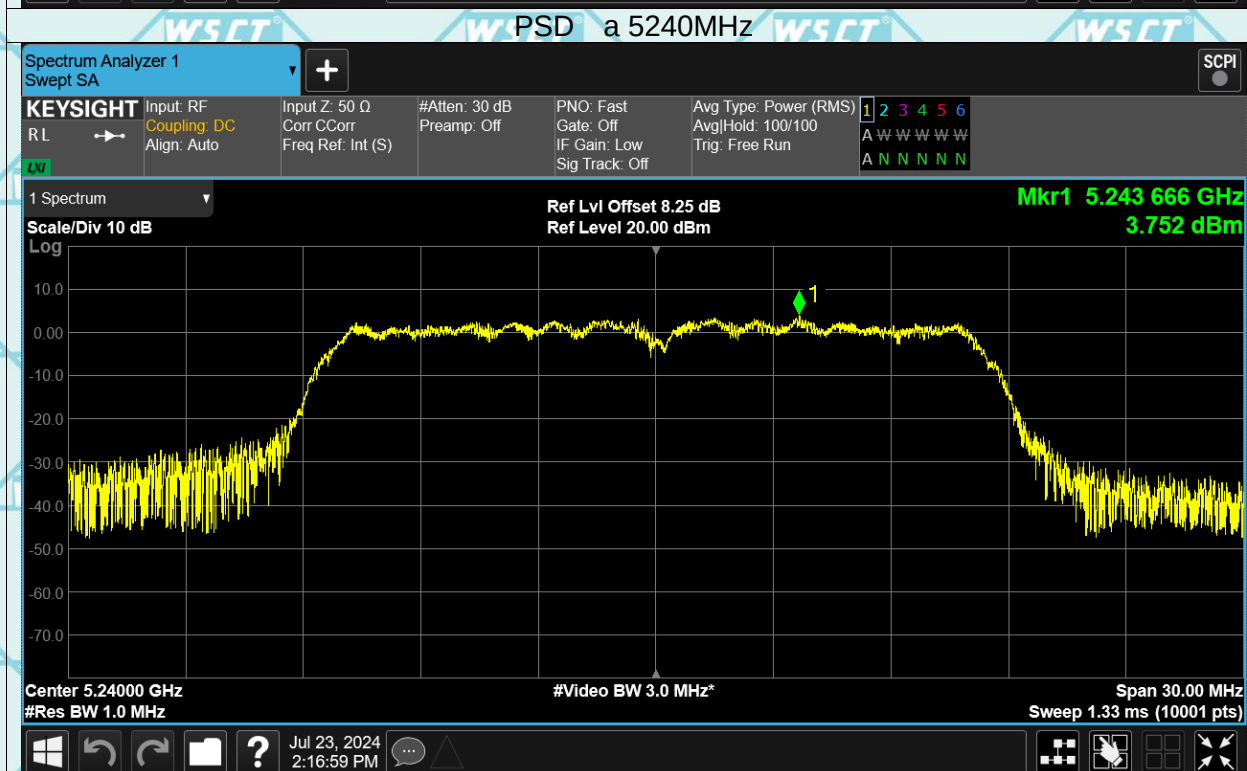
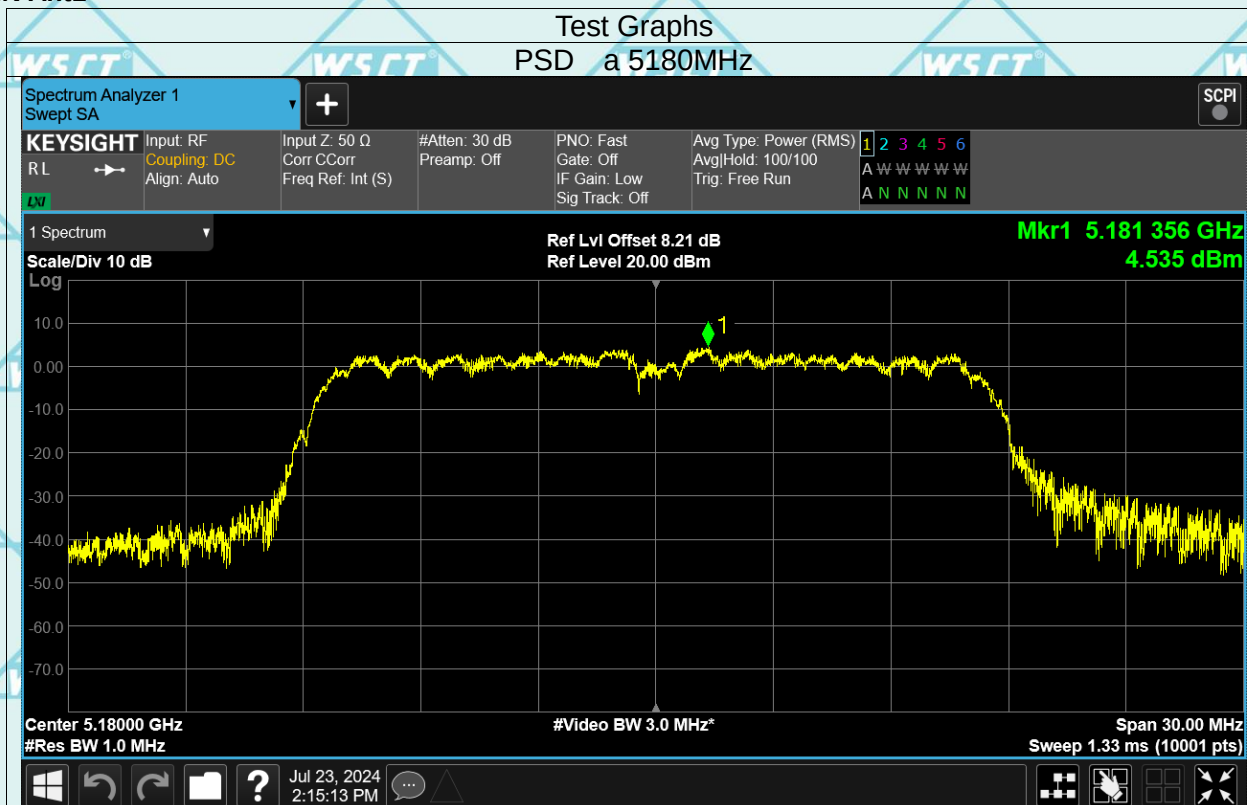
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ax80	5210	-5.28	11	Pass
ax80	5290	-4.58	11	Pass
ax80	5530	-6.73	11	Pass
ax80	5610	-5.39	11	Pass
ax80	5775	-9.82	30	Pass
ax160	5250	-1.01	11	Pass
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MIMO Mode

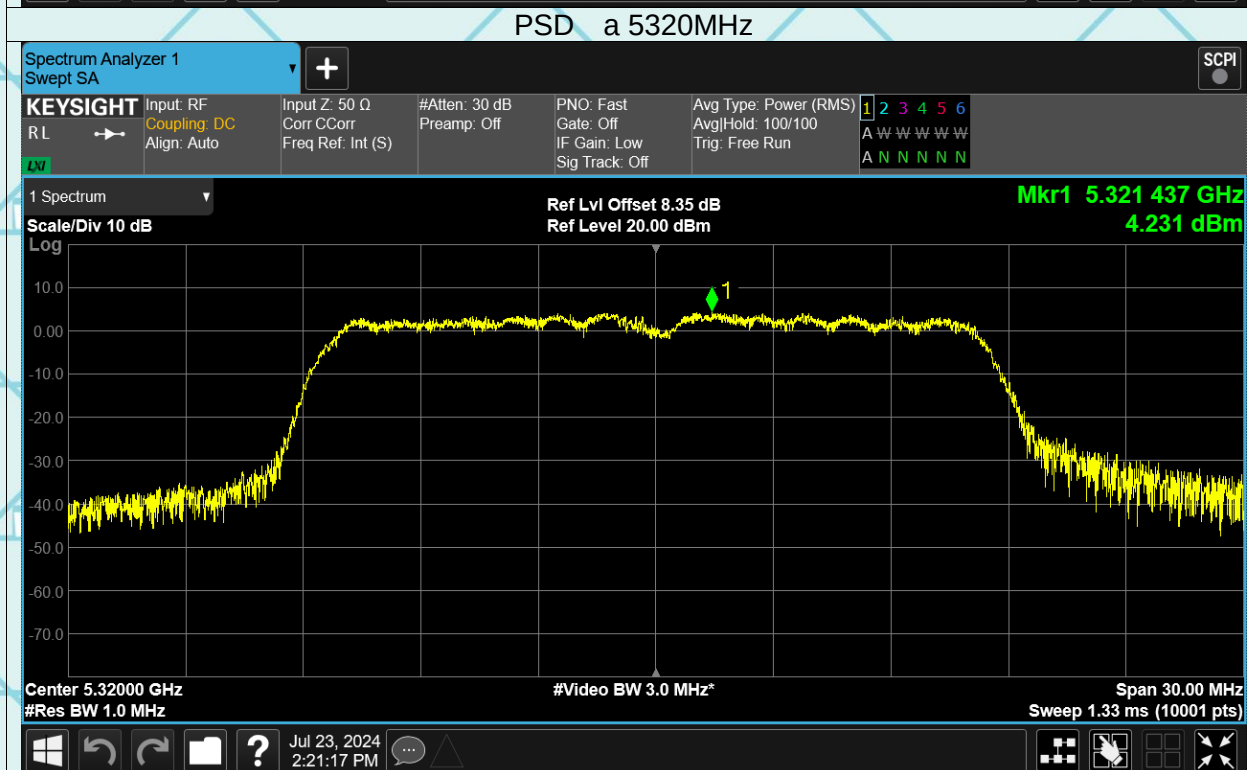
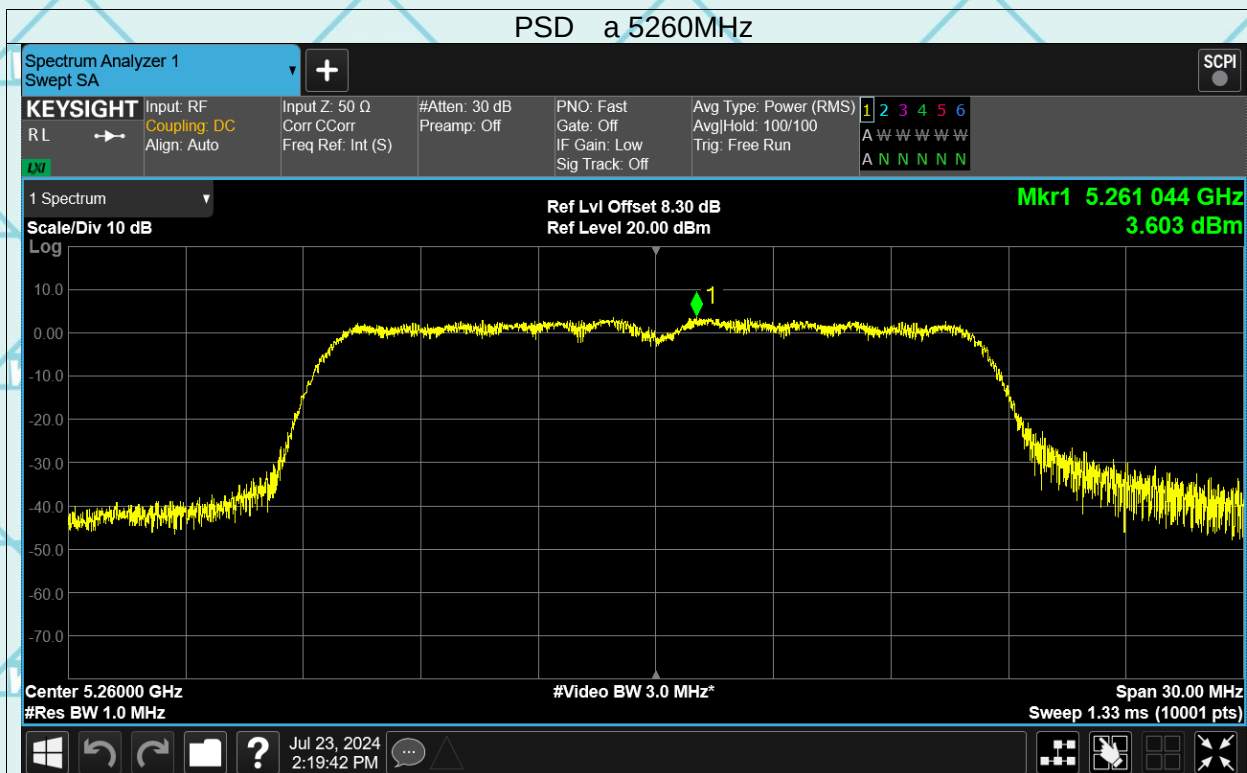
Mode	Frequency (MHz)	Total PSD (dBm)	Limit (dBm)	Verdict
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n20	5240	4.56	11	Pass
n20	5260	5.04	11	Pass
n20	5320	5.95	11	Pass
n20	5500	3.96	11	Pass
n20	5700	4.66	11	Pass
n20	5745	0.61	30	Pass
n20	5825	1.60	30	Pass
n40	5190	1.32	11	Pass
n40	5230	6.32	11	Pass
n40	5270	1.71	11	Pass
n40	5310	2.48	11	Pass
n40	5510	1.29	11	Pass
n40	5670	2.30	11	Pass
n40	5755	-1.48	30	Pass
n40	5795	2.36	30	Pass
ac20	5180	4.49	11	Pass
ac20	5240	4.41	11	Pass
ac20	5260	4.71	11	Pass
ac20	5320	5.42	11	Pass
ac20	5500	3.36	11	Pass
ac20	5700	3.78	11	Pass
ac20	5745	1.05	30	Pass
ac20	5825	1.40	30	Pass
ac40	5190	2.23	11	Pass
ac40	5230	1.22	11	Pass
ac40	5270	1.96	11	Pass
ac40	5310	1.69	11	Pass
ac40	5510	0.21	11	Pass
ac40	5670	1.43	11	Pass
ac40	5755	-2.11	30	Pass
ac40	5795	-2.11	30	Pass
ac80	5210	1.55	11	Pass
ac80	5290	2.69	11	Pass
ac80	5530	-0.91	11	Pass
ac80	5610	2.60	11	Pass
ac80	5775	-3.84	30	Pass
ax20	5180	4.17	11	Pass
ax20	5240	4.61	11	Pass
ax20	5260	4.73	11	Pass
ax20	5320	4.87	11	Pass
ax20	5500	4.24	11	Pass
ax20	5700	3.66	11	Pass
ax20	5745	0.96	30	Pass
ax20	5825	1.35	30	Pass
ax40	5190	3.37	11	Pass
ax40	5230	2.87	11	Pass
ax40	5270	3.44	11	Pass
ax40	5310	3.11	11	Pass
ax40	5510	2.16	11	Pass
ax40	5670	2.45	11	Pass
ax40	5755	-0.25	30	Pass
ax40	5795	0.29	30	Pass
ax80	5210	0.10	11	Pass
ax80	5290	-0.32	11	Pass
ax80	5530	-0.50	11	Pass
ax80	5610	-0.36	11	Pass
ax80	5775	-3.93	30	Pass
ax160	5250	-0.10	11	Pass
ax160	5570	-4.96	11	Pass

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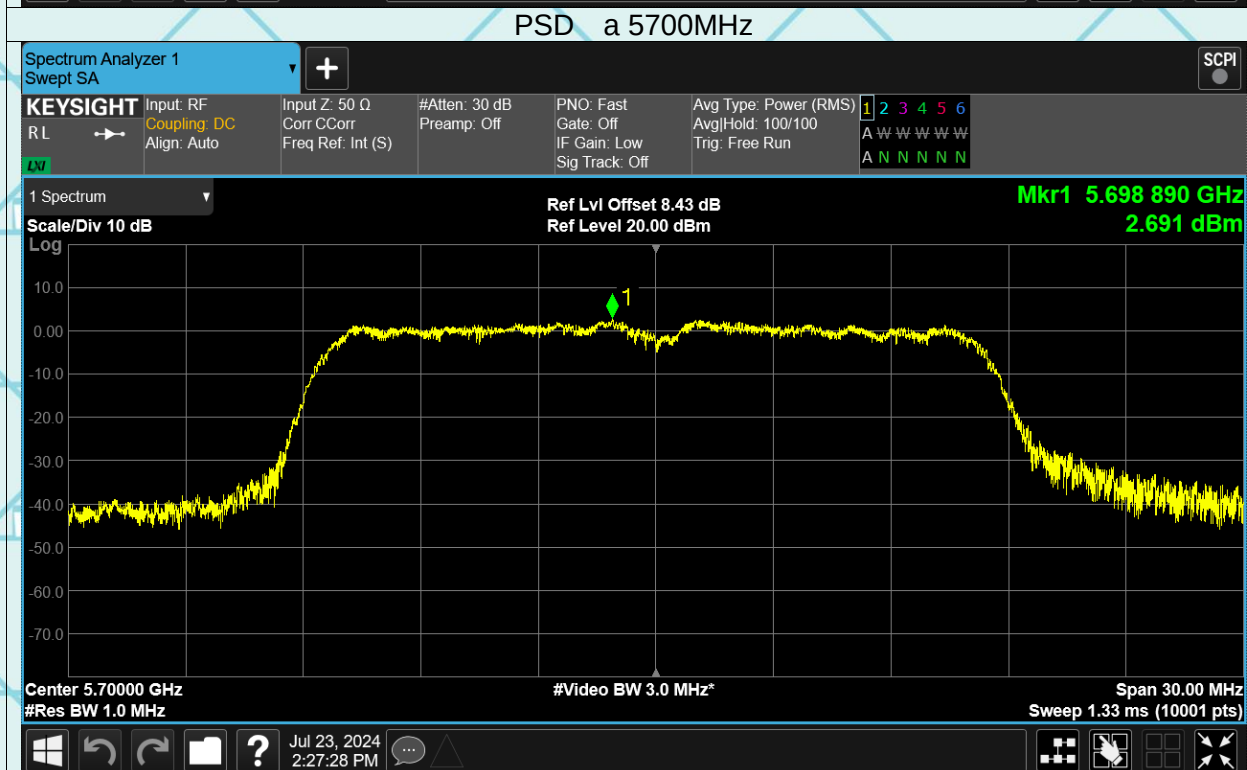
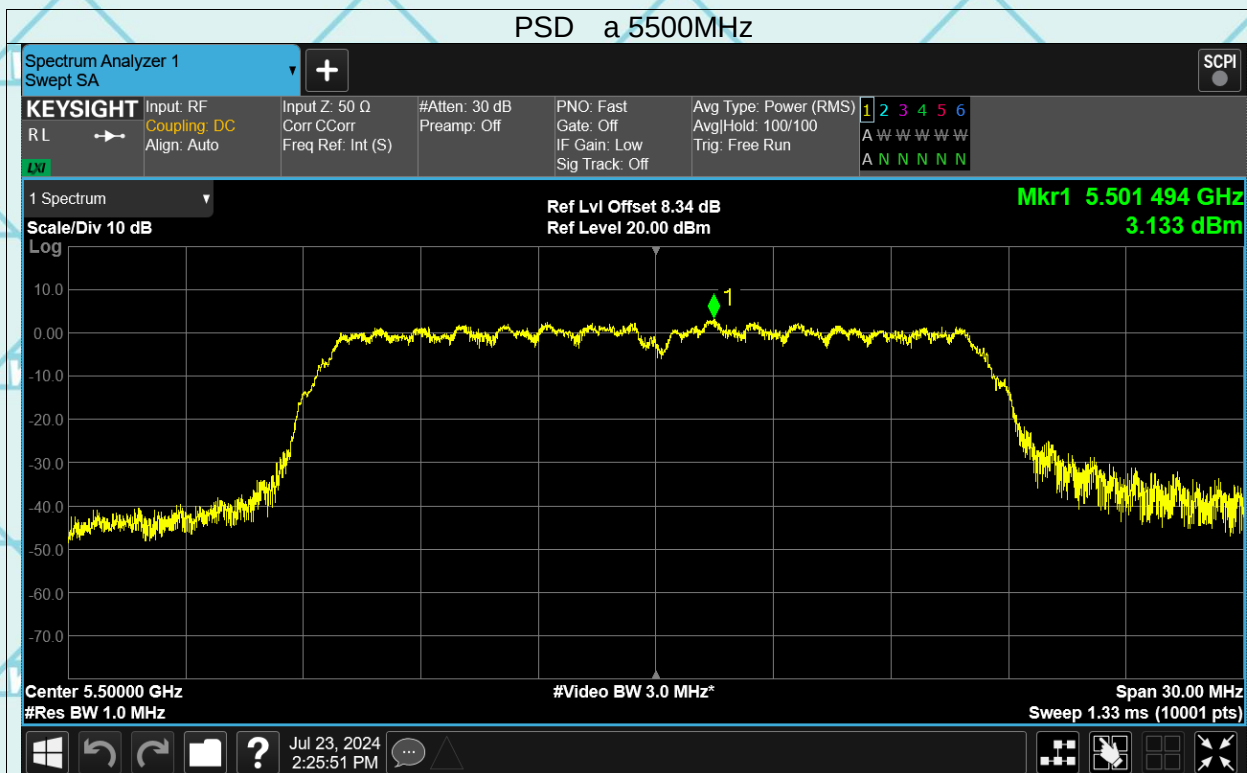
MAIN Ant1



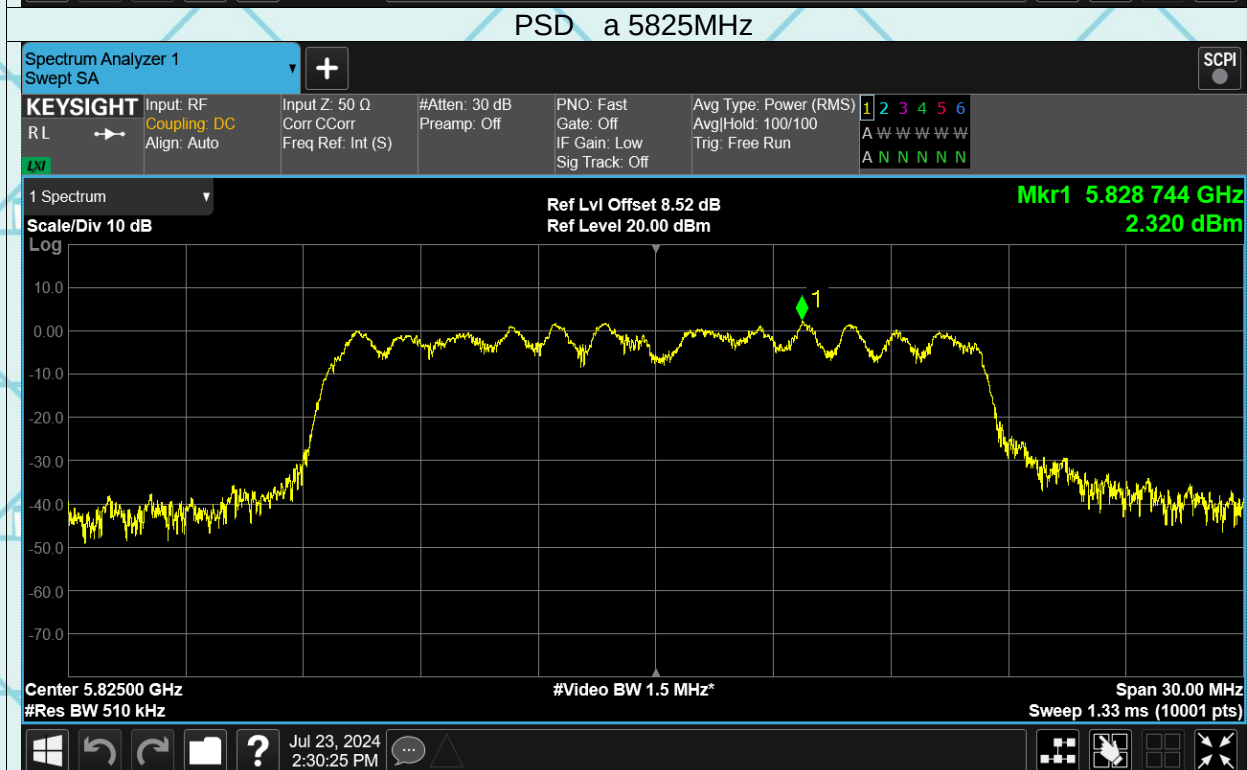
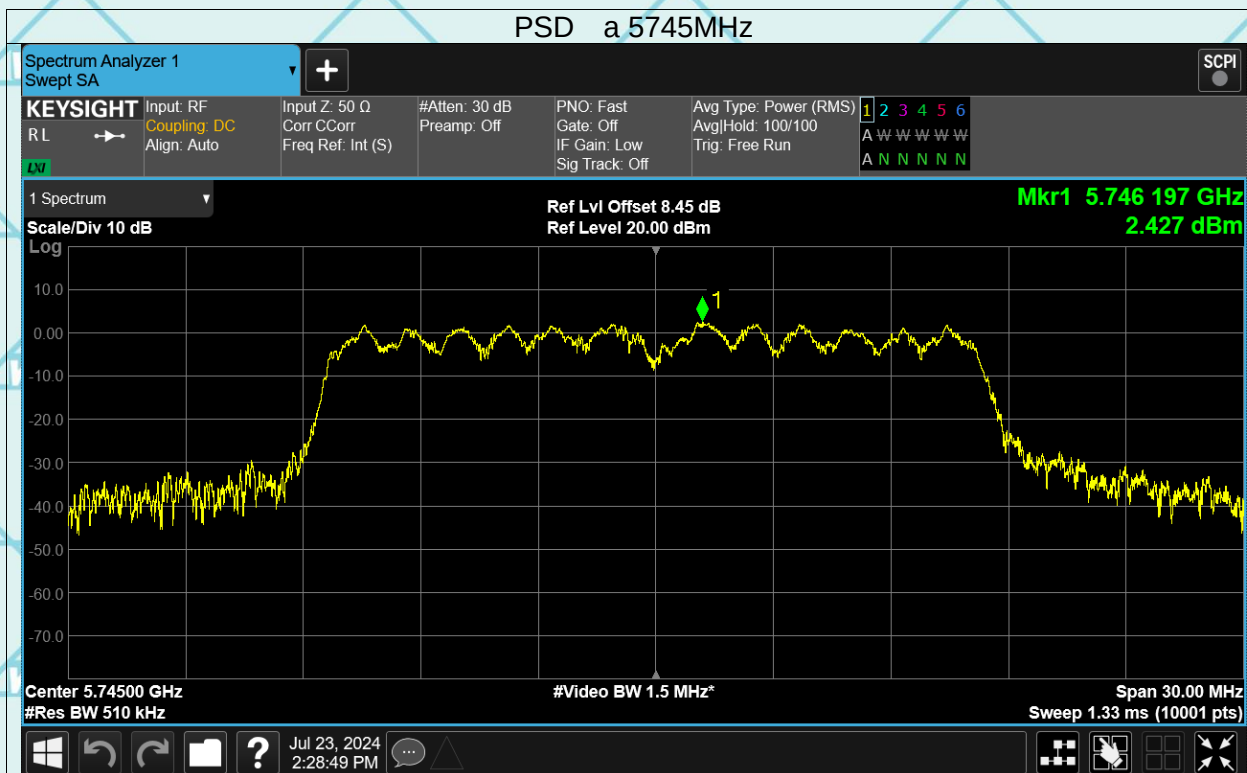
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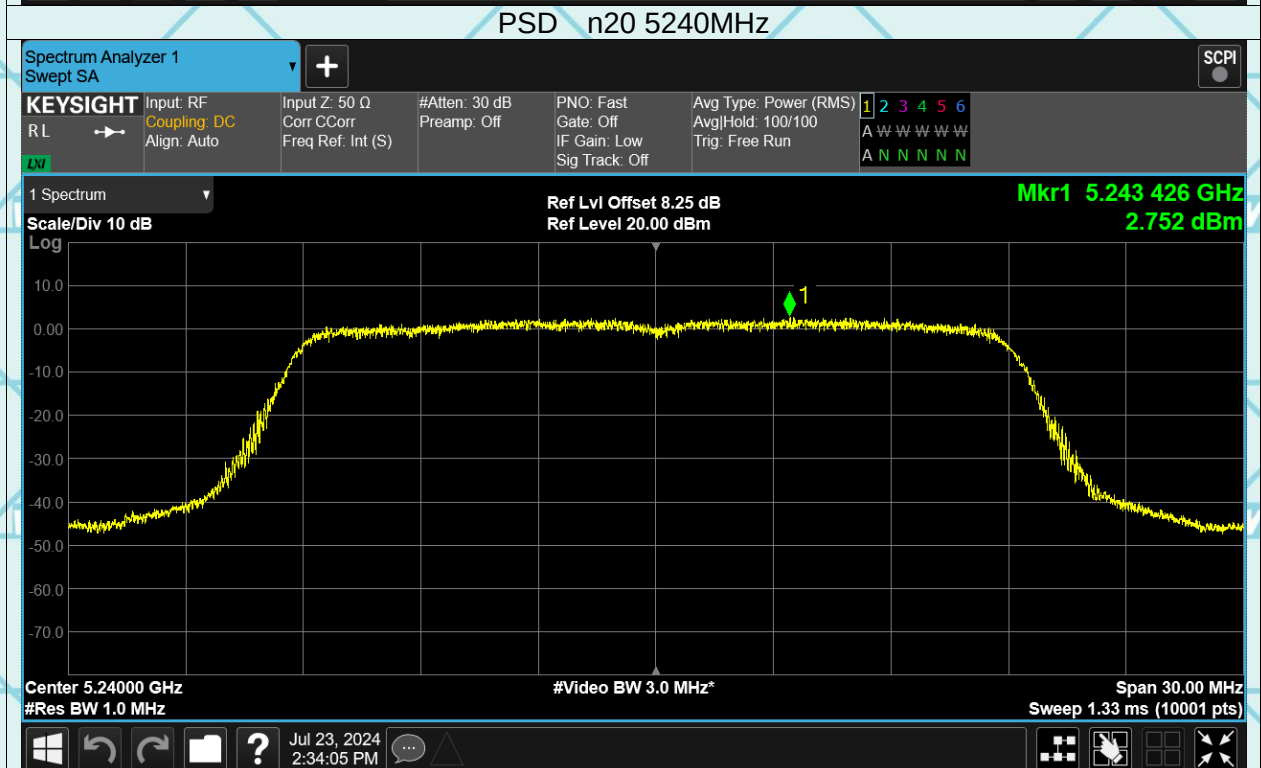
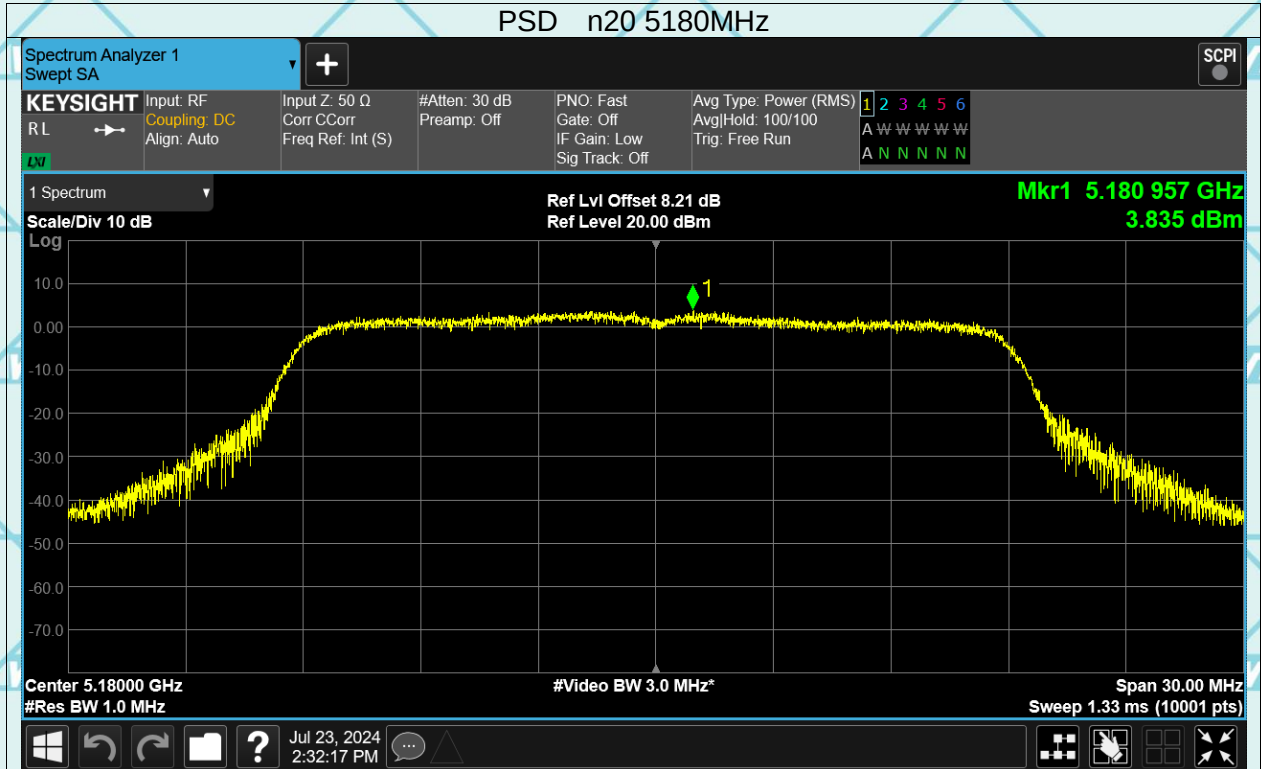
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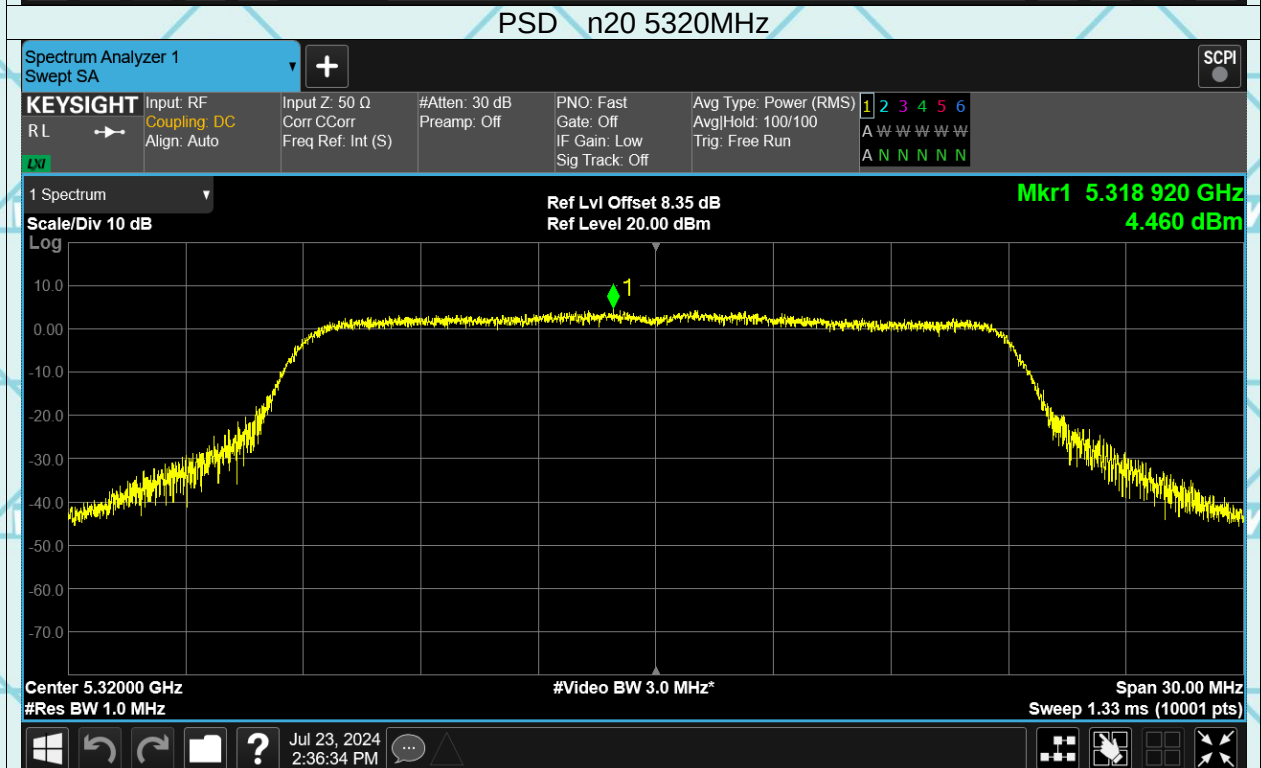
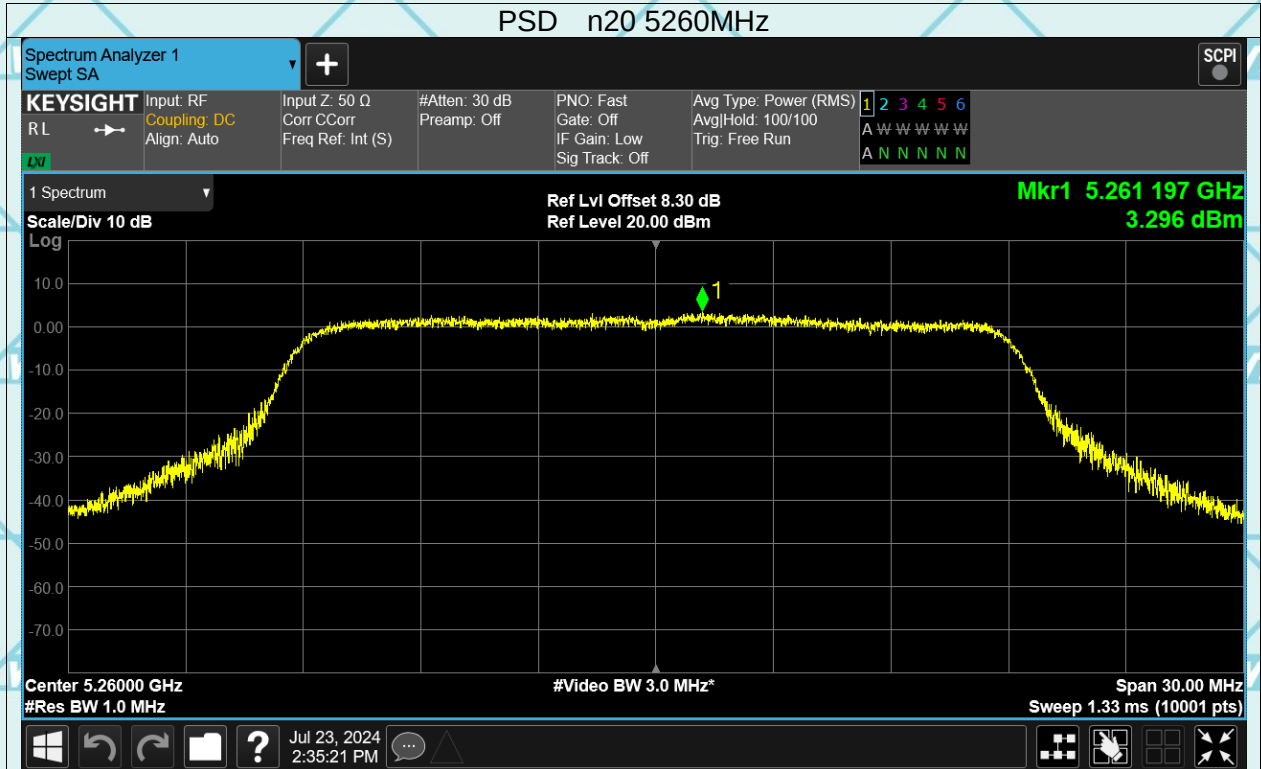
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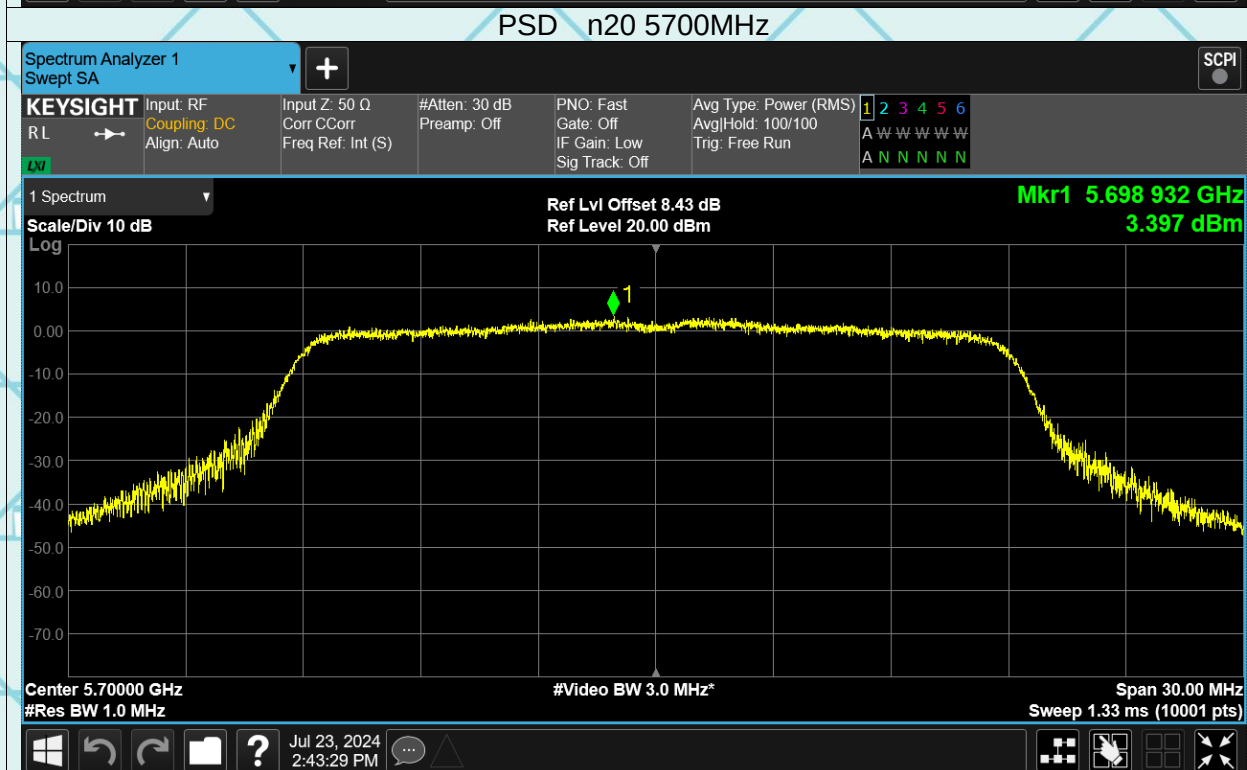
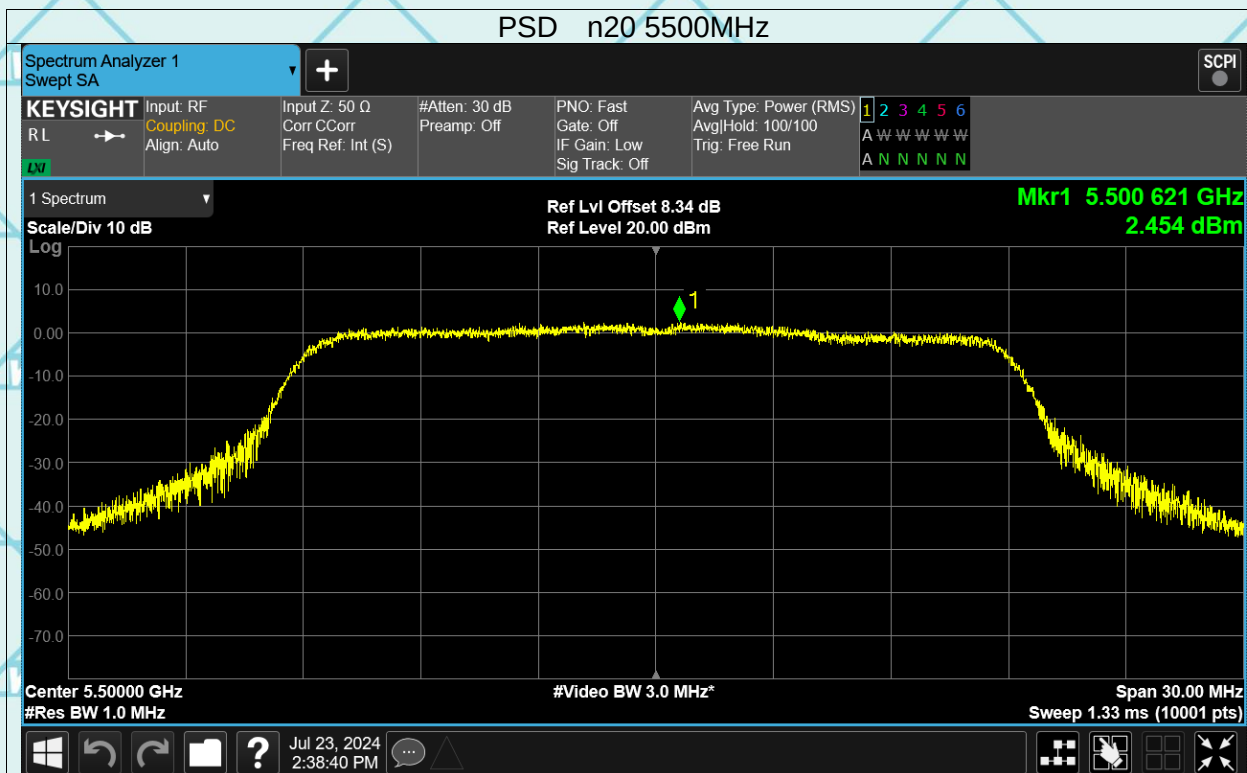
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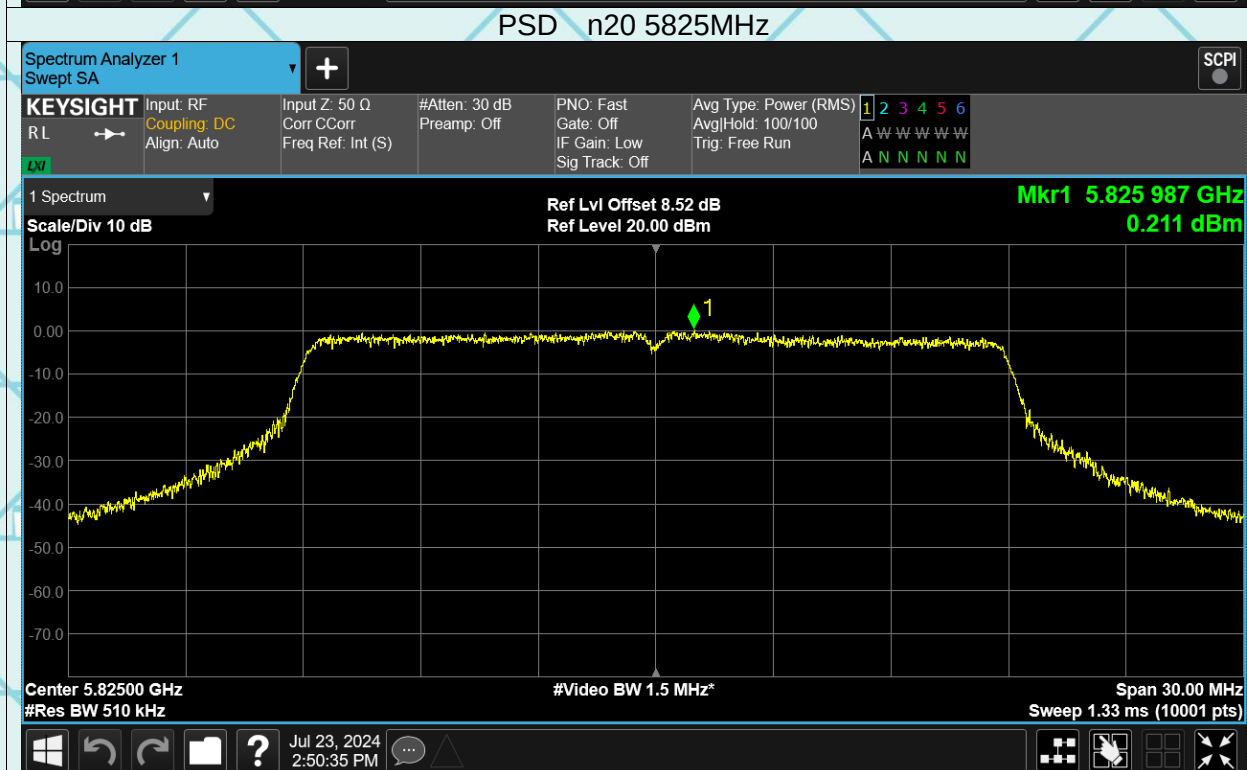
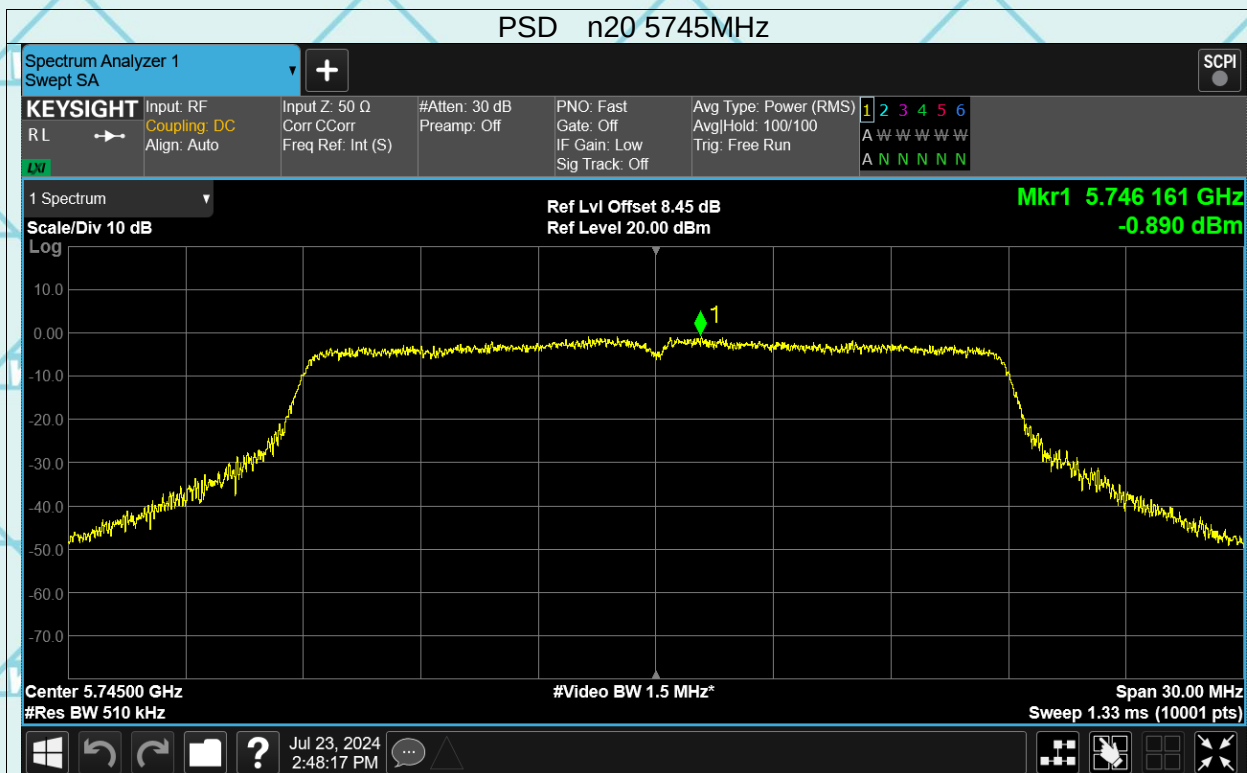
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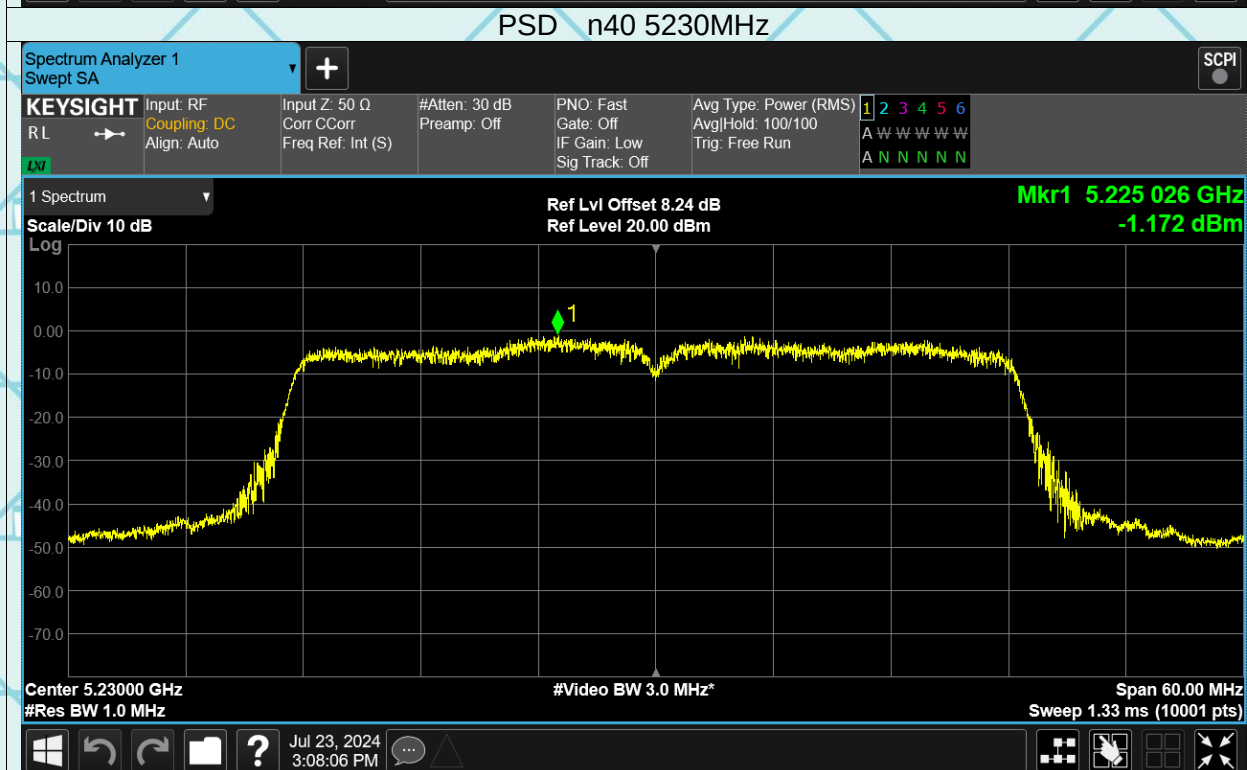
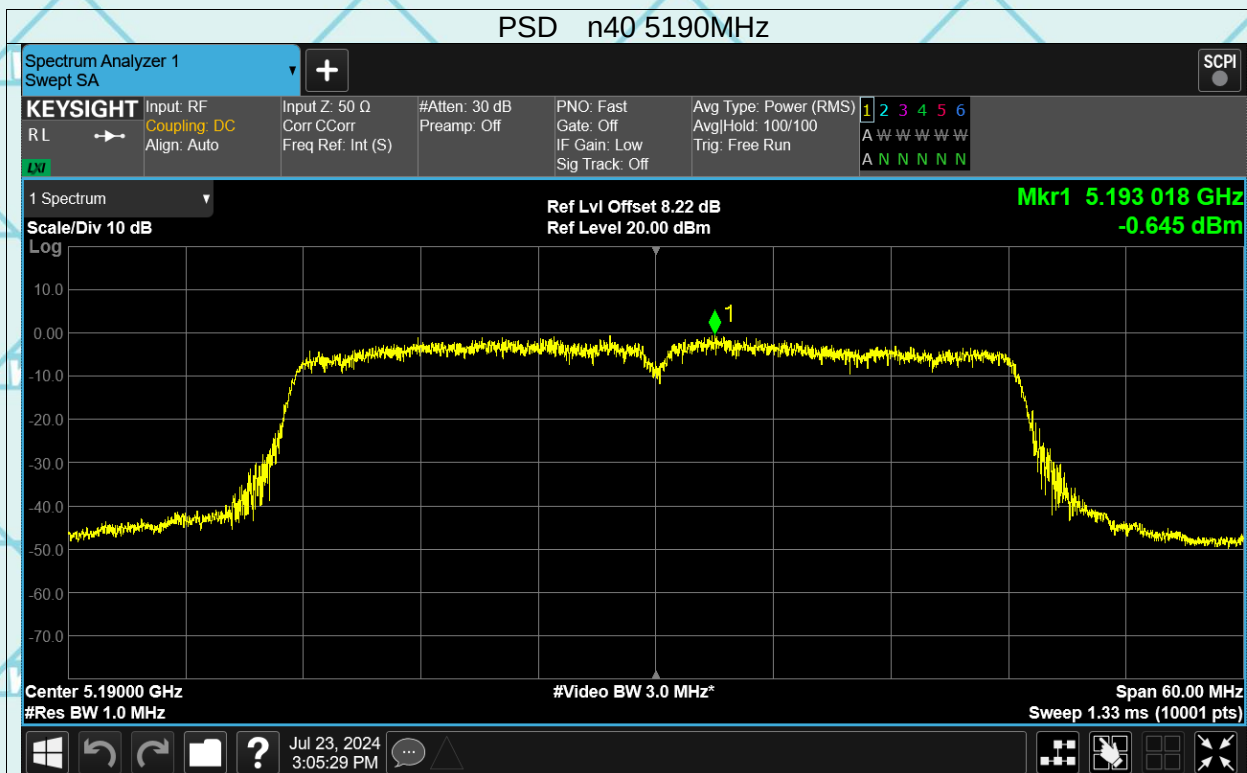
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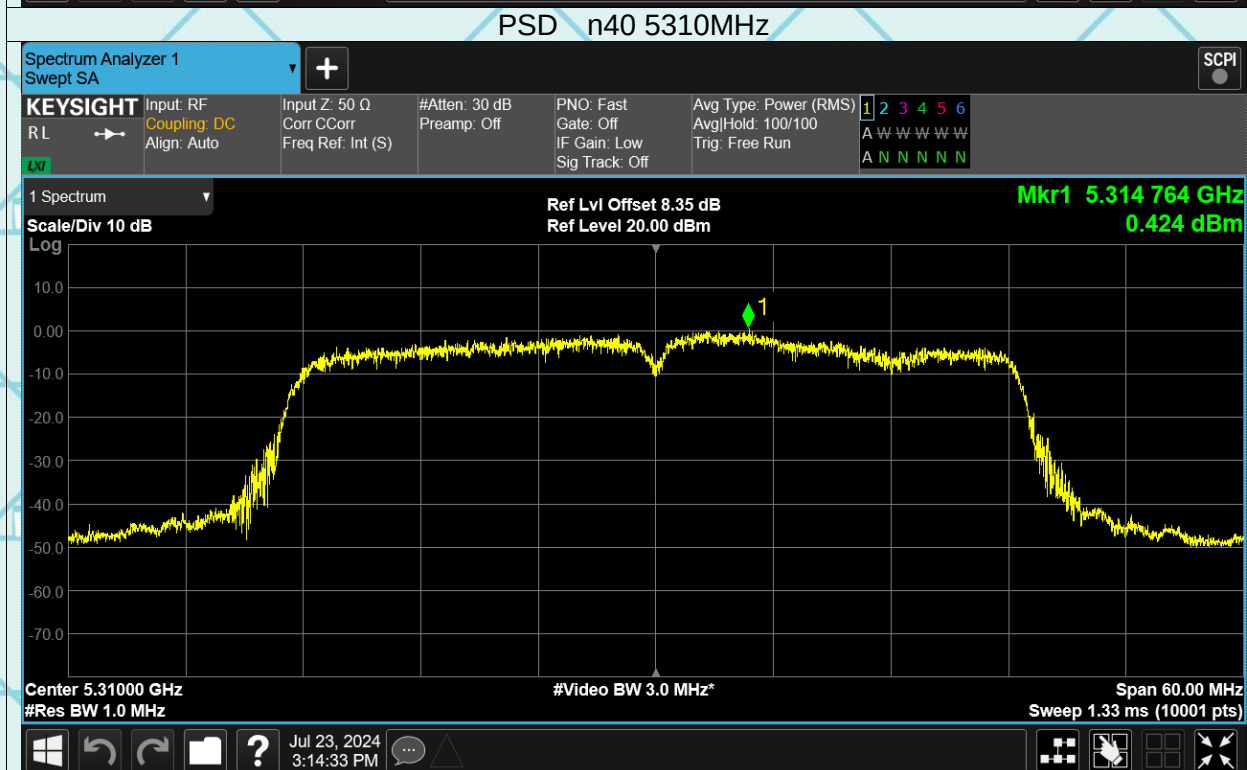
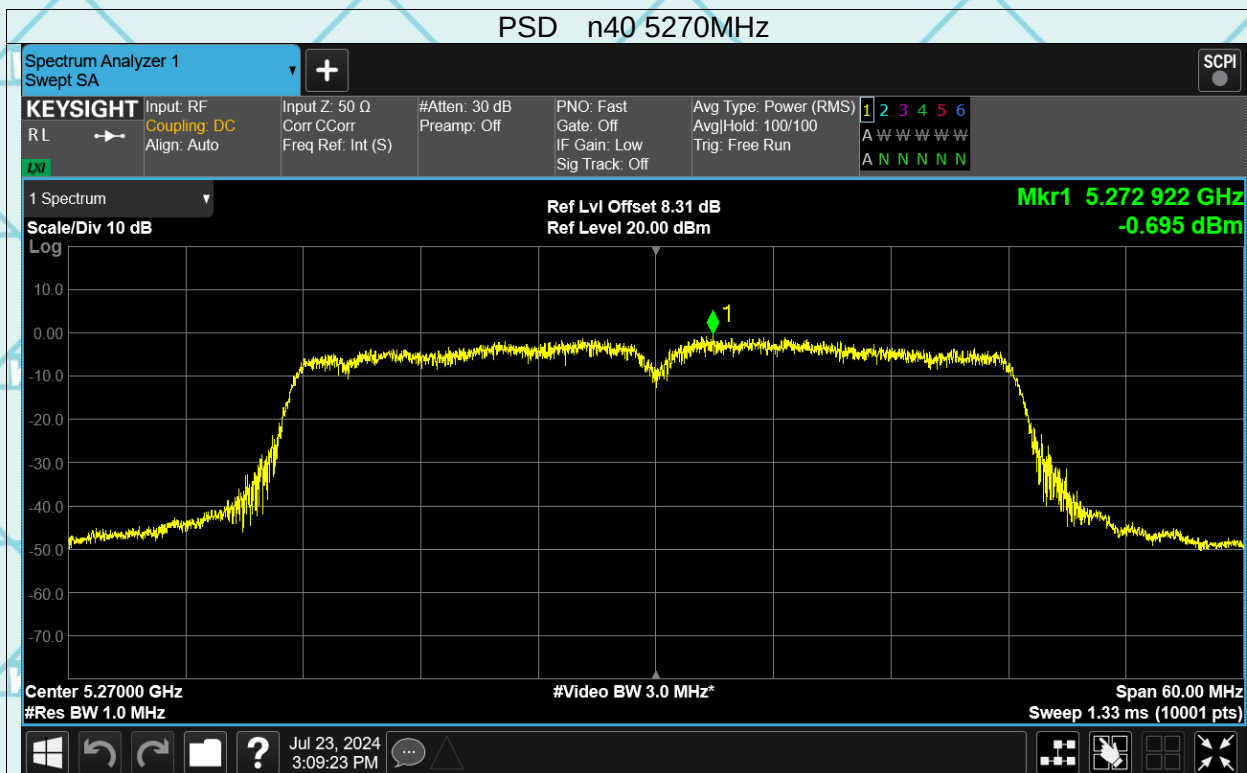
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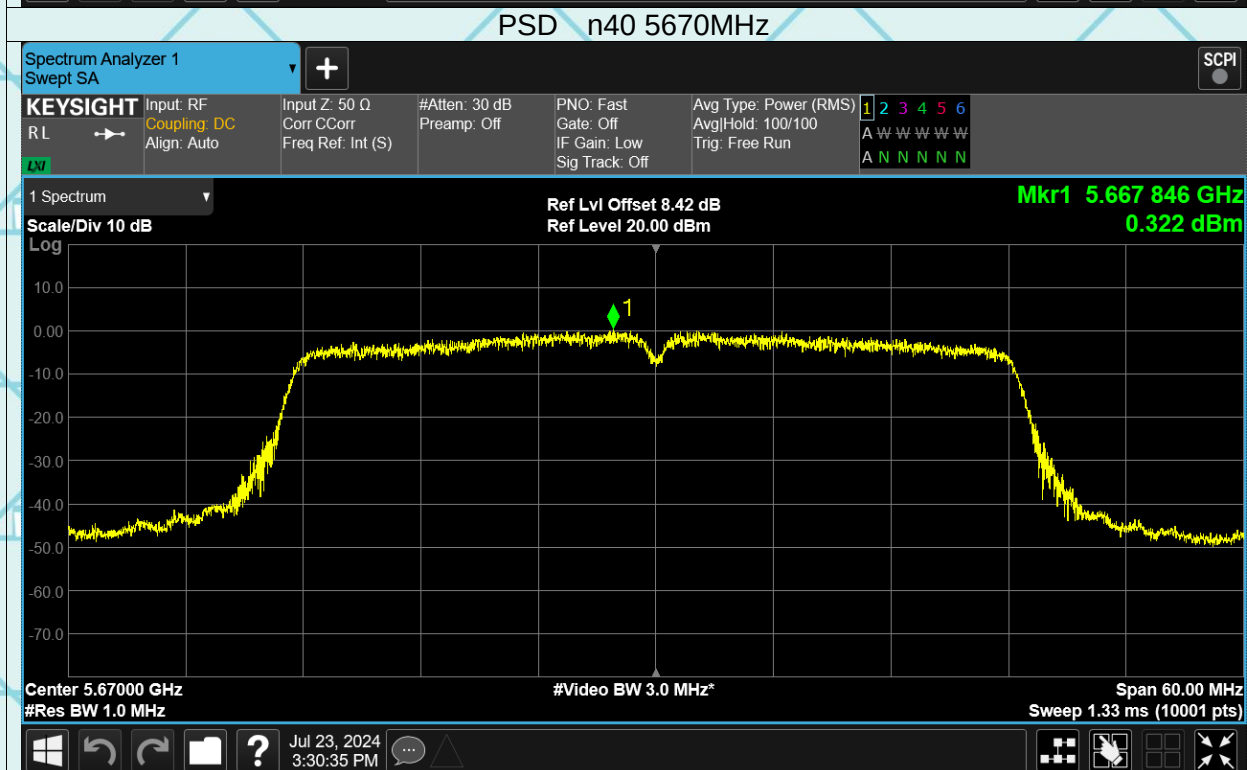
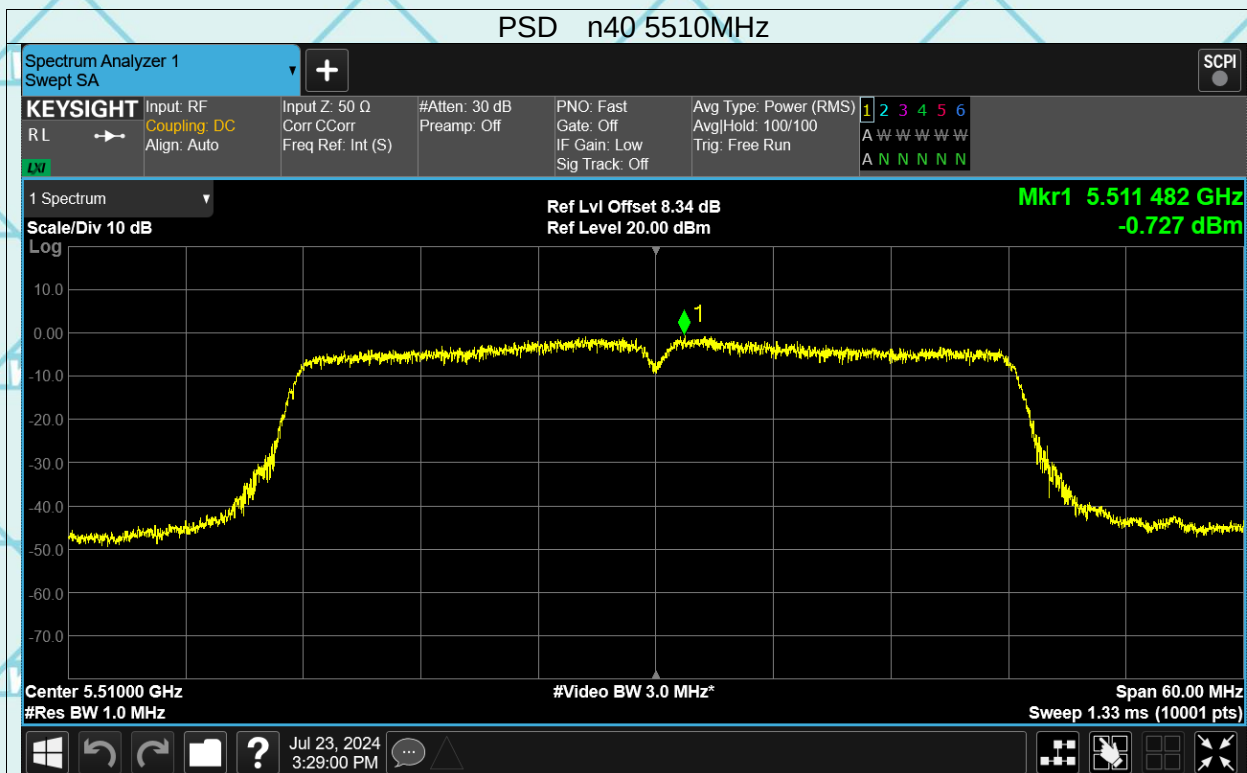
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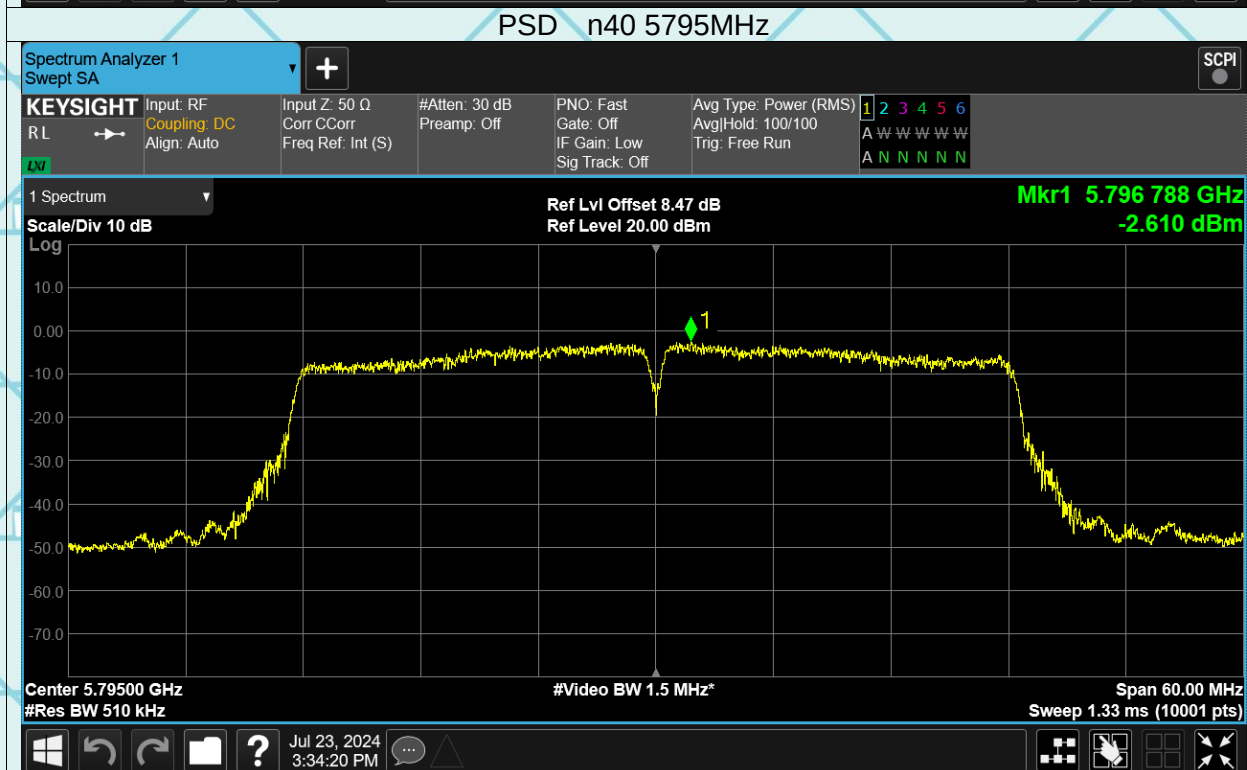
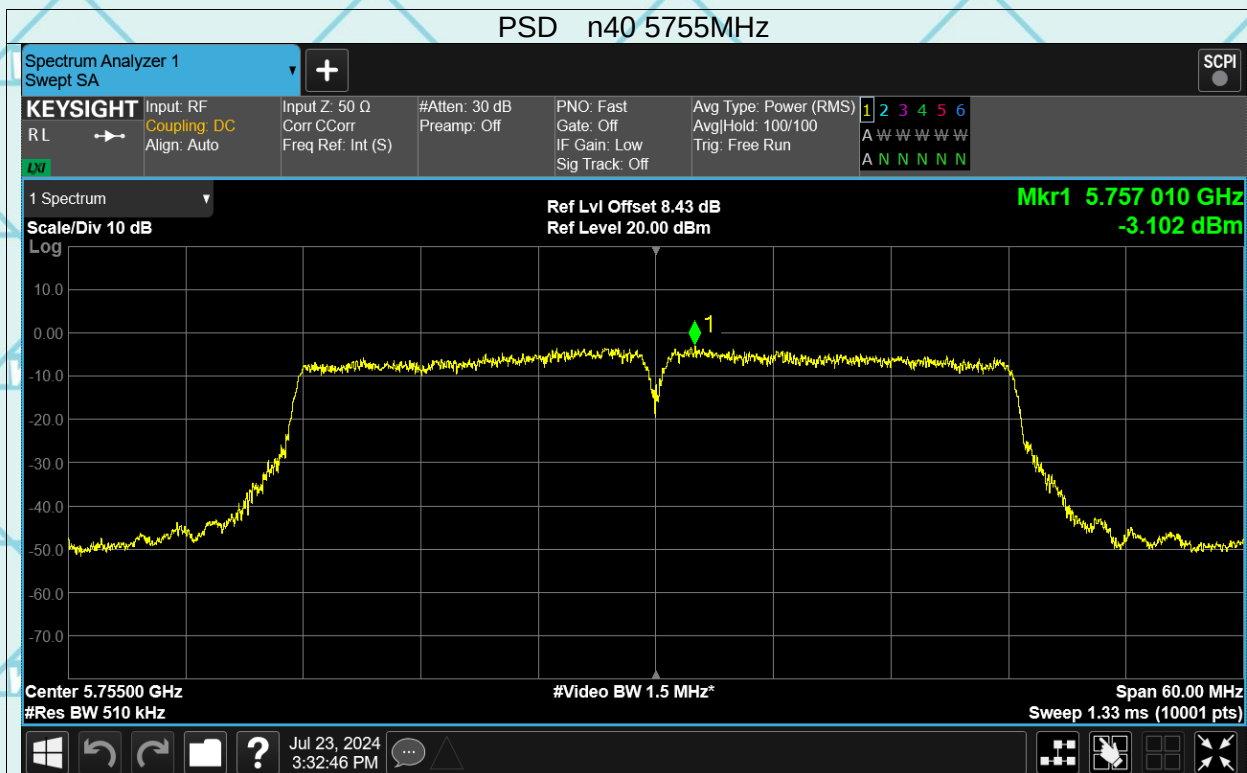
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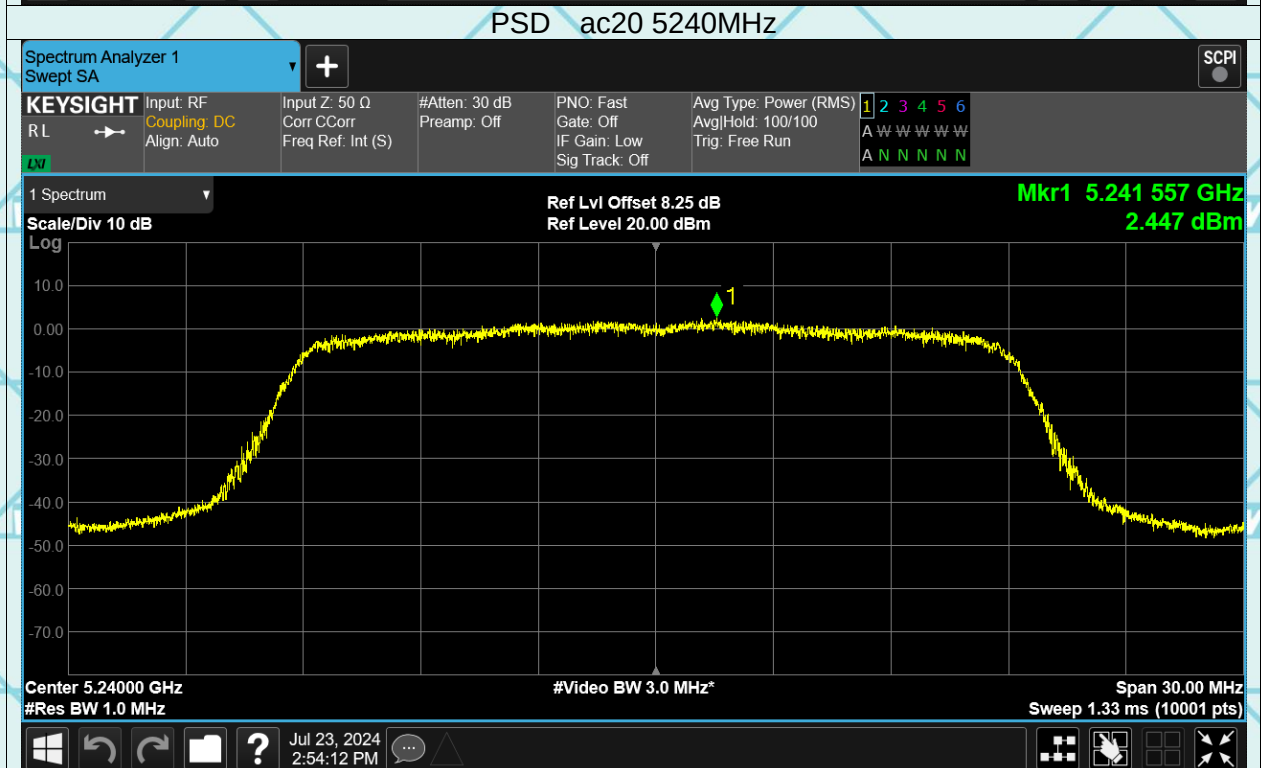
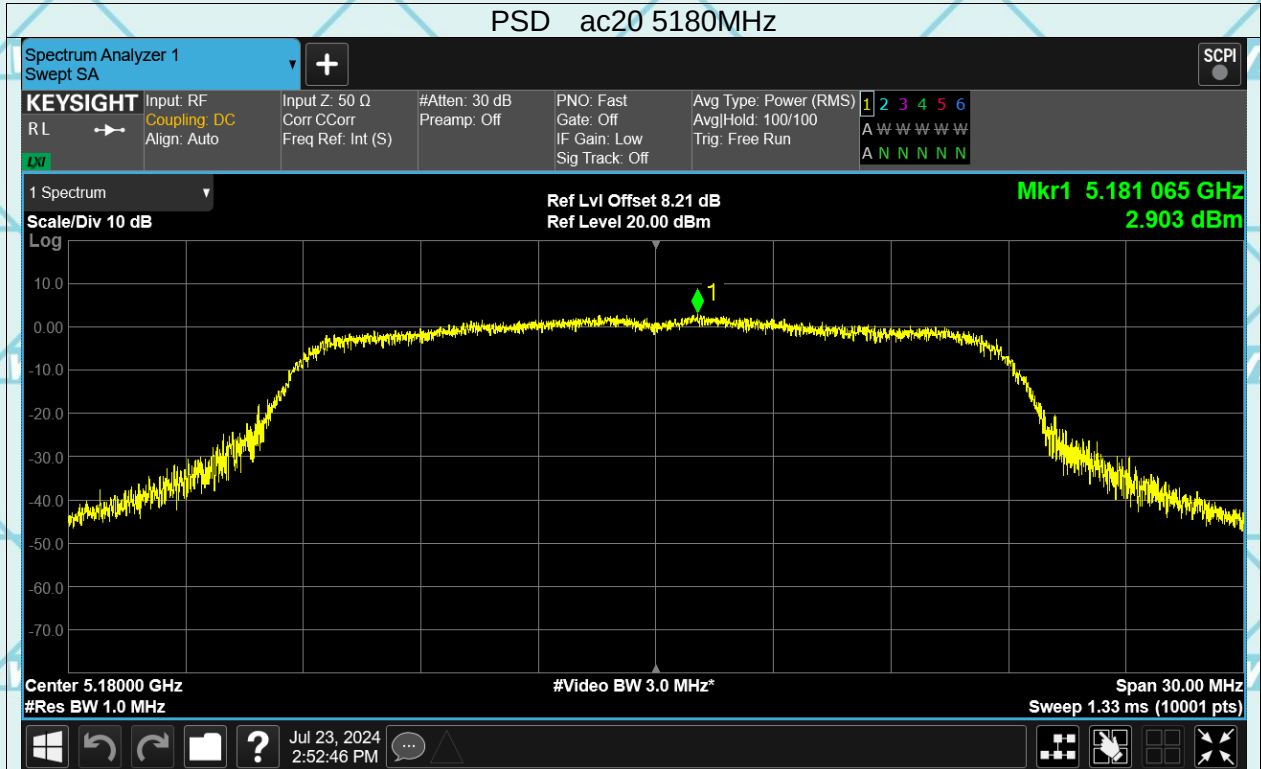
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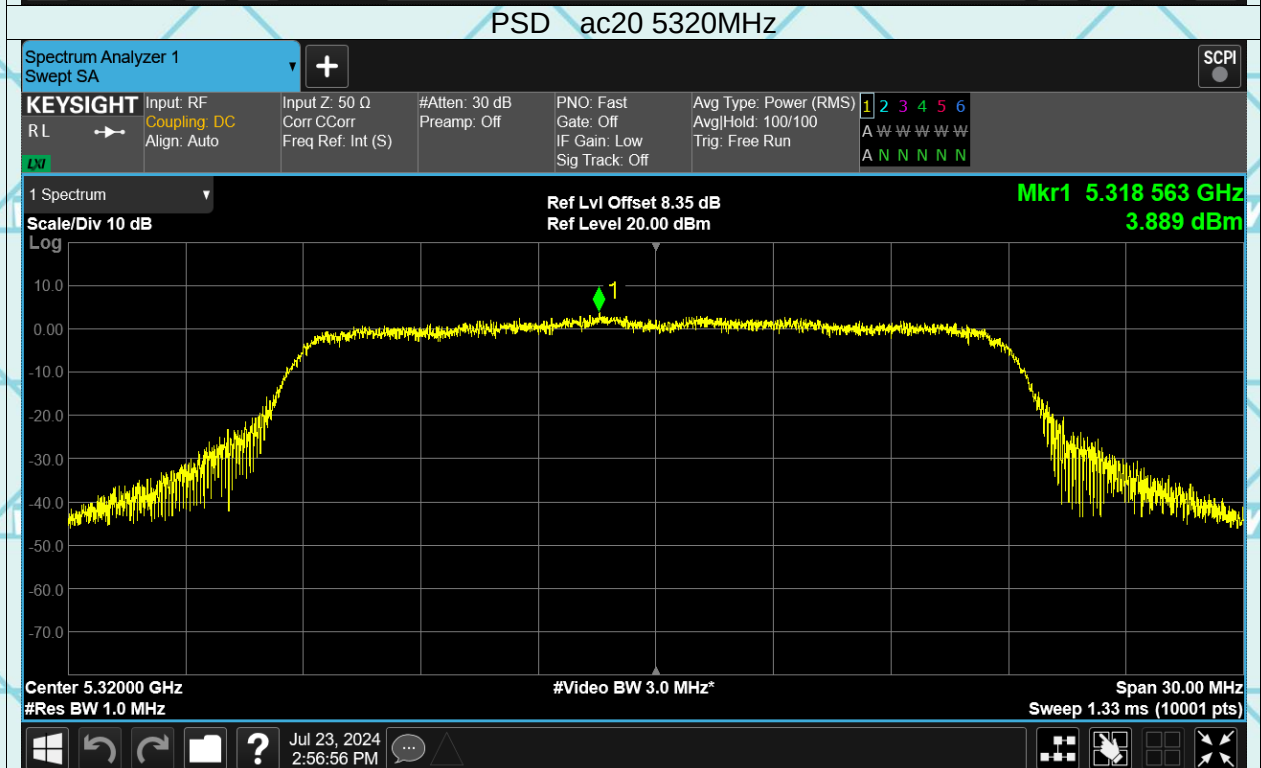
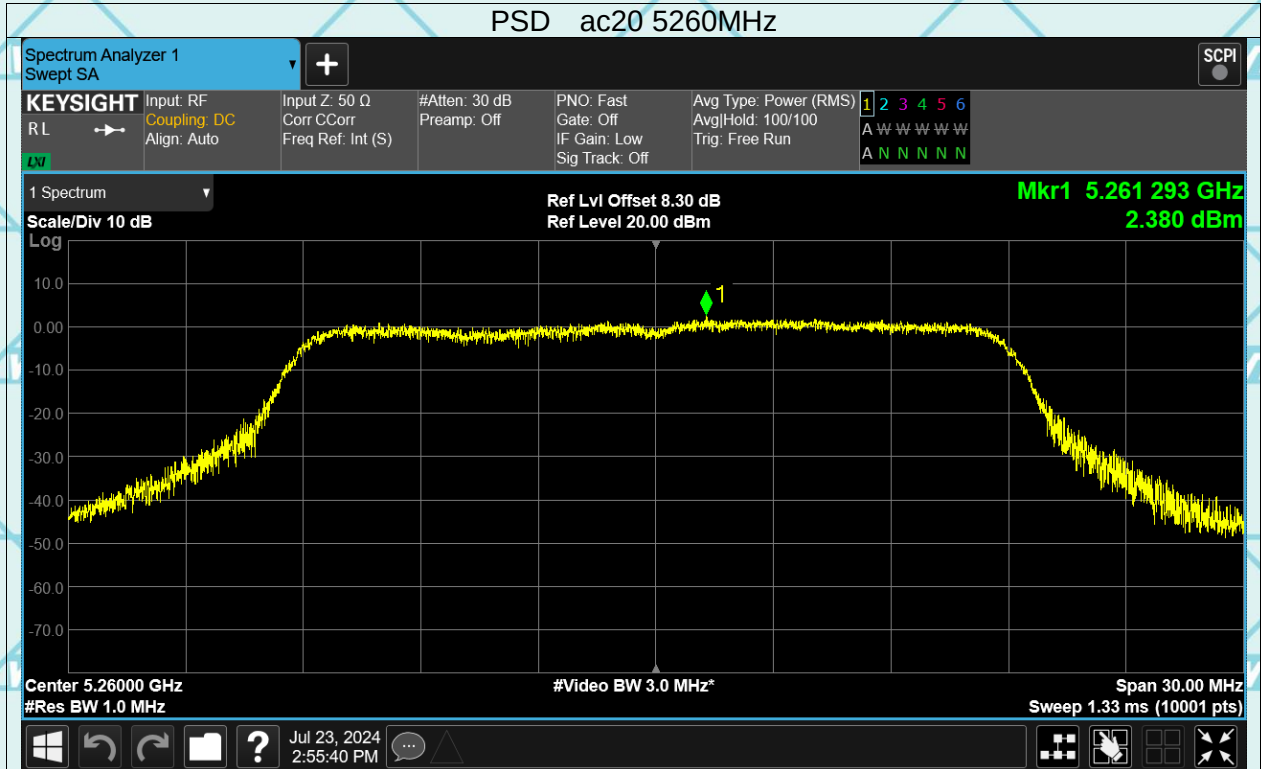
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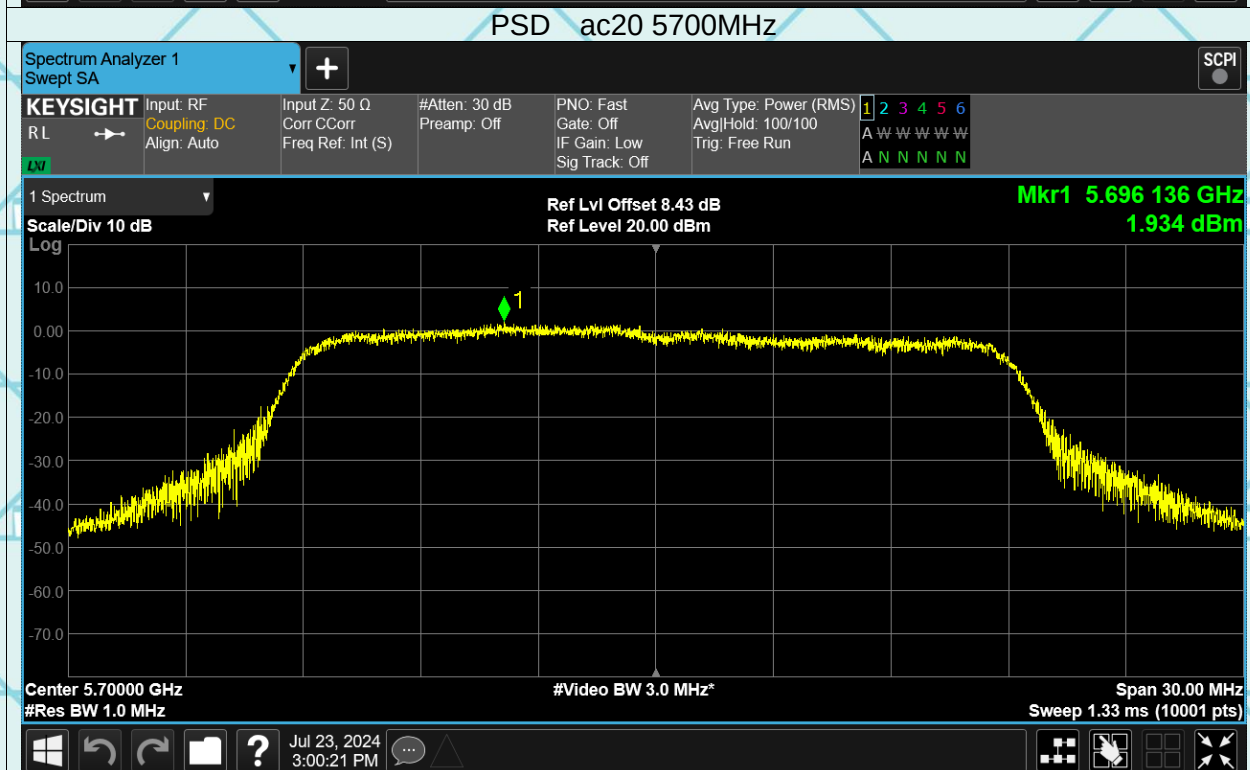
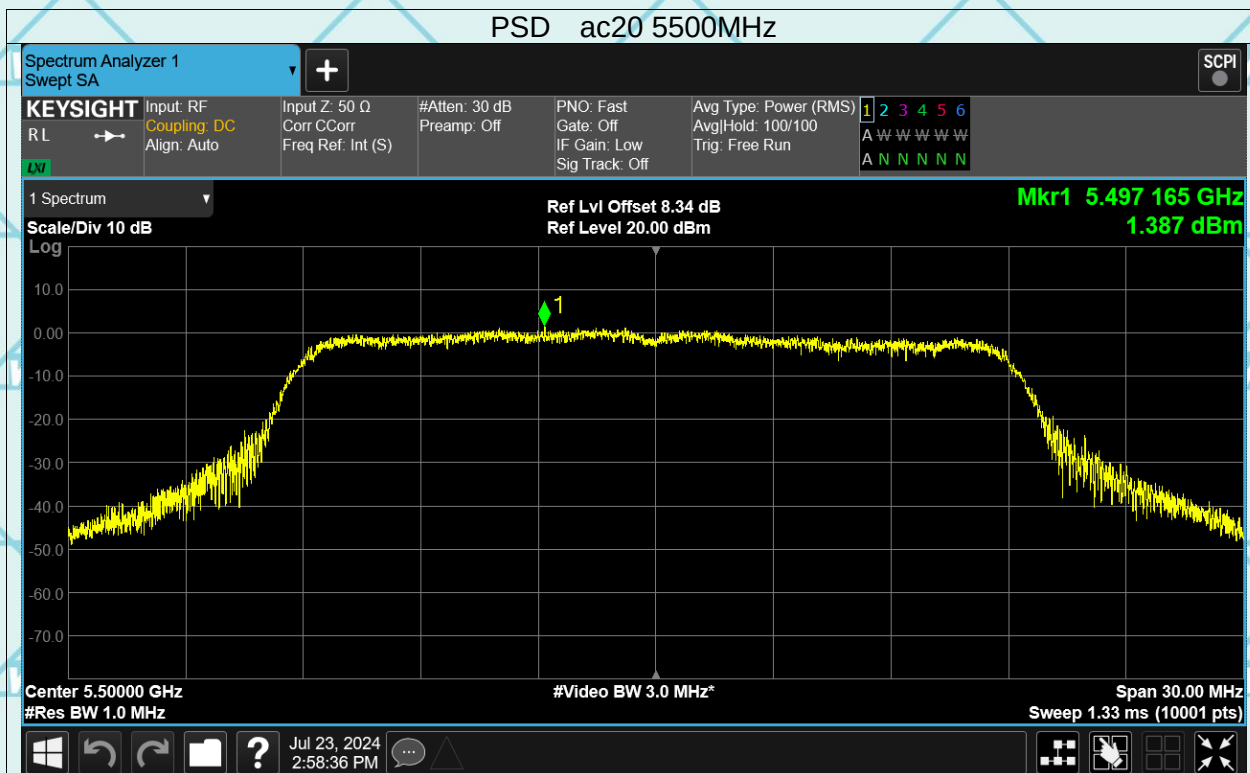
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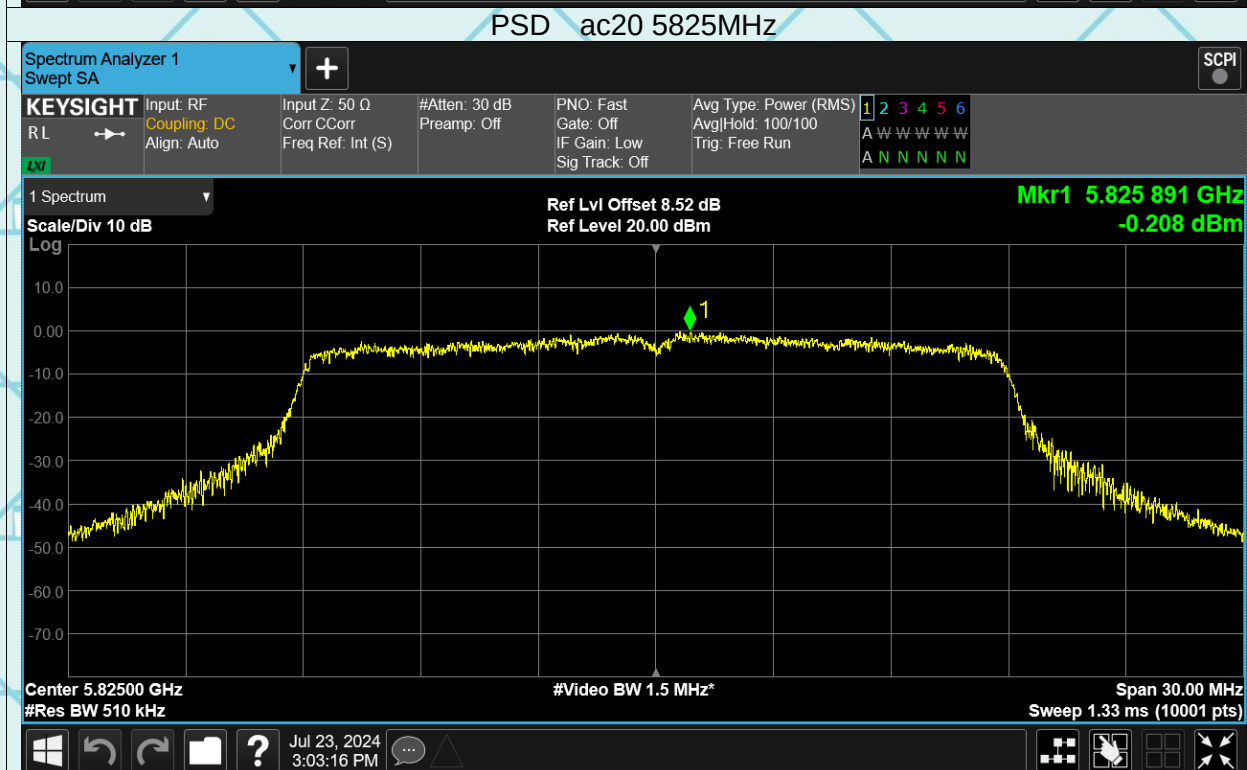
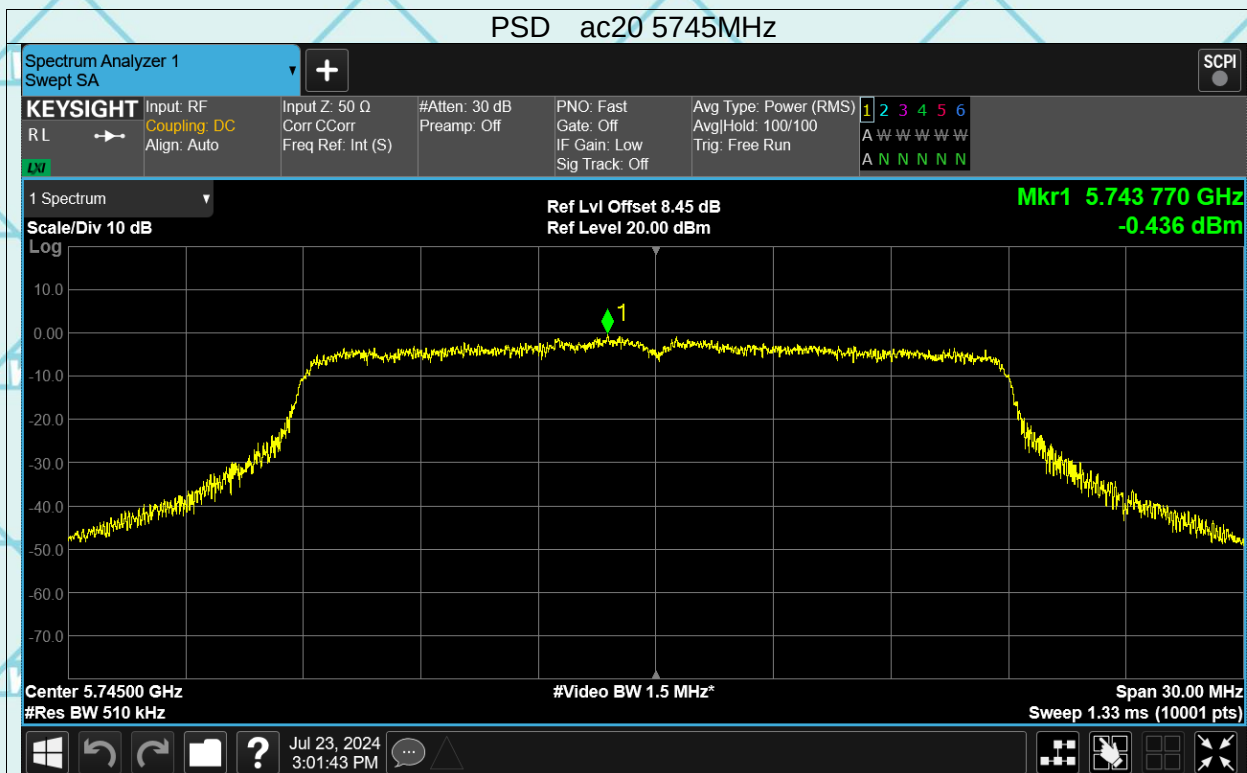
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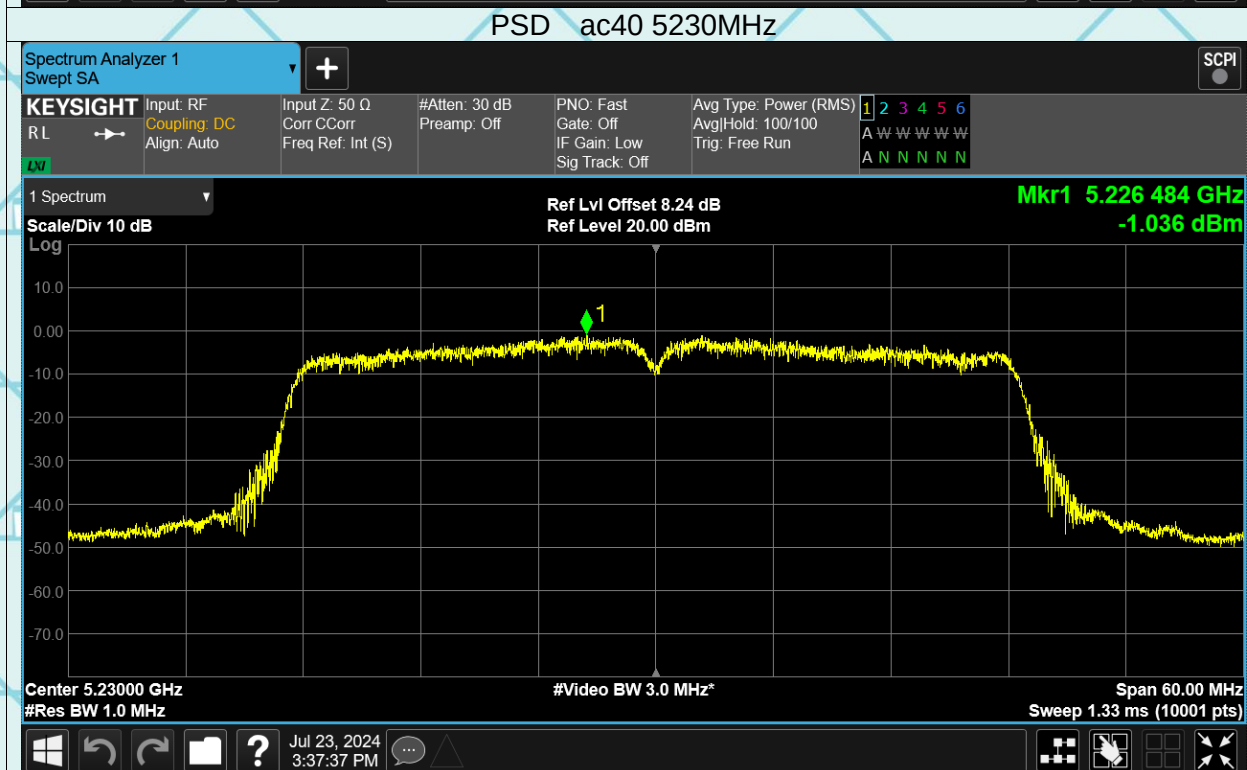
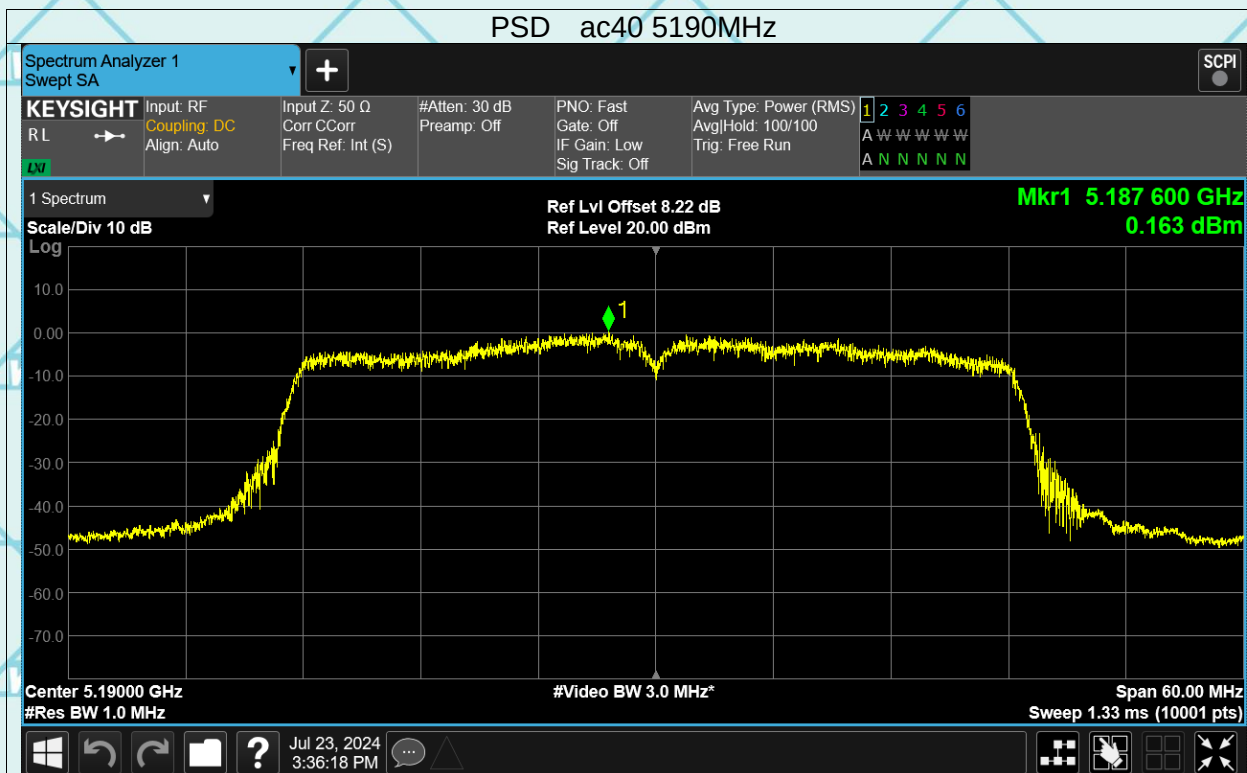
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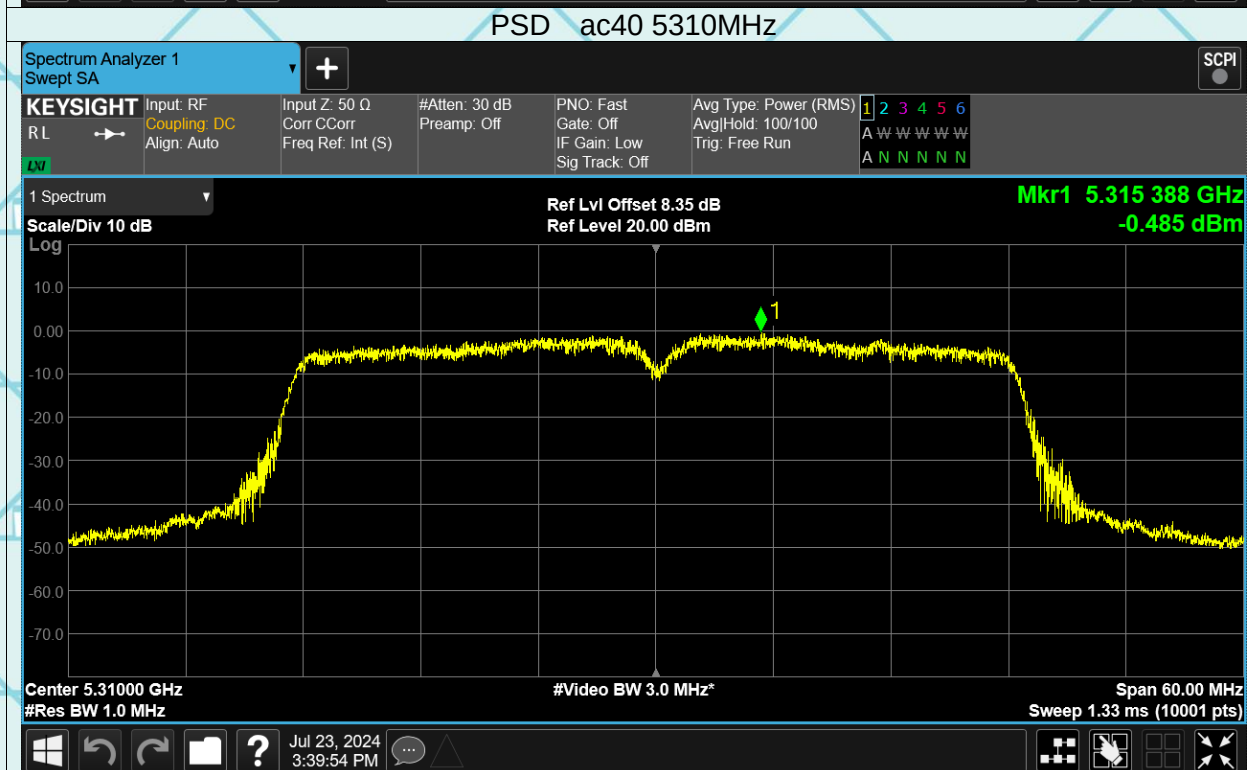
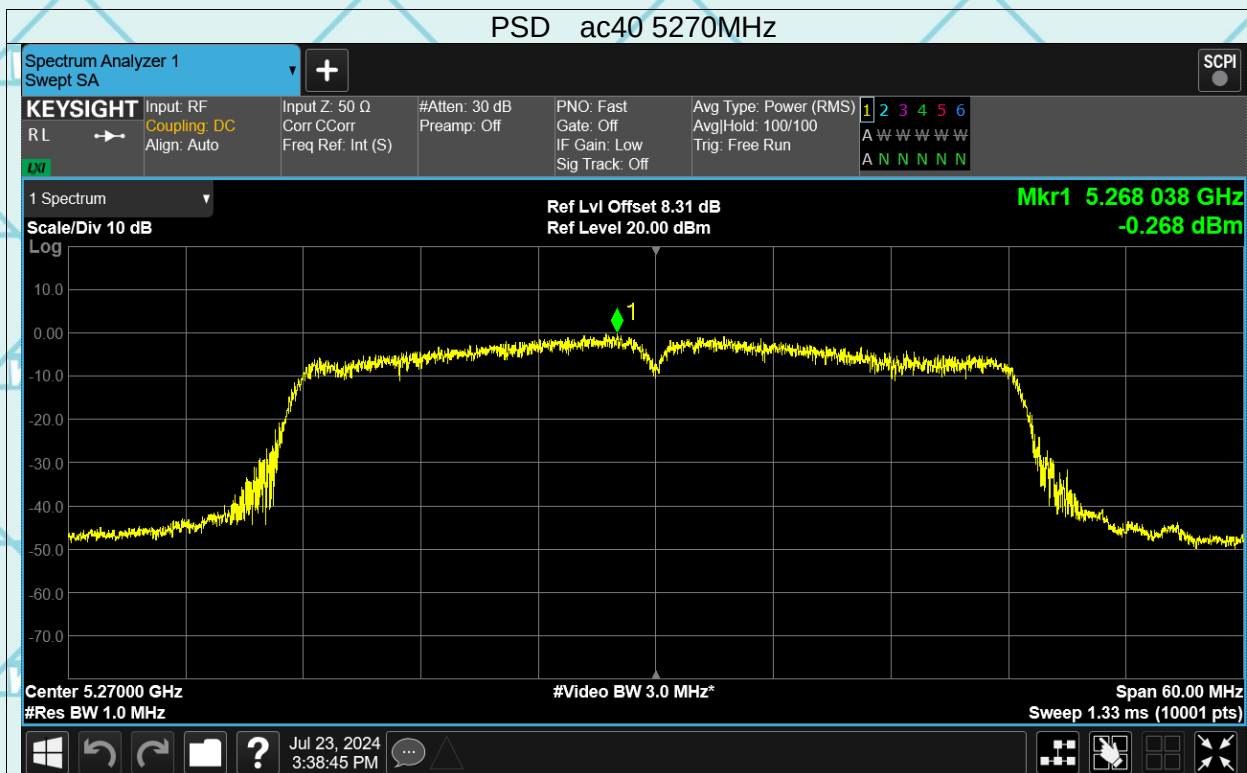
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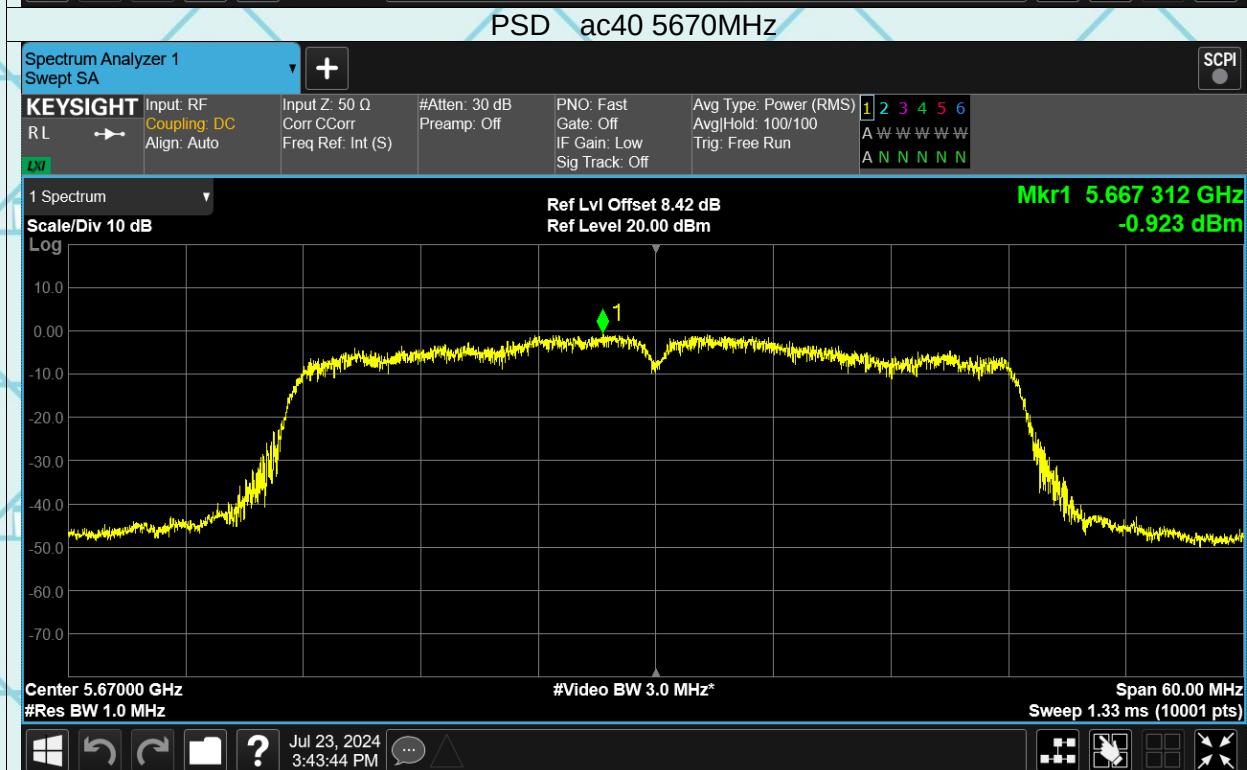
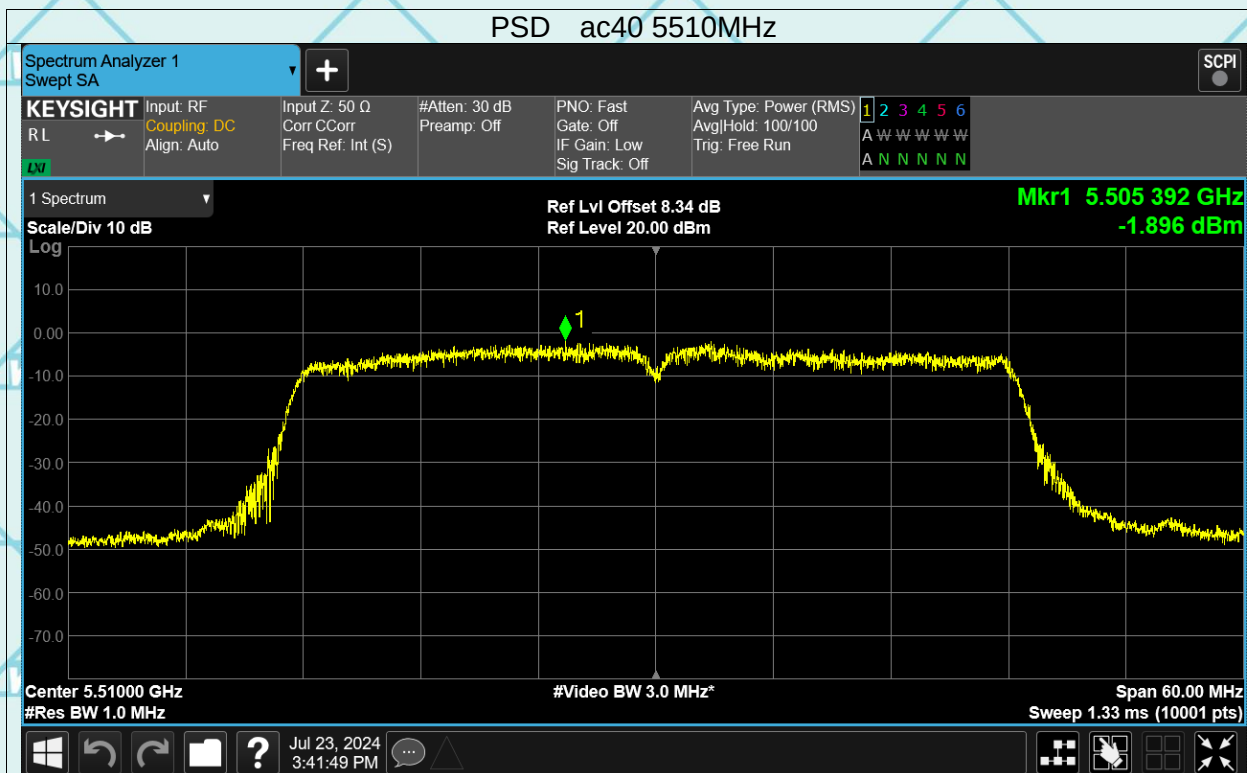
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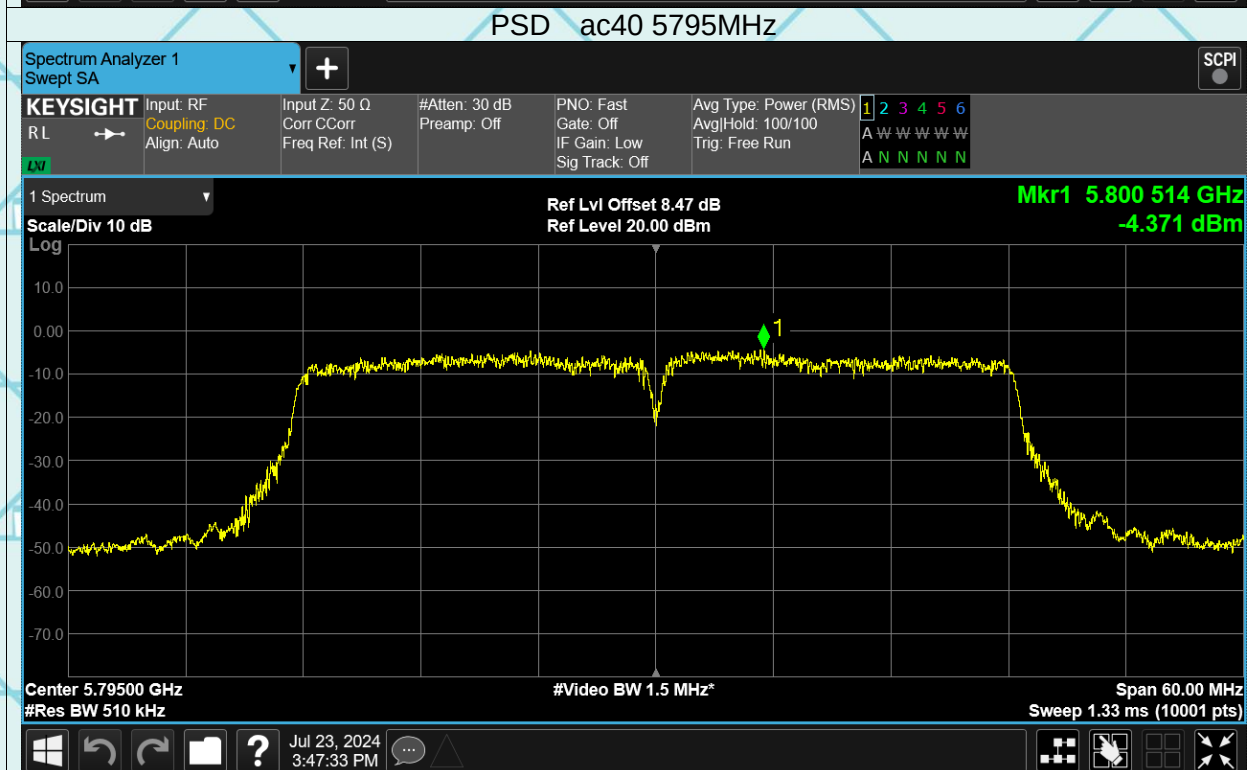
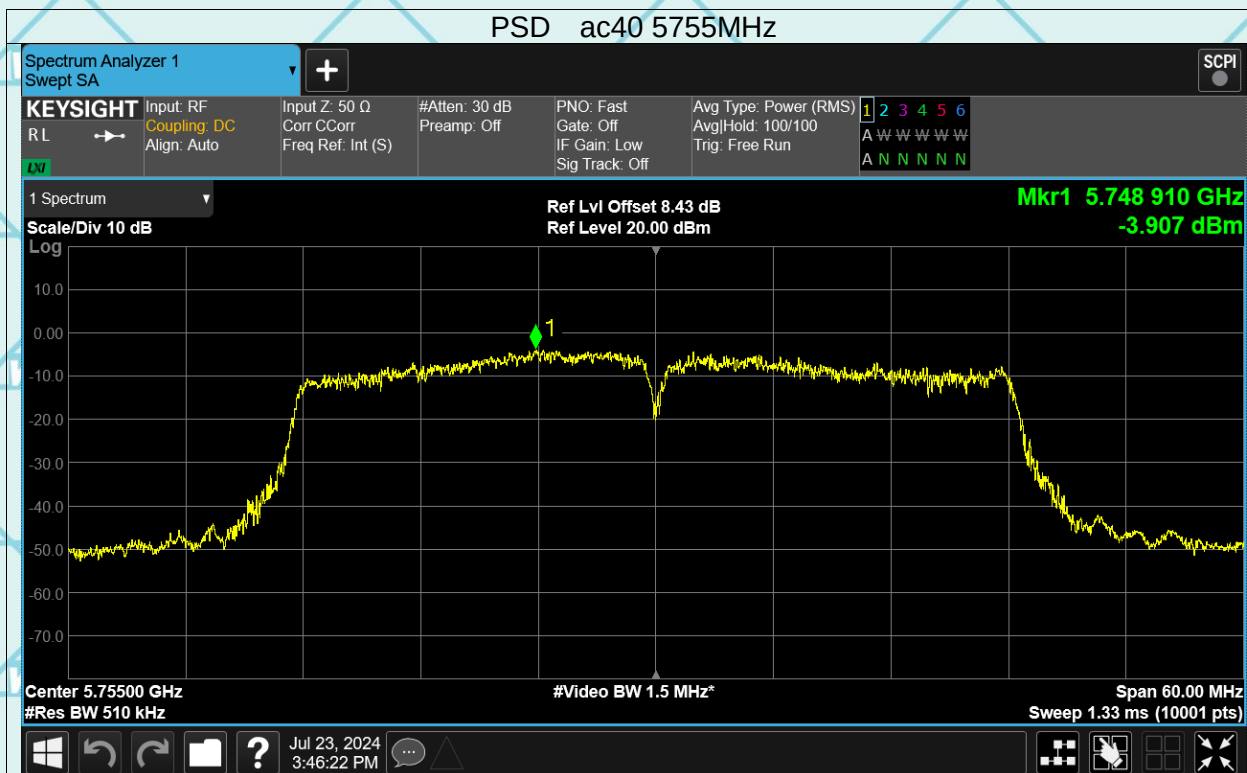
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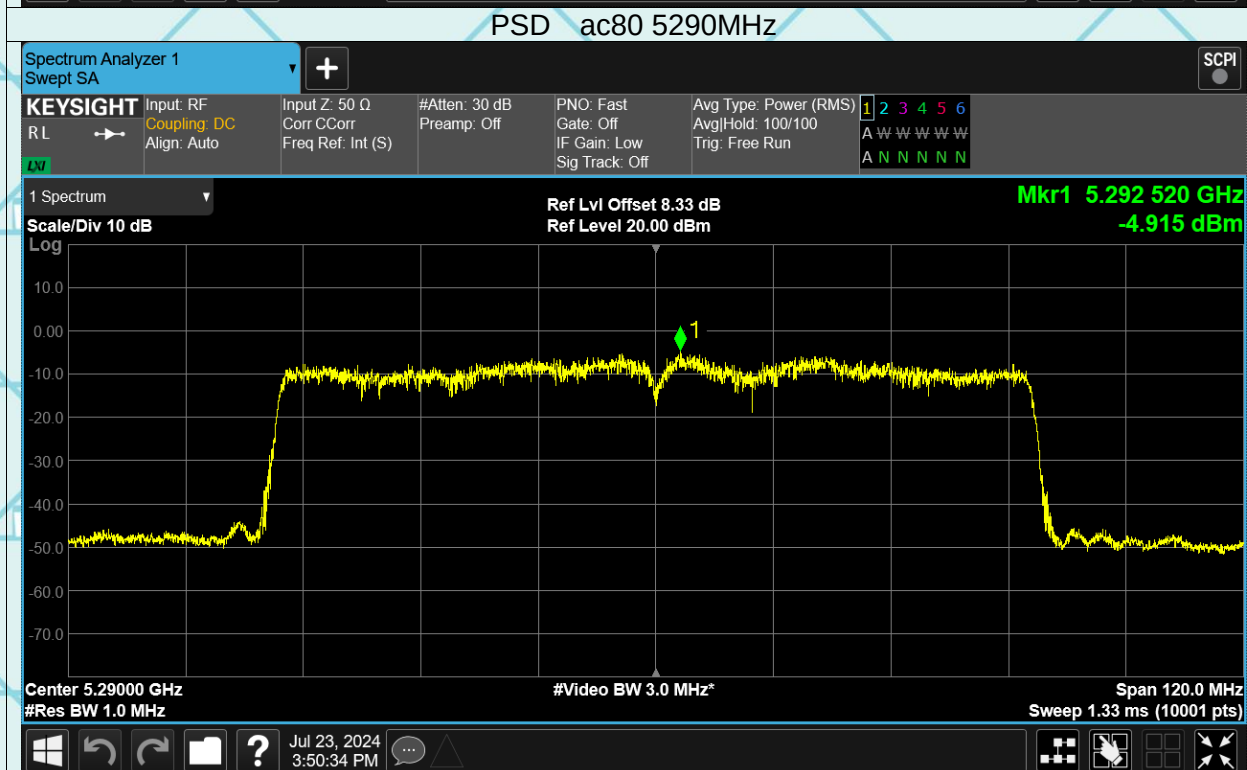
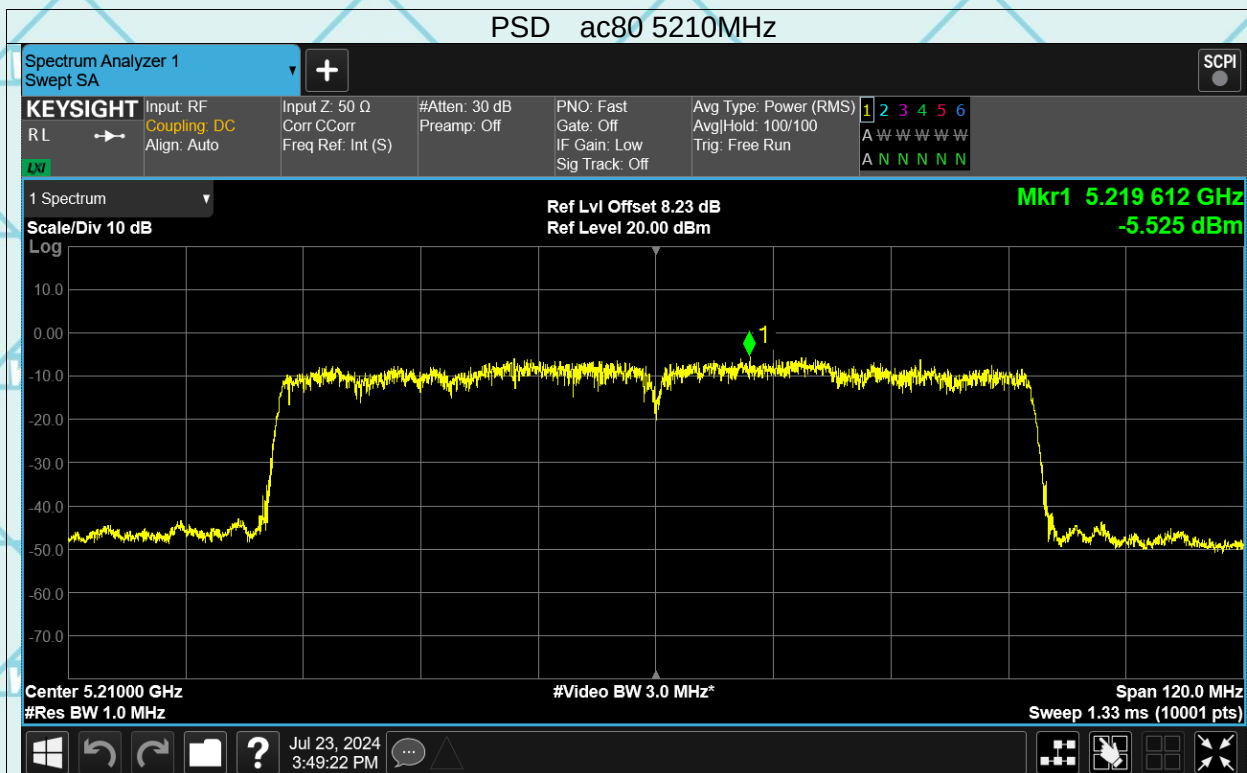
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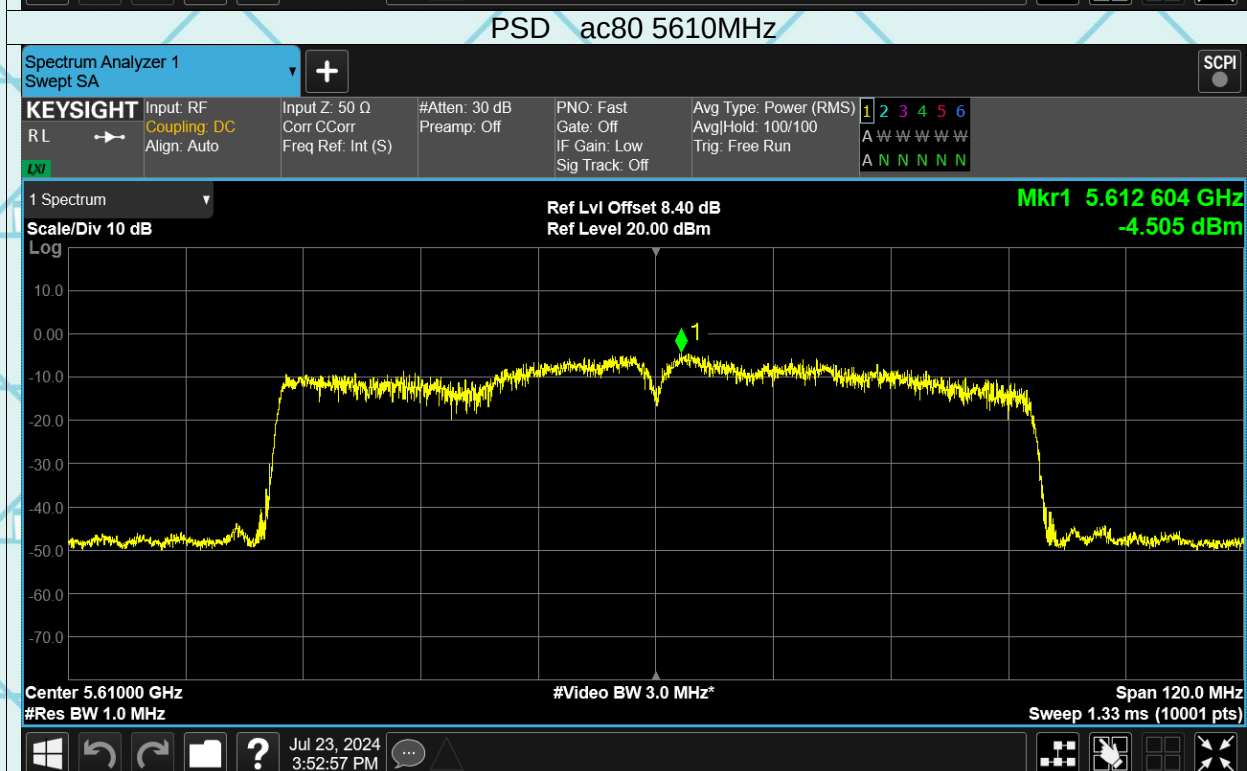
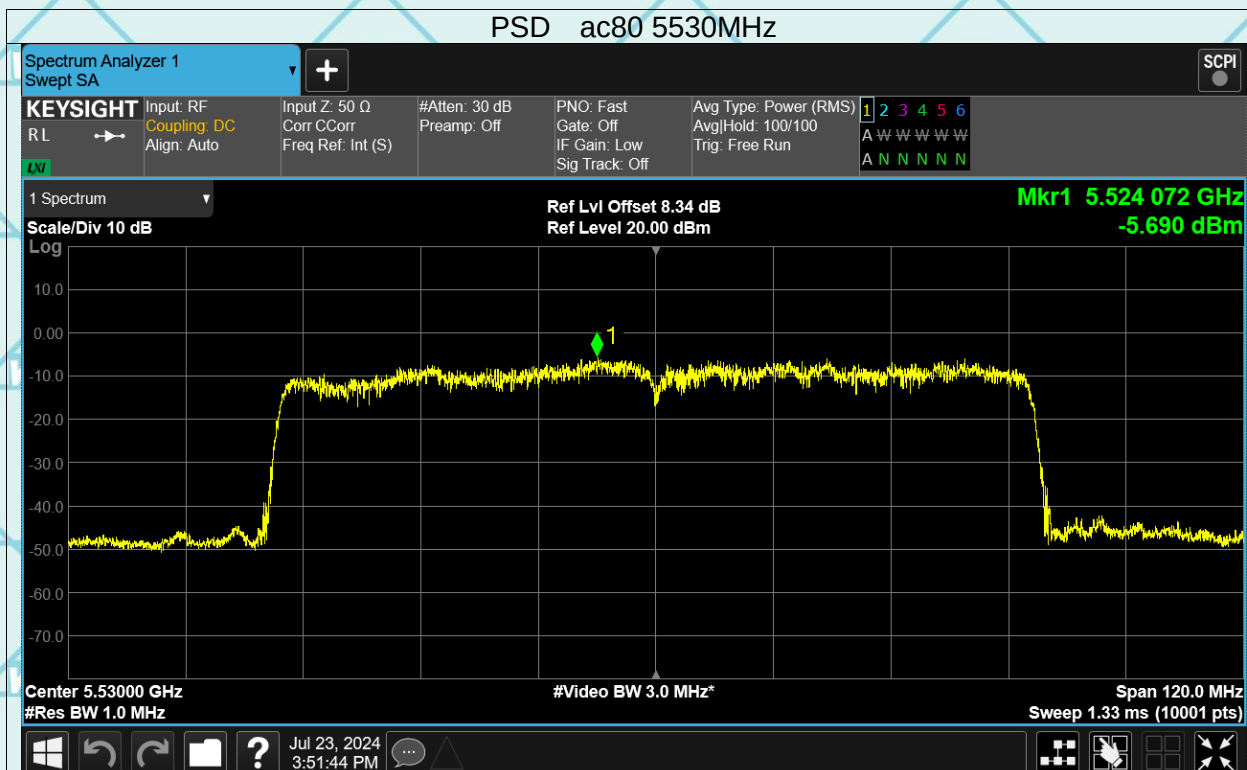
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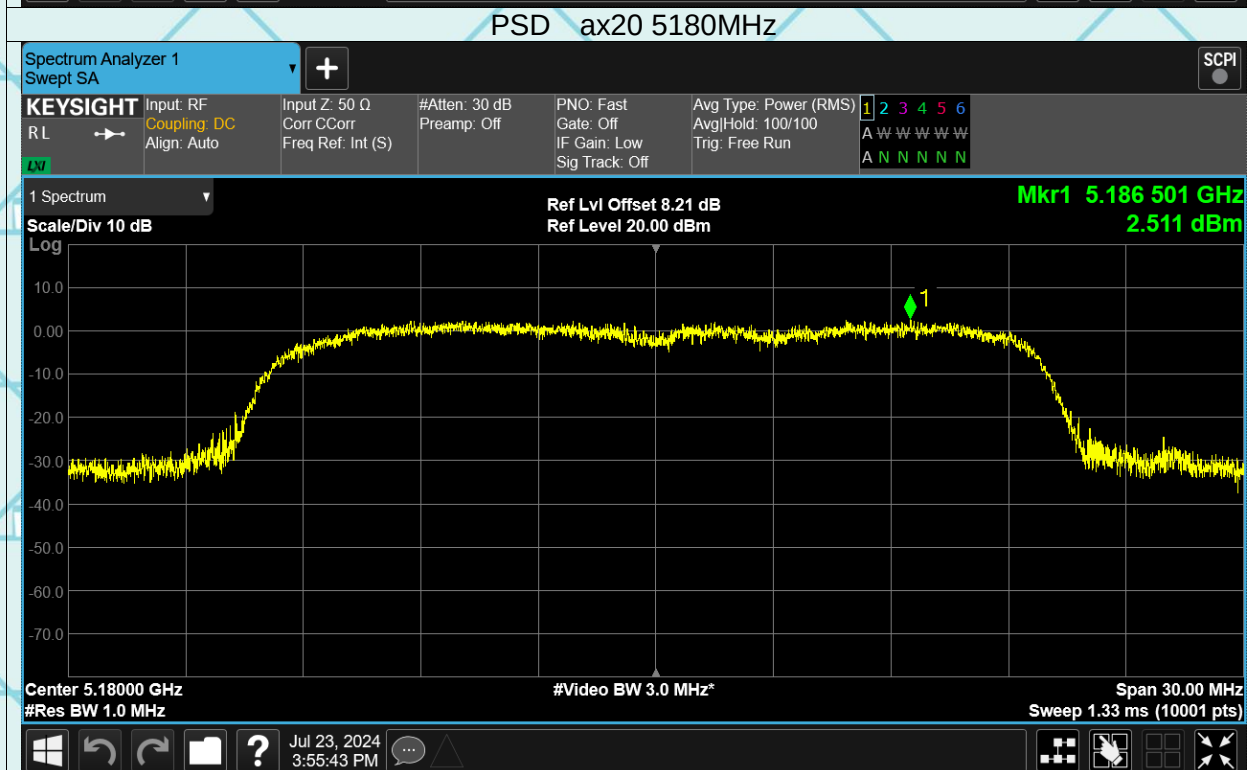
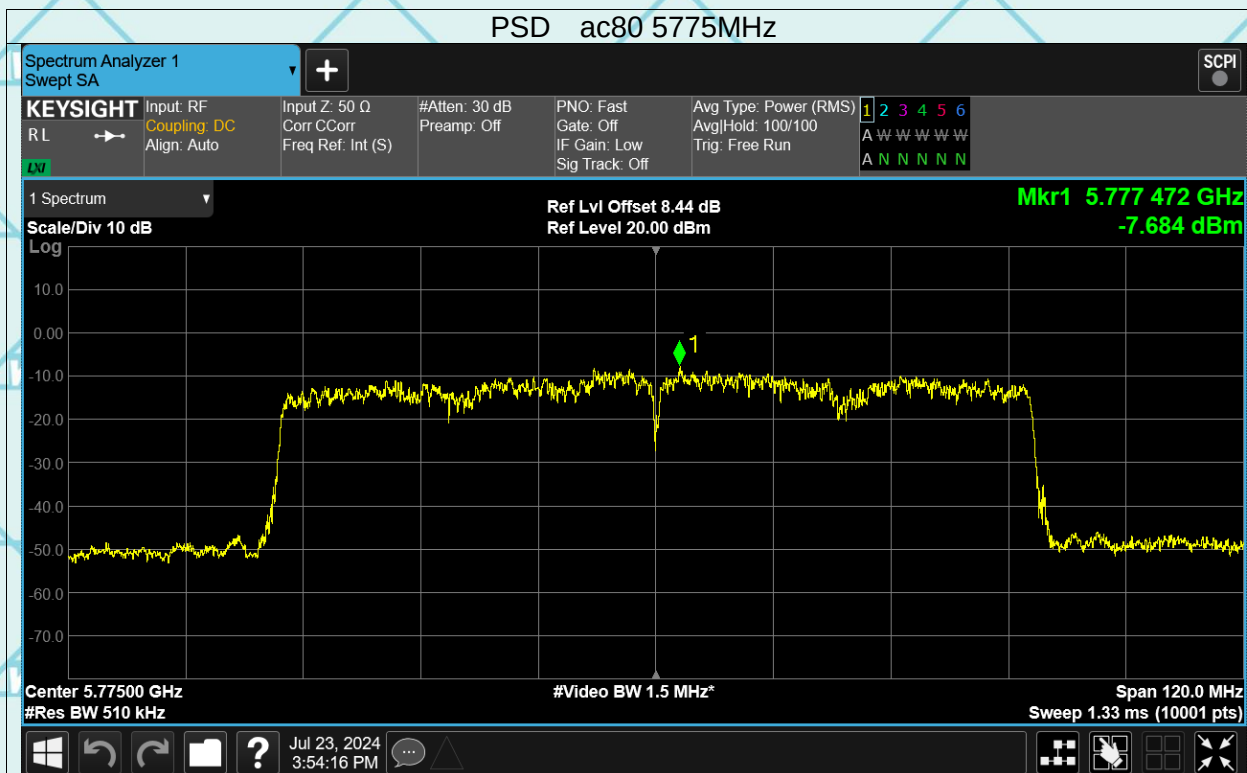
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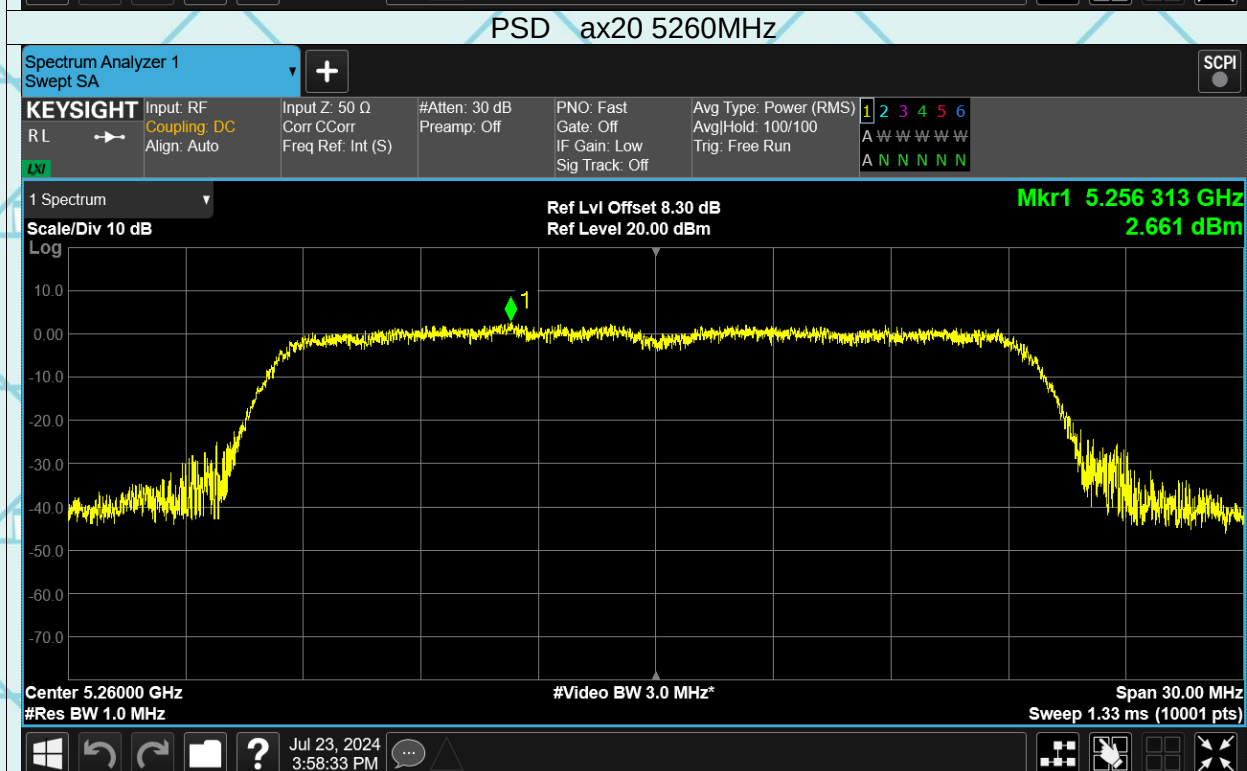
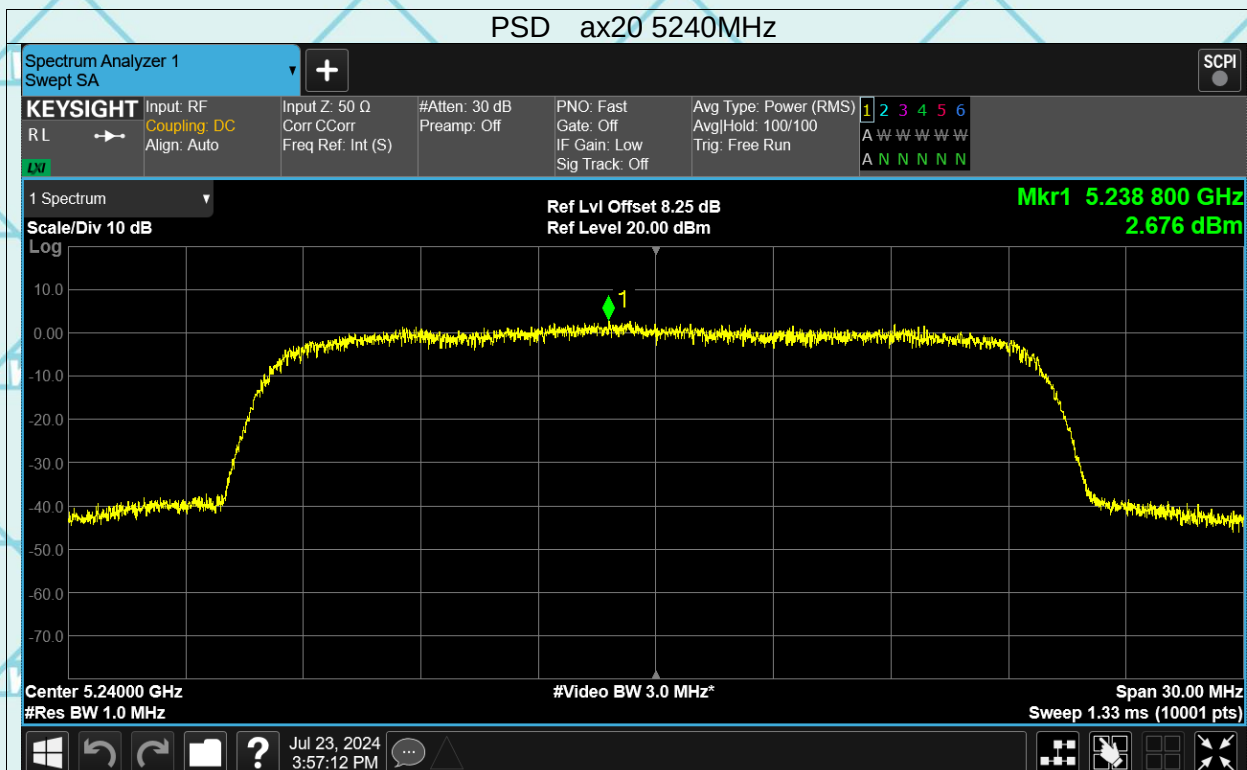
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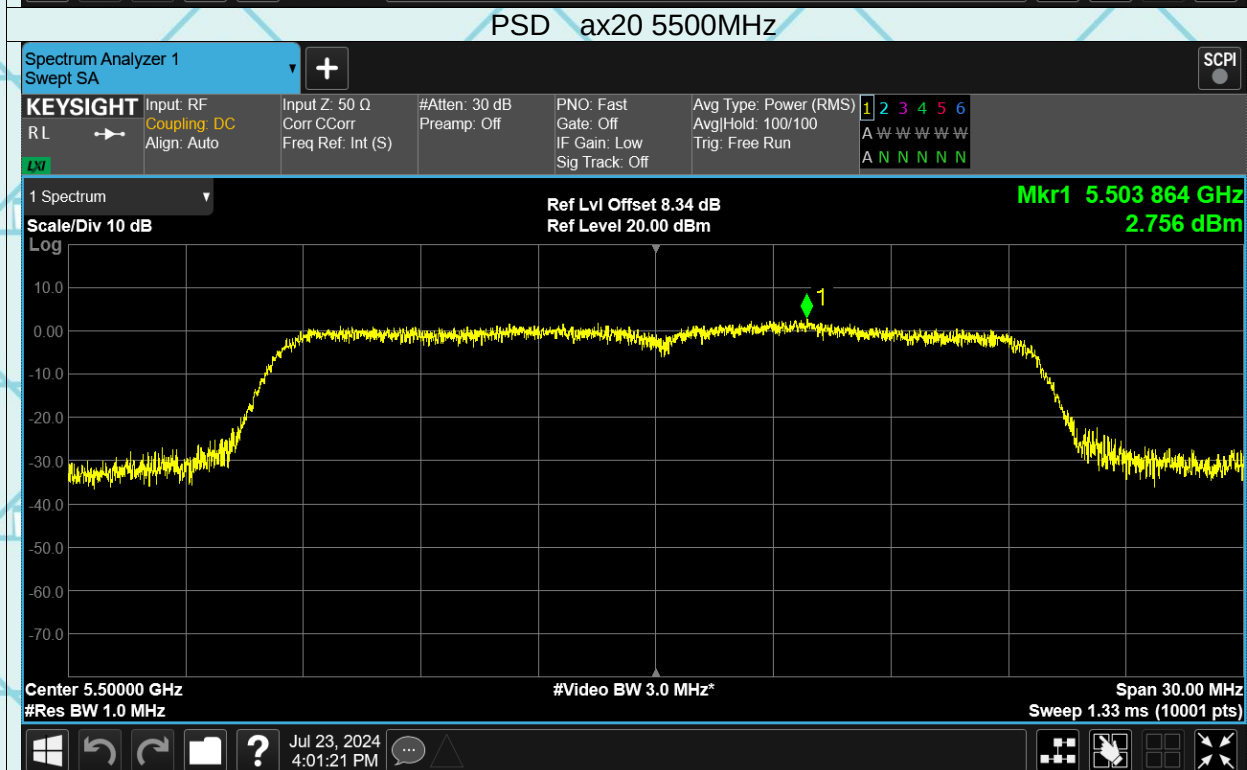
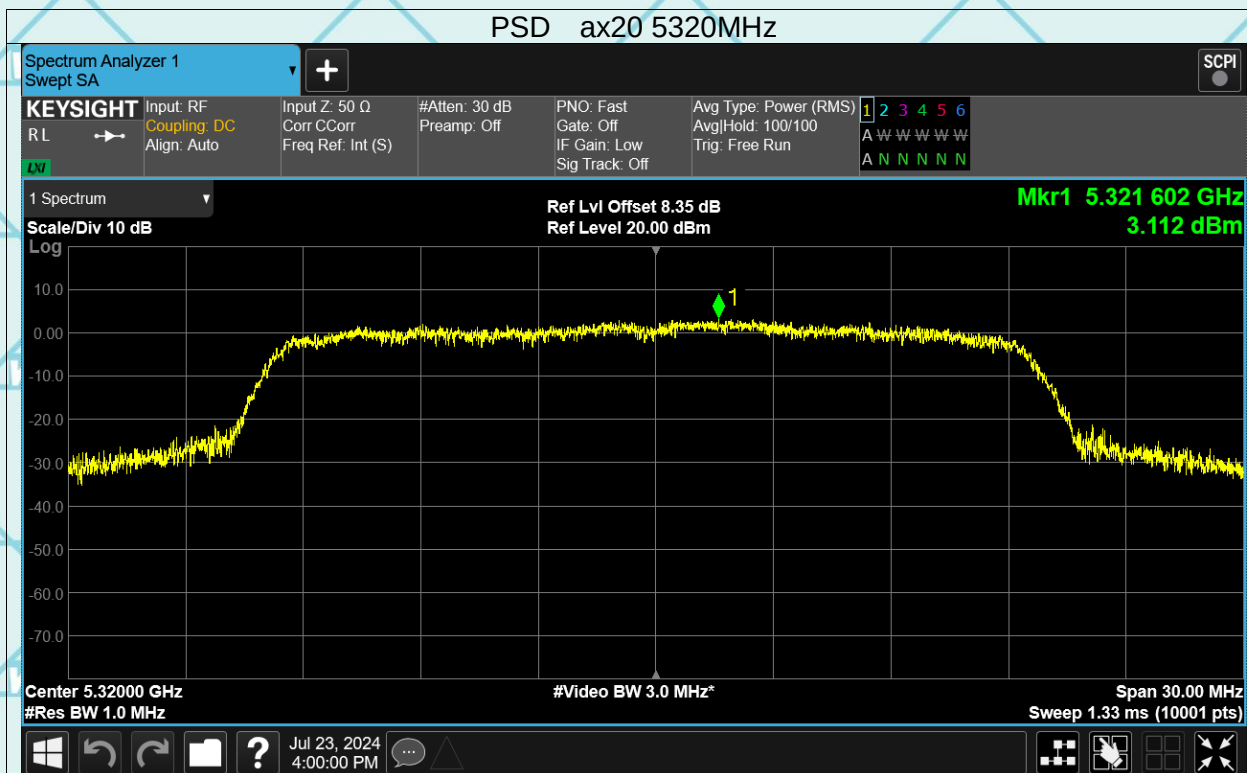
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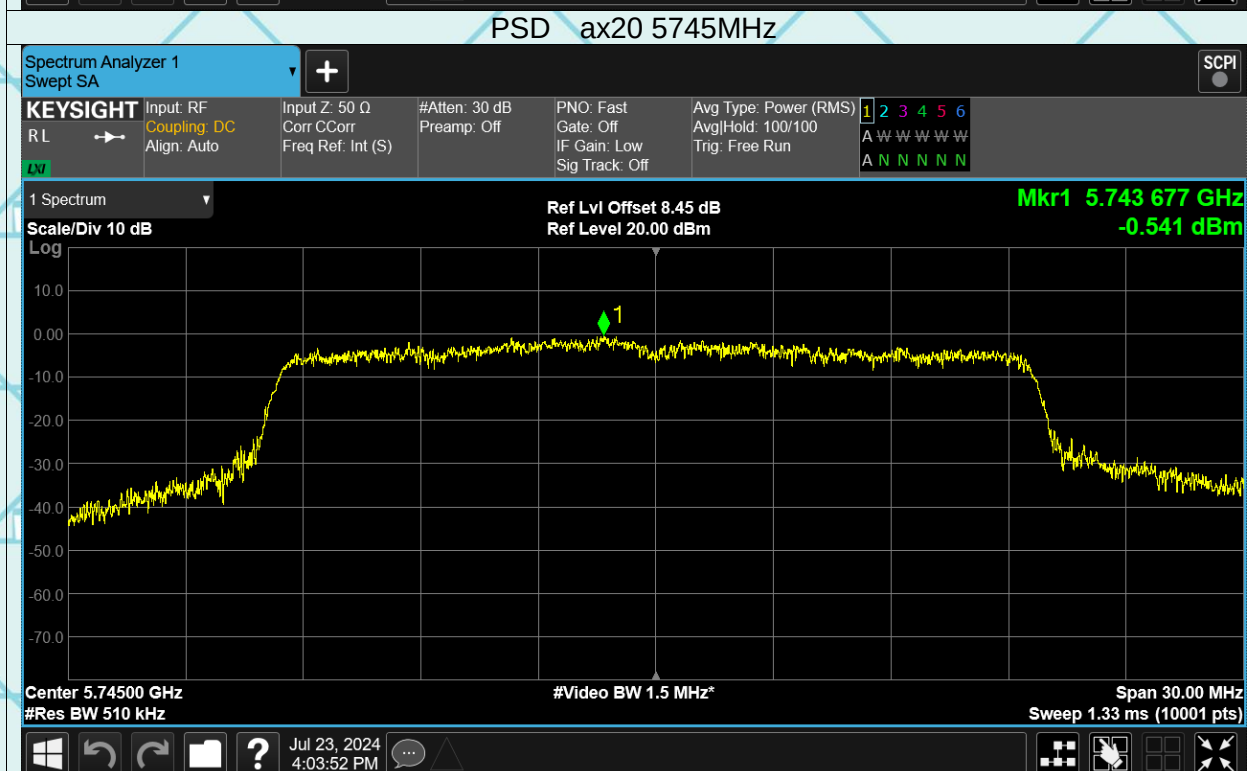
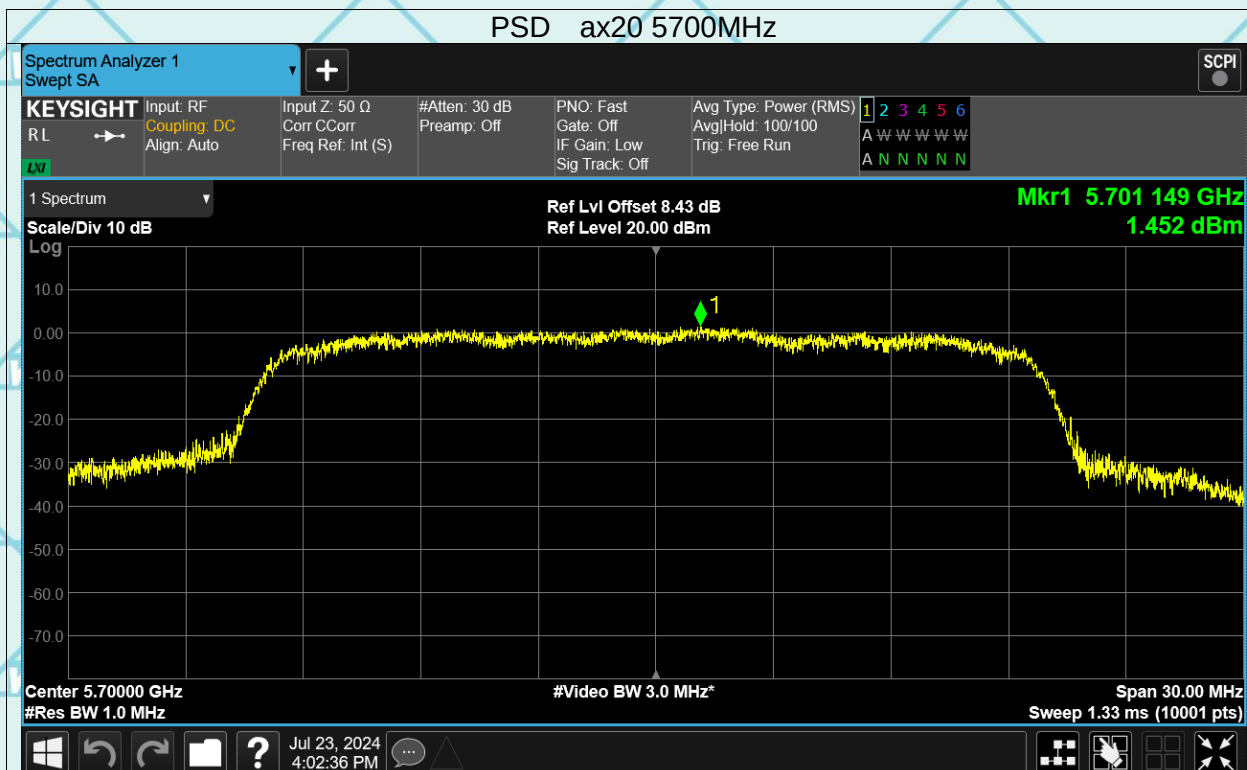
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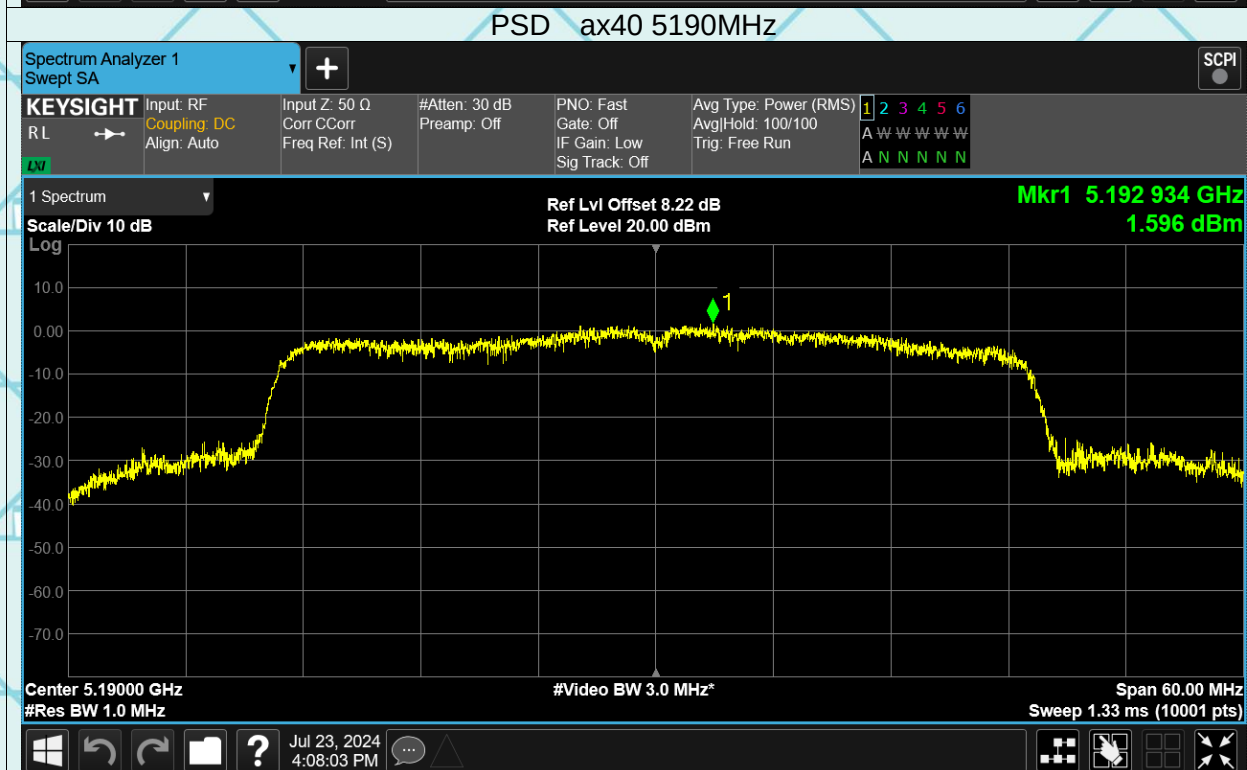
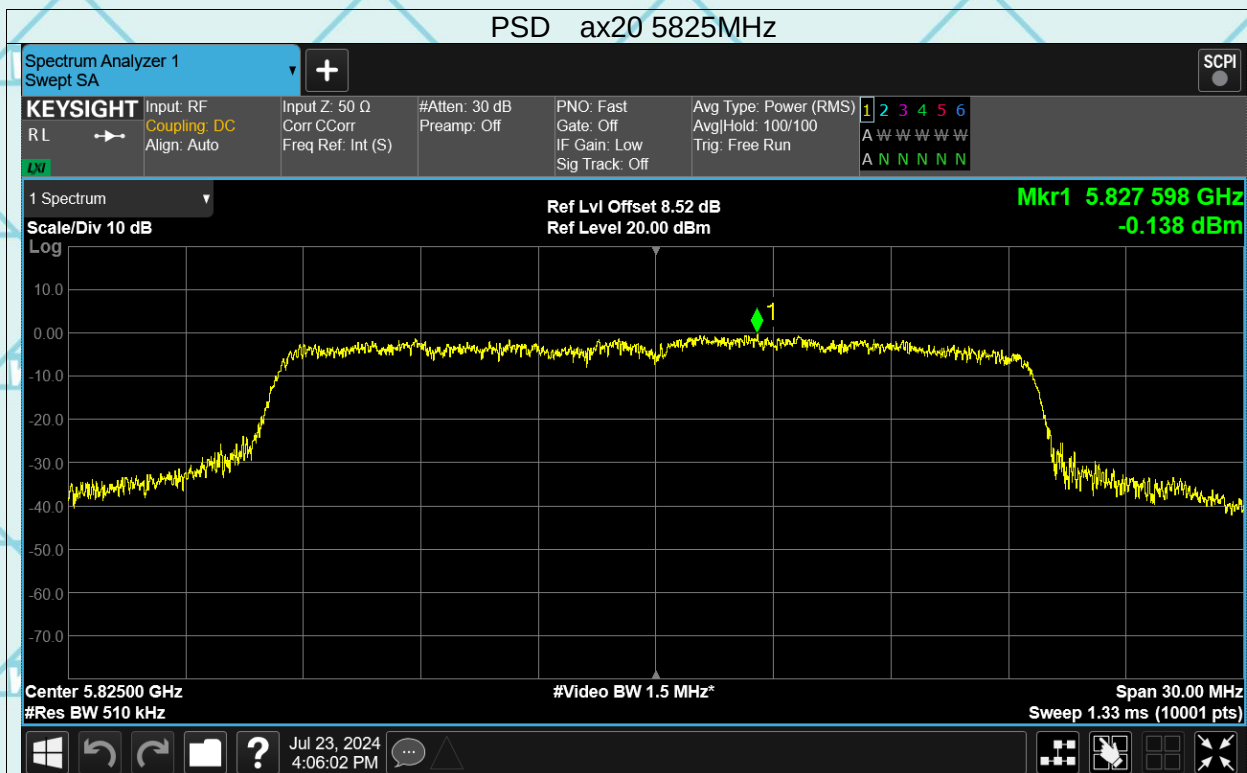
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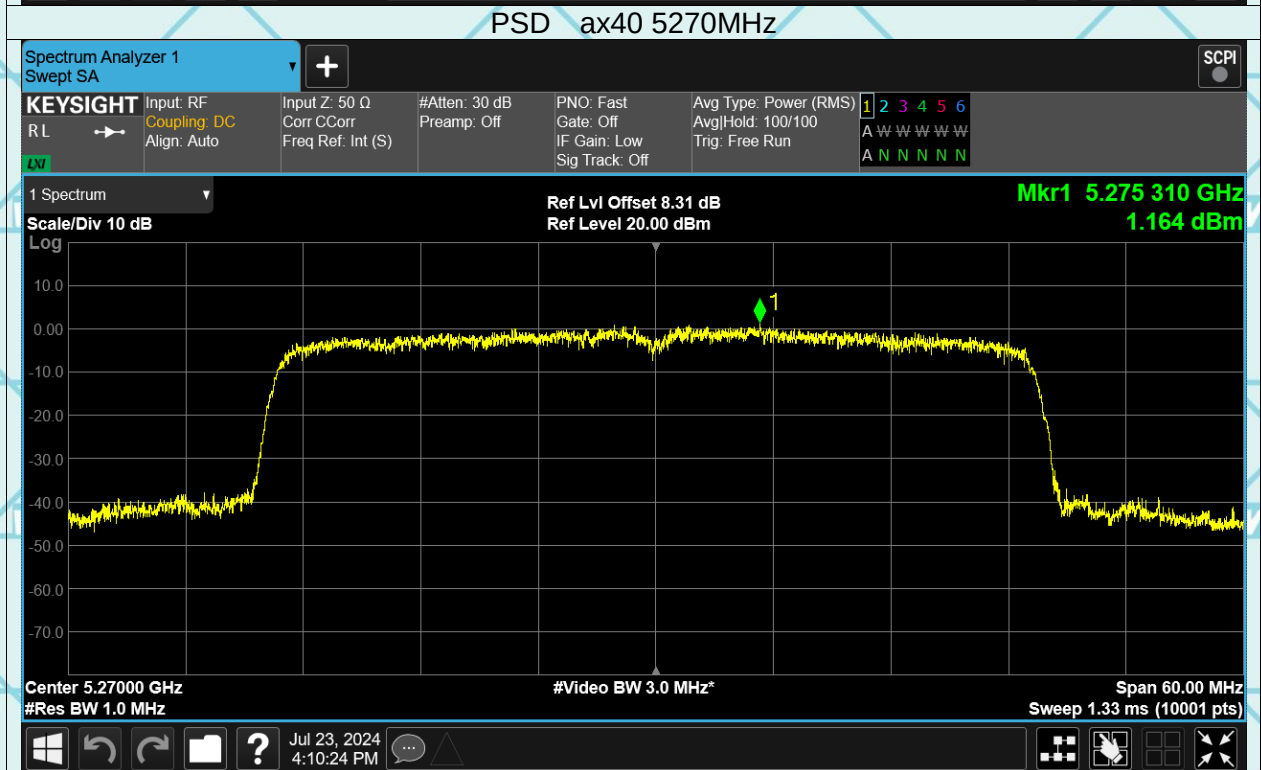
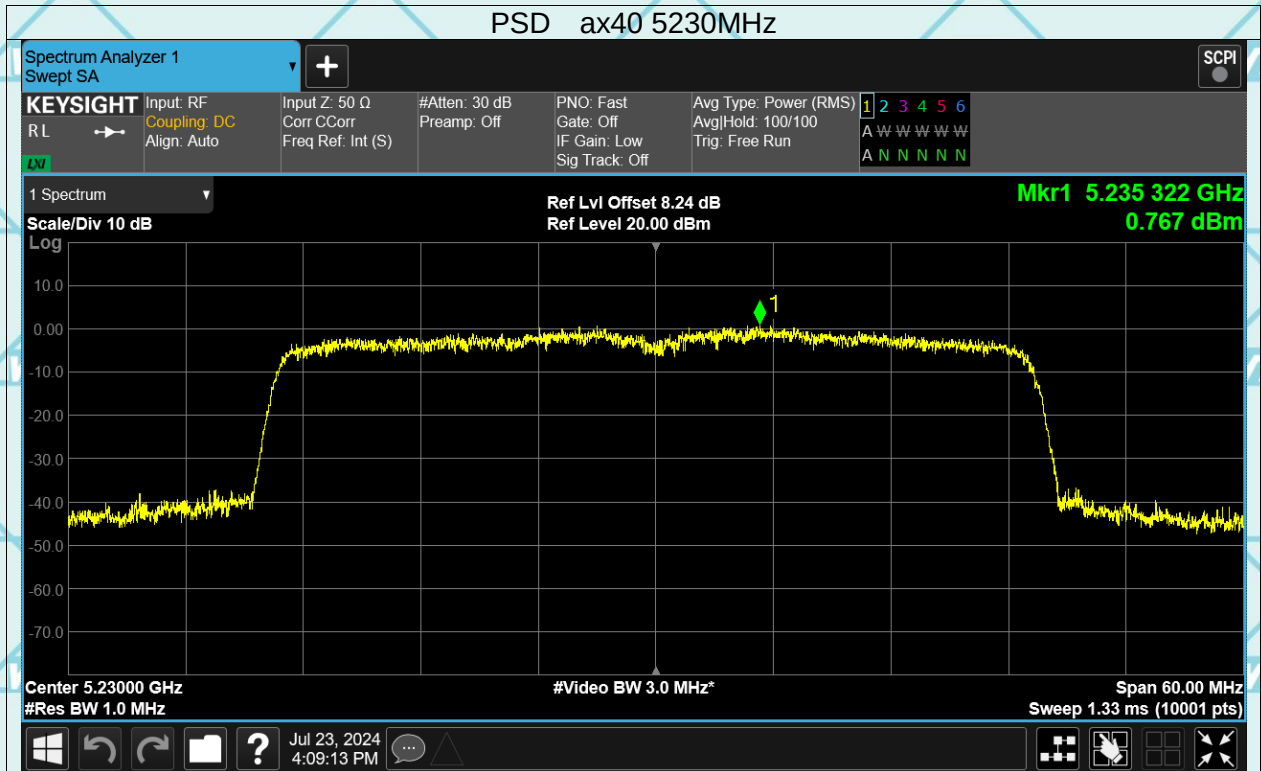
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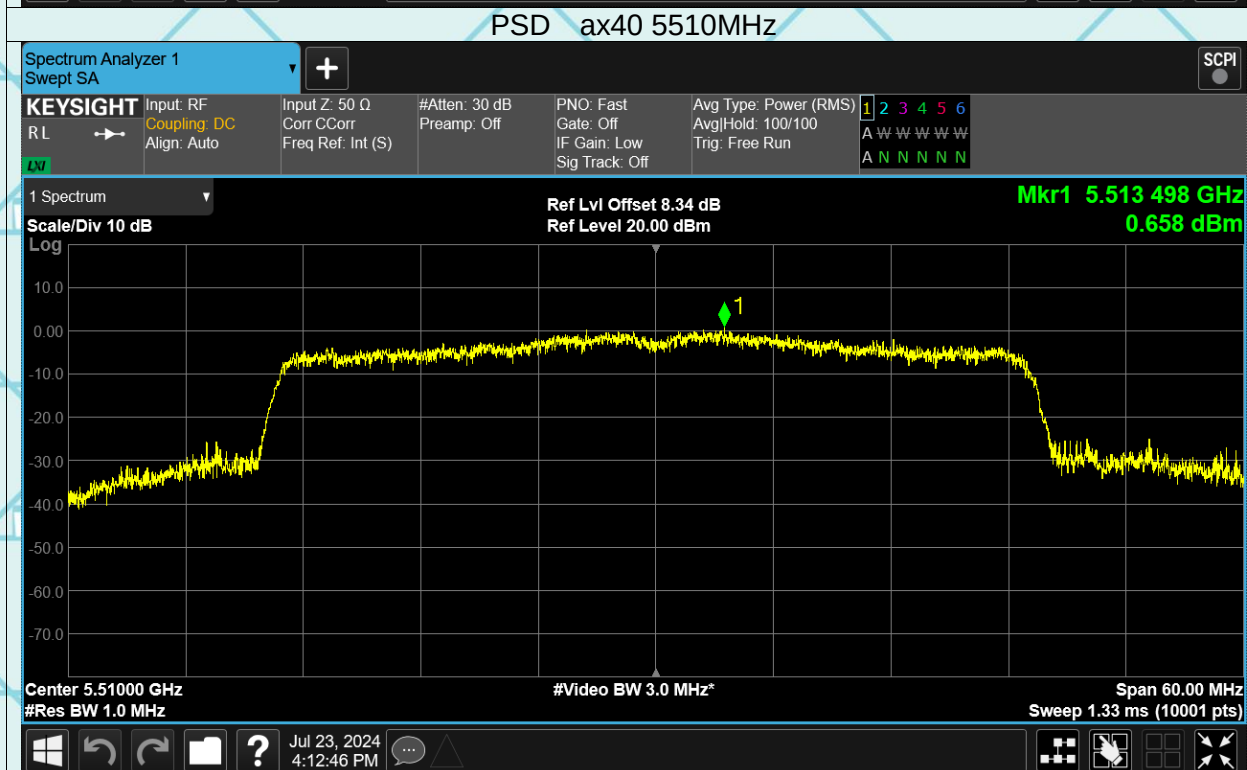
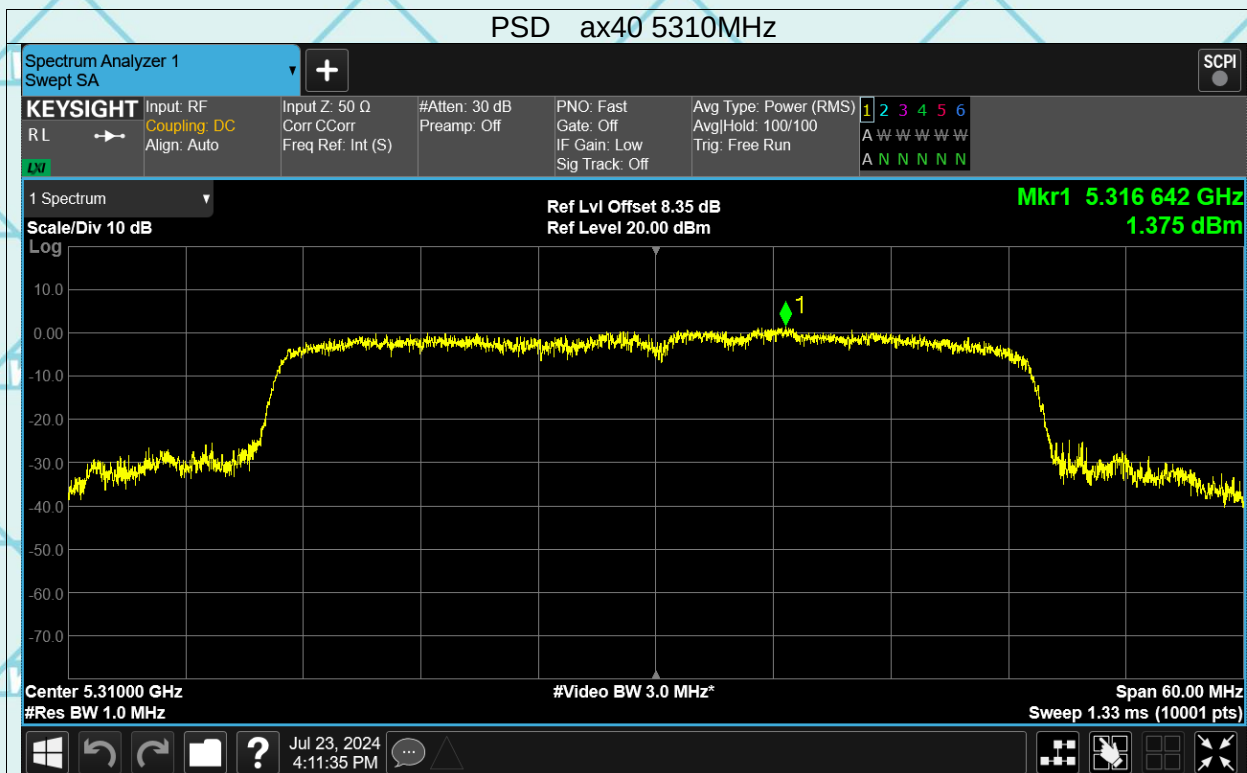
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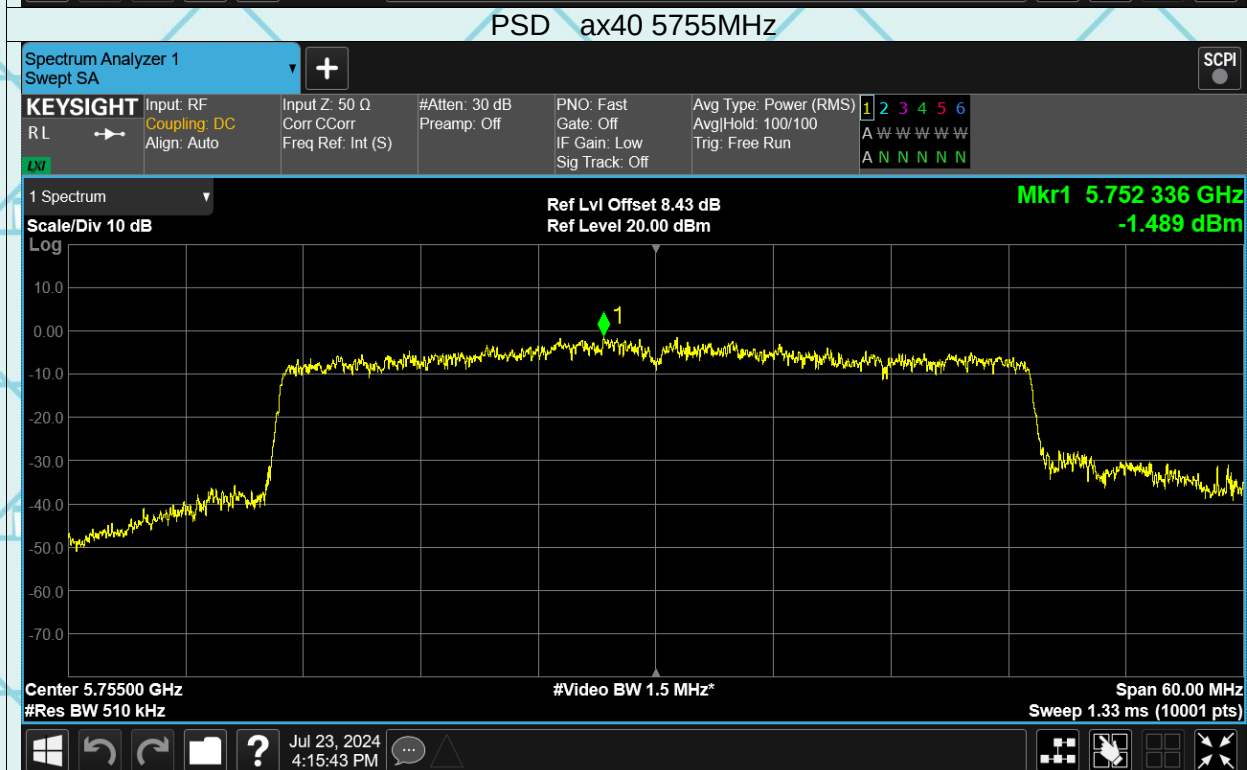
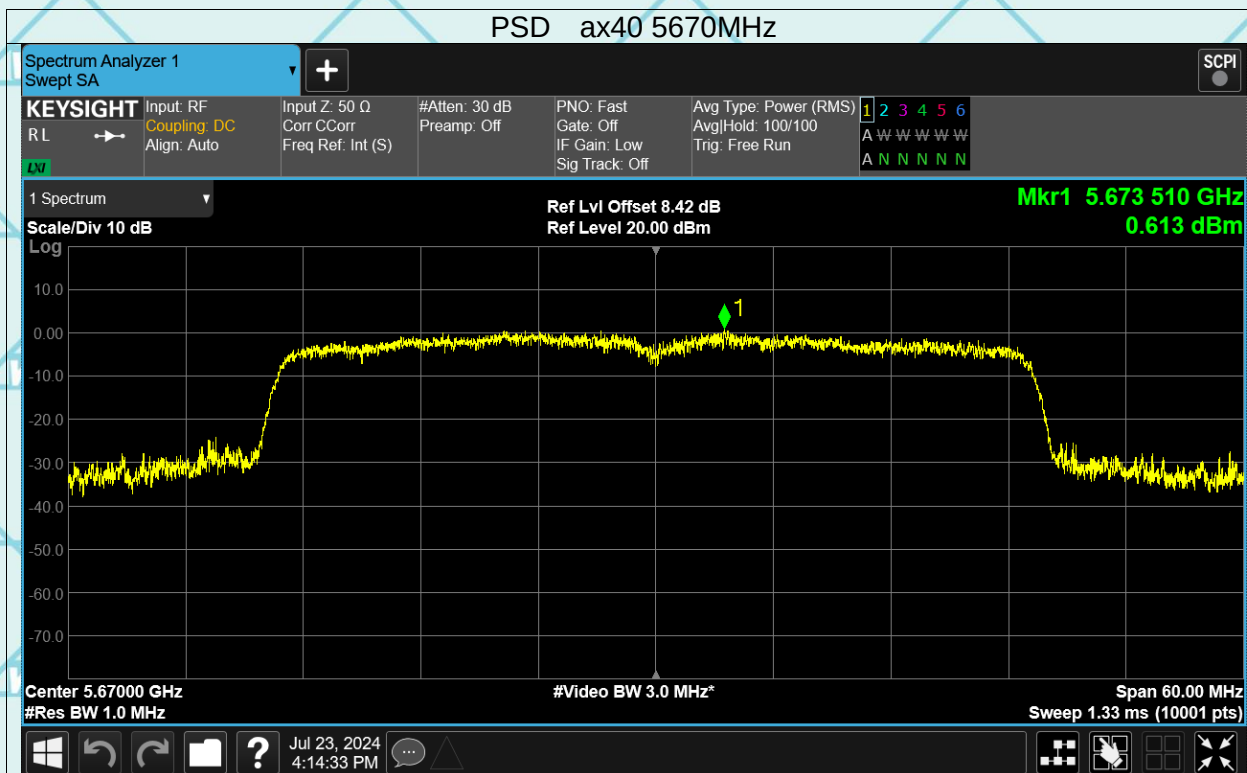
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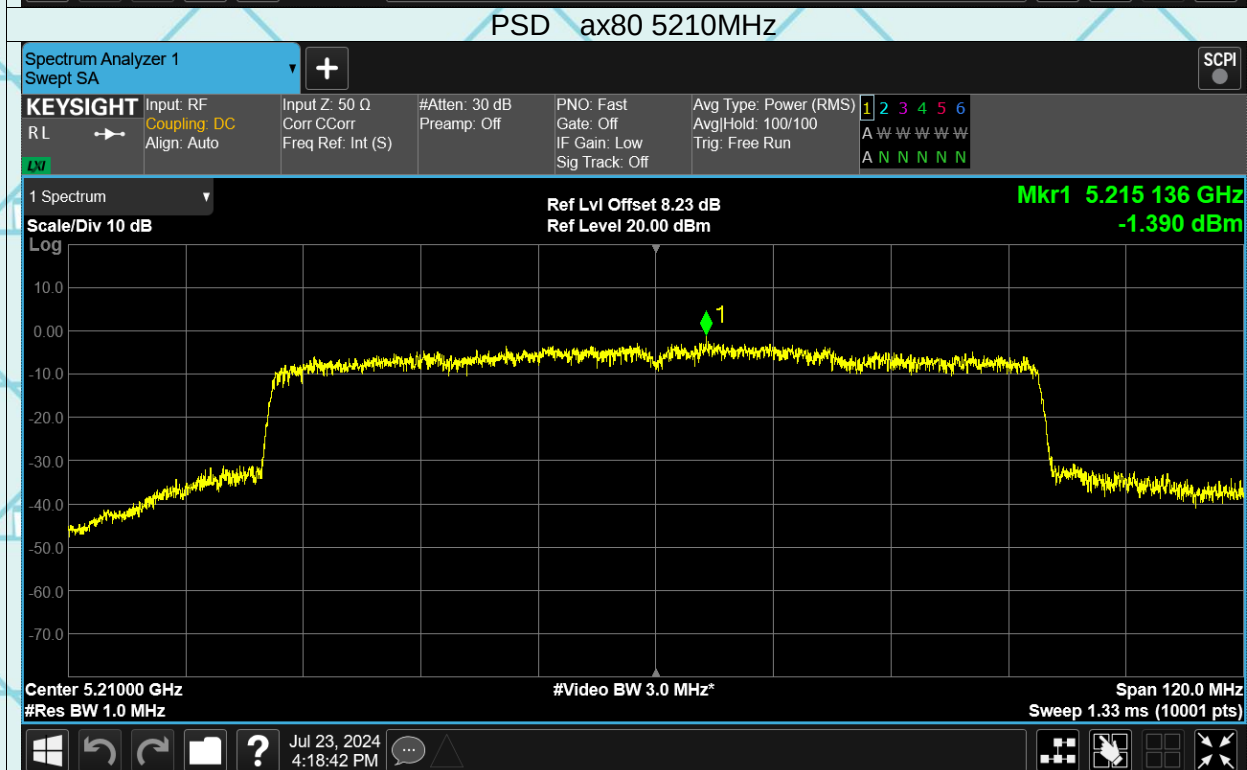
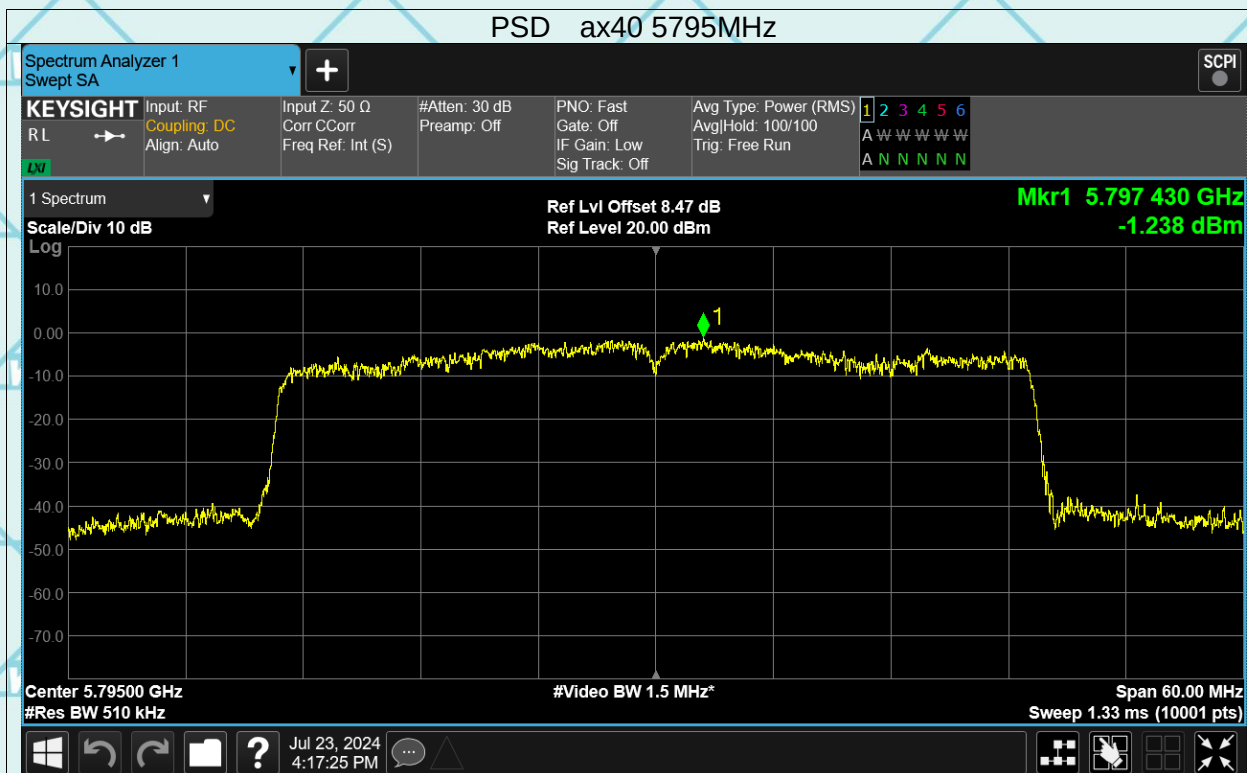
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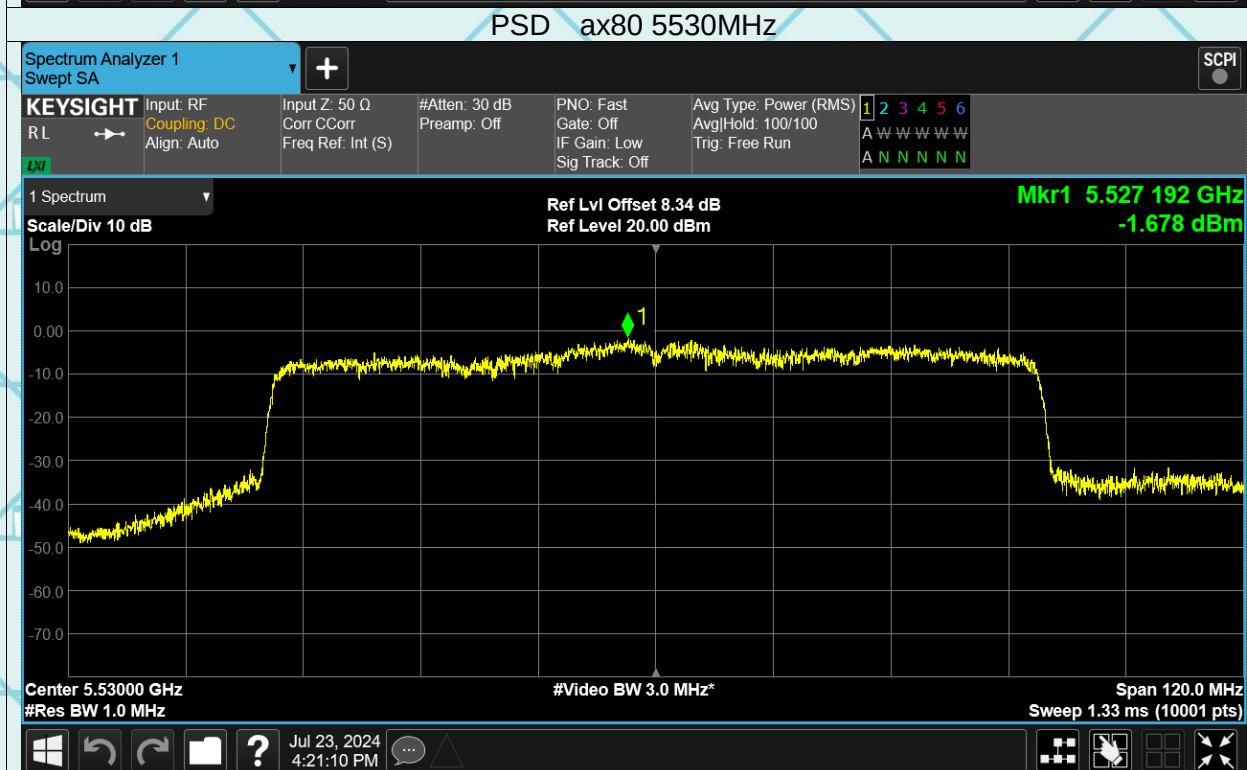
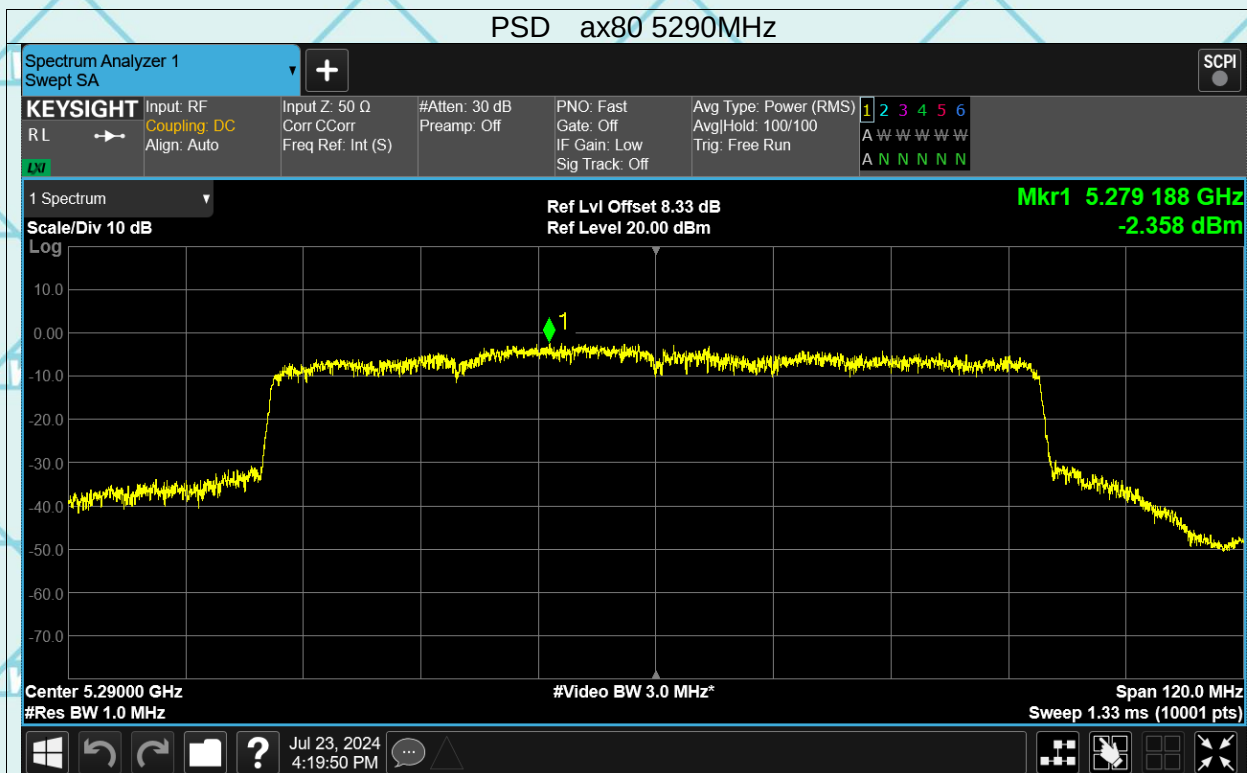
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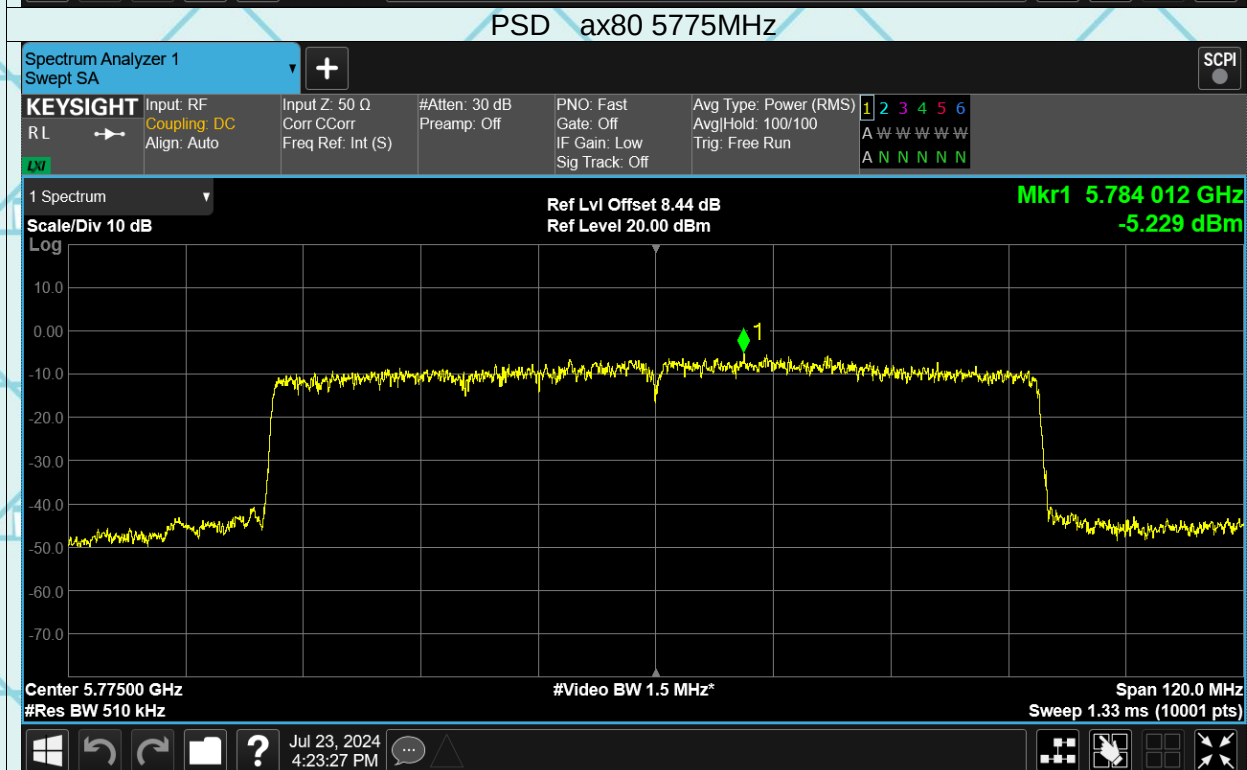
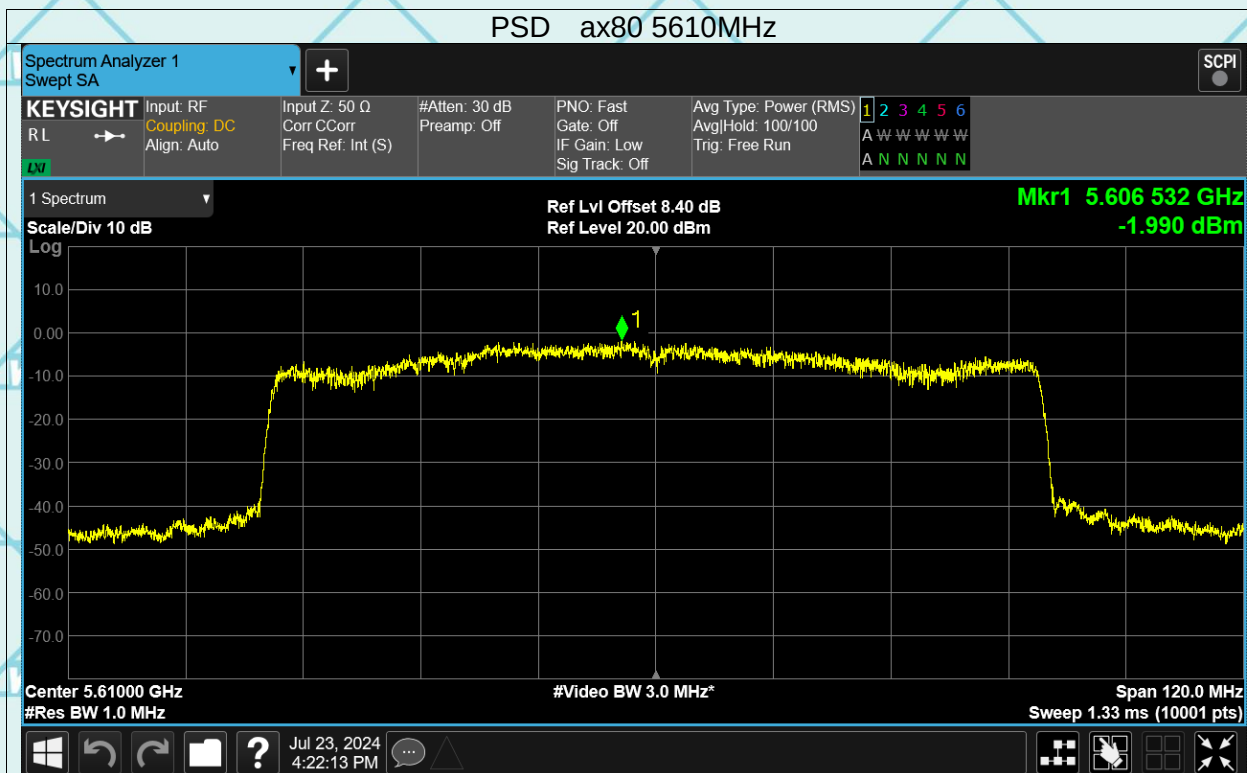
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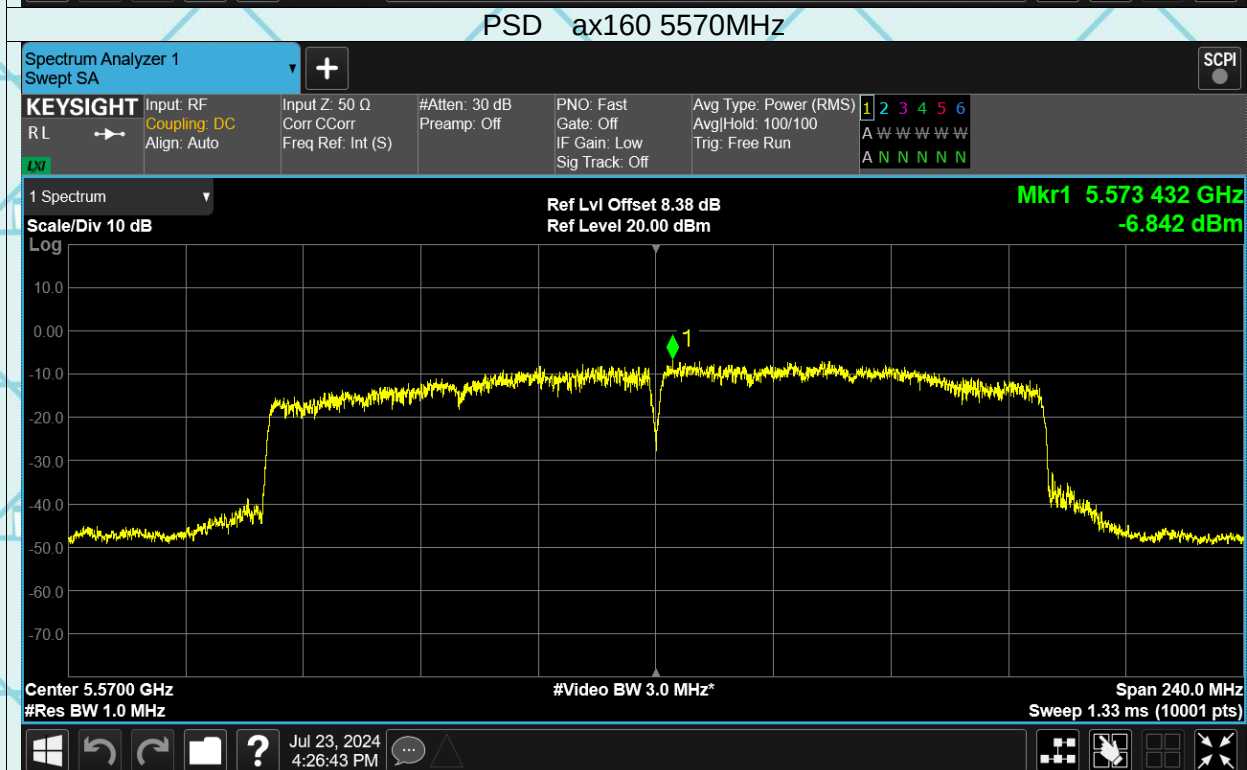
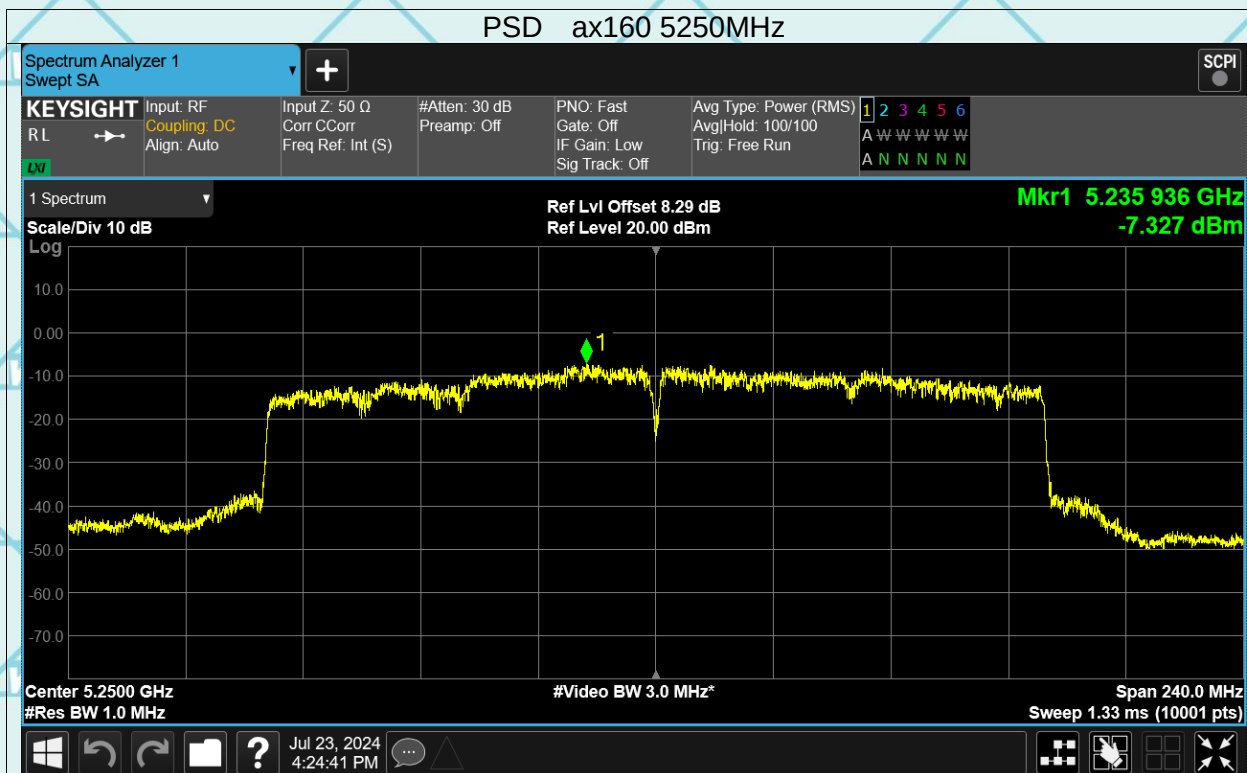
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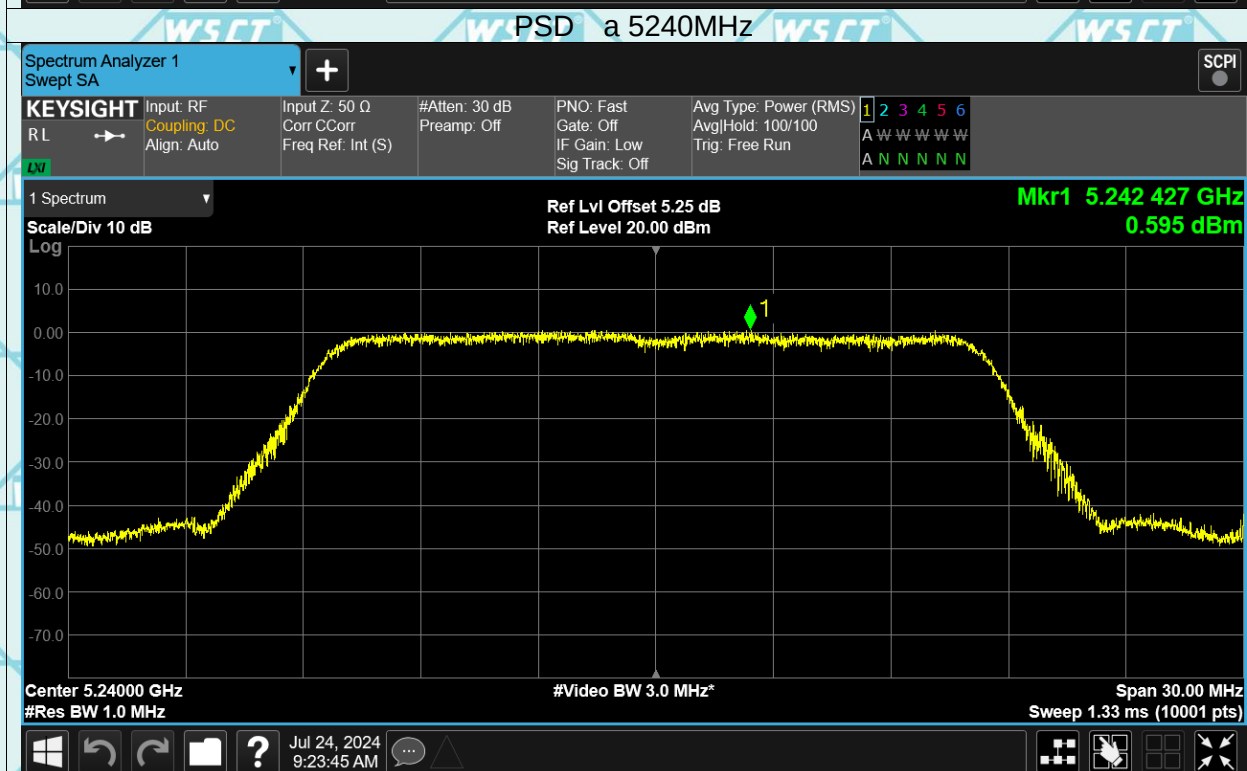
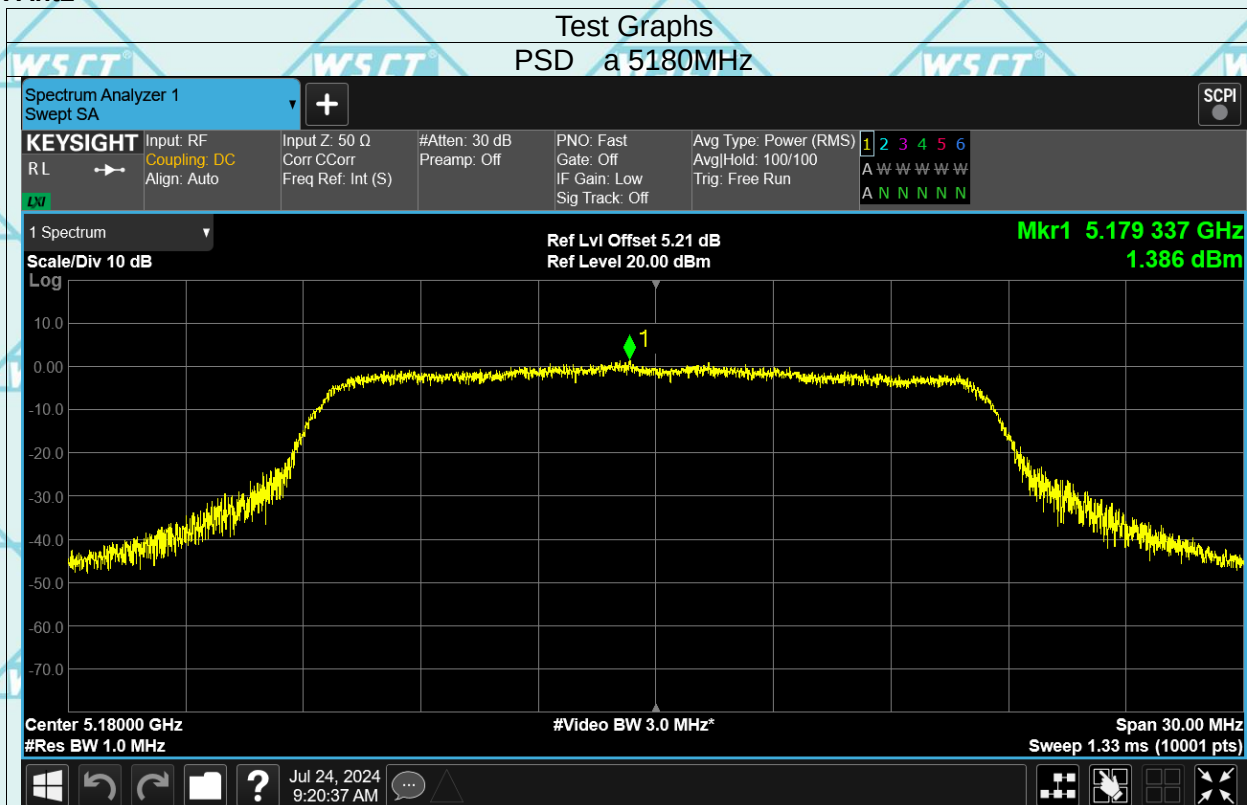


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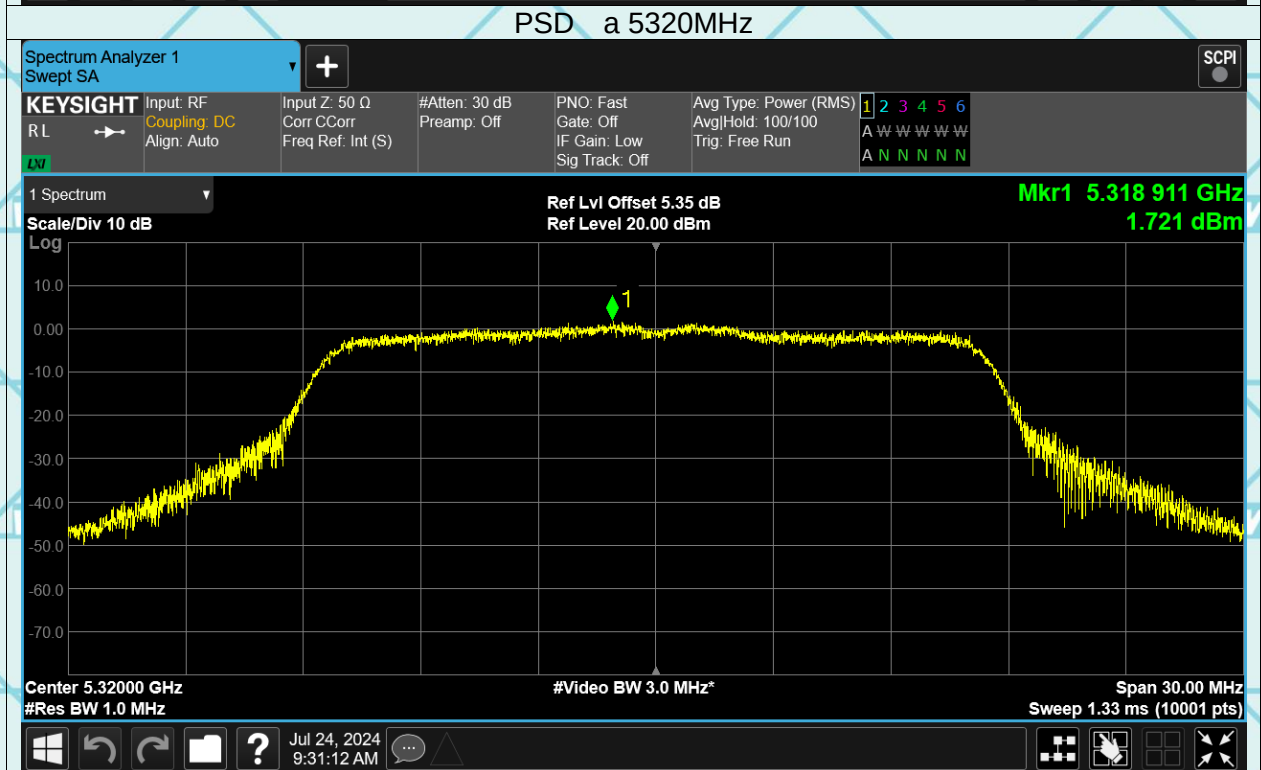
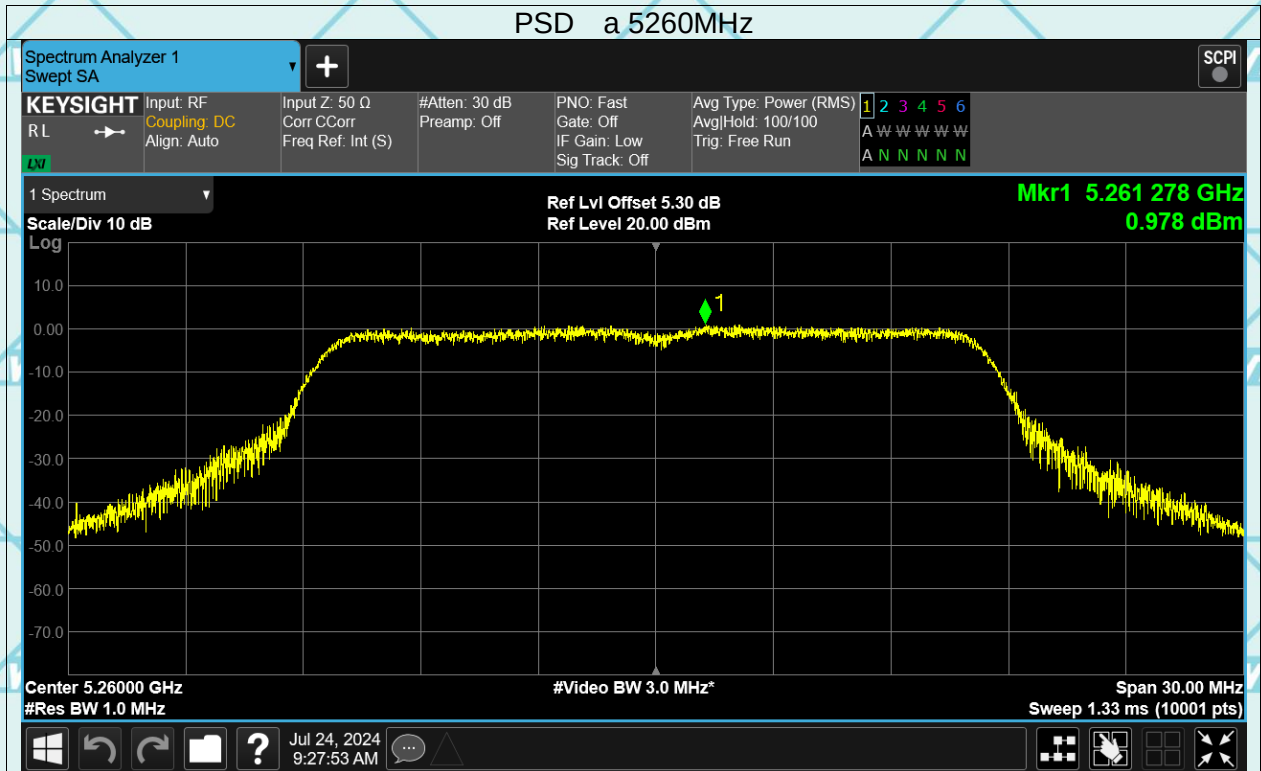


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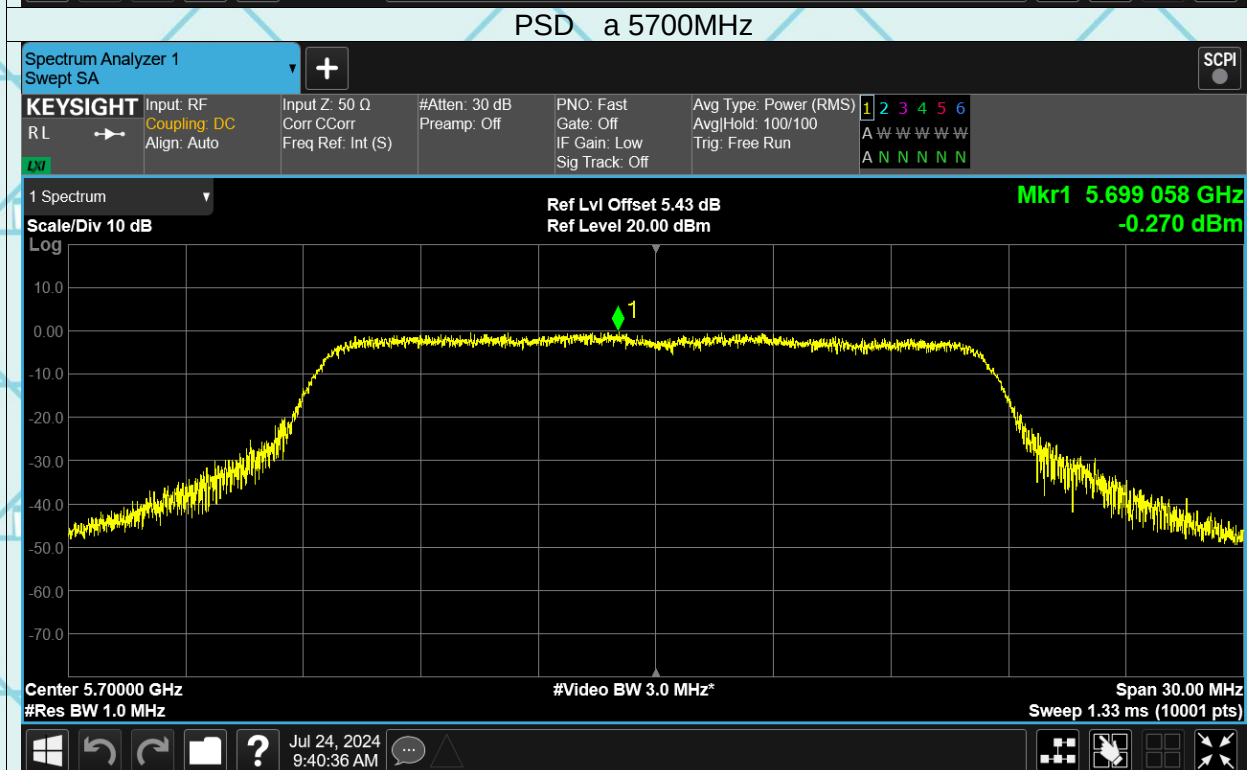
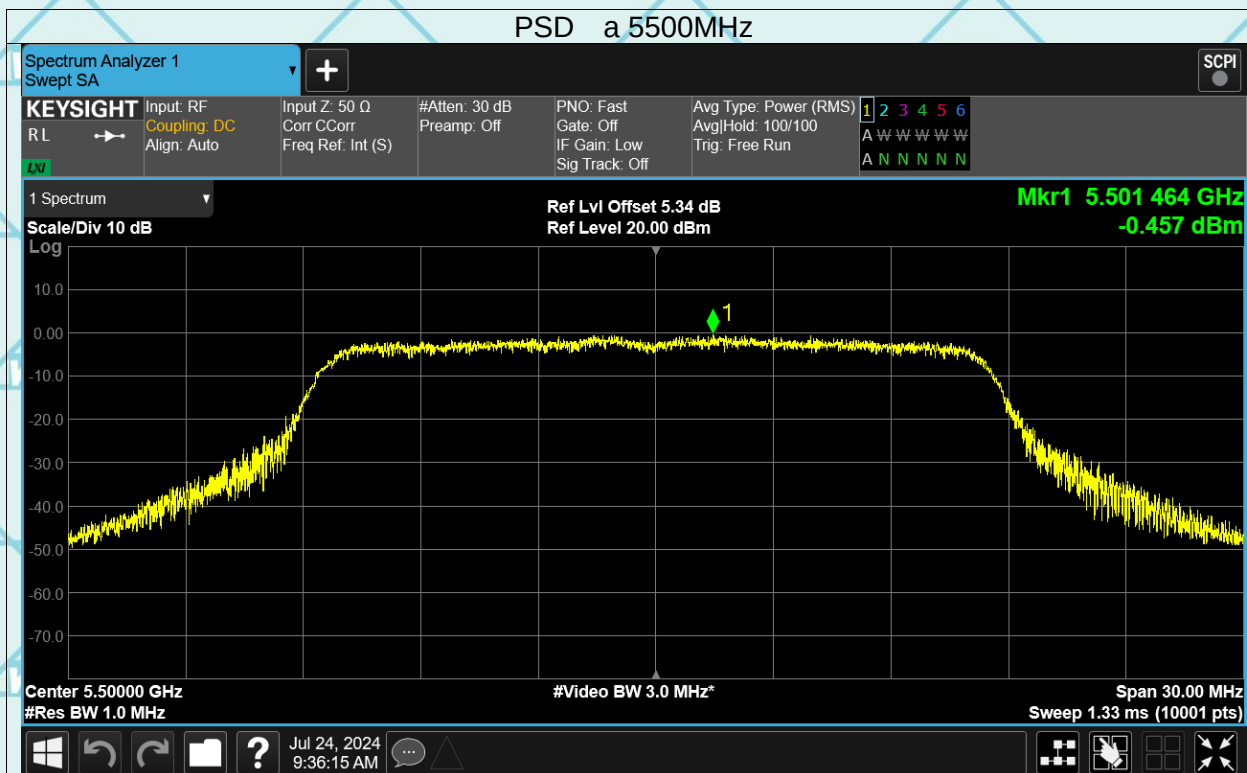
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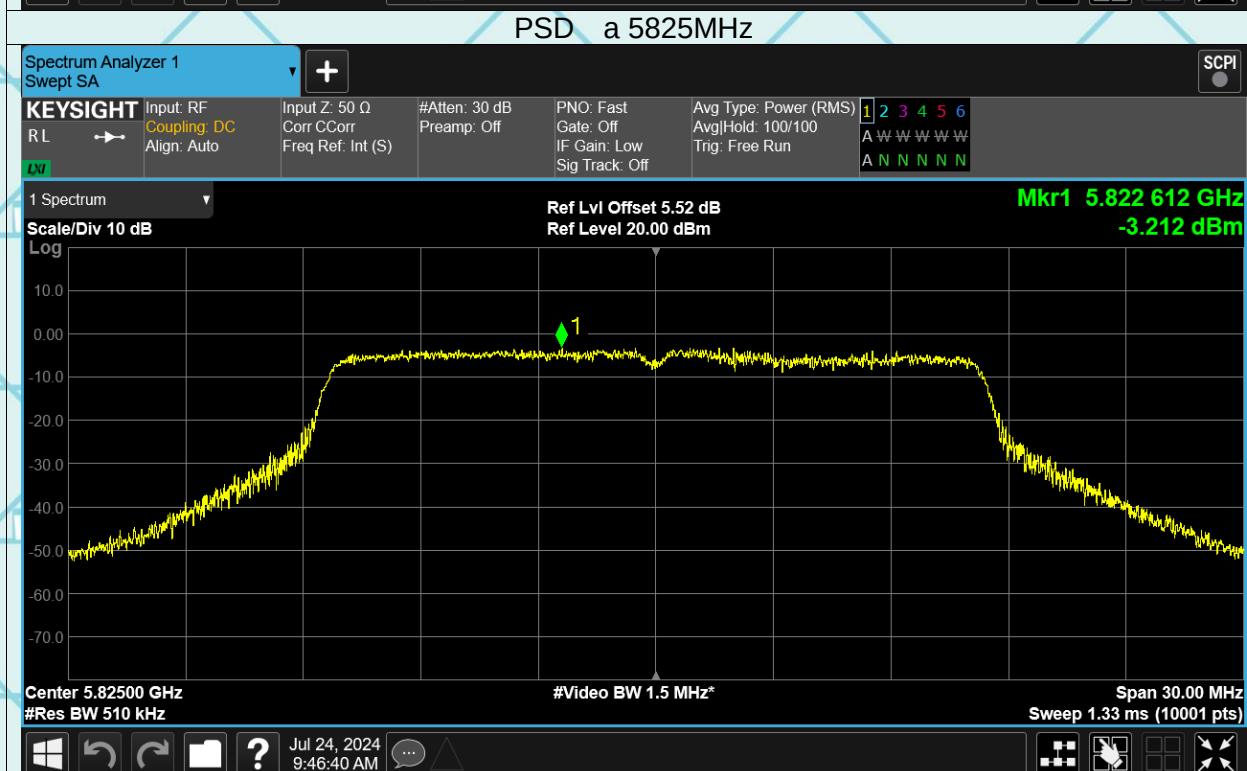
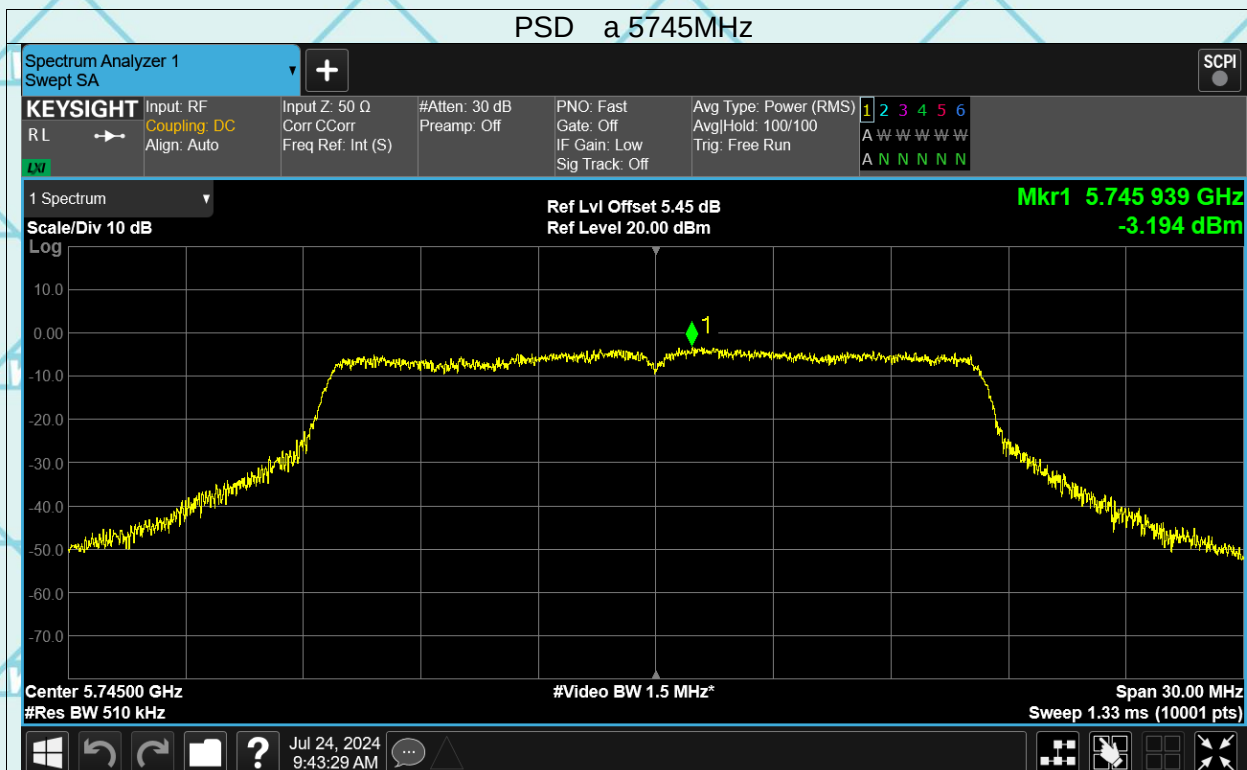
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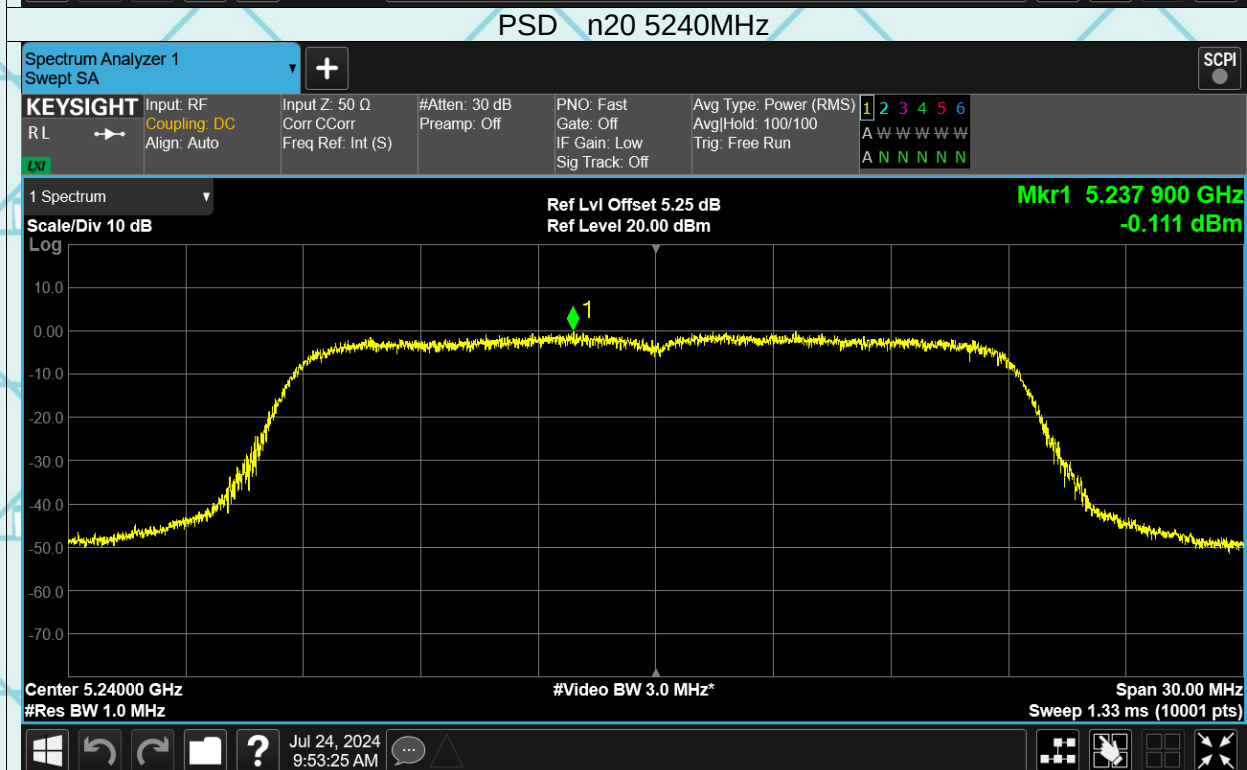
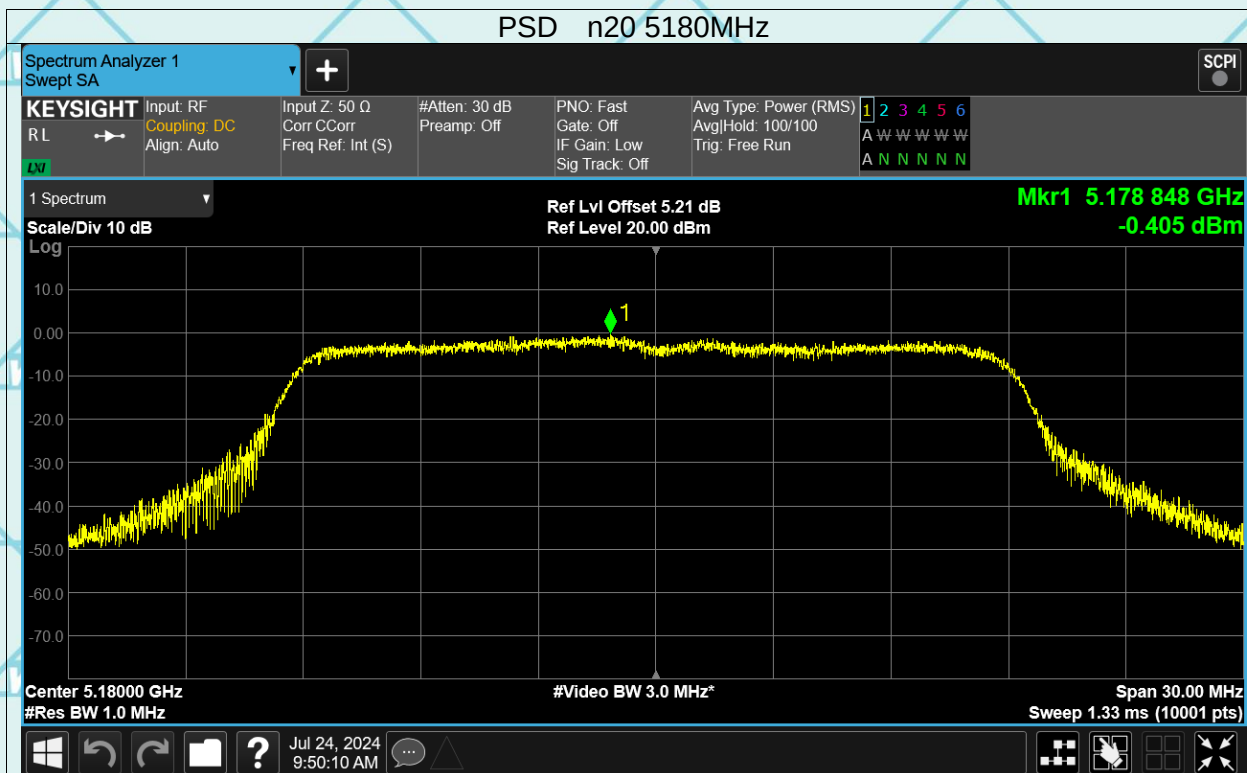
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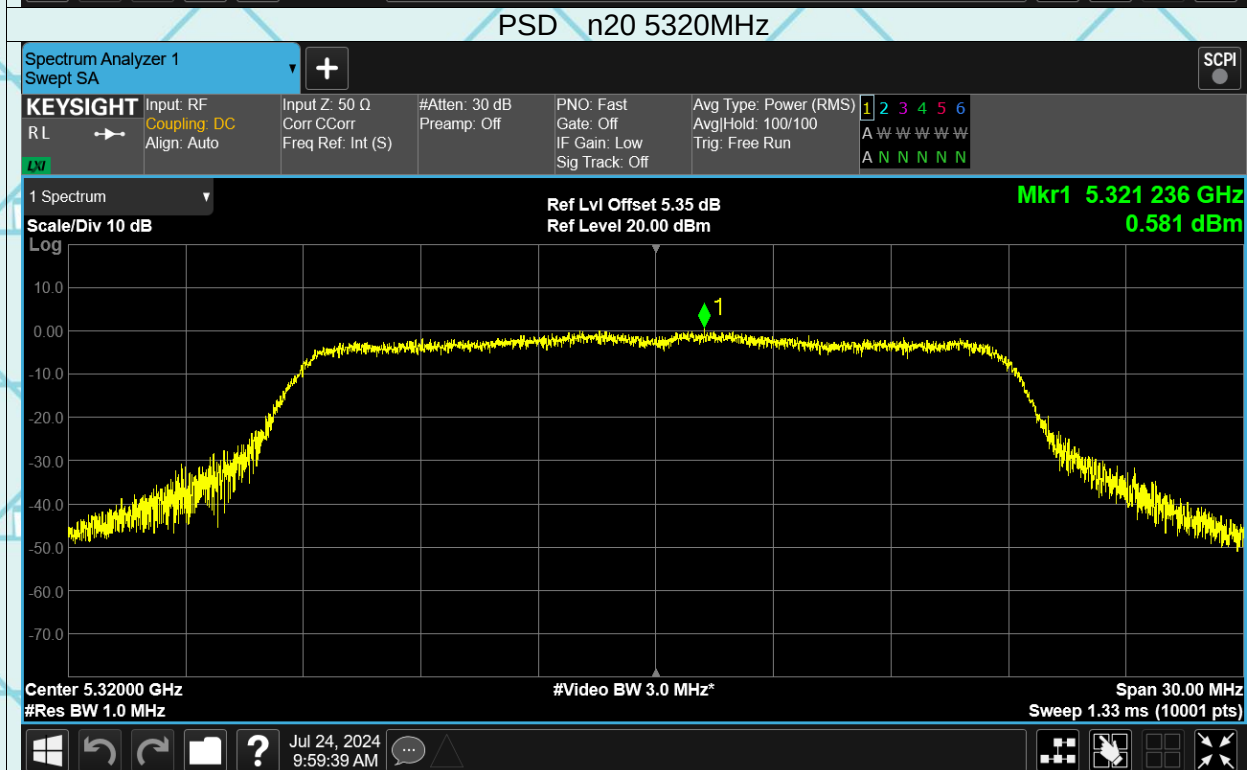
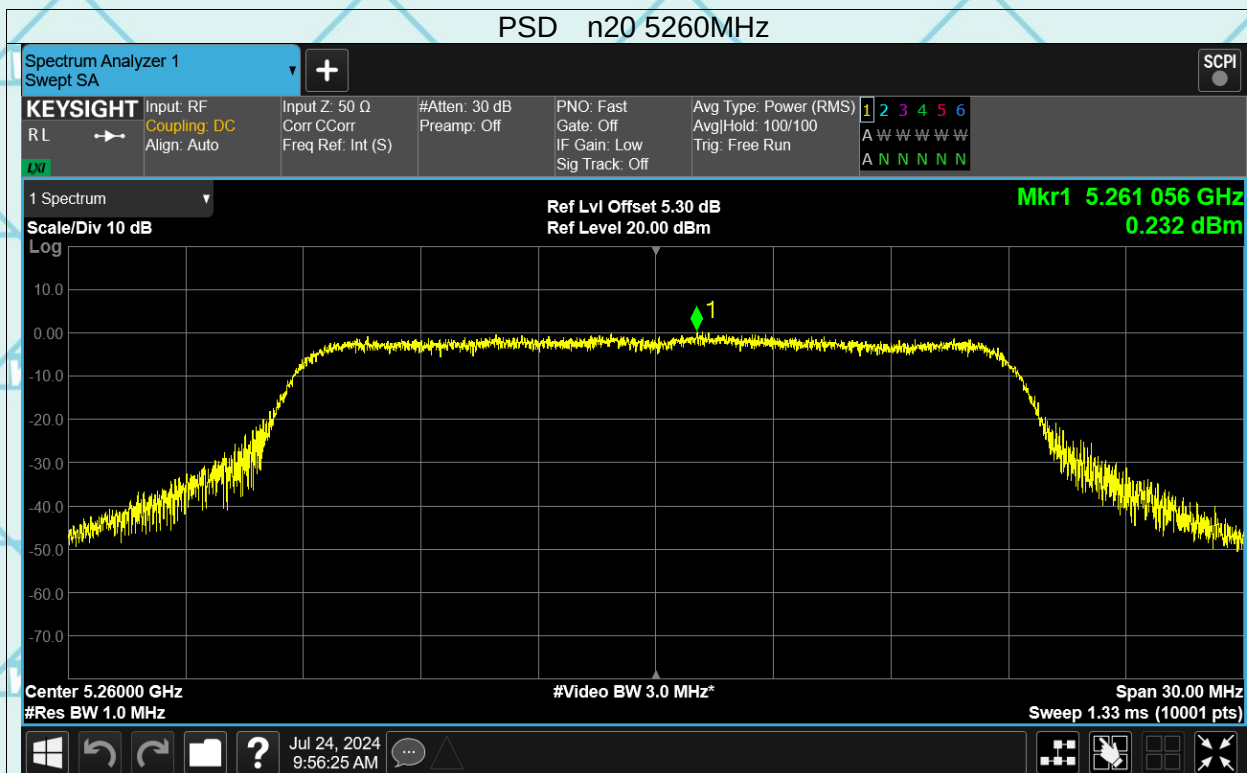
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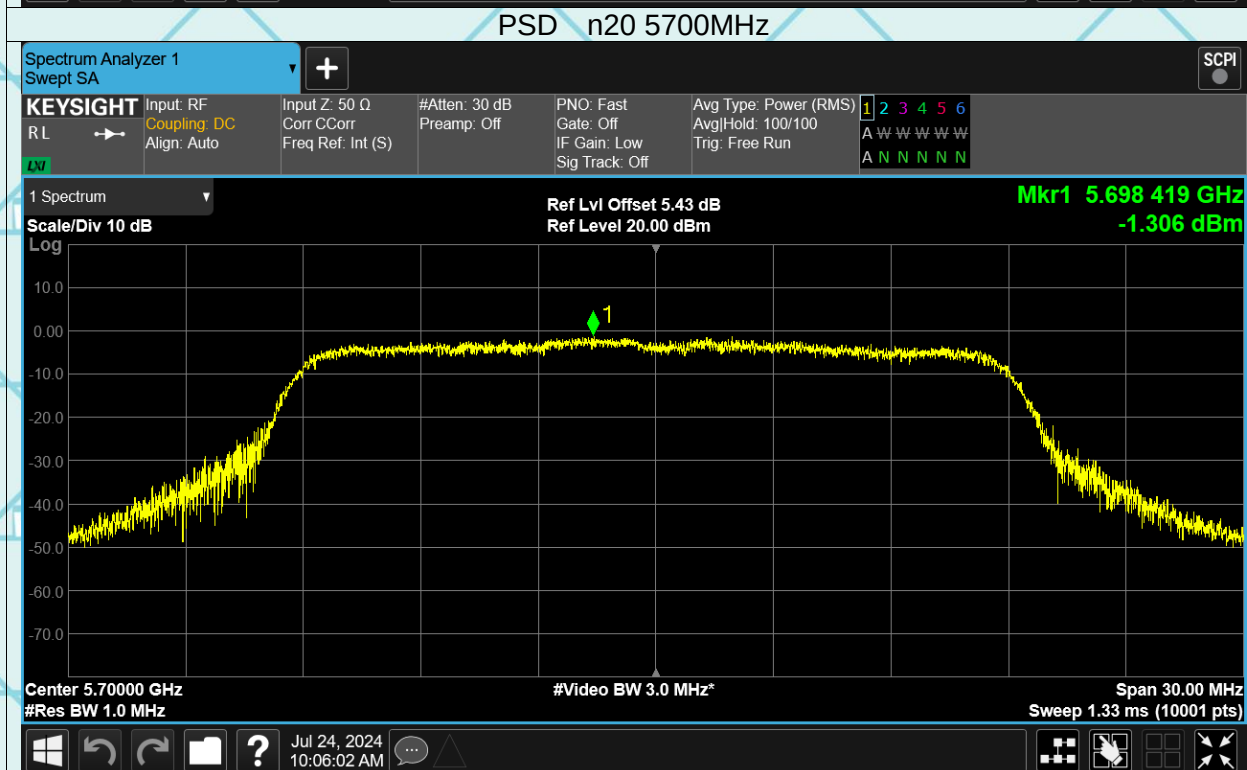
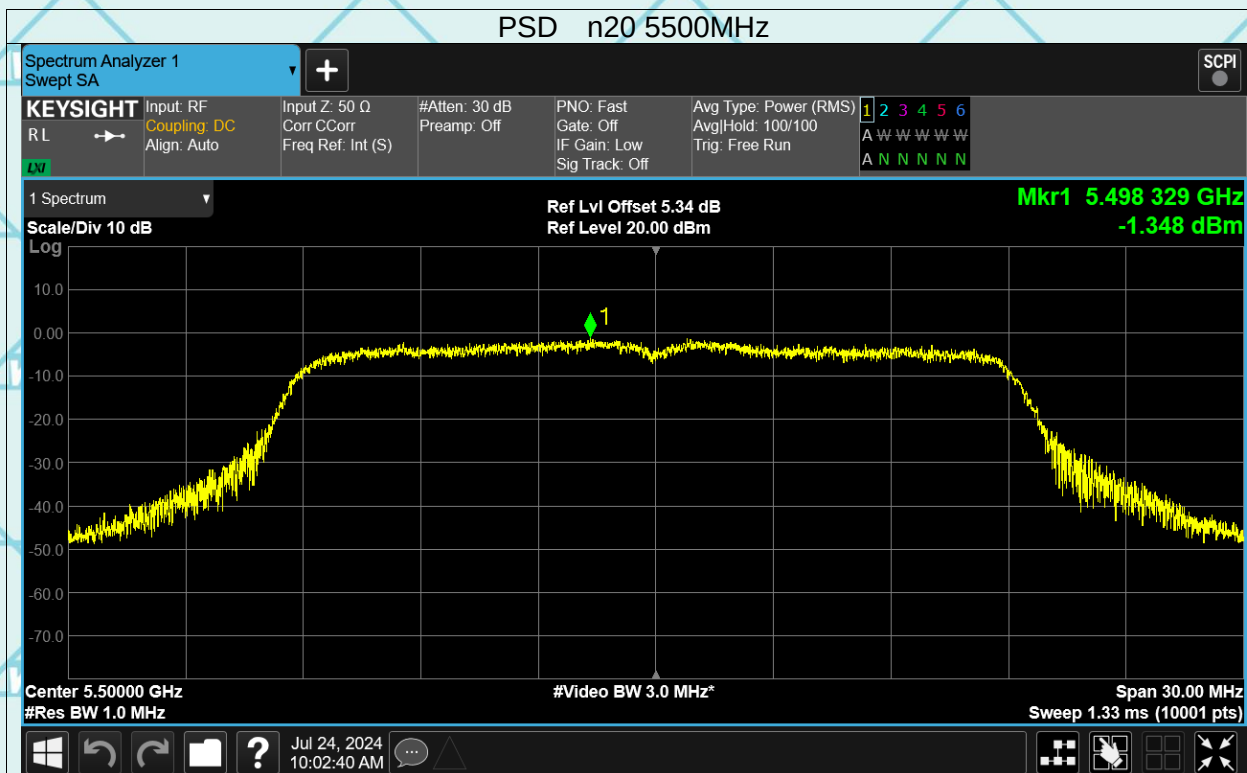
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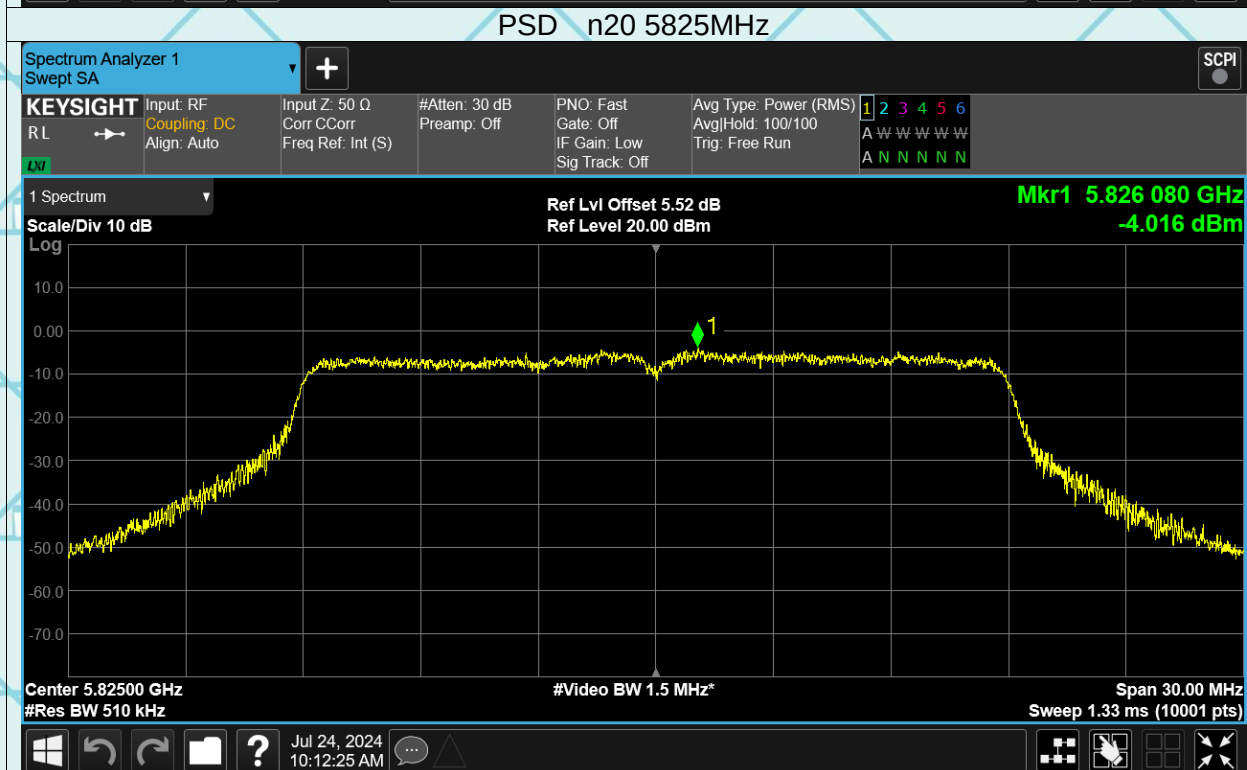
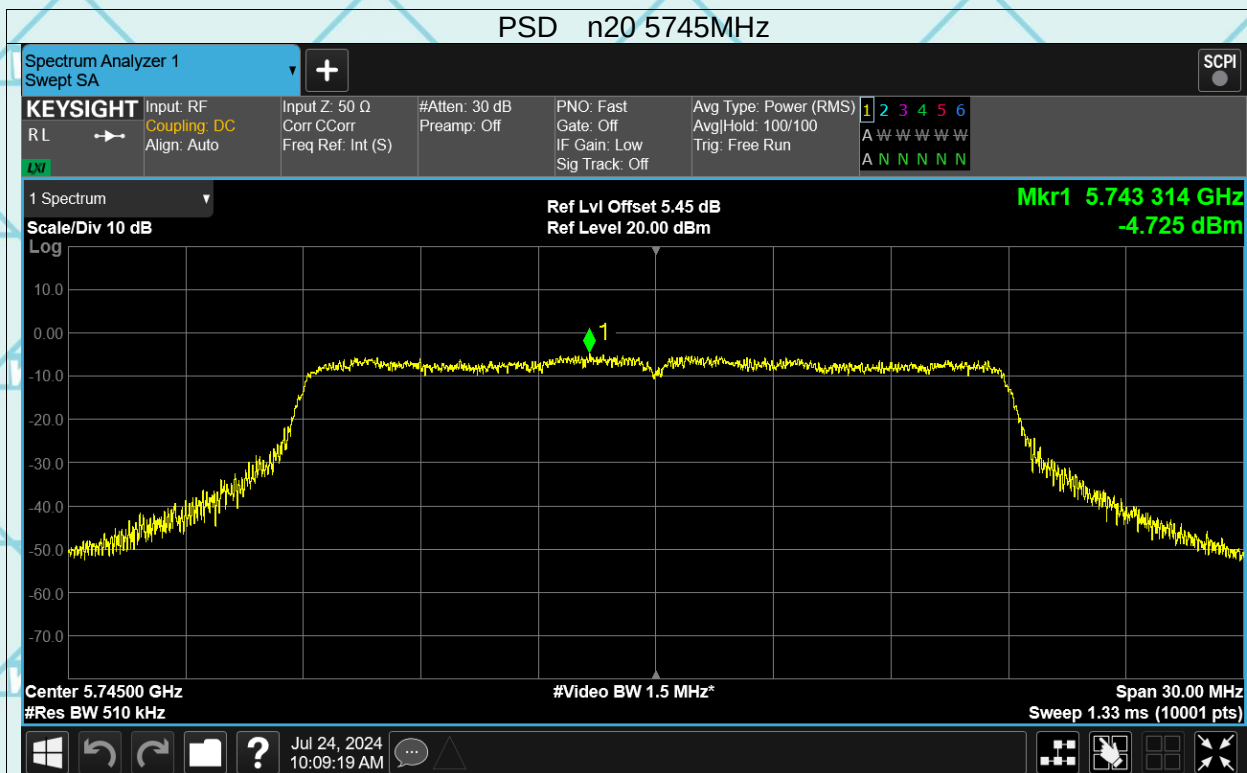
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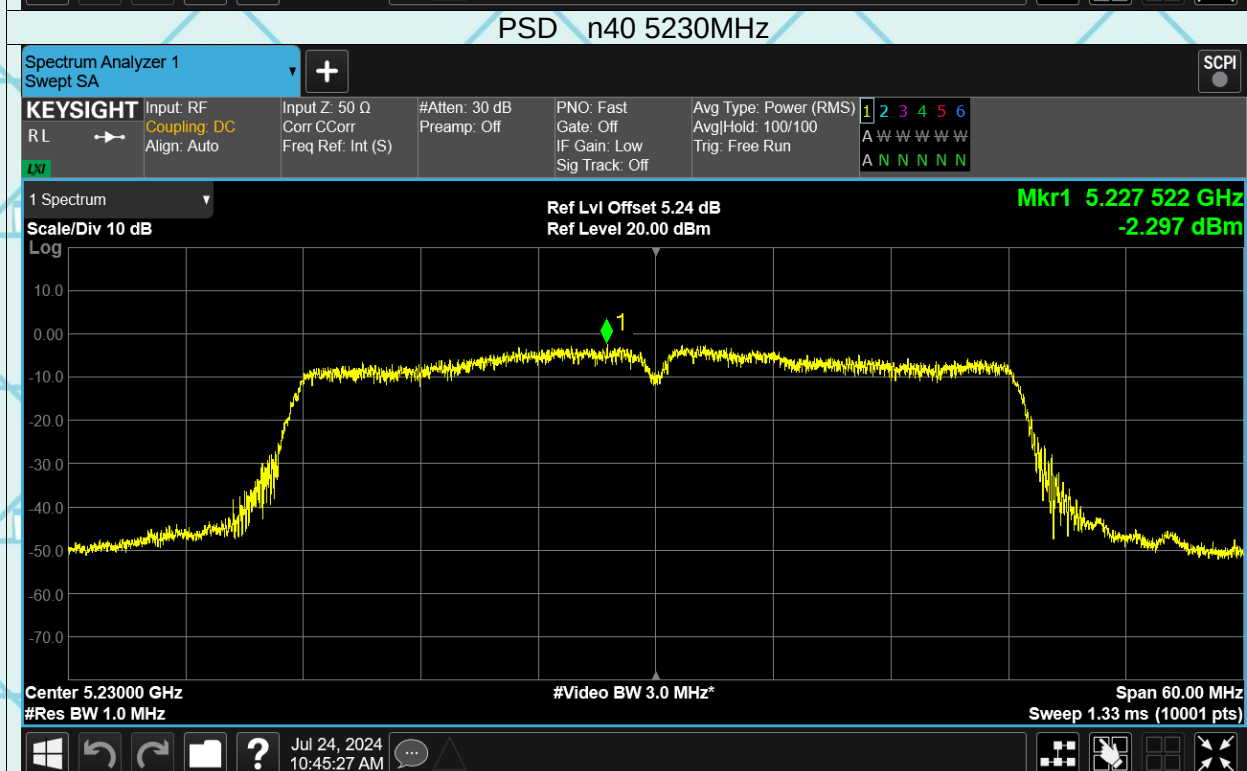
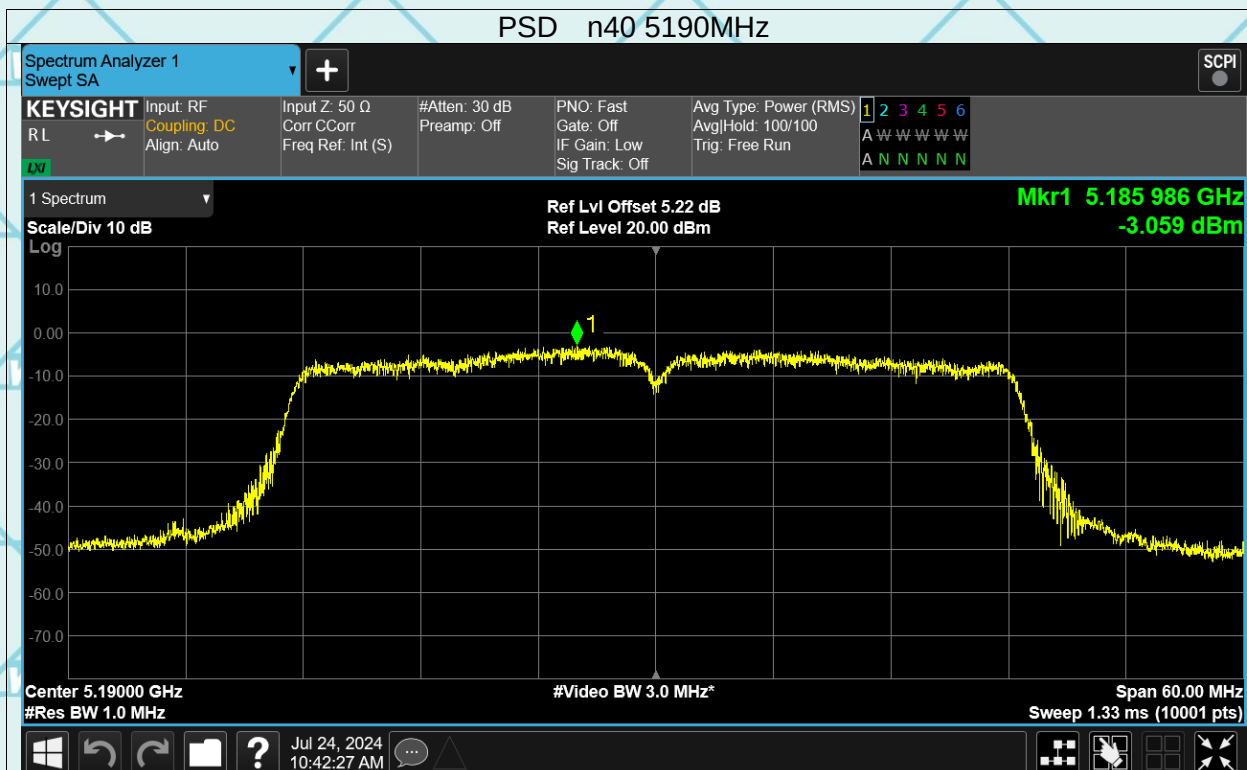
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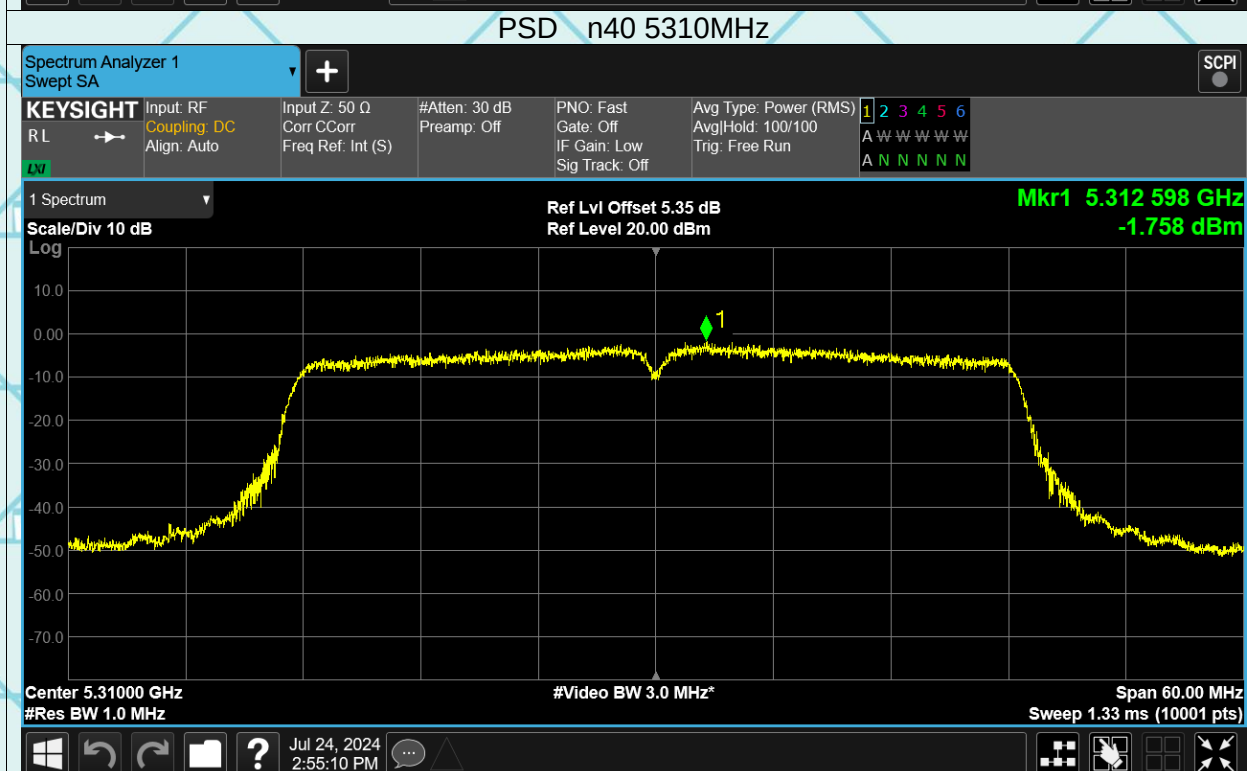
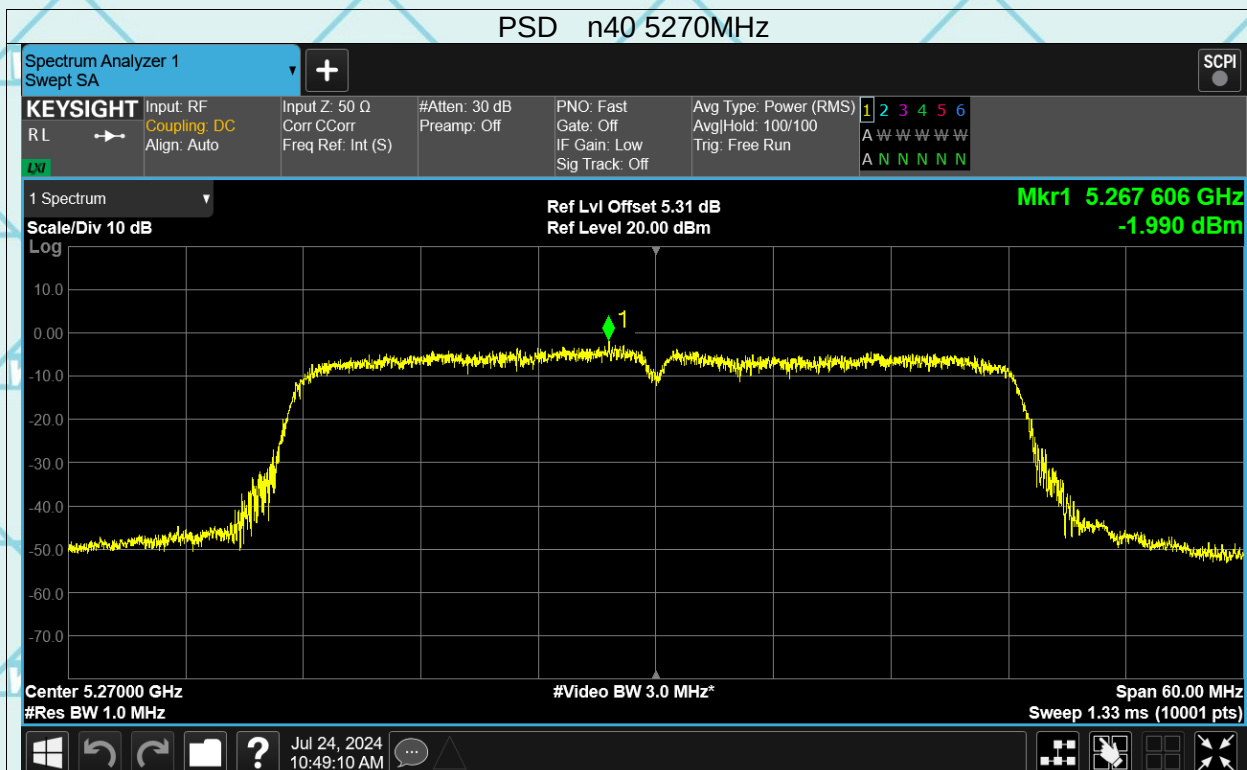
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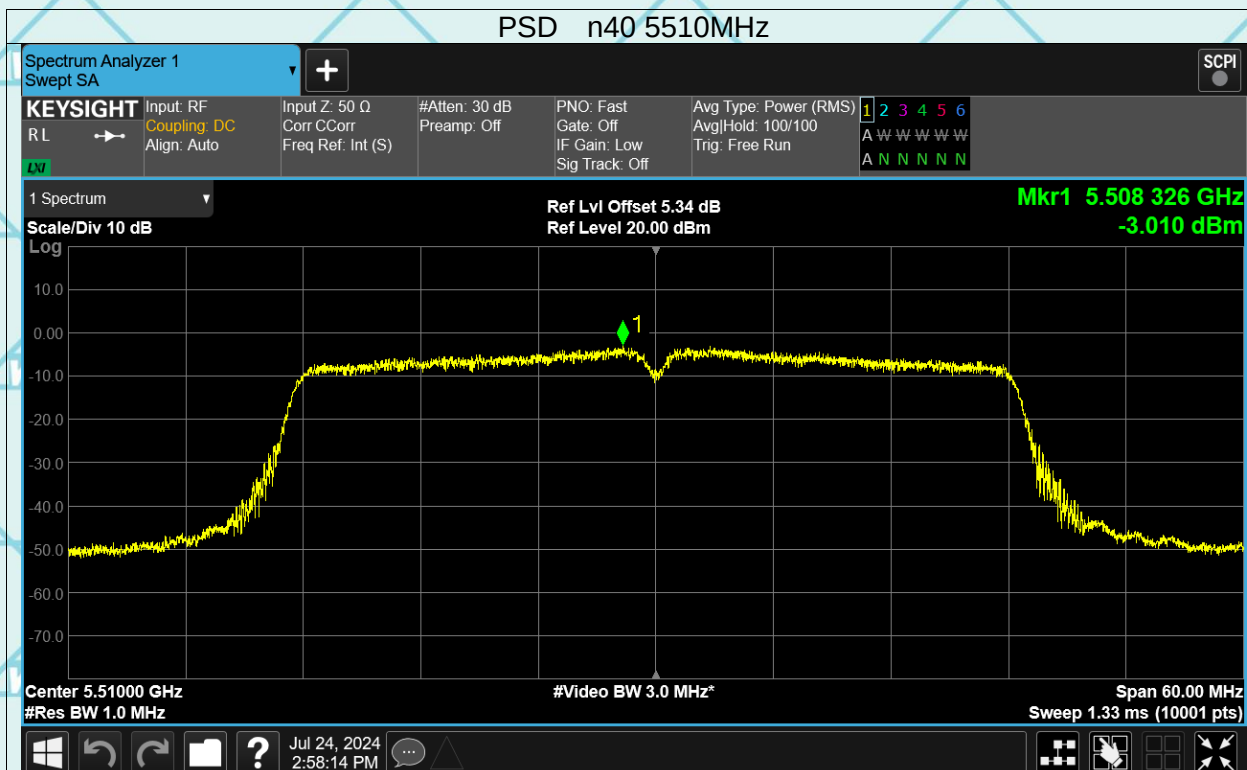
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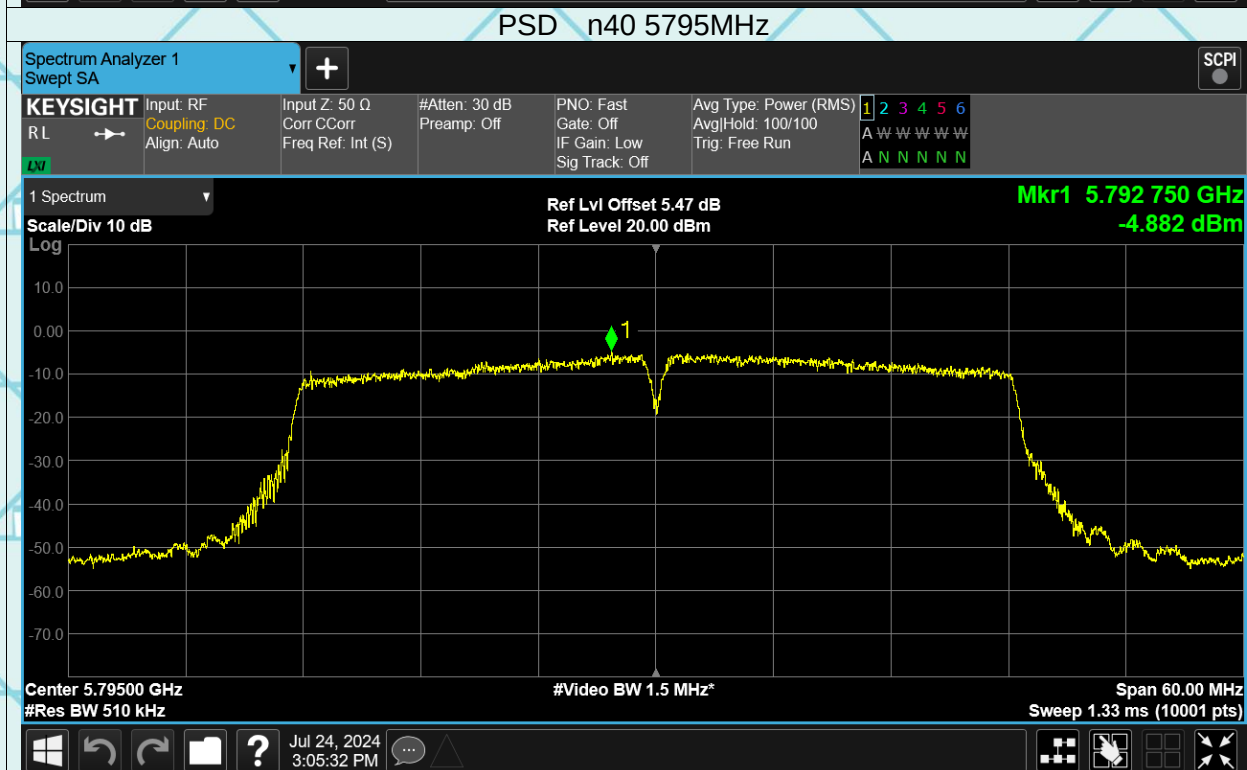
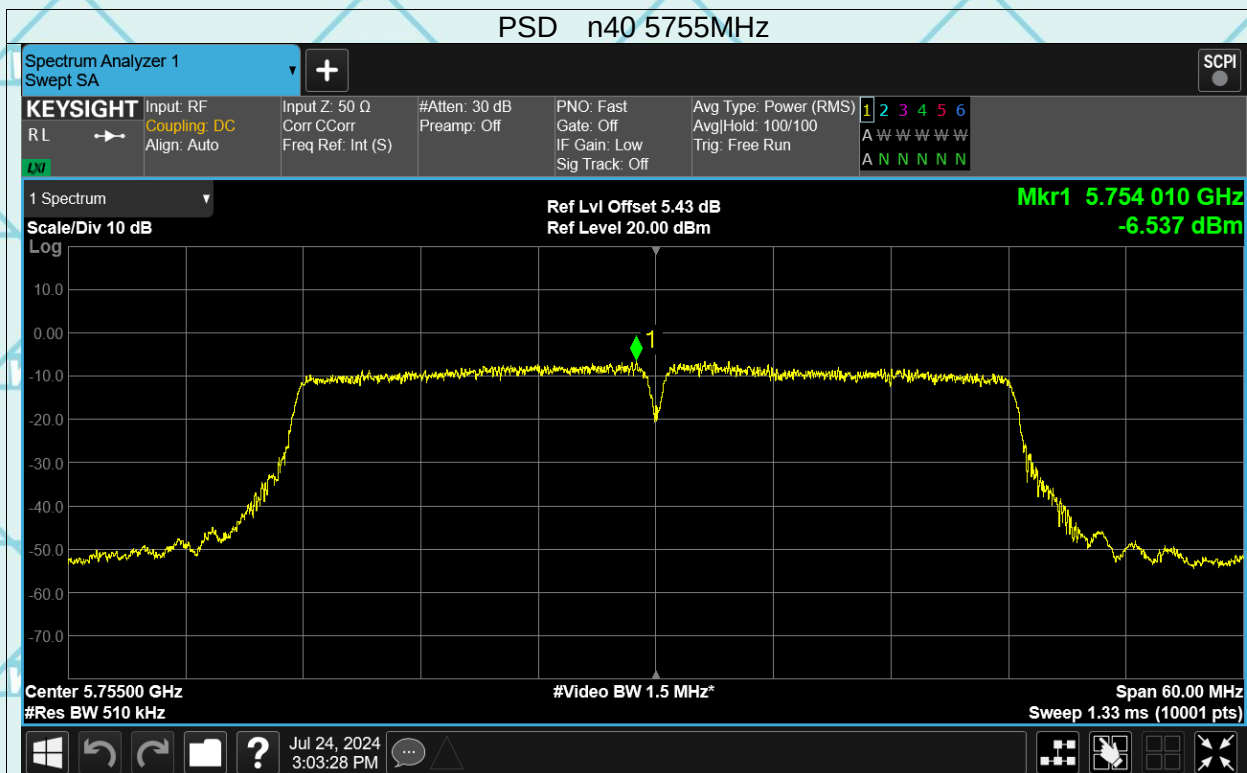
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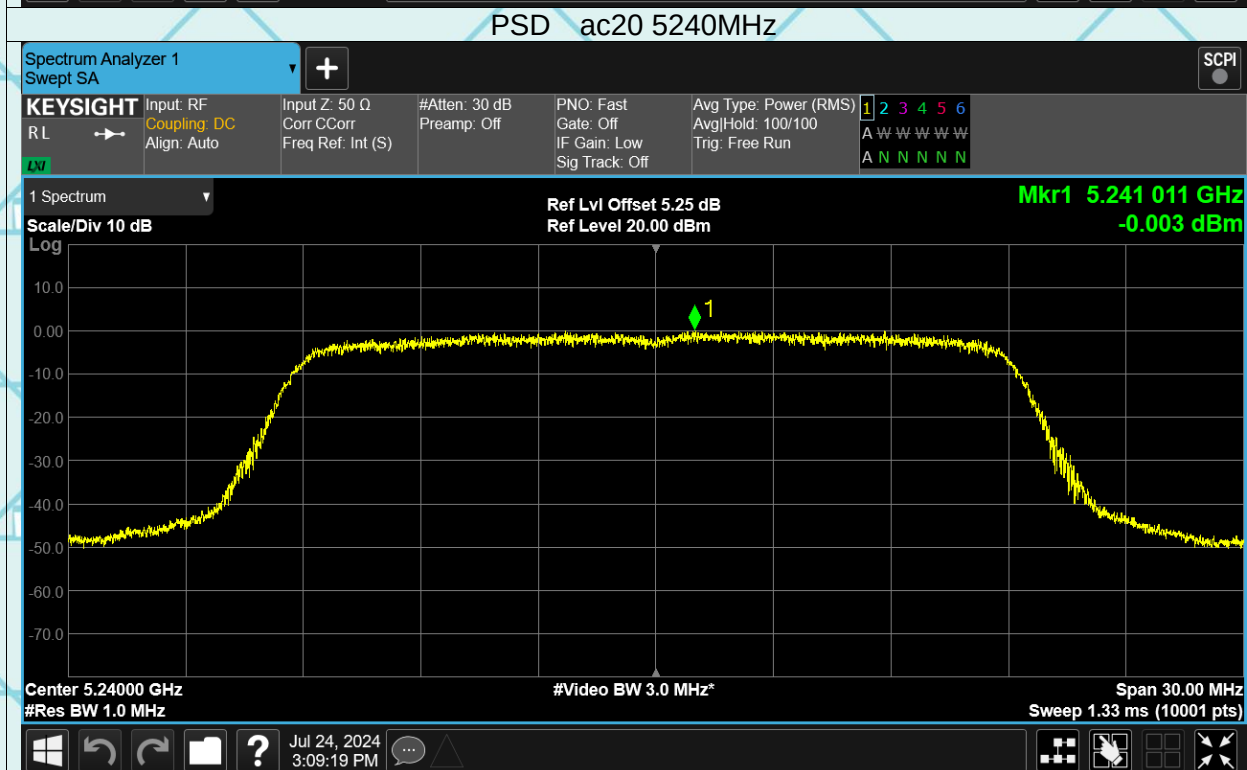
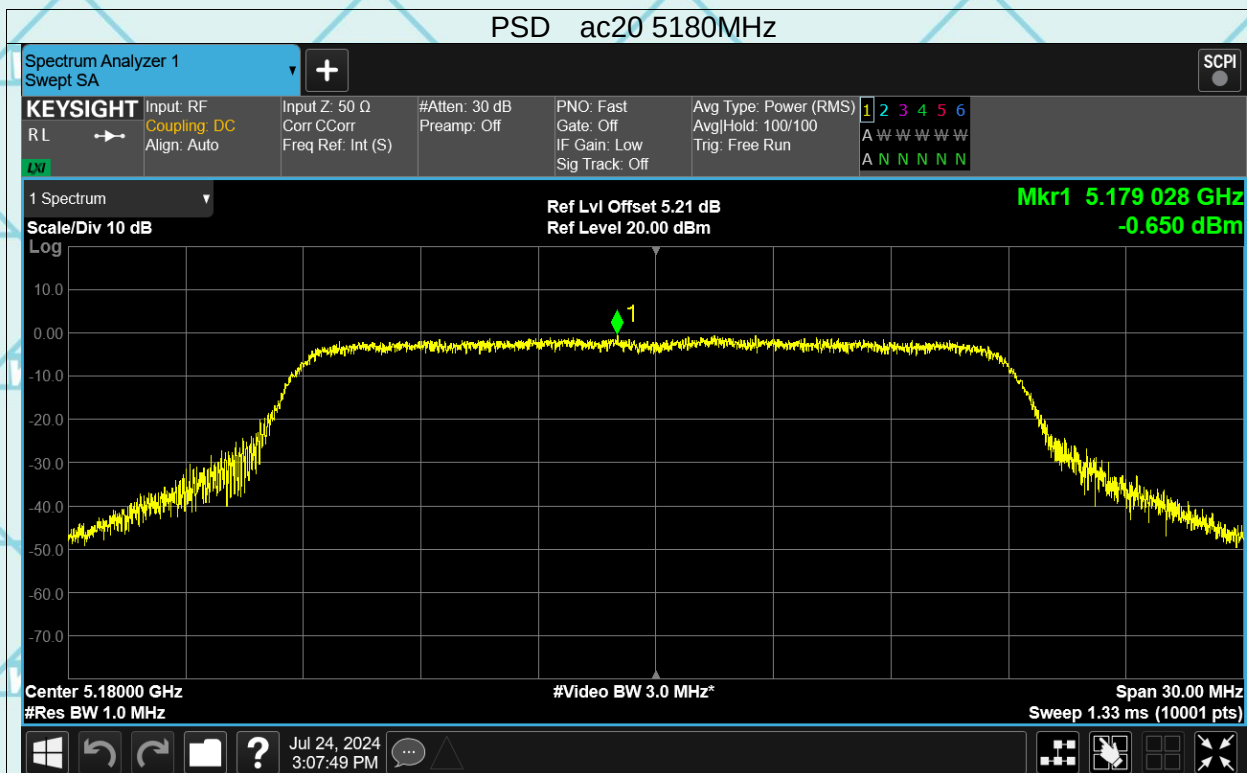
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