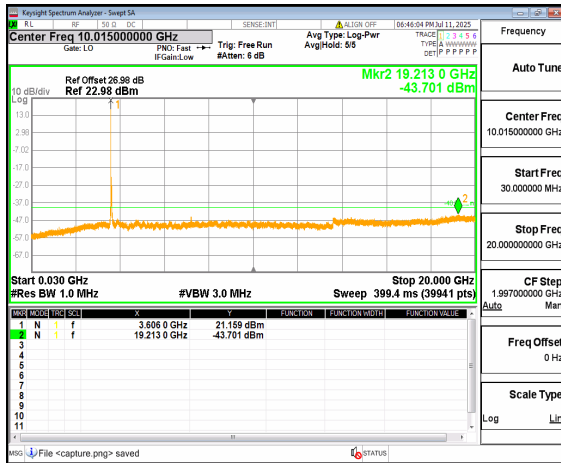
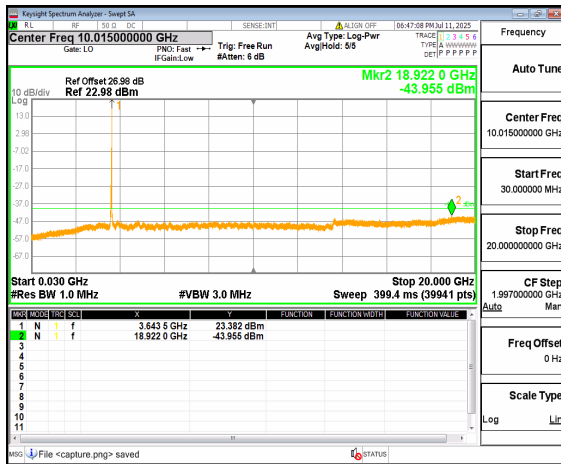




n48 (30MHz-20GHz) 40M DFT-s-OFDM
QPSK Inner_1RB_Left Mid



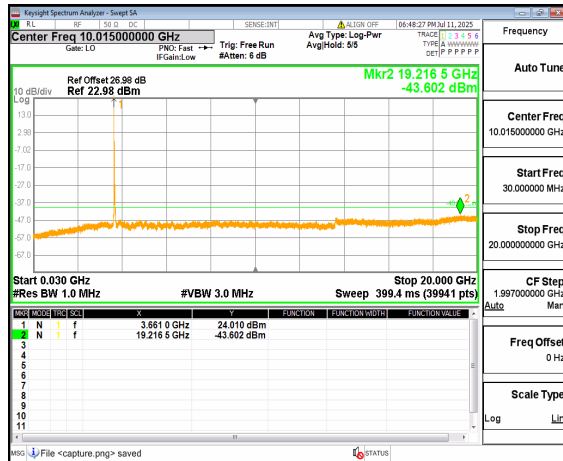
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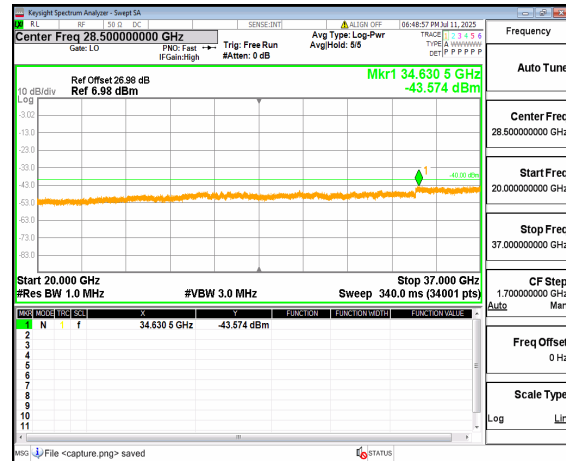
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QPSK Inner_1RB_Right Mid



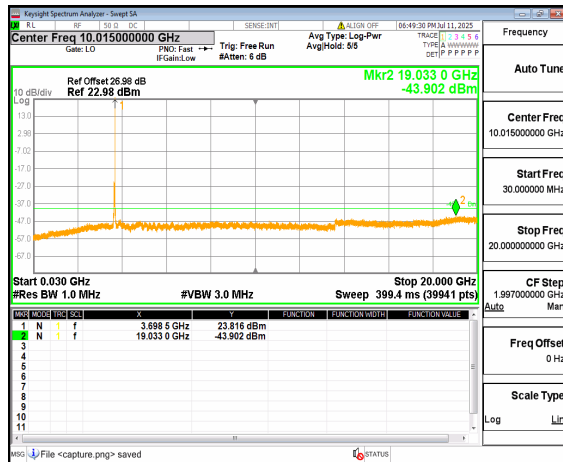
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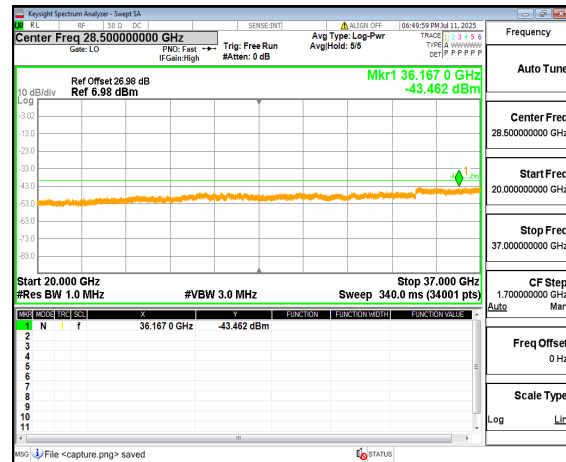
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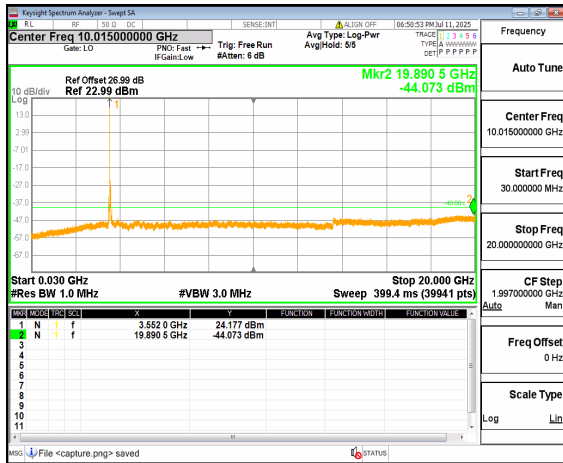
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n48 (30MHz-20GHz) 40M DFT-s-OFDM
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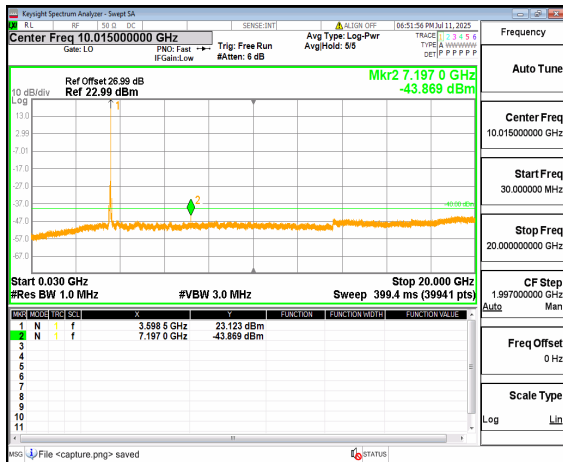
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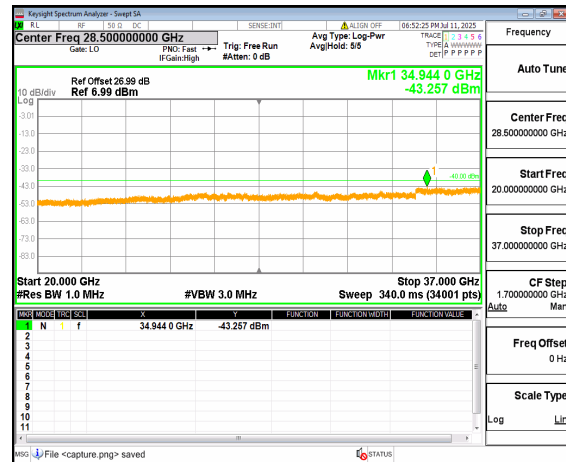
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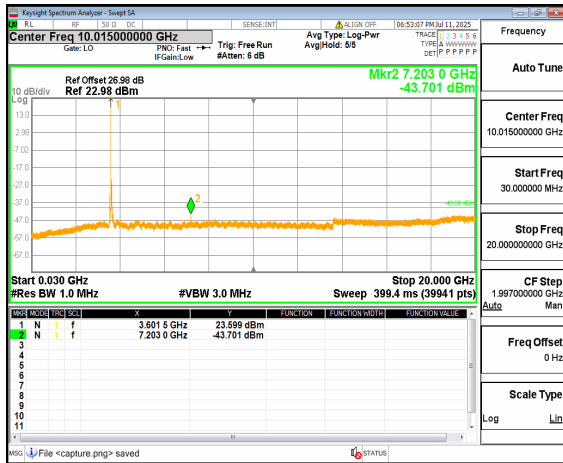
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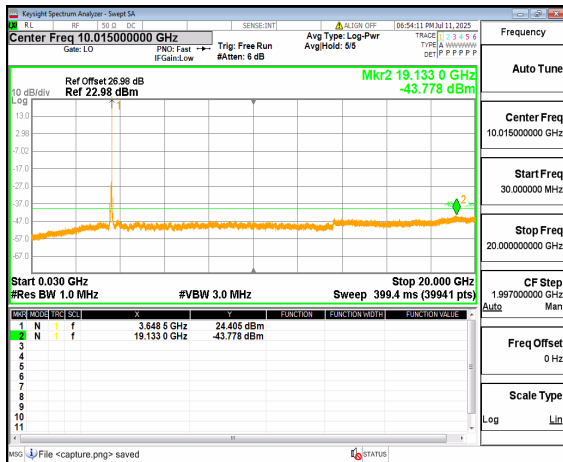
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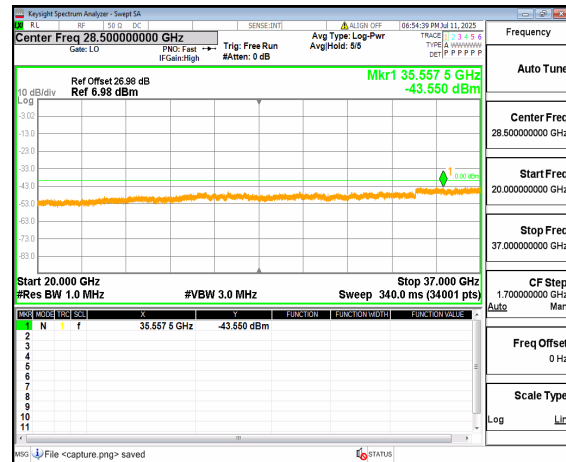
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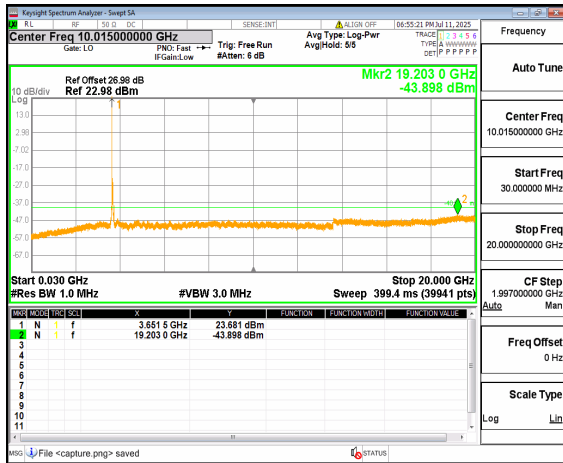
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n48 (30MHz-20GHz) 50M DFT-s-OFDM
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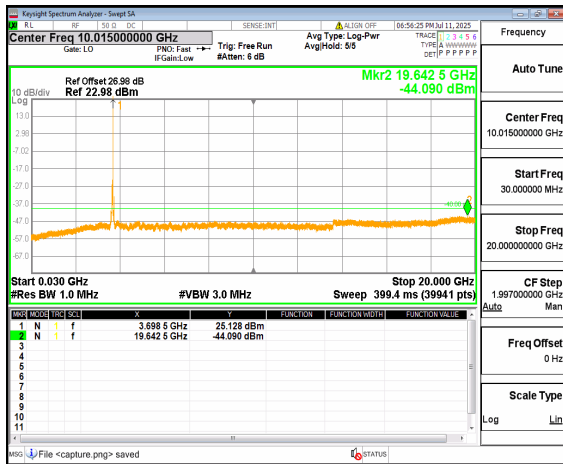
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n48 (30MHz-20GHz) 50M DFT-s-OFDM
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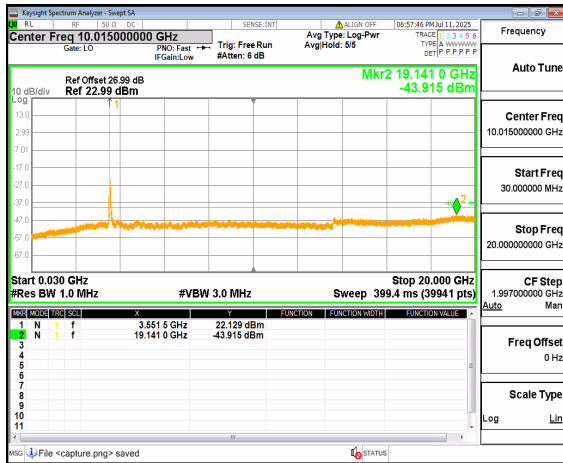
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QPSK Inner_1RB_Left High



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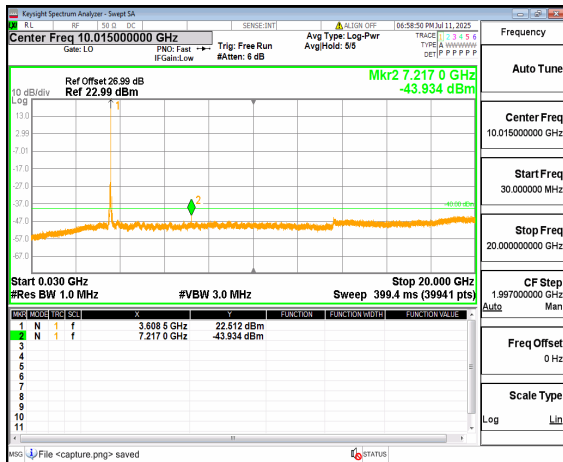
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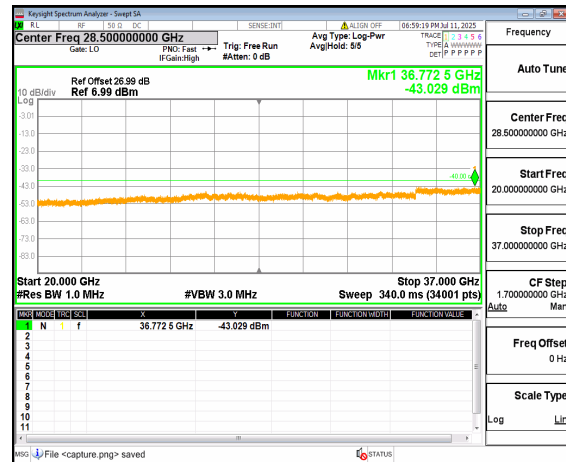
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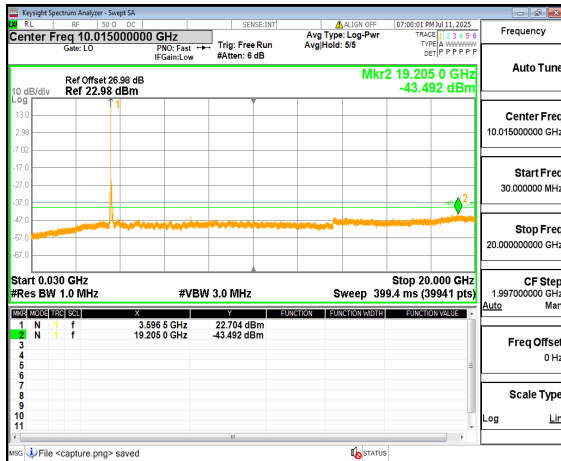
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n48 (30MHz-20GHz) 60M DFT-s-OFDM
QPSK Inner_1RB_Right Low



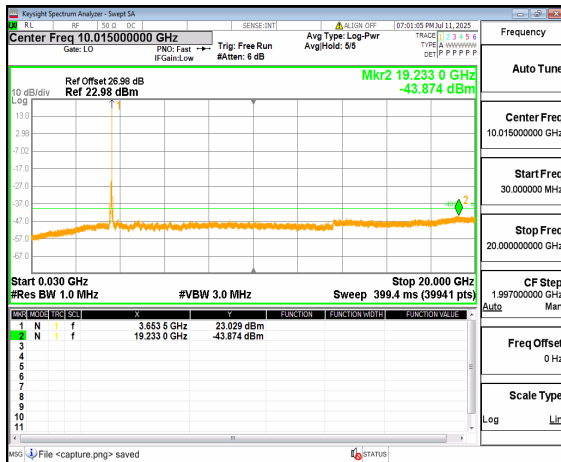
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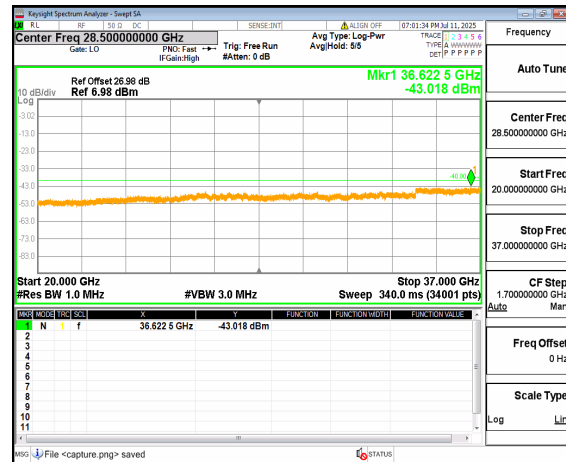
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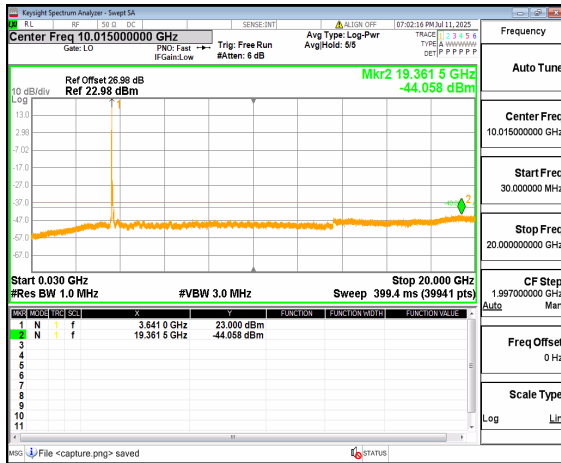
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QPSK Inner_1RB_Left Mid



n48 (30MHz-20GHz) 60M DFT-s-OFDM
QPSK Inner_1RB_Right Mid



n48 (20GHz-37GHz) 60M DFT-s-OFDM
QPSK Inner_1RB_Right Mid



n48 (30MHz-20GHz) 60M DFT-s-OFDM
QPSK Inner_1RB_Left High



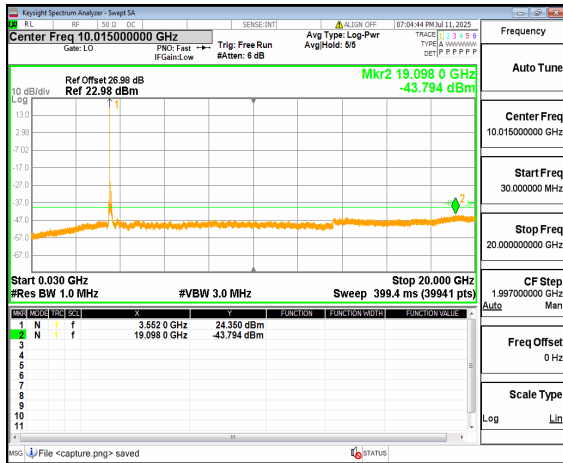
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n48 (30MHz-20GHz) 60M DFT-s-OFDM
QPSK Inner_1RB_Right High



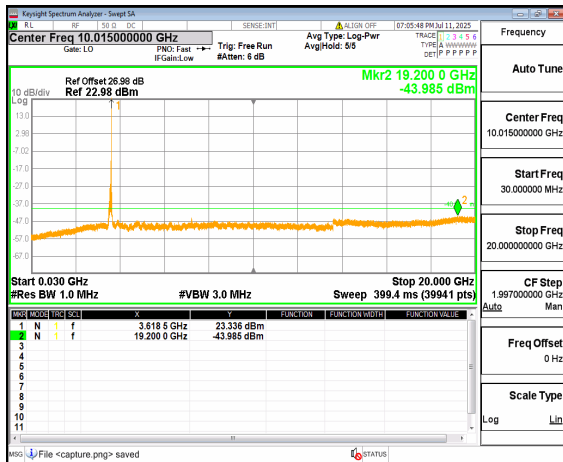
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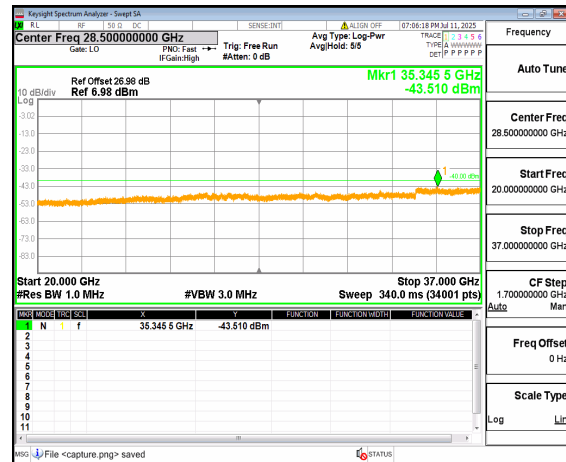
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QPSK Inner_1RB_Left Low



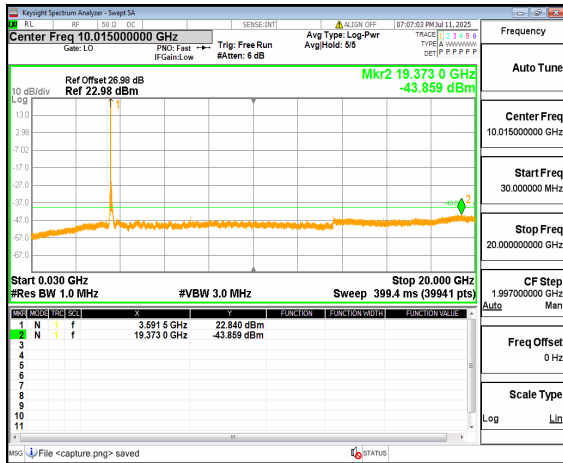
n48 (20GHz-37GHz) 70M DFT-s-OFDM
QPSK Inner_1RB_Left Low



n48 (30MHz-20GHz) 70M DFT-s-OFDM
QPSK Inner_1RB_Right Low



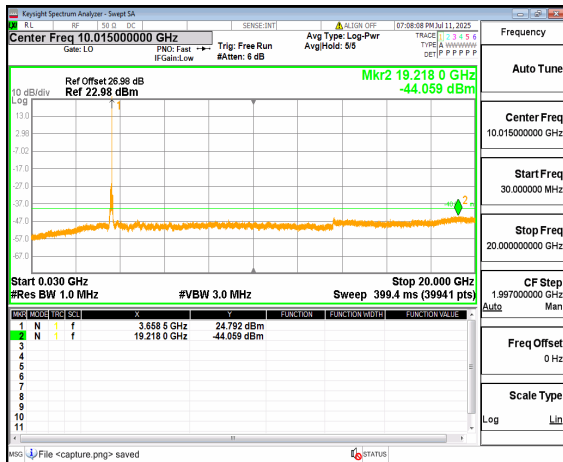
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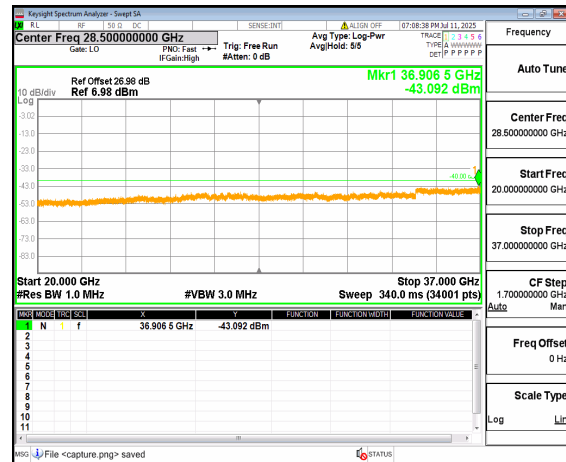
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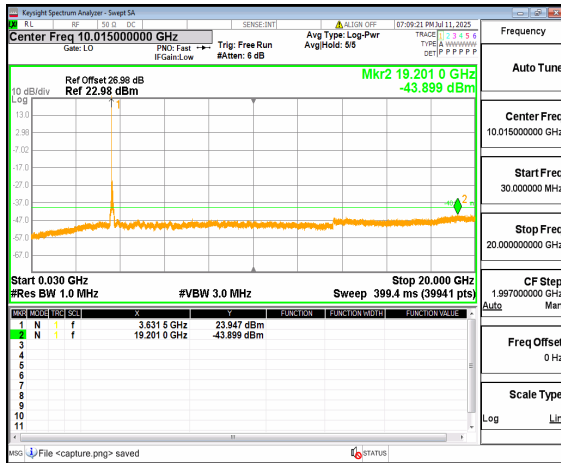
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QPSK Inner_1RB_Right Mid



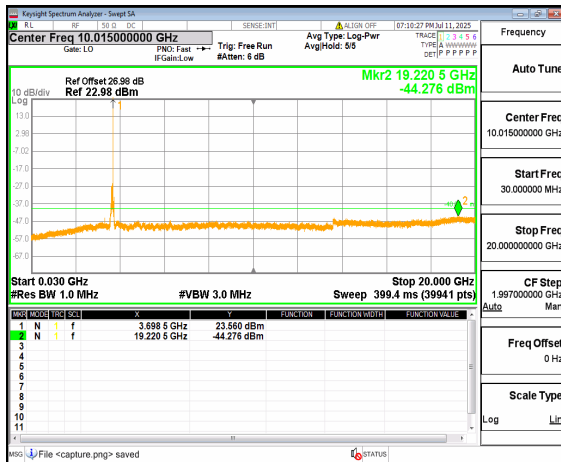
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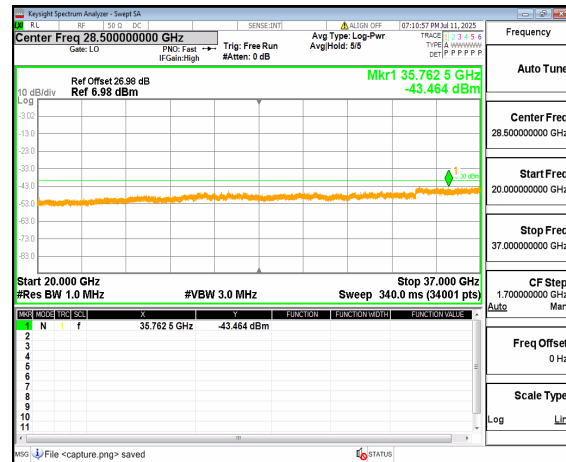
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QPSK Inner_1RB_Left High



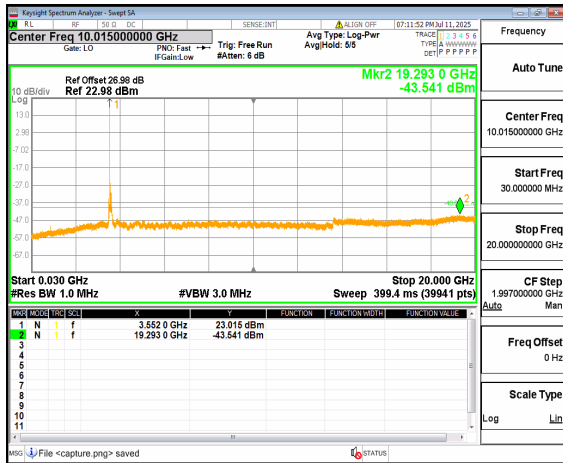
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n48 (30MHz-20GHz) 70M DFT-s-OFDM
QPSK Inner_1RB_Right High



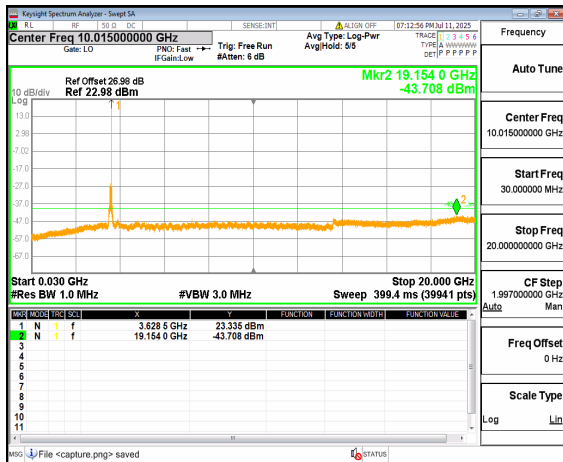
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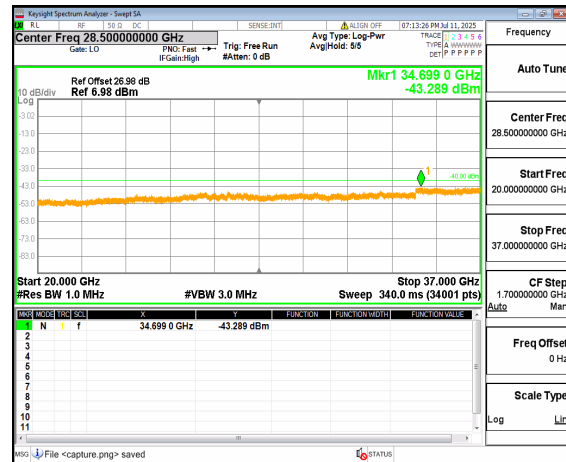
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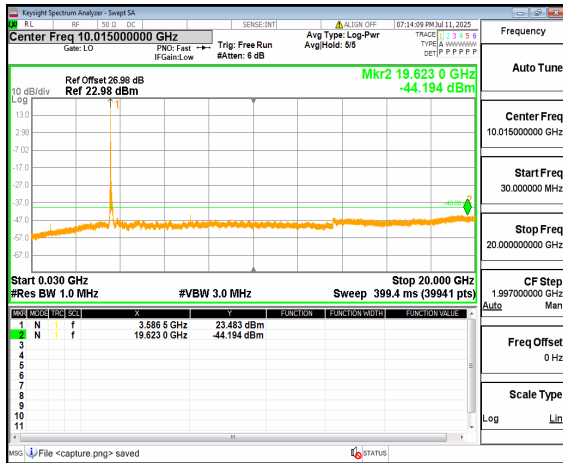
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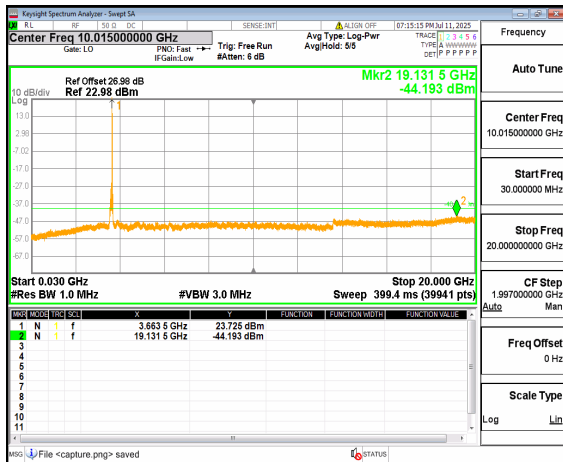
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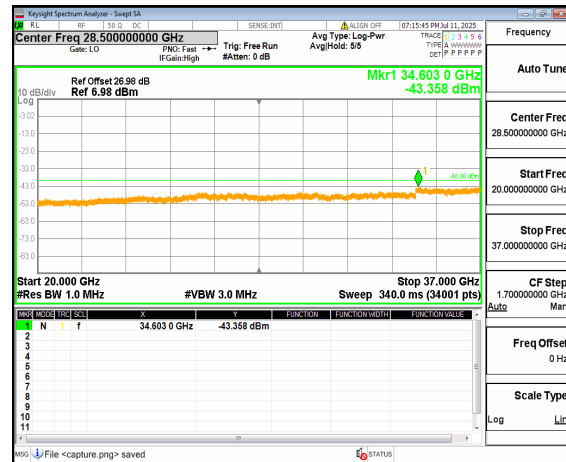
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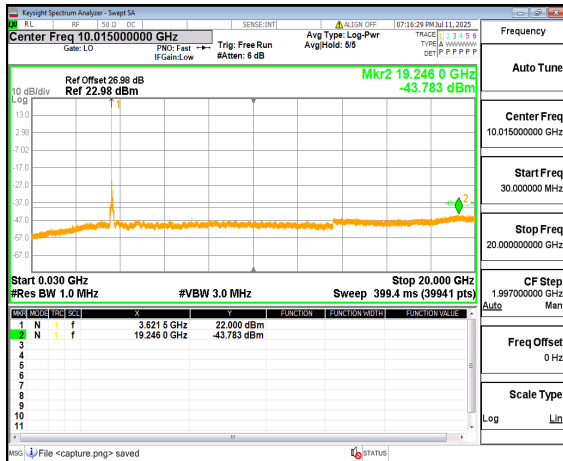
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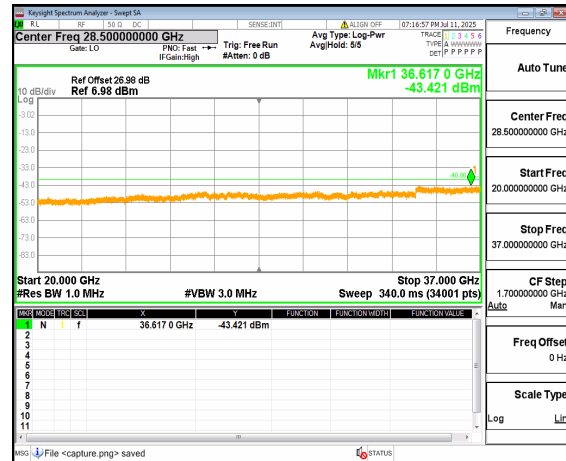
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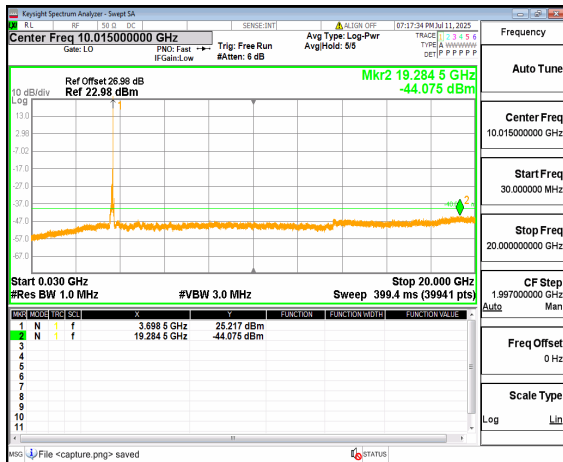
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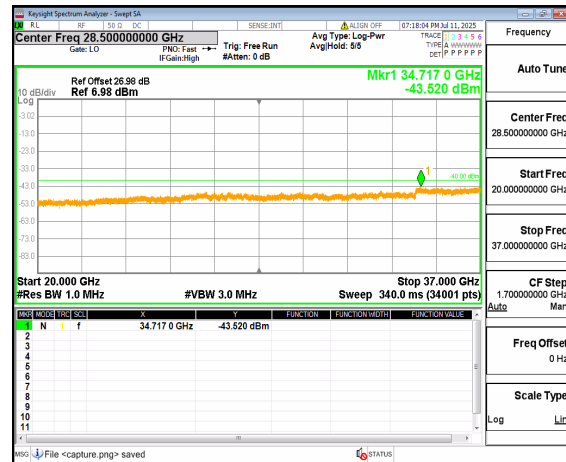
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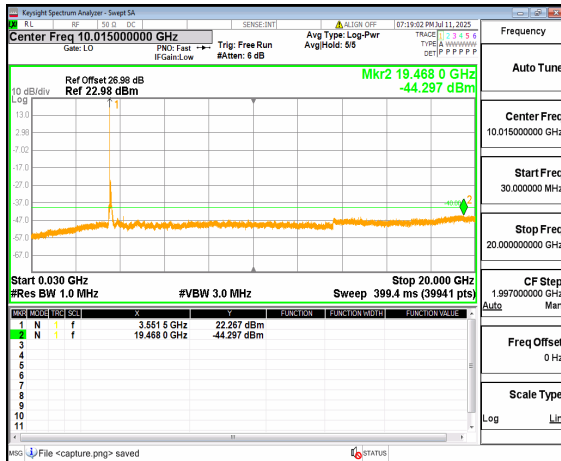
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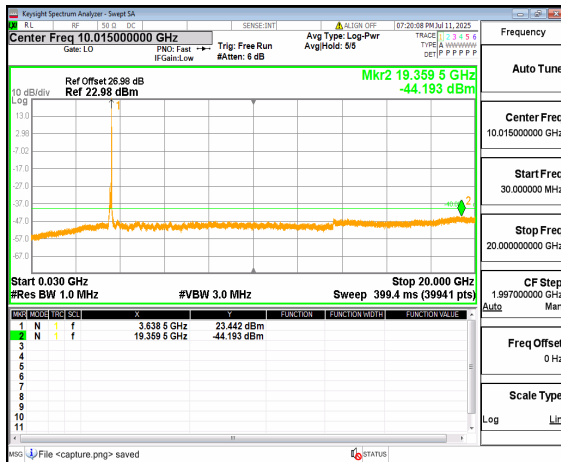
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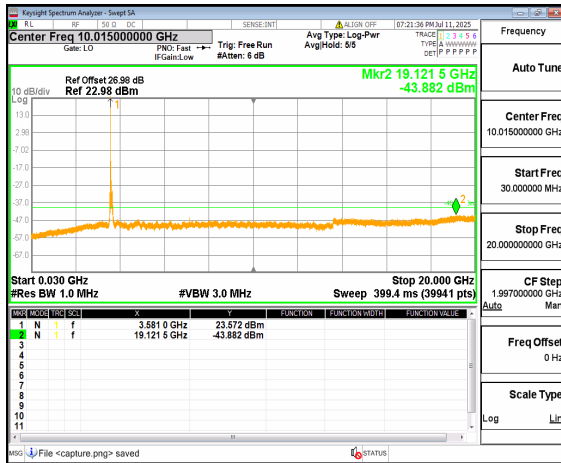
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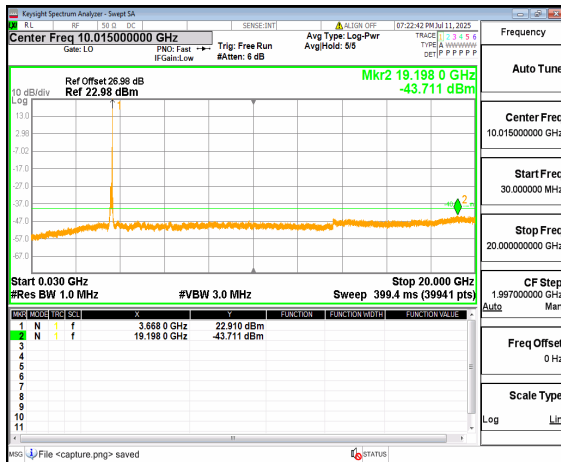
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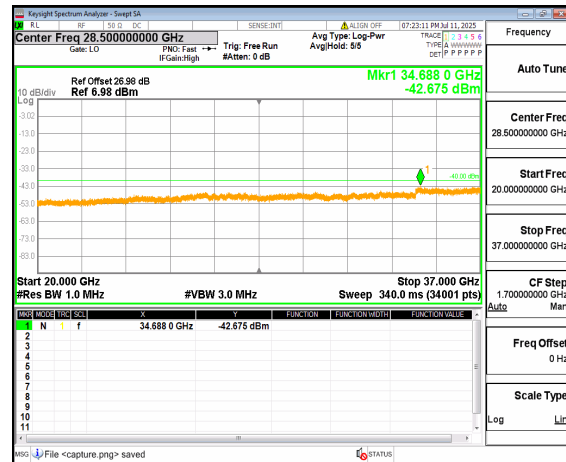
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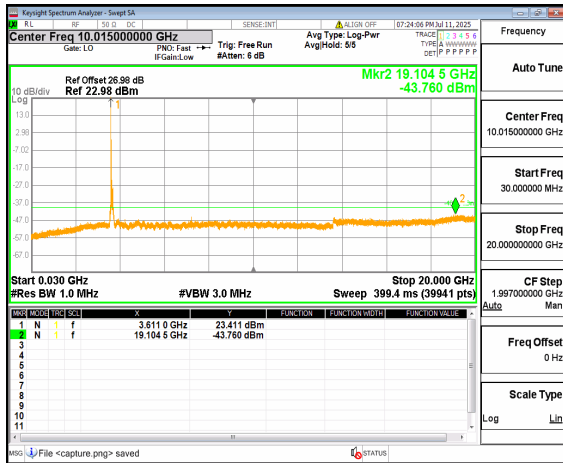
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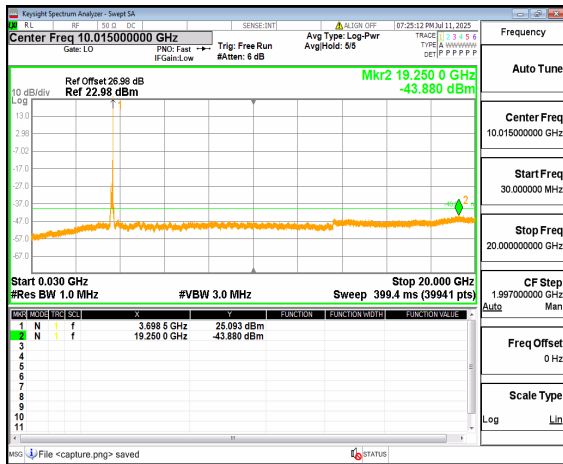
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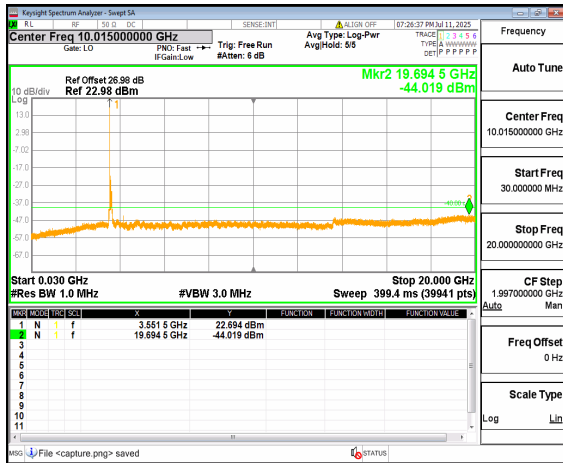
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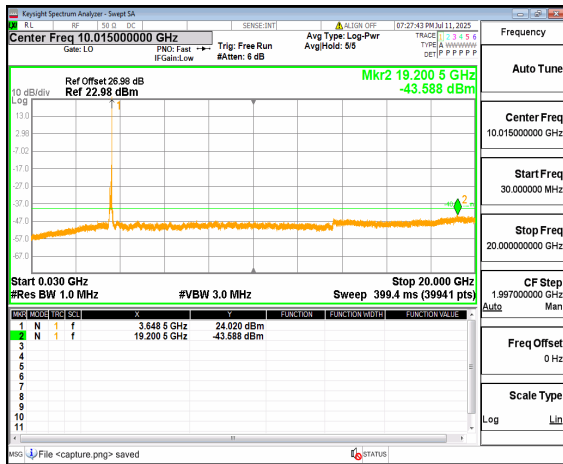
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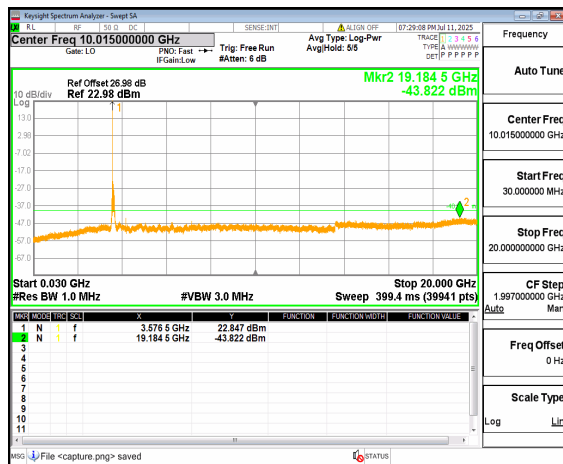
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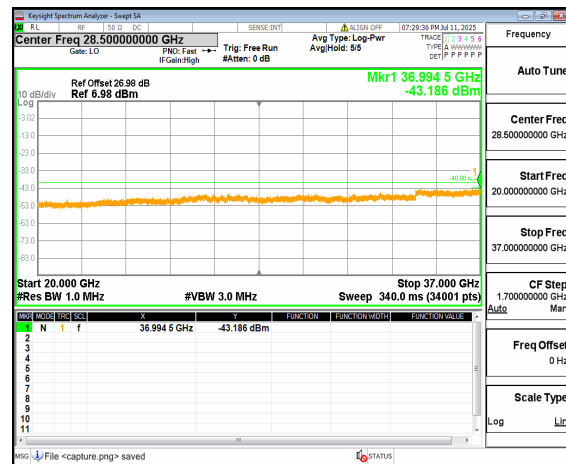
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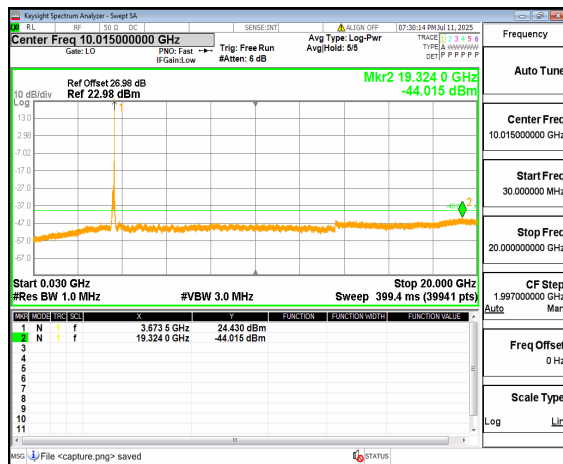
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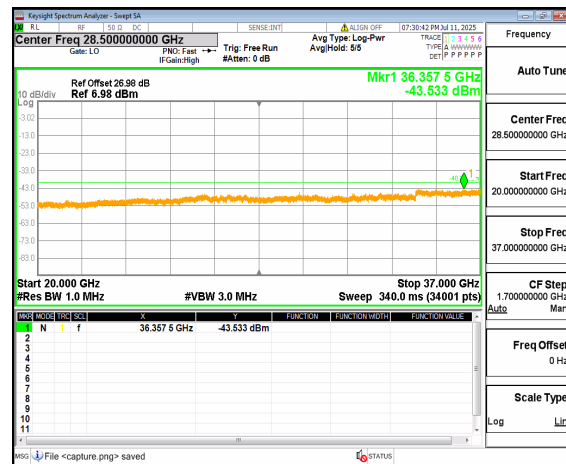
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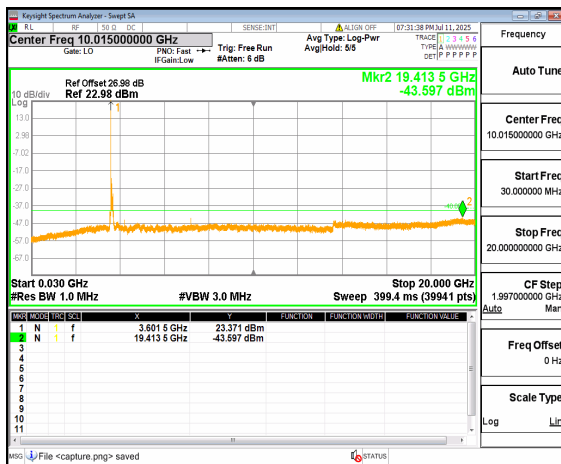
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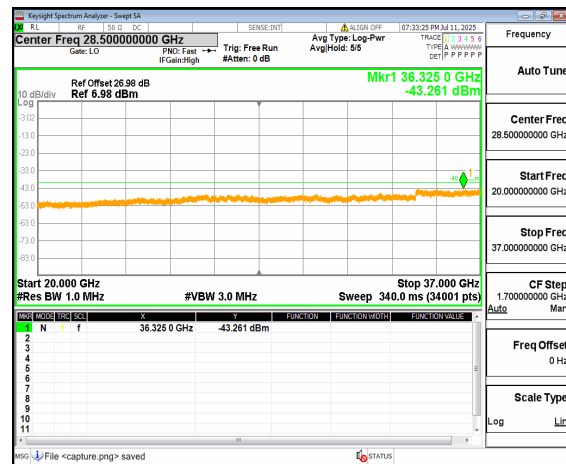
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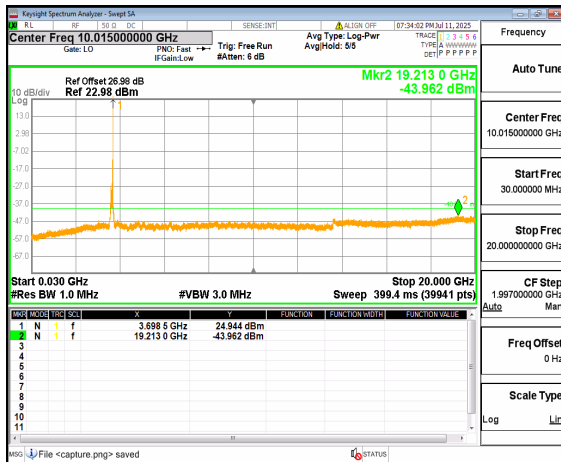
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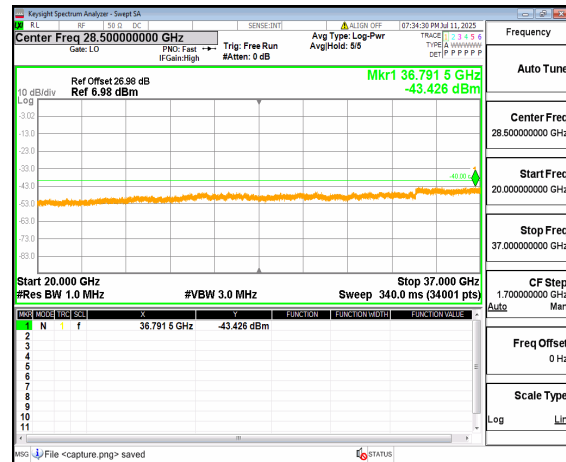
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QPSK Inner_1RB_Left High



n48 (20GHz-37GHz) 100M DFT-s-OFDM
QPSK Inner_1RB_Left High



n48 (30MHz-20GHz) 100M DFT-s-OFDM
QPSK Inner_1RB_Right High



n48 (20GHz-37GHz) 100M DFT-s-OFDM
QPSK Inner_1RB_Right High

2.6. Band Edge

2.6.1. Requirement

Part 96.41(e)(1)(i)

For channel and frequency assignments made by the SAS to CBSDs, the conducted power of any CBSD emission outside the fundamental emission bandwidth as specified in paragraph (e)(3) of this section (whether the emission is inside or outside of the authorized band) shall not exceed -13 dBm/MHz within 0-10 megahertz above the upper SAS-assigned channel edge and within 0-10 megahertz below the lower SAS-assigned channel edge. At all frequencies greater than 10 megahertz above the upper SAS assigned channel edge and less than 10 MHz below the lower SAS assigned channel edge, the conducted power of any CBSD emission shall not exceed -25 dBm/MHz.

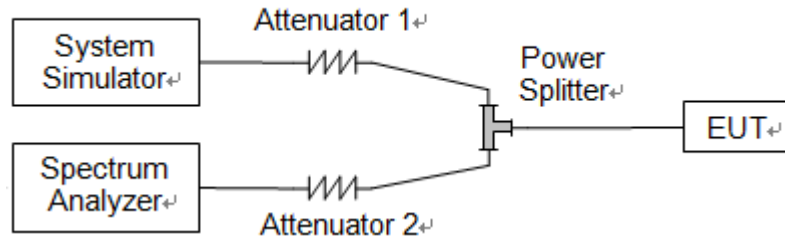
Part 96.41(e)(1)(ii)

For channel and frequency assignments made by a CBSD to End User Devices, the conducted power of any End User Device emission outside the fundamental emission (whether in or outside of the authorized band) shall not exceed -13 dBm/MHz within 0 to B megahertz (where B is the bandwidth in megahertz of the assigned channel or multiple contiguous channels of the End User Device) above the upper CBSD-assigned channel edge and within 0 to B megahertz below the lower CBSD-assigned channel edge. At all frequencies greater than B megahertz above the upper CBSD assigned channel edge and less than B megahertz below the lower CBSD-assigned channel edge, the conducted power of any End User Device emission shall not exceed -25 dBm/MHz.

Part 96.41(e)(2)

For CBSDs and End User Devices, the conducted power of emissions below 3540 MHz or above 3710 MHz shall not exceed -25 dBm/MHz, and the conducted power of emissions below 3530 MHz or above 3720 MHz shall not exceed -40 dBm/MHz.

2.6.2.Test Description



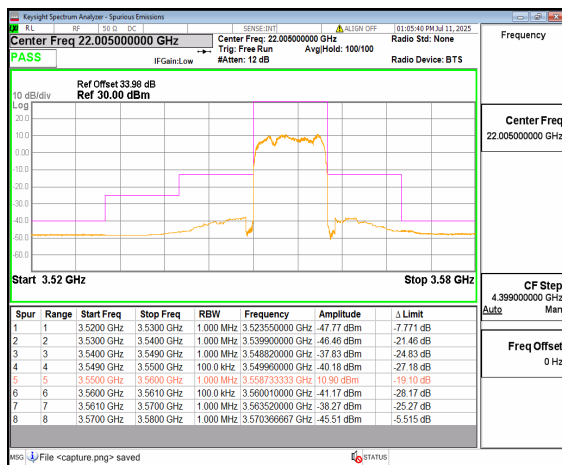
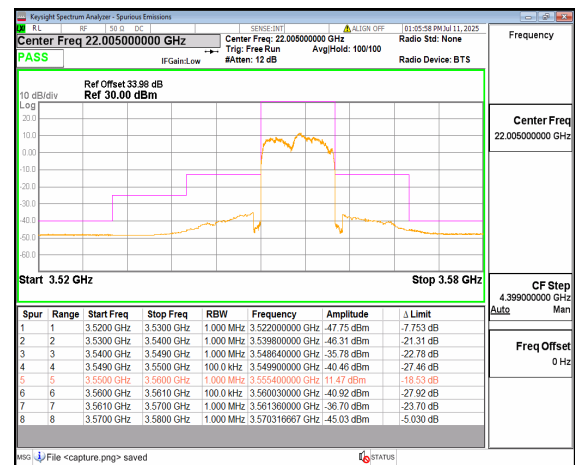
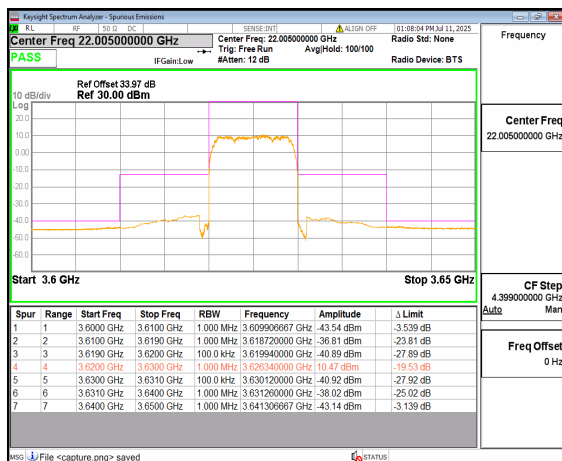
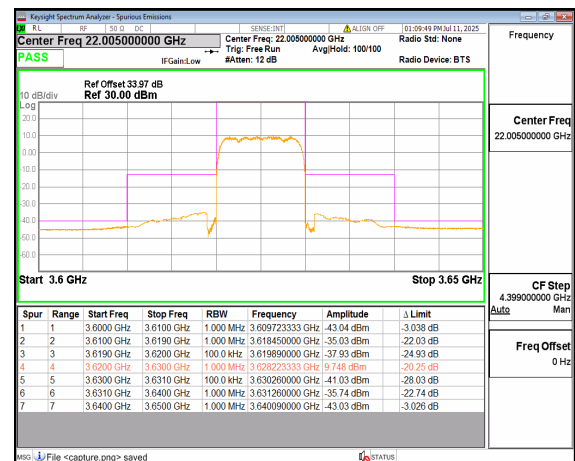
The EUT is coupled to the Spectrum Analyzer (SA) and the System Simulator (SS) with Attenuators through the Power Splitter; the RF load attached to the EUT antenna terminal is 50Ohm; the path loss as the factor is calibrated to correct the reading. The EUT is commanded by the SS to operate at the maximum output power. A call is established between the EUT and the SS.

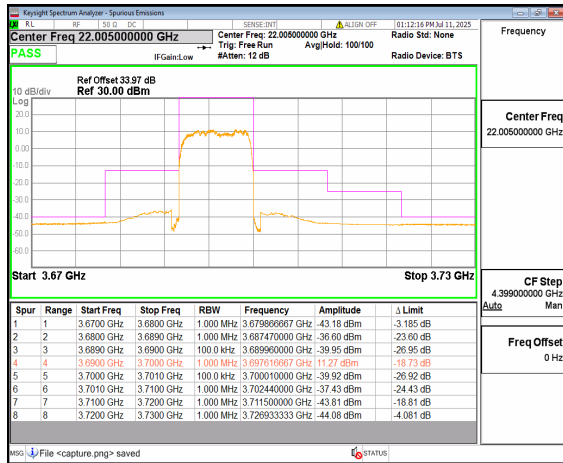
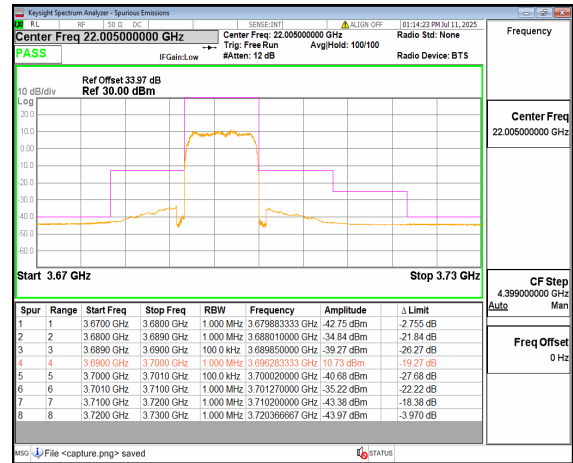
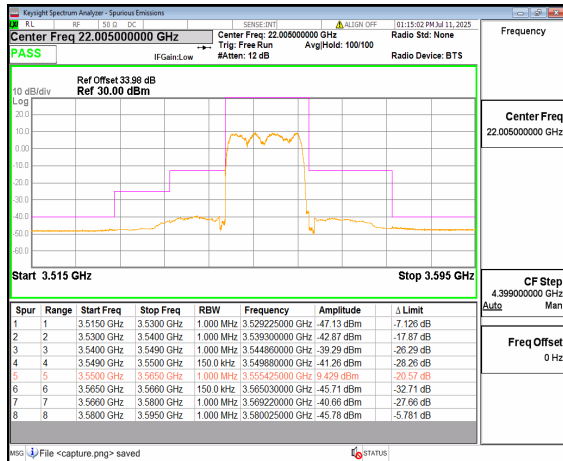
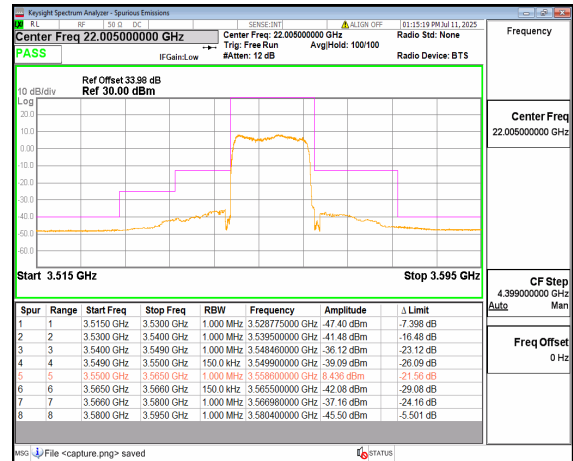
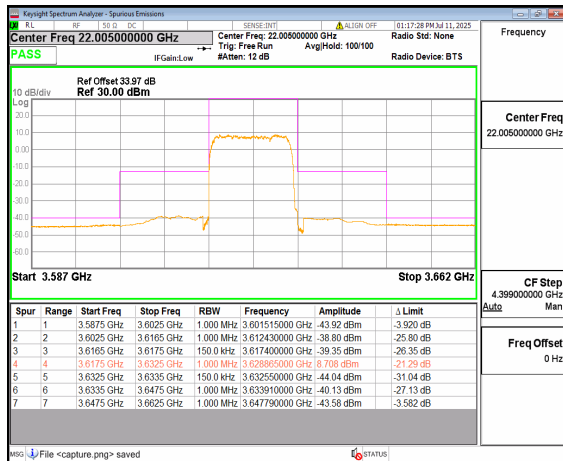
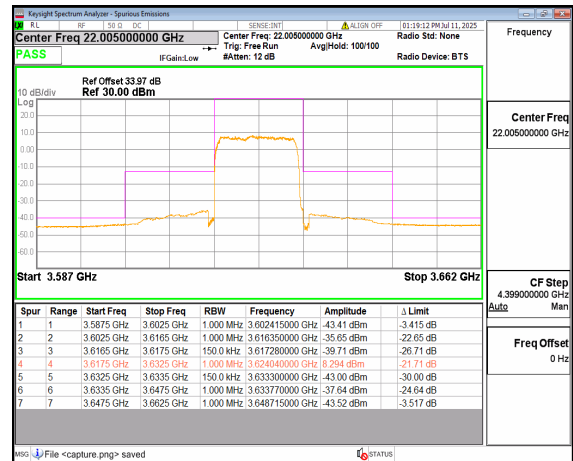
2.6.3.Test procedure

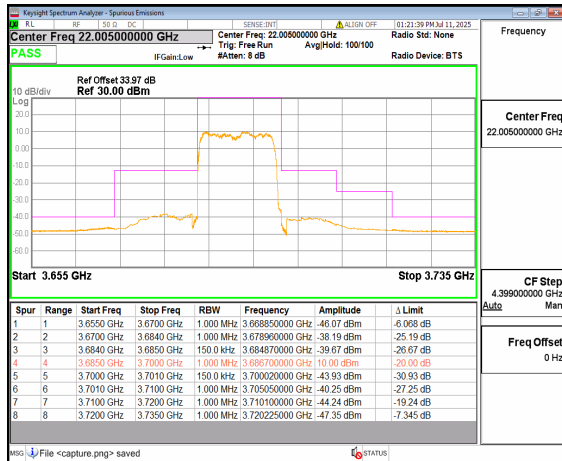
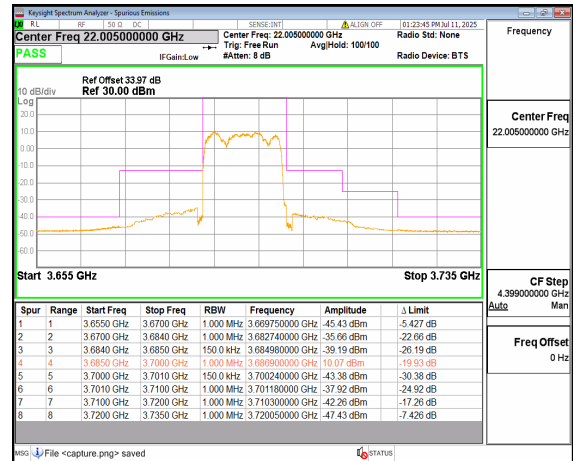
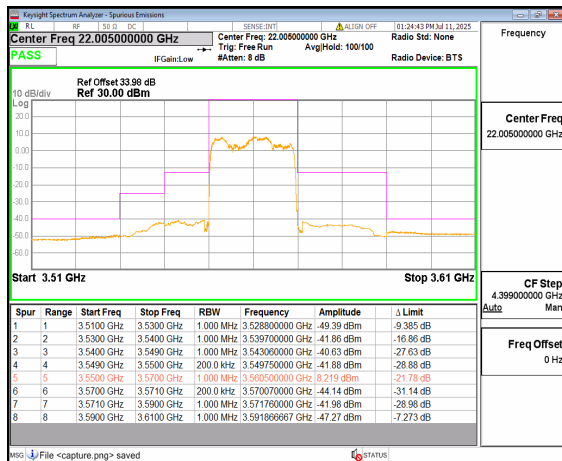
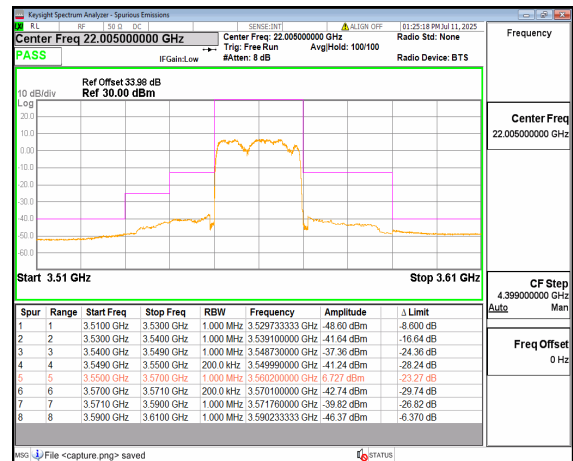
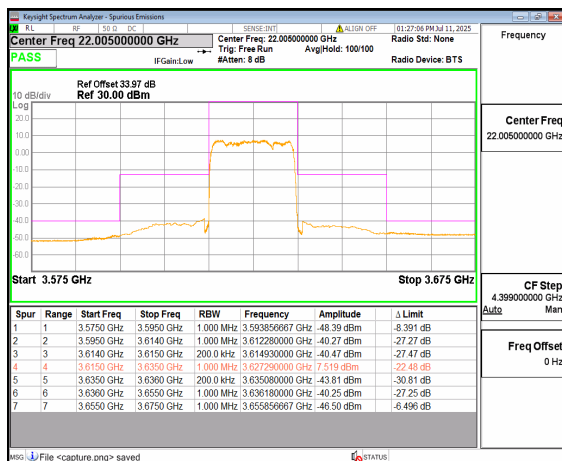
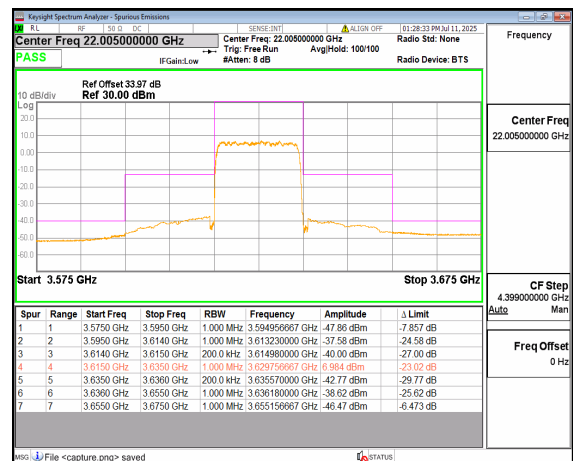
KDB 971168 D01v03 Section 6.0 and ANSI/TIA-603-E-2016.

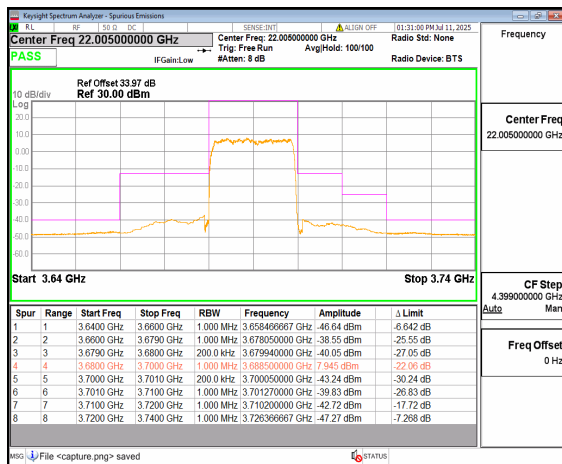
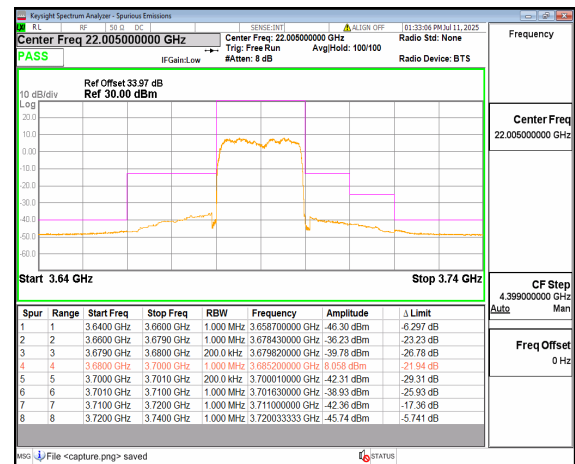
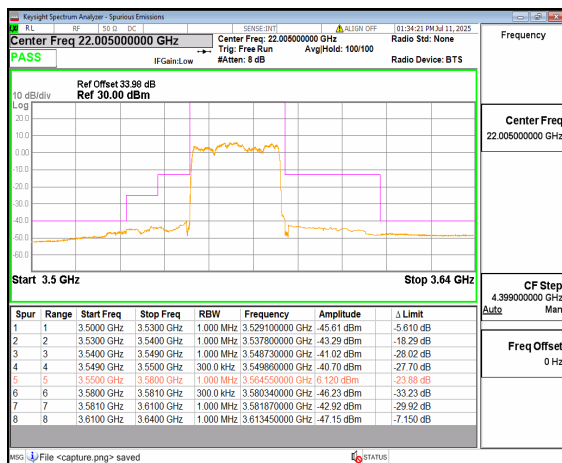
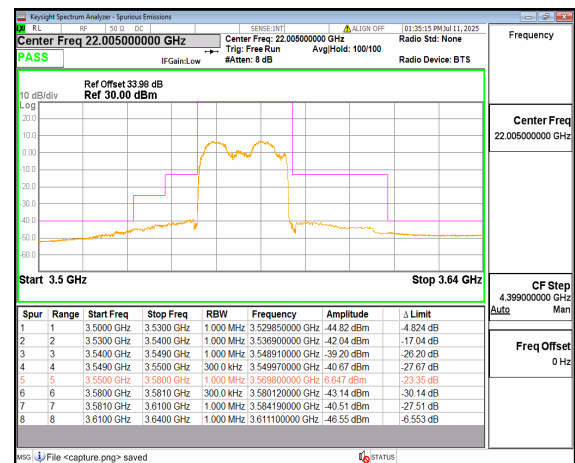
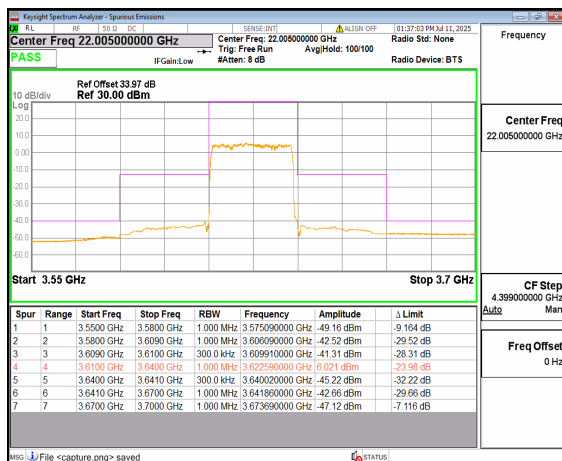
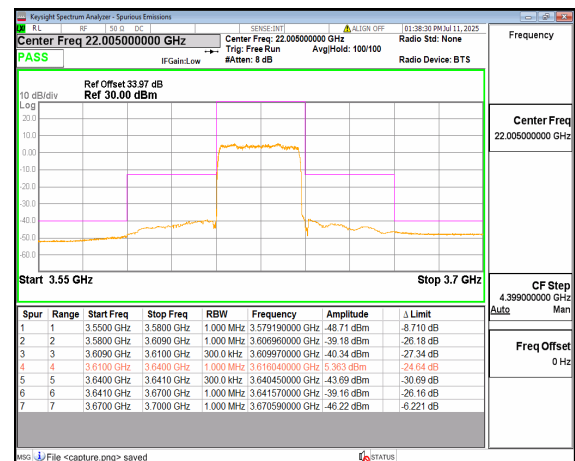


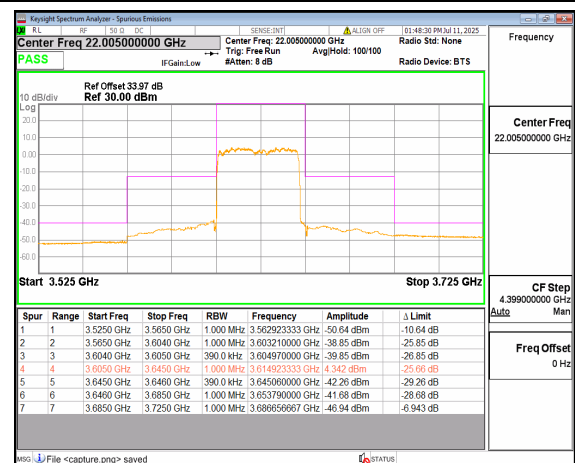
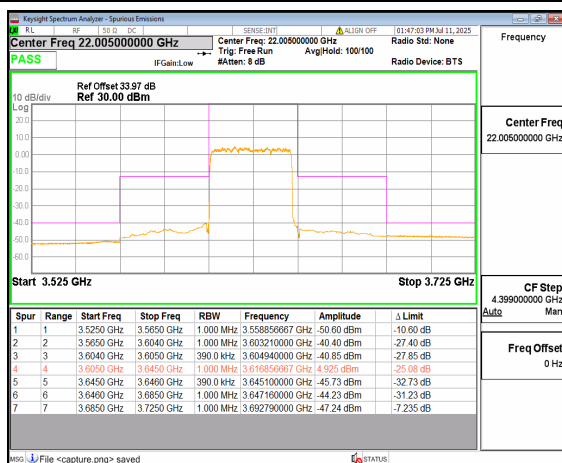
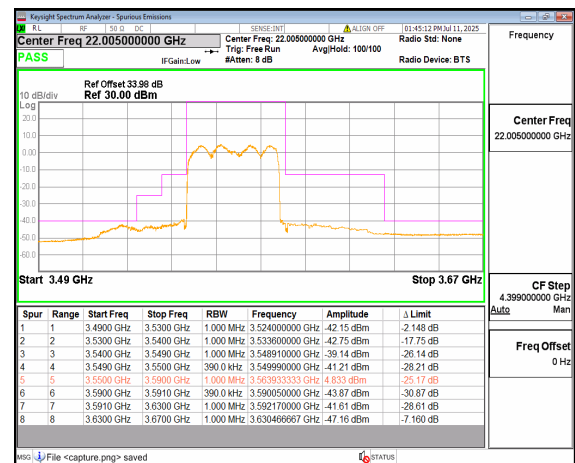
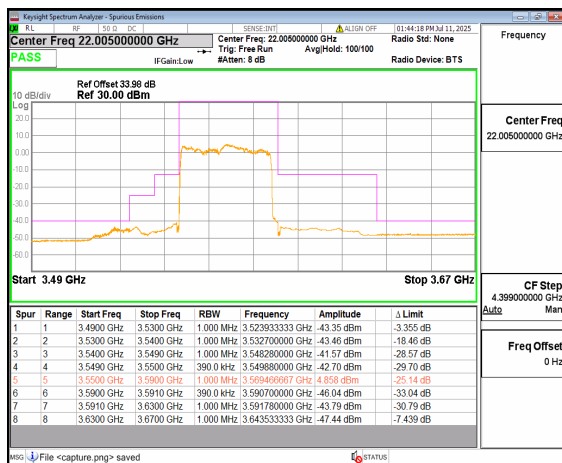
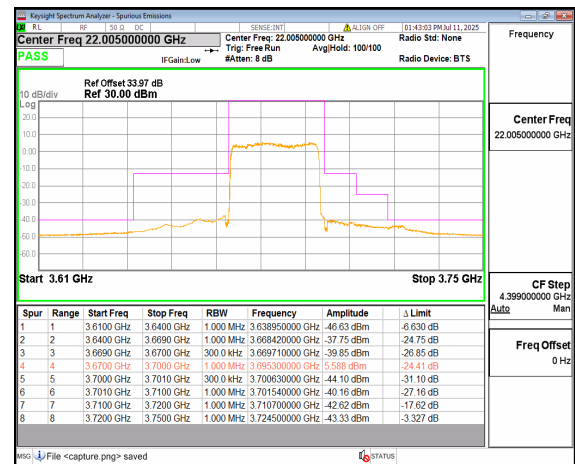
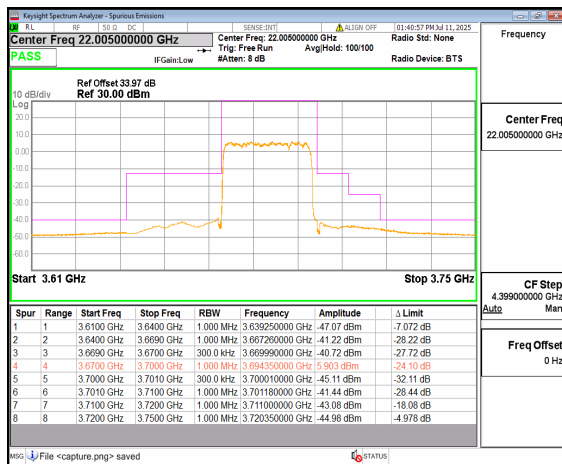
2.6.4. Test Result

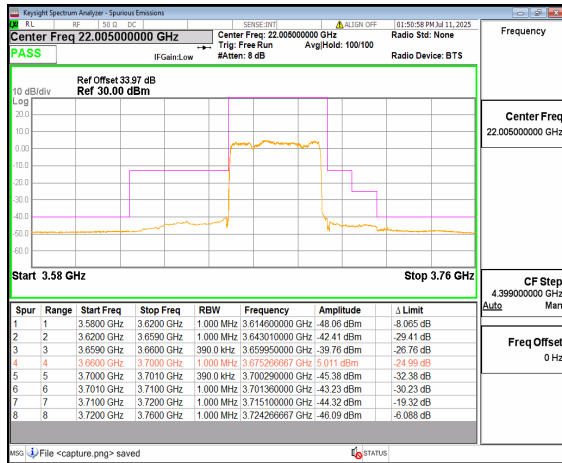
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Lown48 10M DFT-s-OFDM QPSK Outer_Full
Lown48 10M DFT-s-OFDM BPSK Outer_Full
Midn48 10M DFT-s-OFDM QPSK Outer_Full
Mid

n48 10M DFT-s-OFDM BPSK Outer_Full
Highn48 10M DFT-s-OFDM QPSK Outer_Full
Highn48 15M DFT-s-OFDM BPSK Outer_Full
Lown48 15M DFT-s-OFDM QPSK Outer_Full
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Midn48 15M DFT-s-OFDM QPSK Outer_Full
Mid

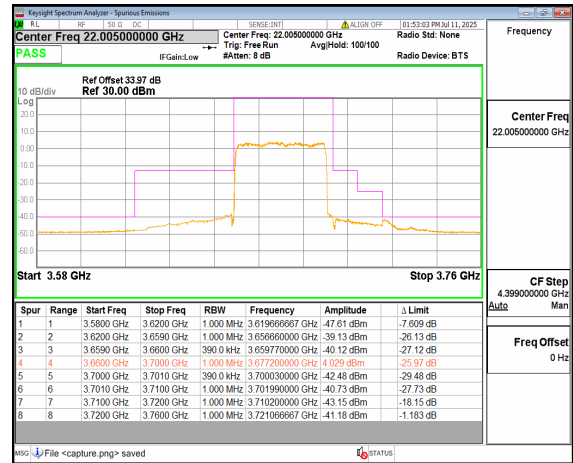
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Highn48 15M DFT-s-OFDM QPSK Outer_Full
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Lown48 20M DFT-s-OFDM BPSK Outer_Full
Midn48 20M DFT-s-OFDM QPSK Outer_Full
Mid

n48 20M DFT-s-OFDM BPSK Outer_Full
Highn48 20M DFT-s-OFDM QPSK Outer_Full
Highn48 30M DFT-s-OFDM BPSK Outer_Full
Lown48 30M DFT-s-OFDM QPSK Outer_Full
Lown48 30M DFT-s-OFDM BPSK Outer_Full
Midn48 30M DFT-s-OFDM QPSK Outer_Full
Mid

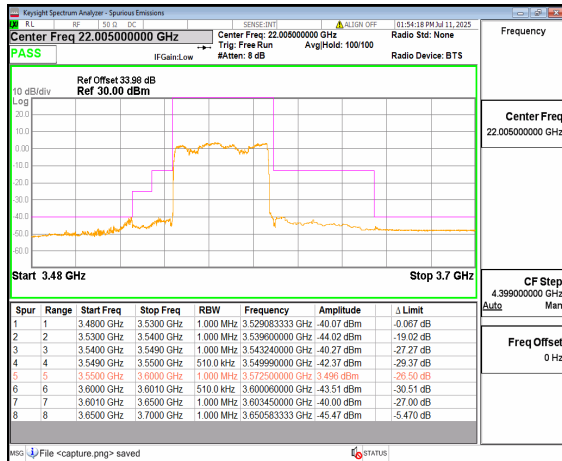




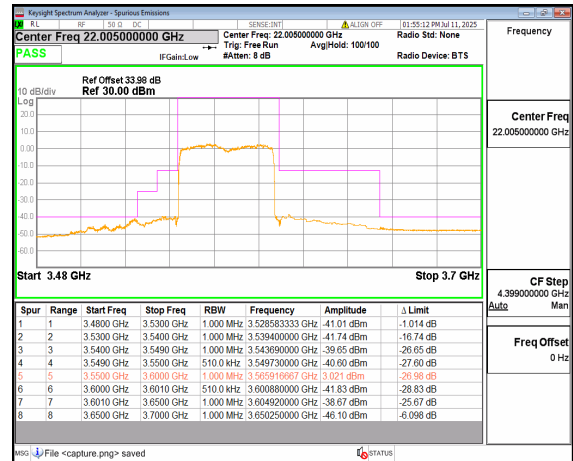
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High



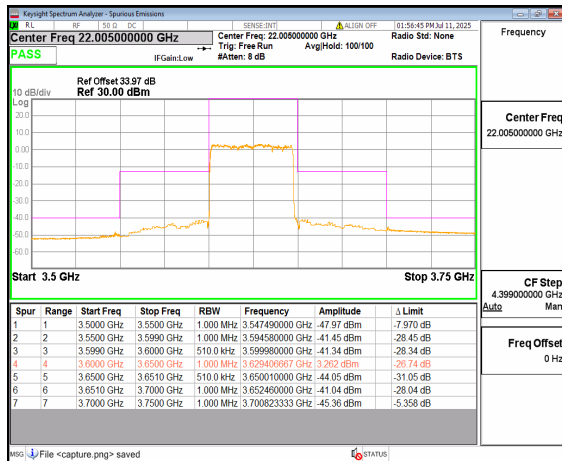
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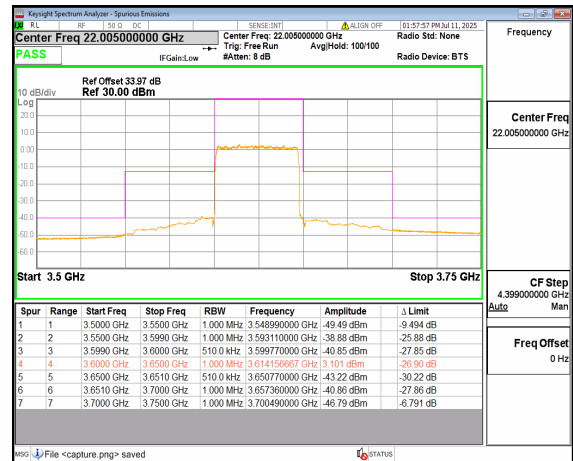
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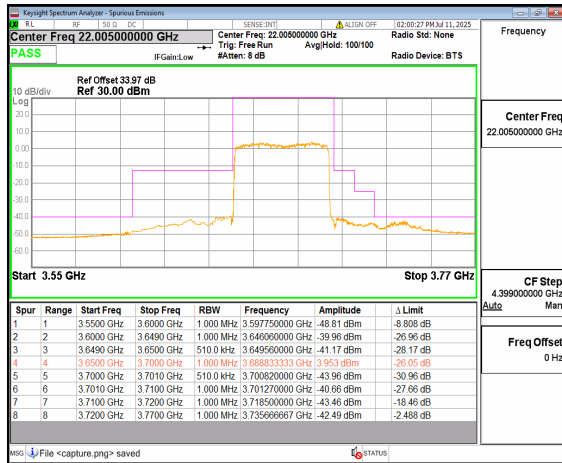
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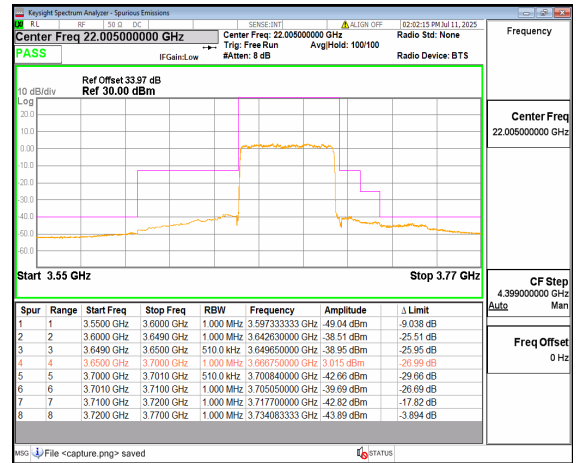
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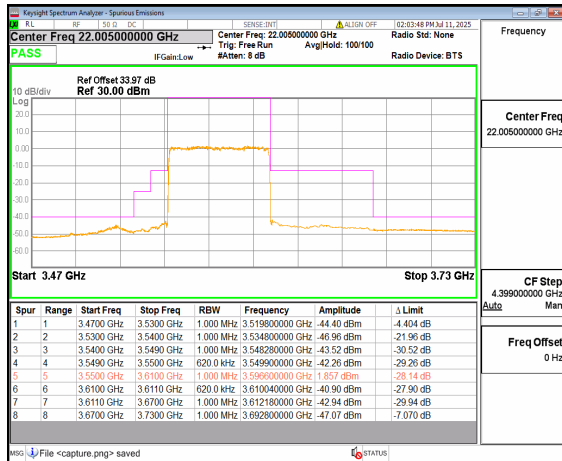
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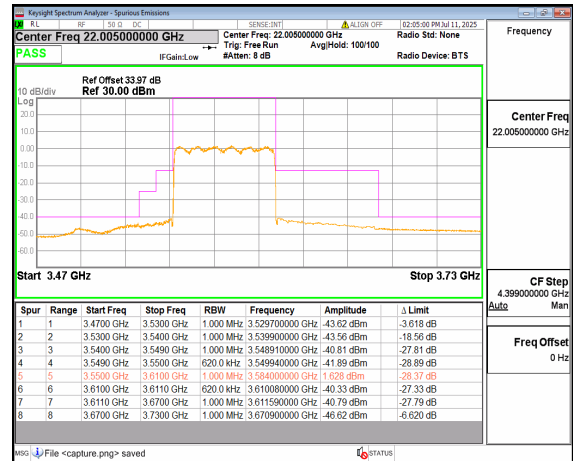
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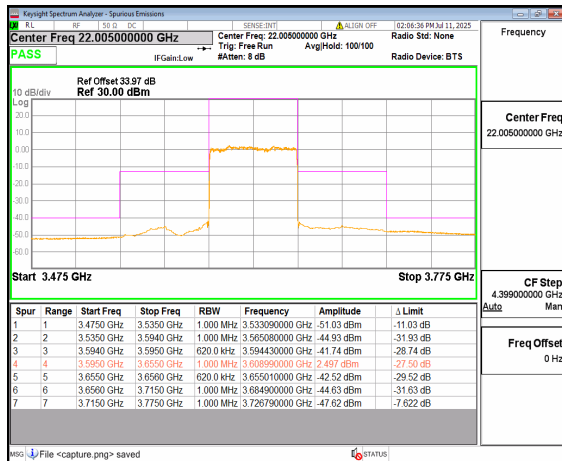
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High



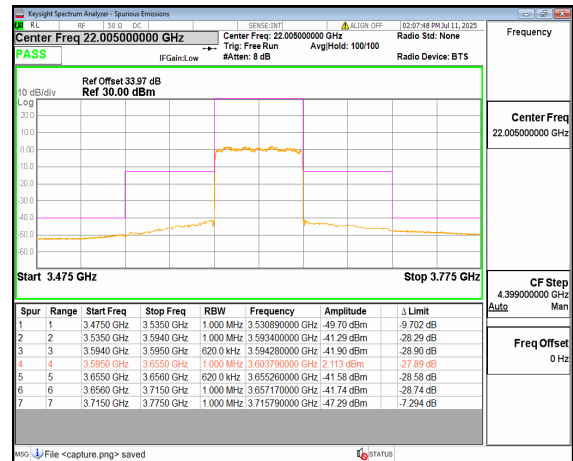
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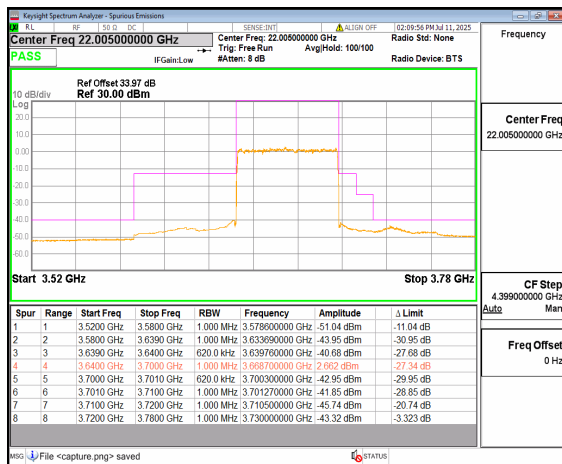
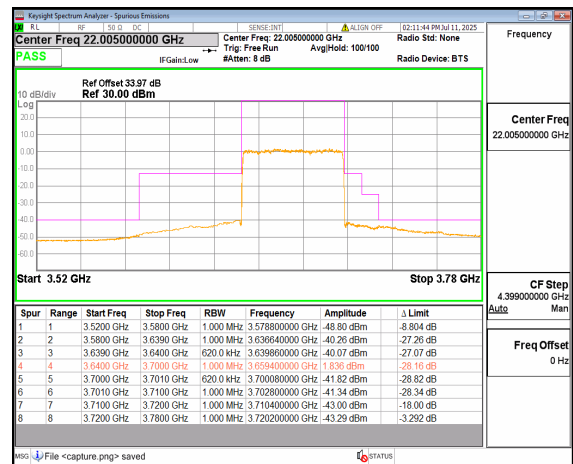
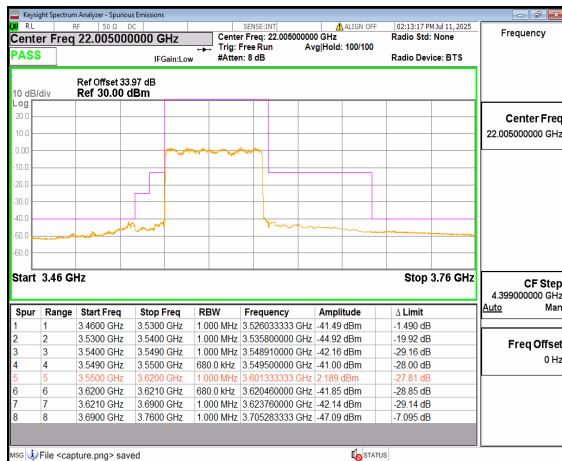
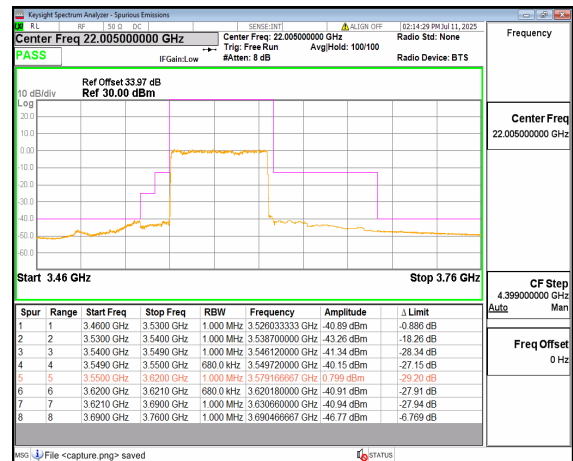
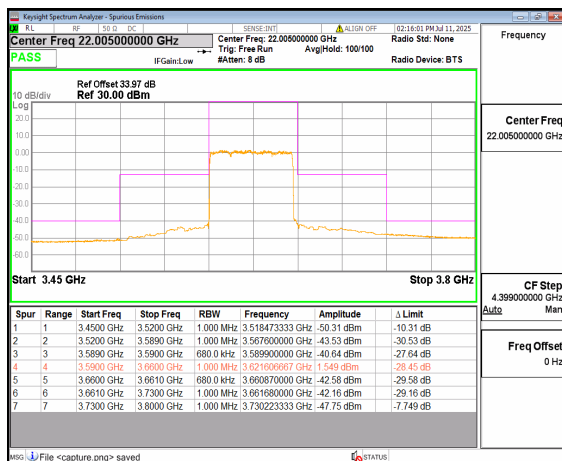
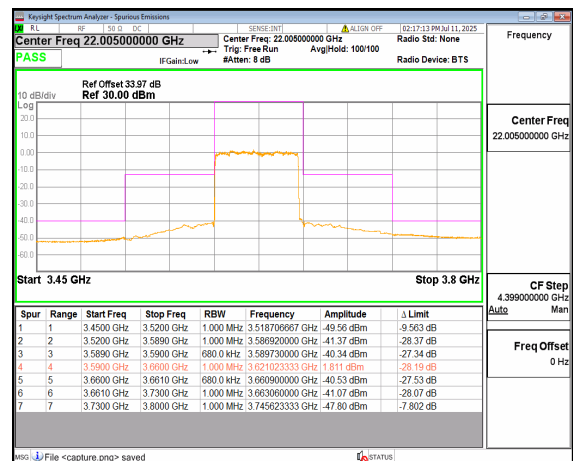
n48 60M DFT-s-OFDM QPSK Outer_Full
Low



n48 60M DFT-s-OFDM BPSK Outer_Full
Mid



n48 60M DFT-s-OFDM QPSK Outer_Full
Mid

n48 60M DFT-s-OFDM BPSK Outer_Full
Highn48 60M DFT-s-OFDM QPSK Outer_Full
Highn48 70M DFT-s-OFDM BPSK Outer_Full
Lown48 70M DFT-s-OFDM QPSK Outer_Full
Lown48 70M DFT-s-OFDM BPSK Outer_Full
Midn48 70M DFT-s-OFDM QPSK Outer_Full
Mid