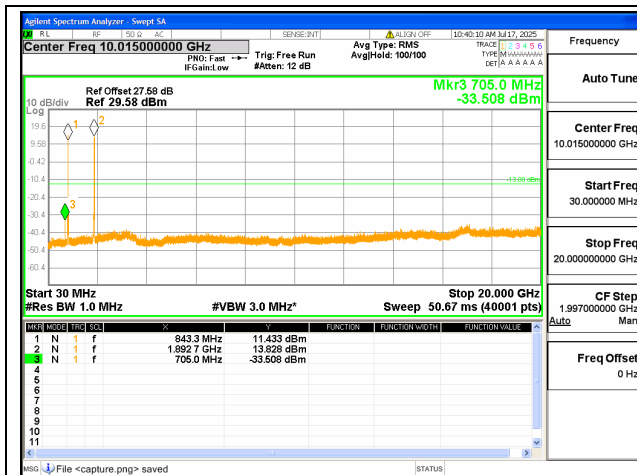
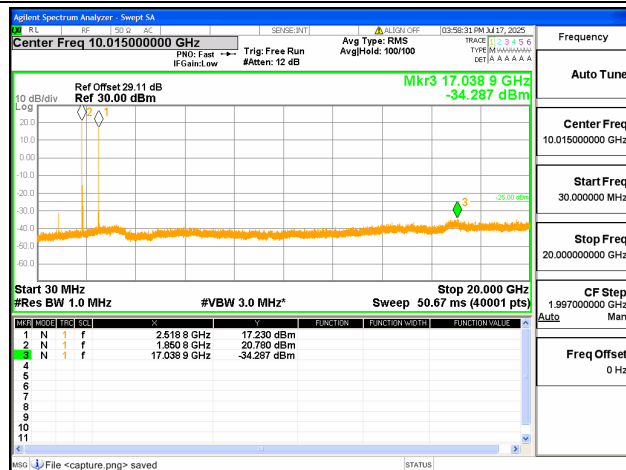
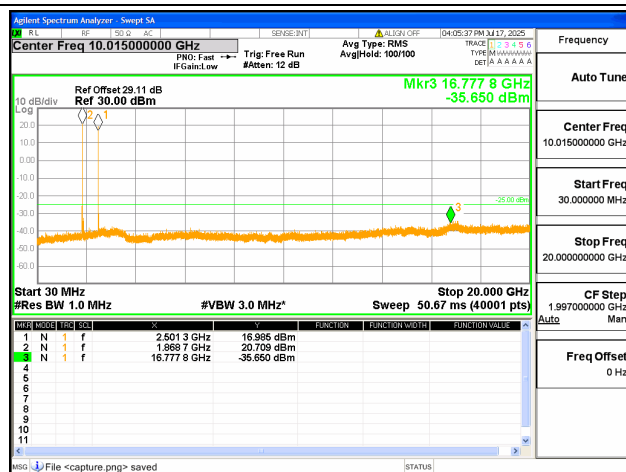
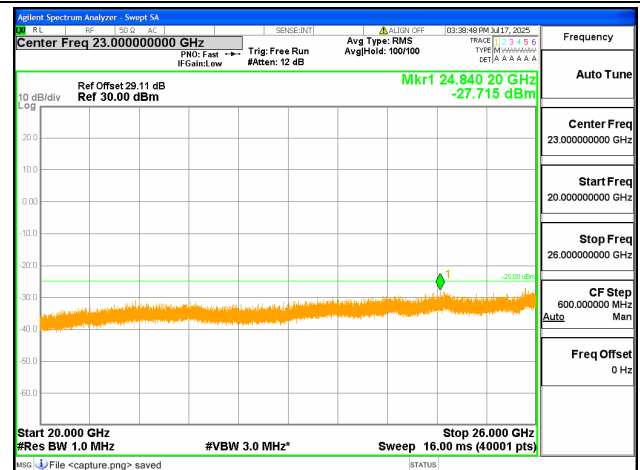
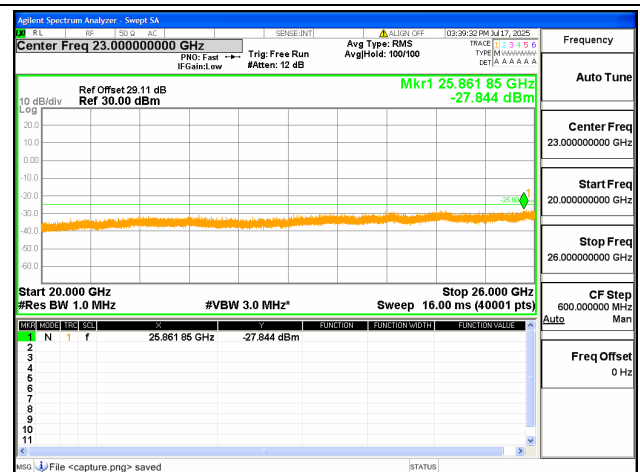
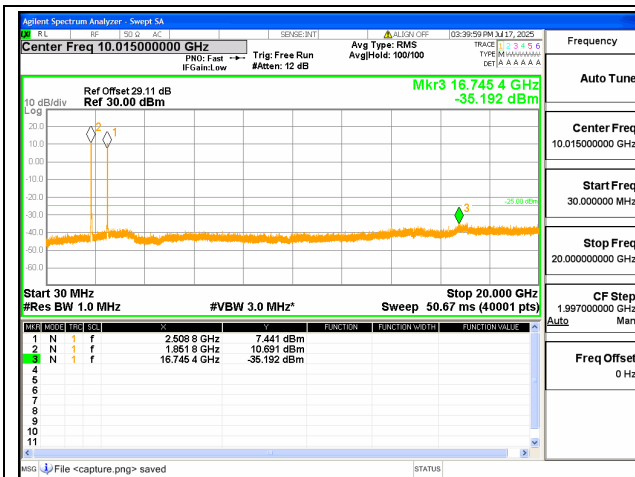
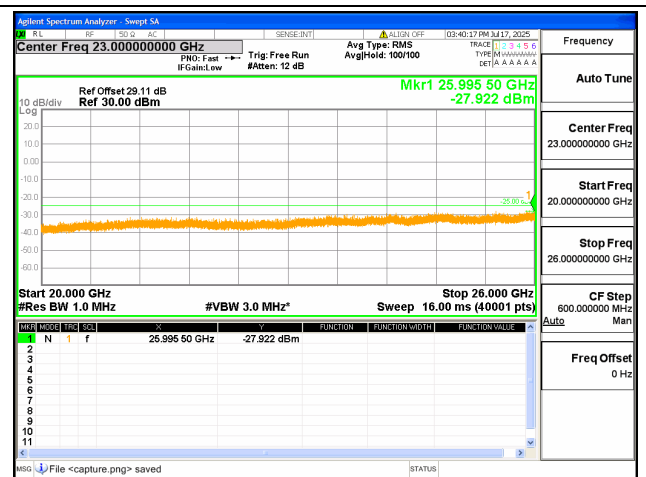


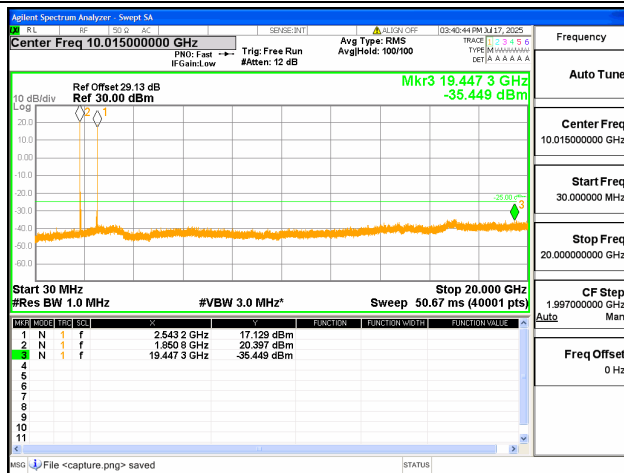
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Low+Low CH / 1@0-1@992A-7A-30M-20G / 20+20MHz / QPSK /
Low+Low CH / 1@99-1@02A-7A-20G-26G / 20+20MHz / QPSK /
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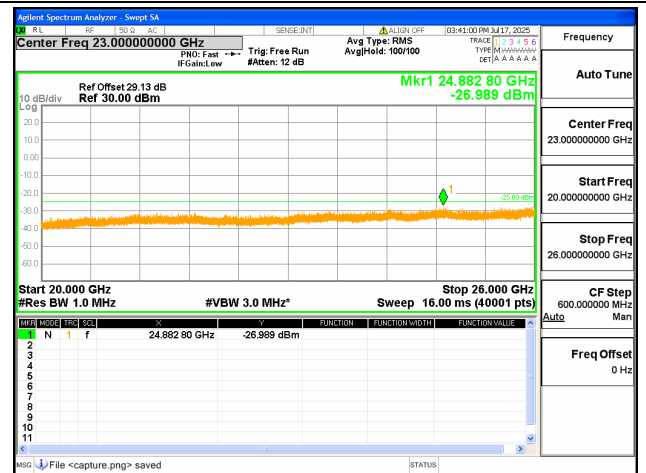
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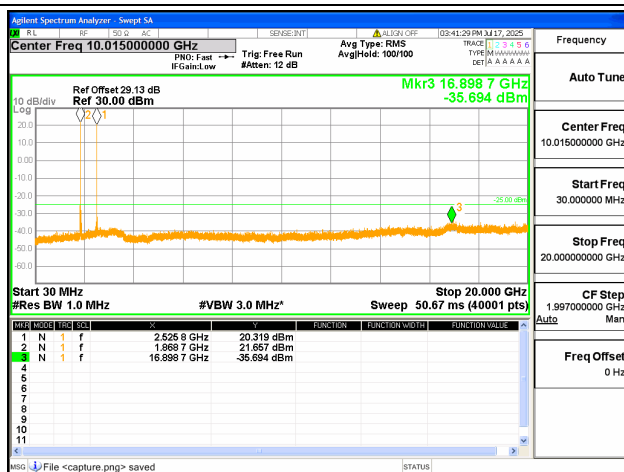
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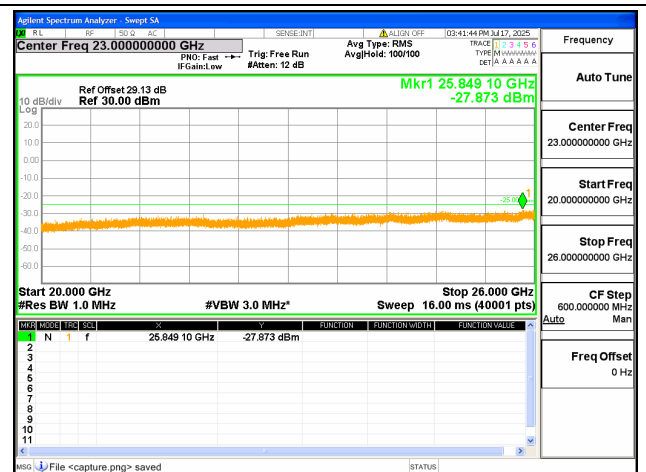
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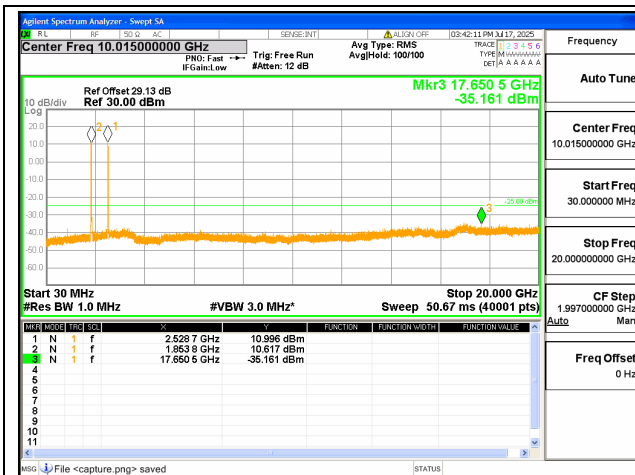
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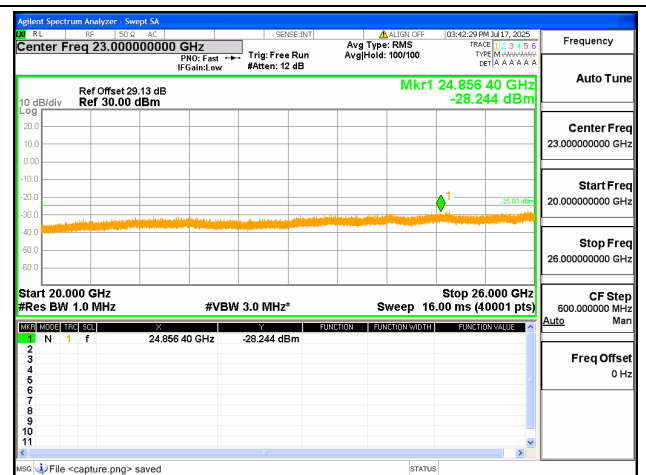
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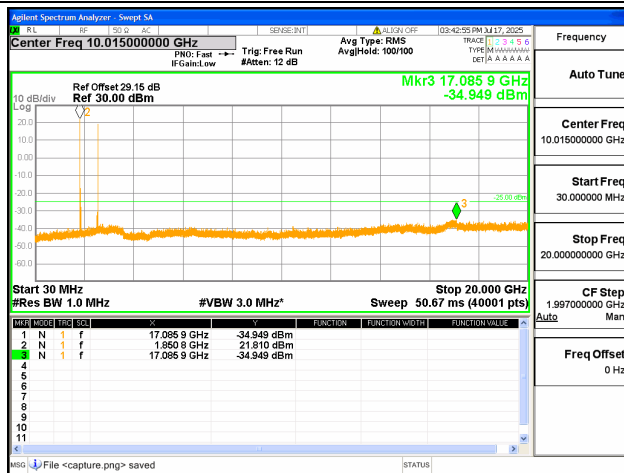
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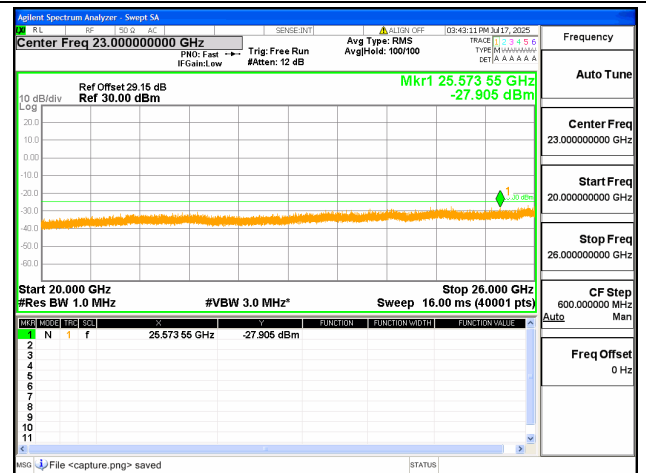
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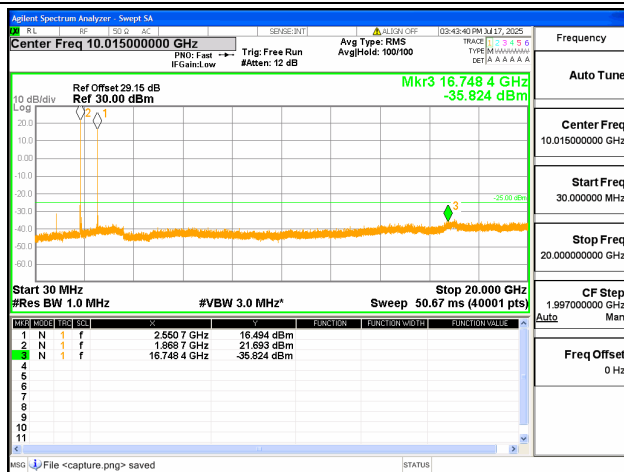
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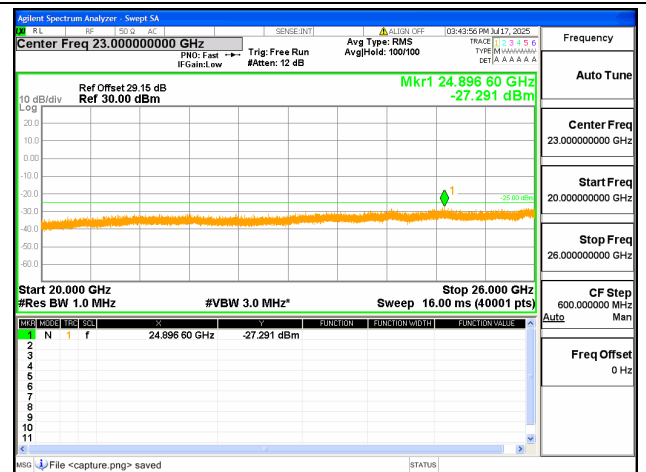
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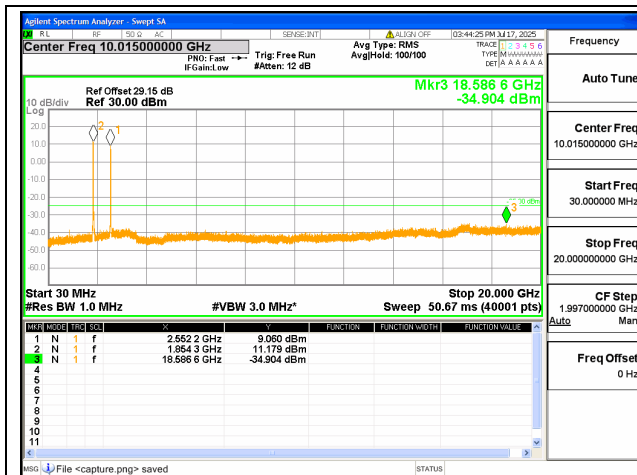
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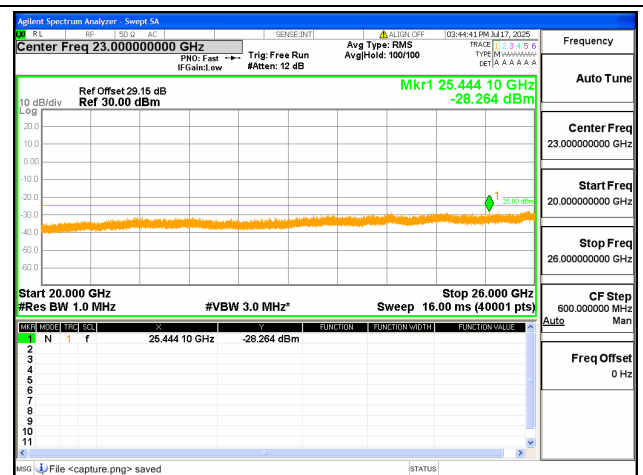
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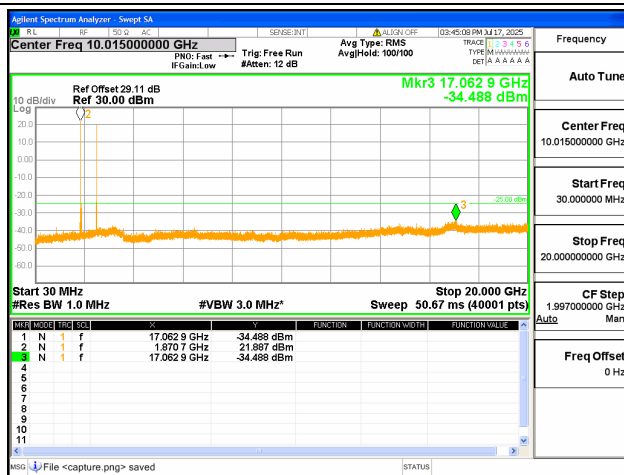
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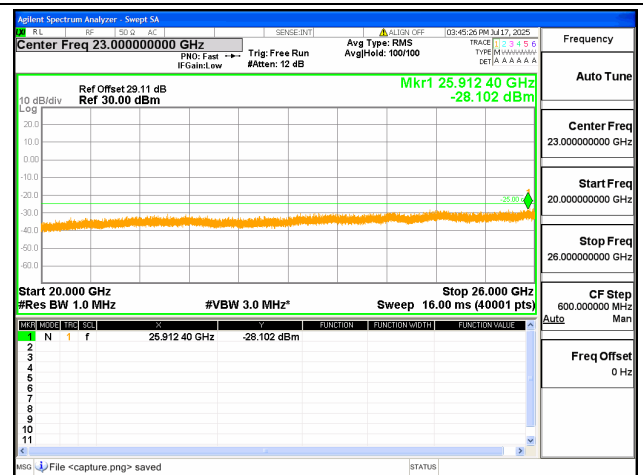
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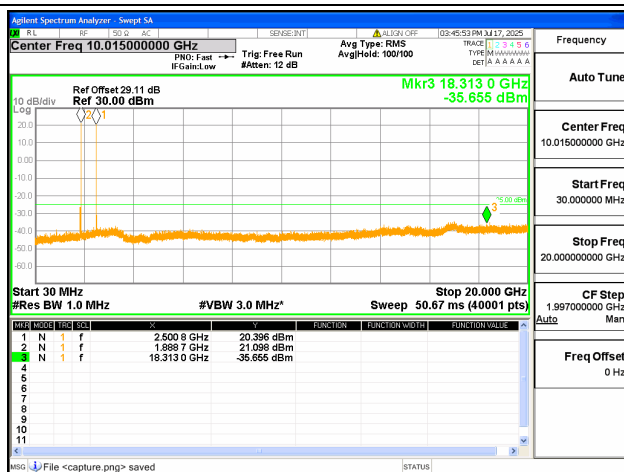
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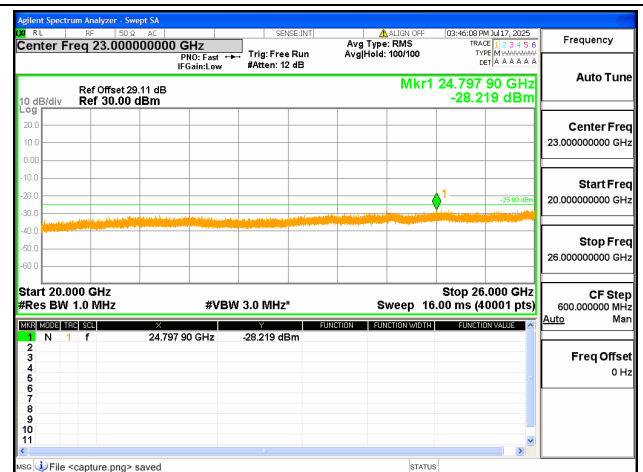
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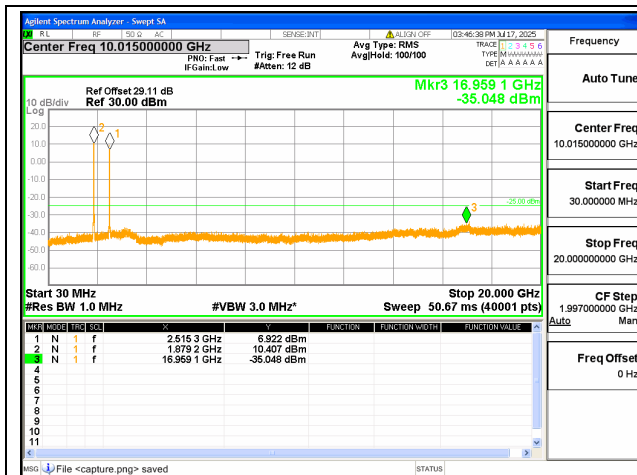
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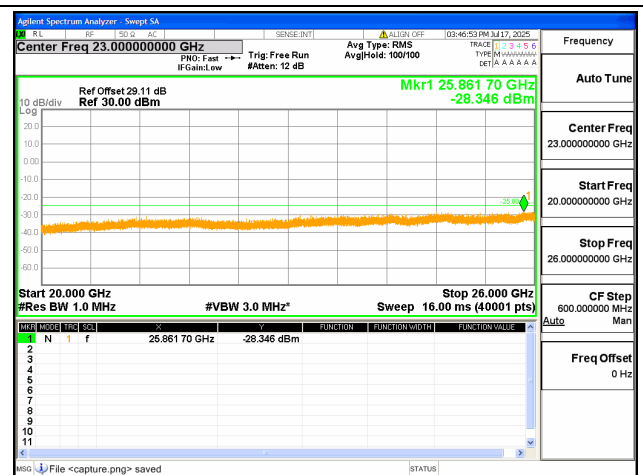
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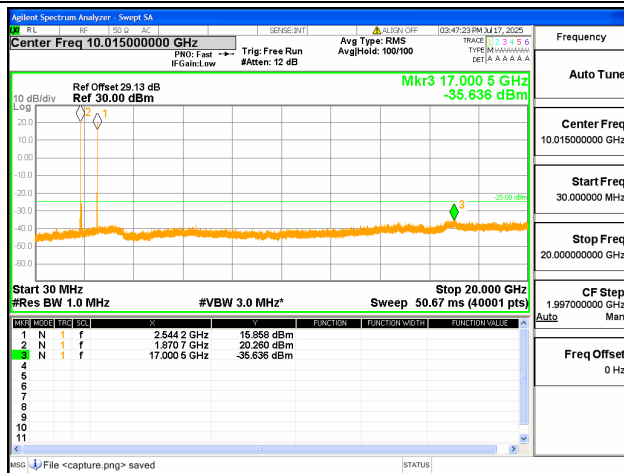
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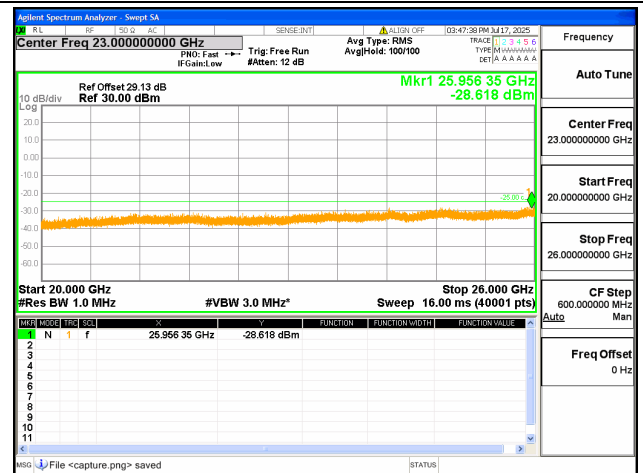
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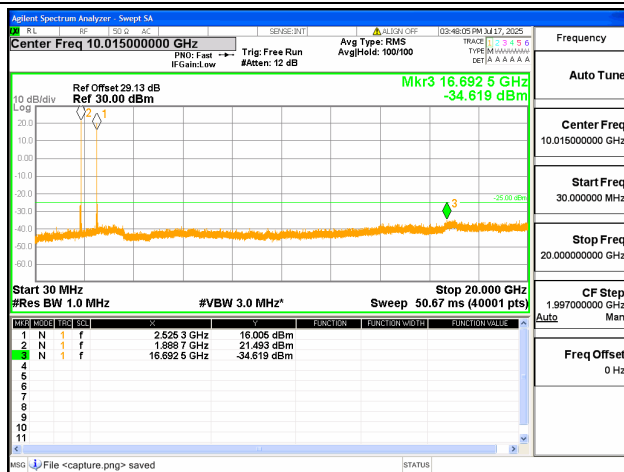
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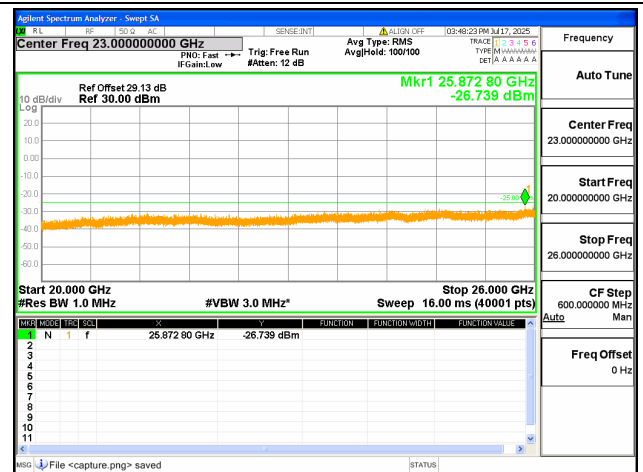
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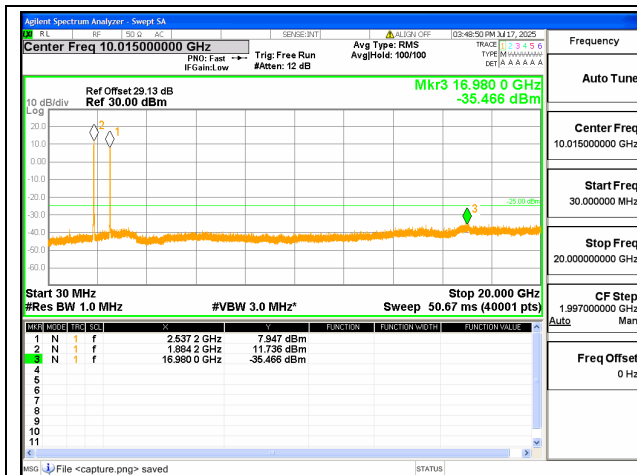
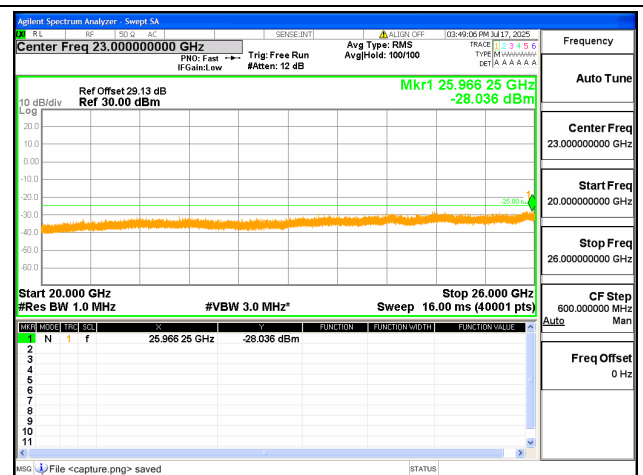
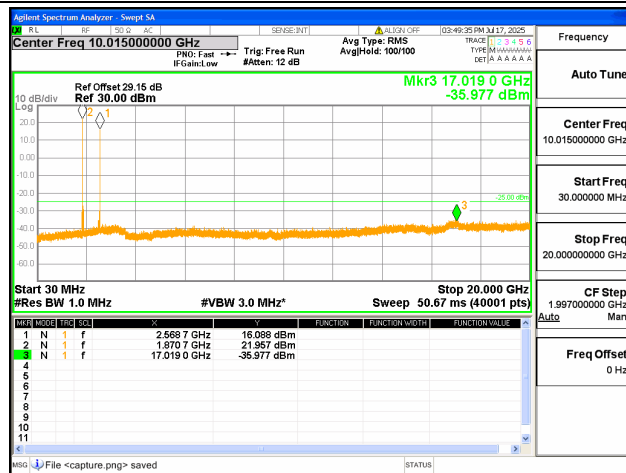
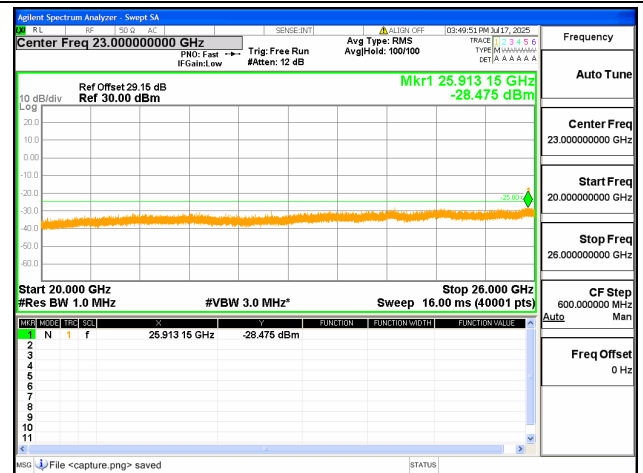
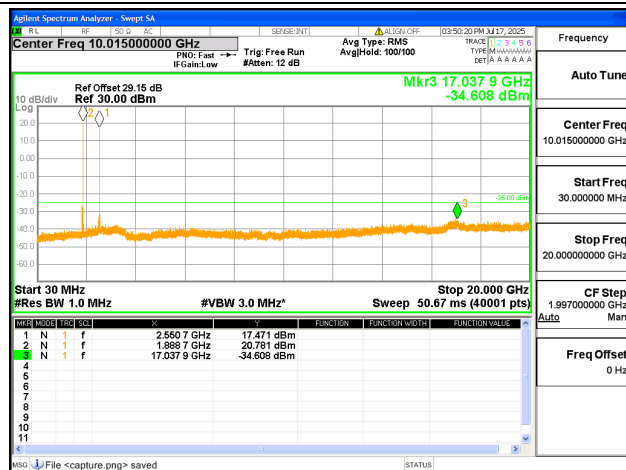
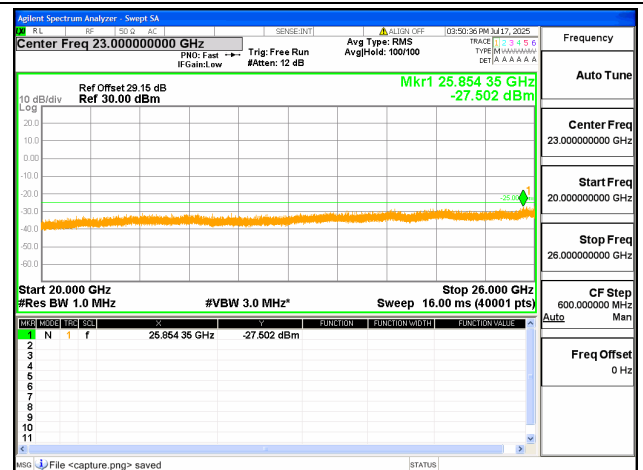
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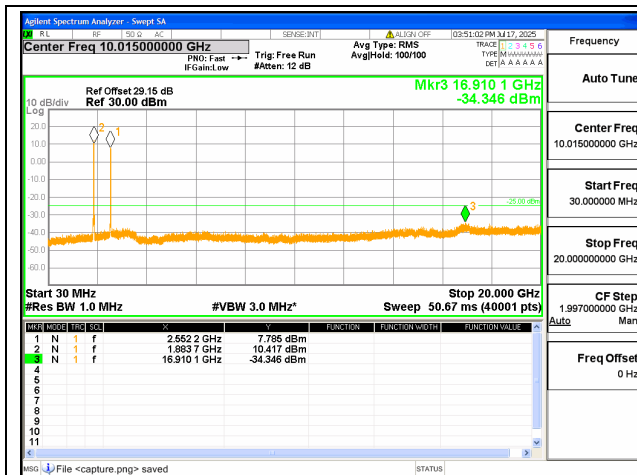


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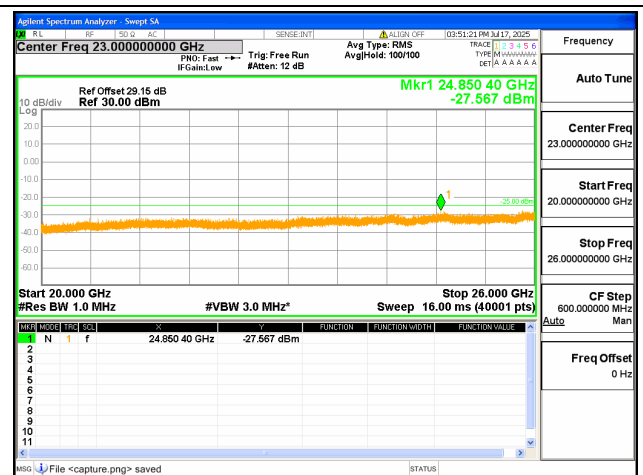


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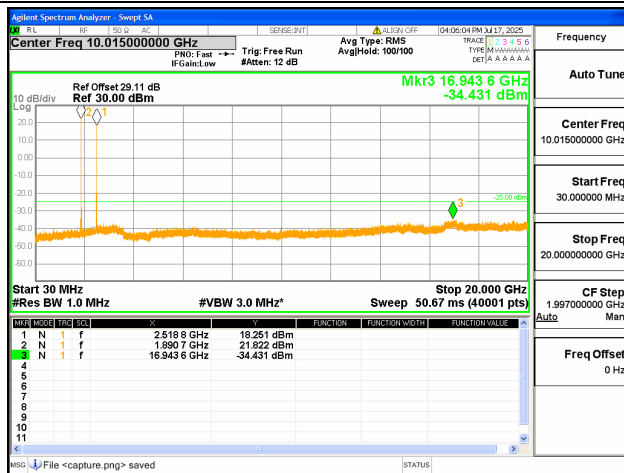
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CH / 100@0-100@02A-7A-30M-20G / 20+20MHz / QPSK /
Mid+High CH / 1@0-1@992A-7A-20G-26G / 20+20MHz / QPSK /
Mid+High CH / 1@0-1@992A-7A-30M-20G / 20+20MHz / QPSK /
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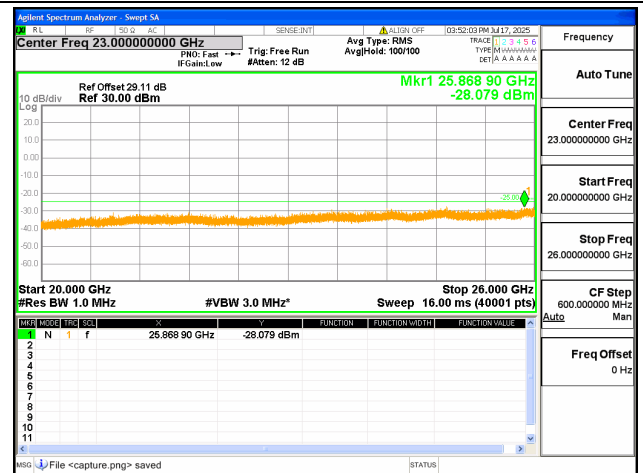
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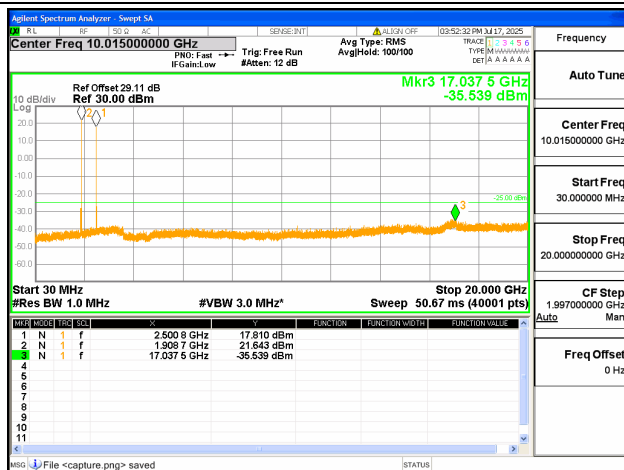
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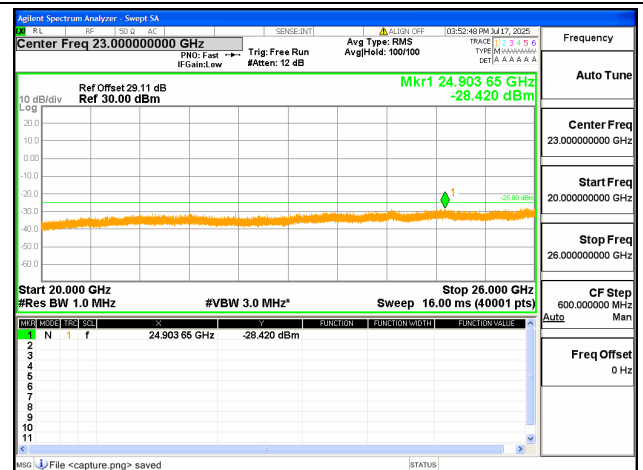
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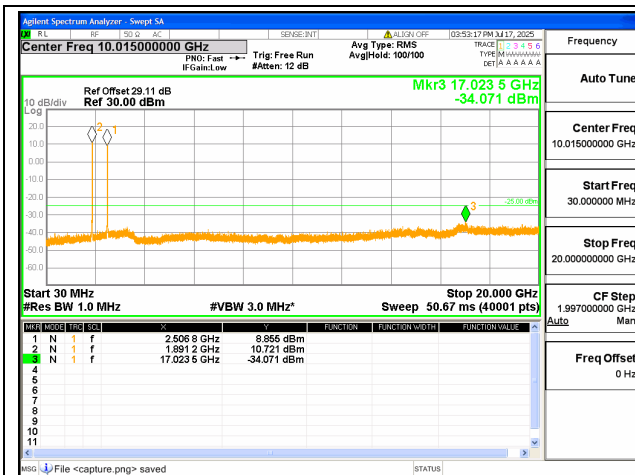
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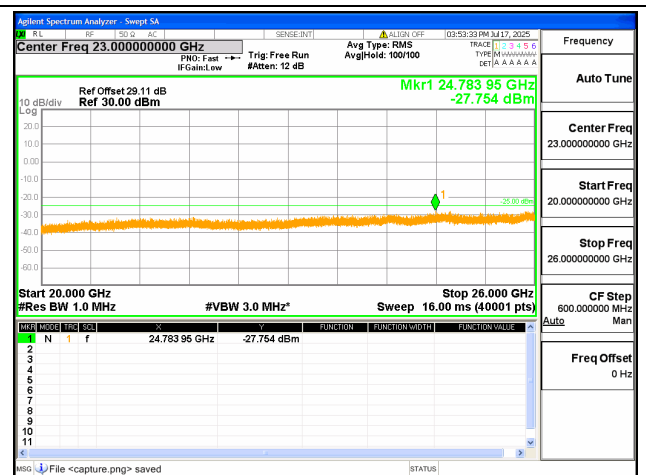
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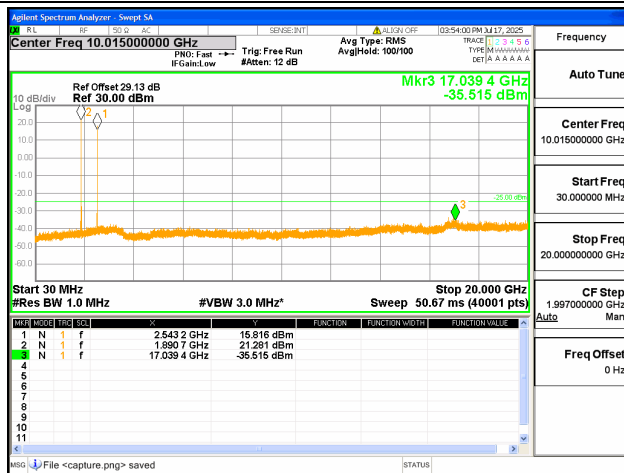
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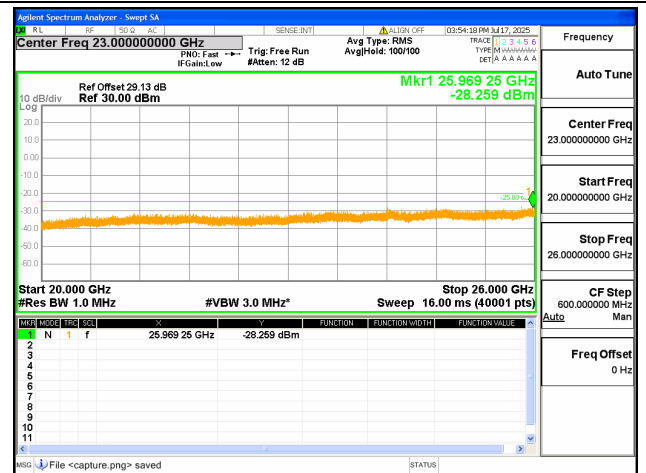
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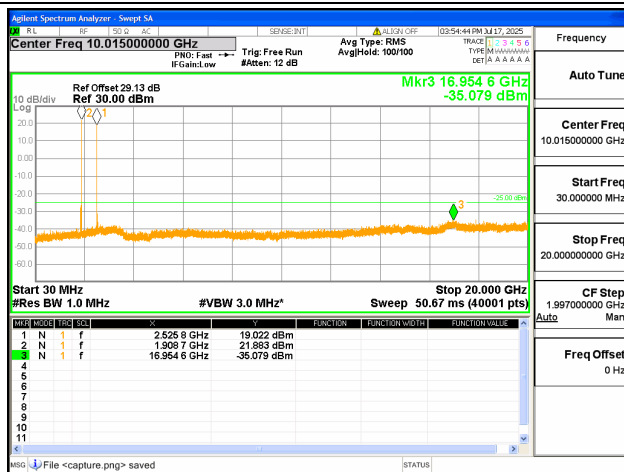
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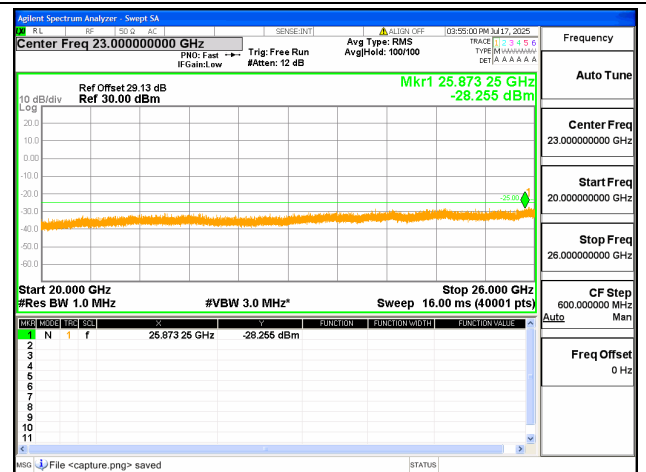
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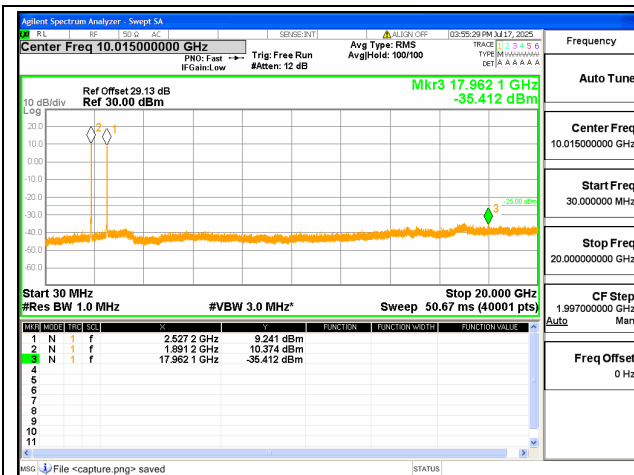
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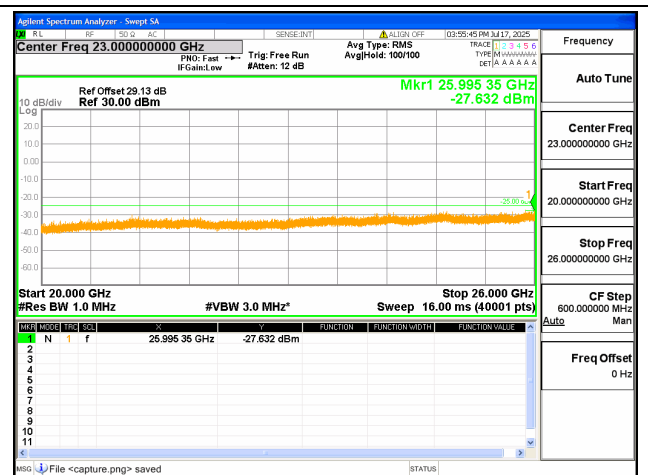
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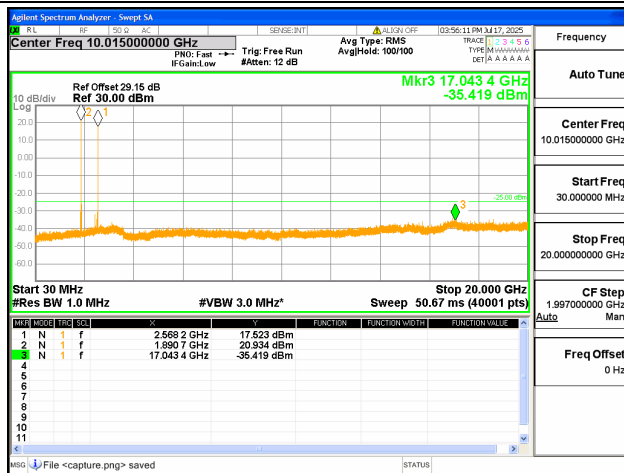
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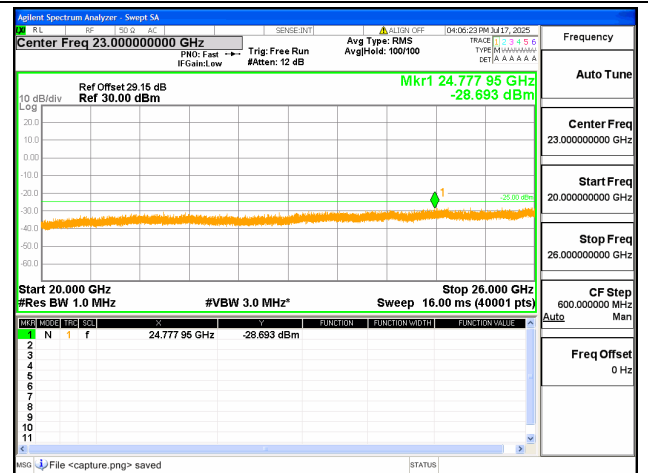
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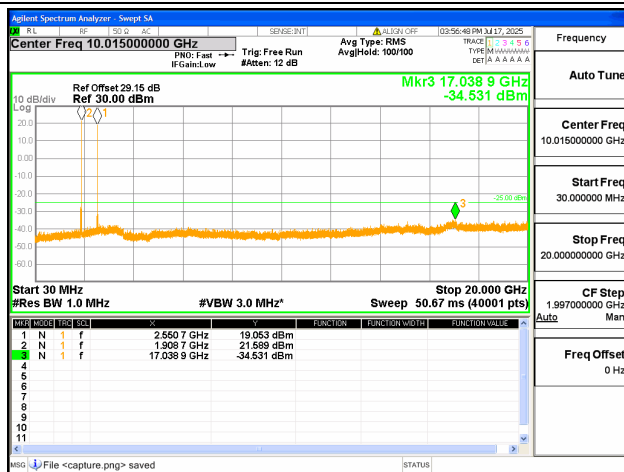
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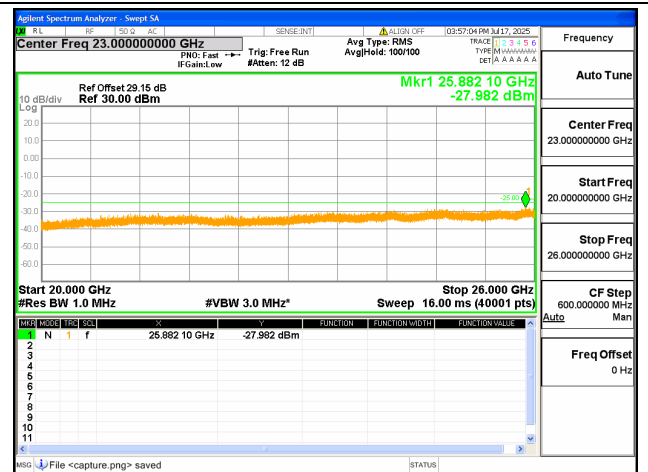
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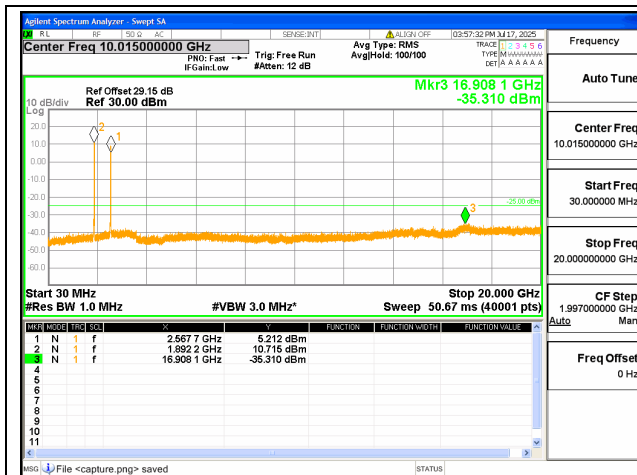
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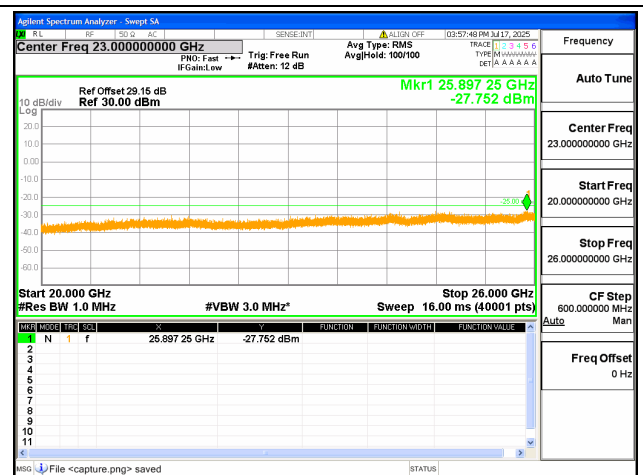
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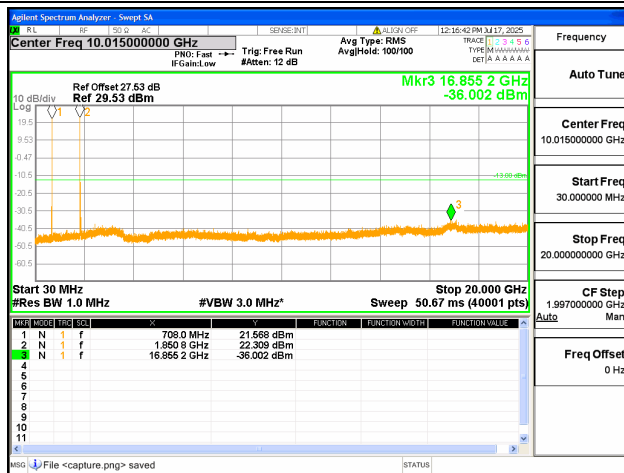
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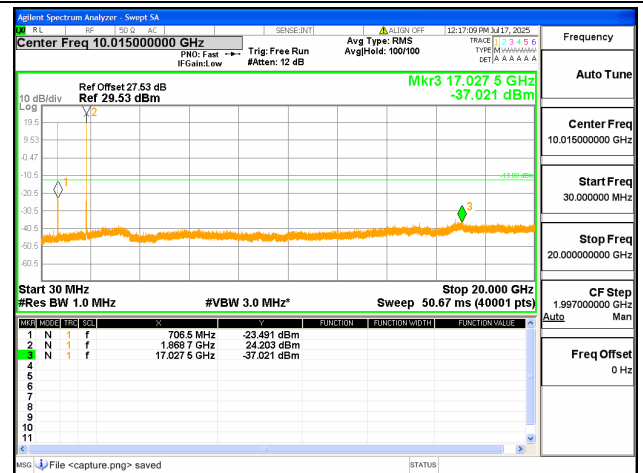
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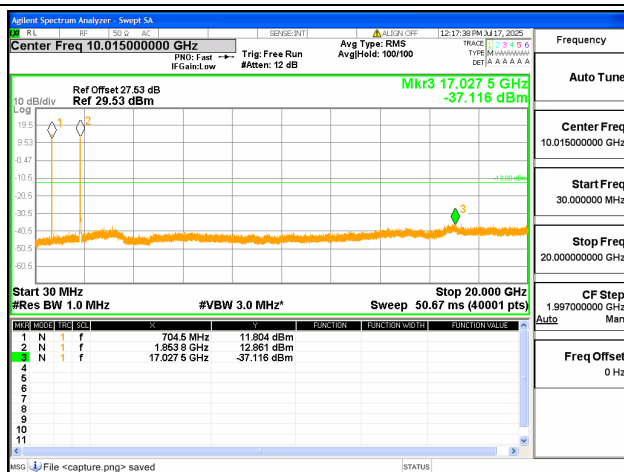
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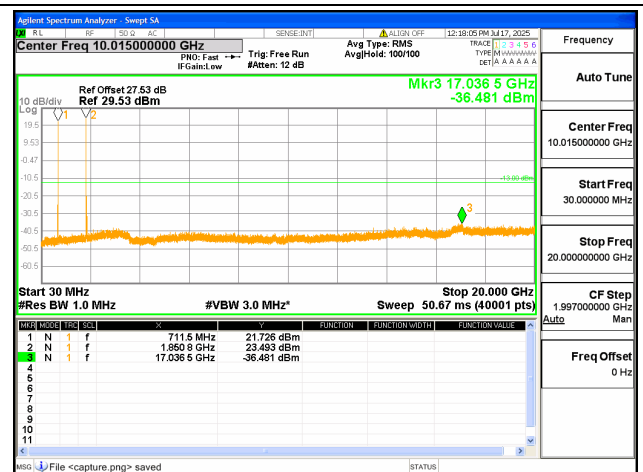
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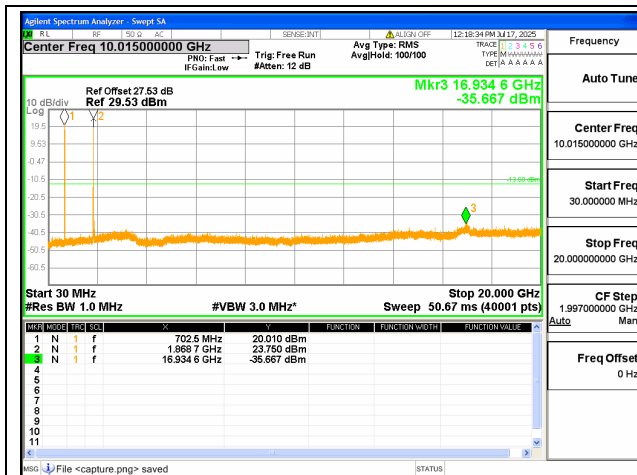
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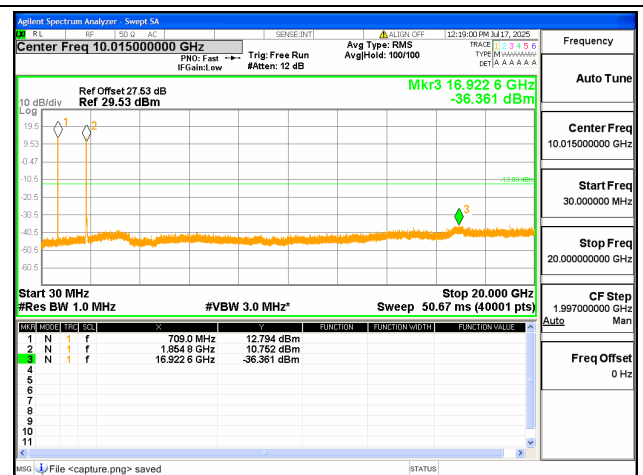
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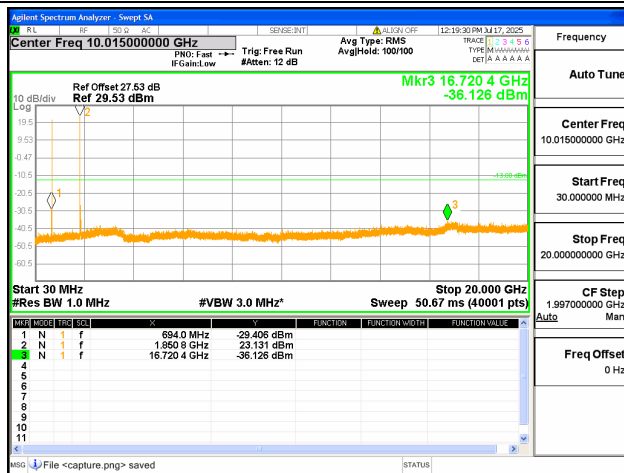
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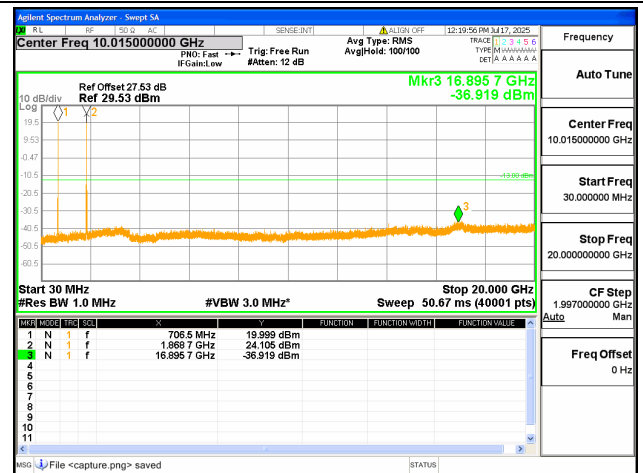
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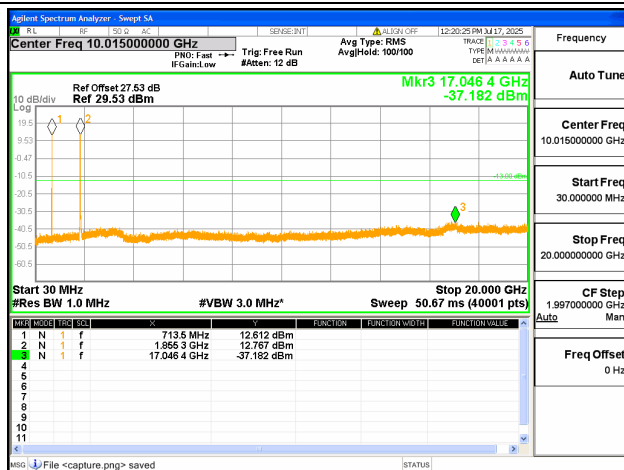
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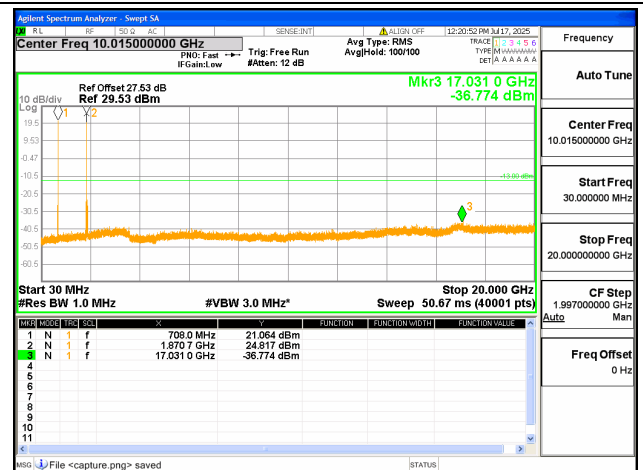
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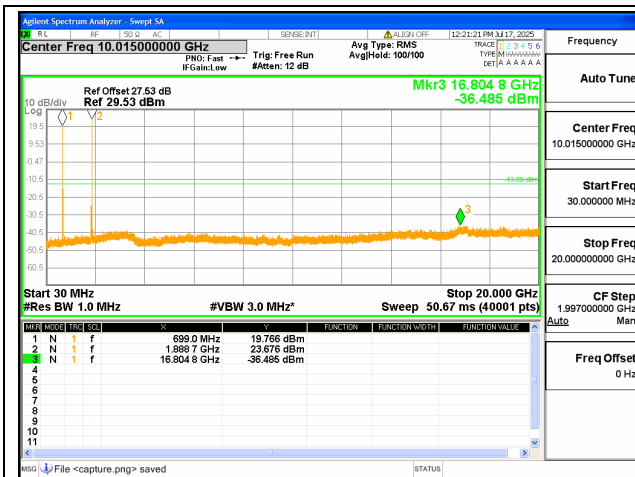
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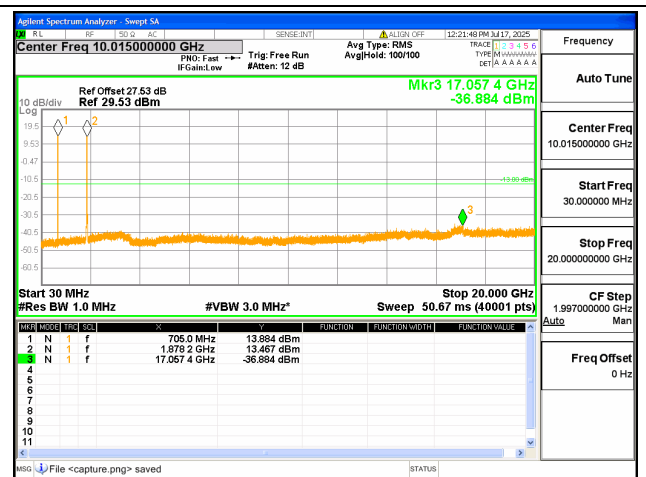
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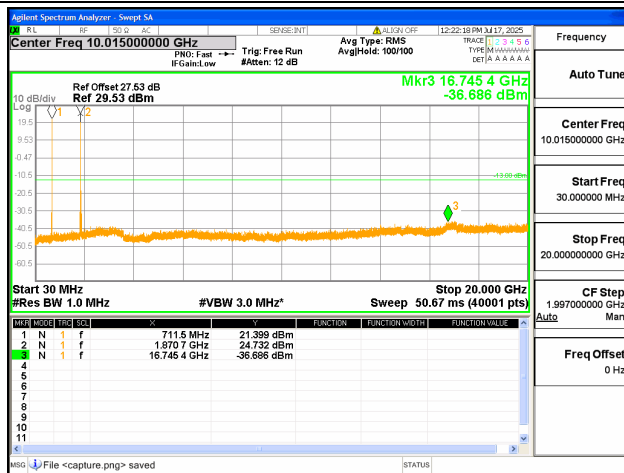
2A-12A / 20+10MHz / QPSK / Mid+Low CH /
1@0-1@49



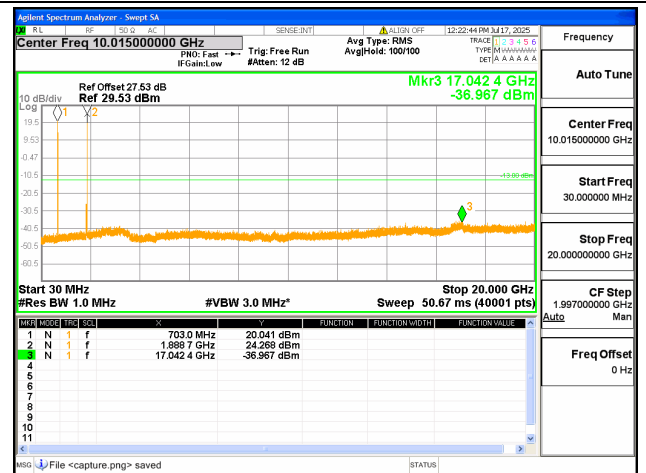
2A-12A / 20+10MHz / QPSK / Mid+Low CH /
1@99-1@0



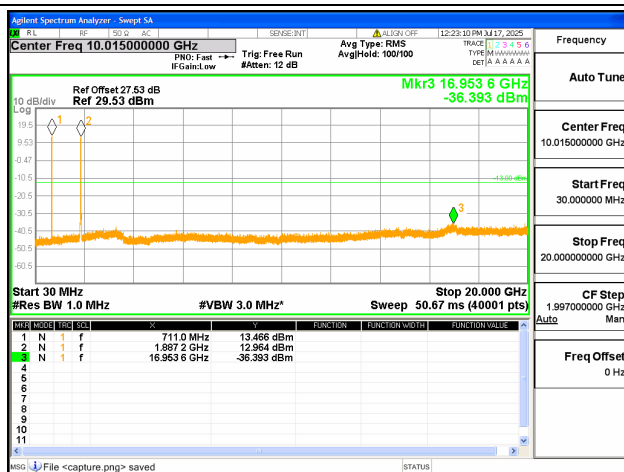
2A-12A / 20+10MHz / QPSK / Mid+Low CH /
100@0-50@0



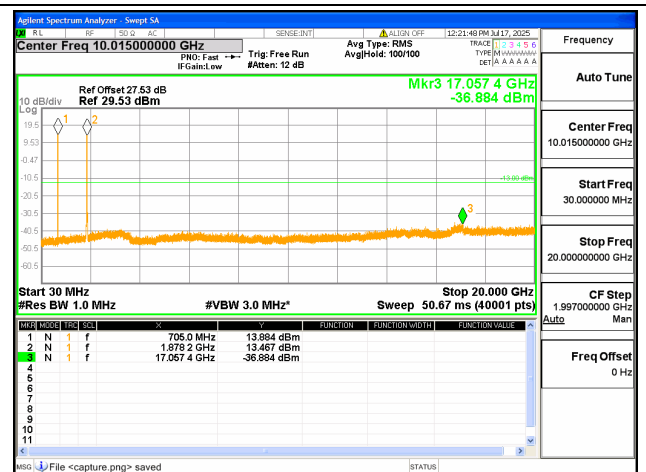
2A-12A / 20+10MHz / QPSK / Mid+Mid CH /
1@0-1@49



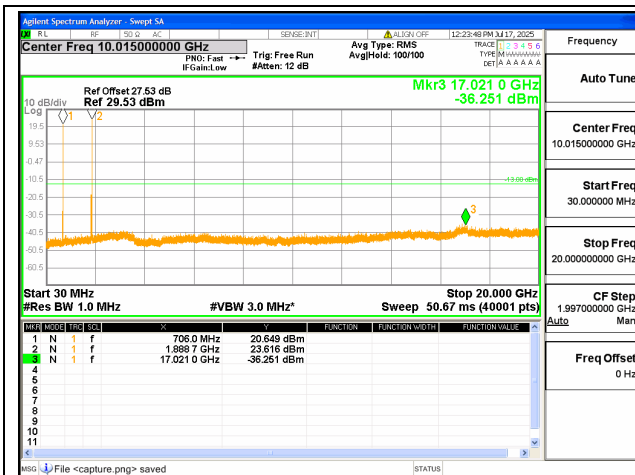
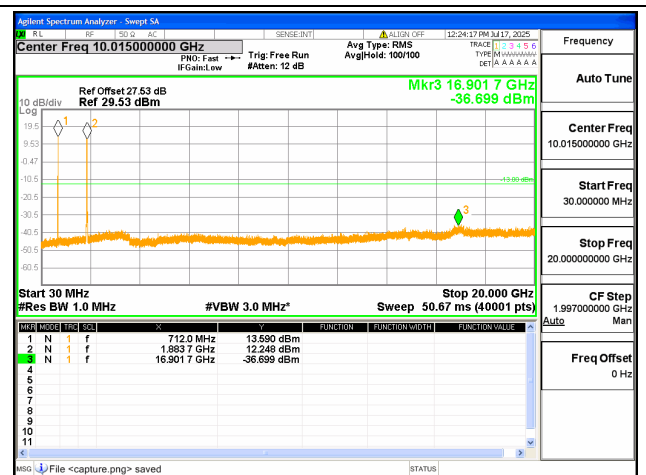
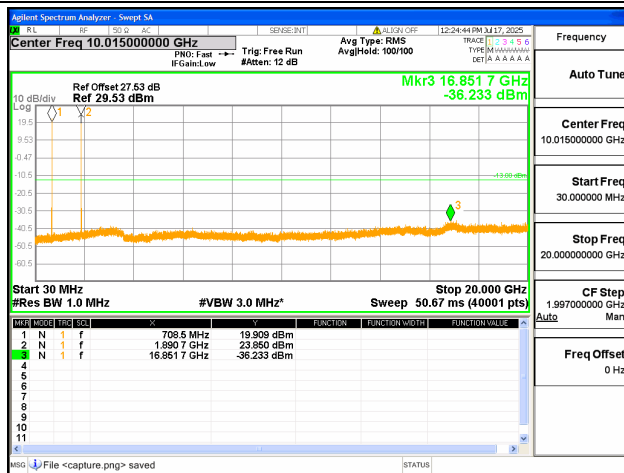
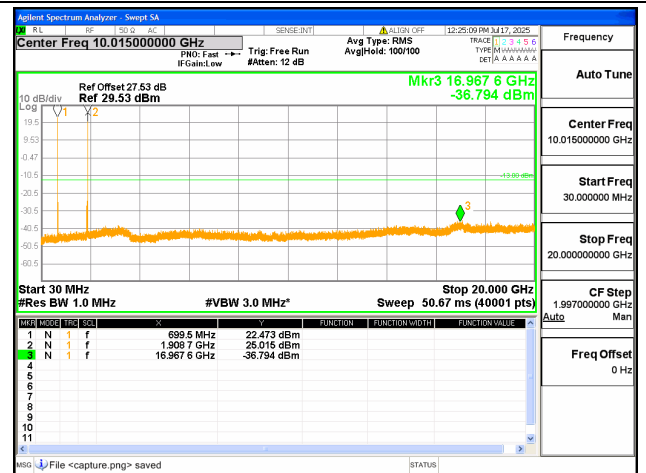
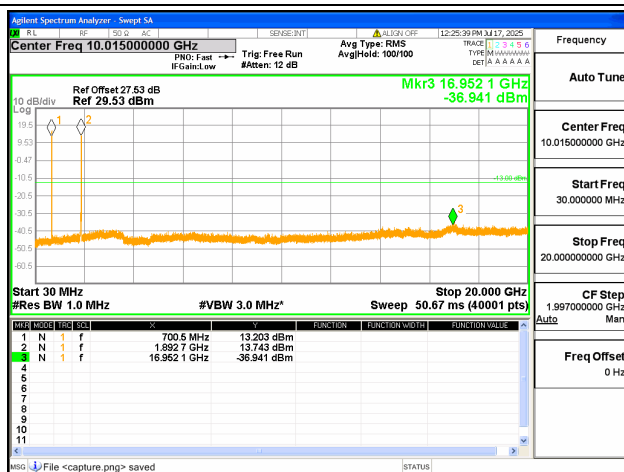
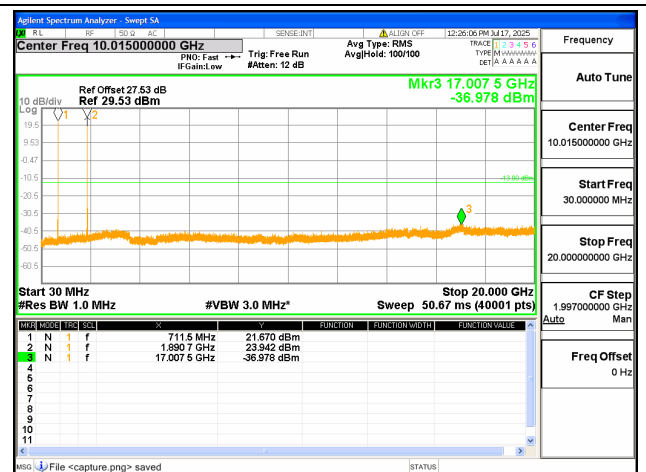
2A-12A / 20+10MHz / QPSK / Mid+Mid CH /
1@99-1@0

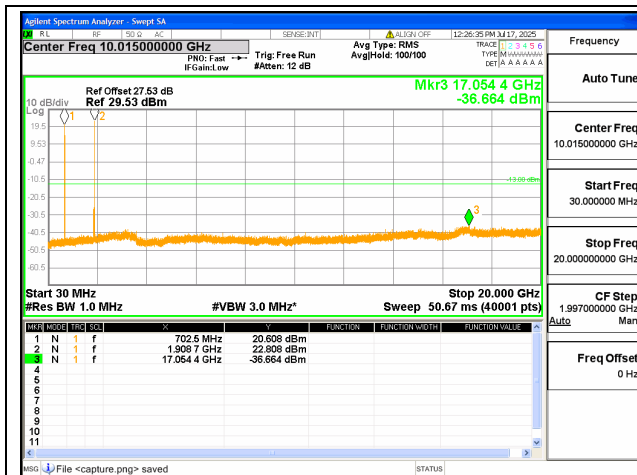
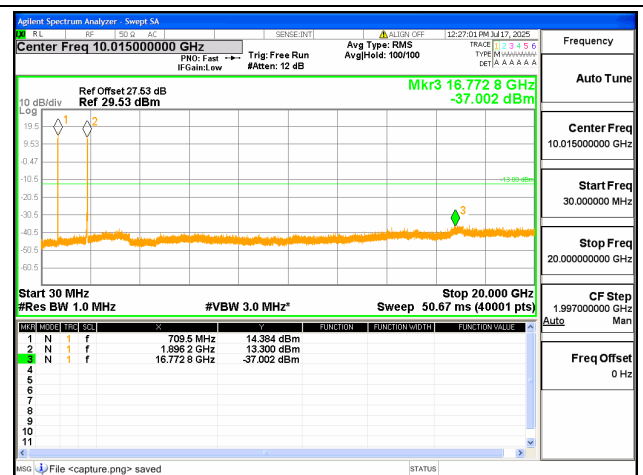
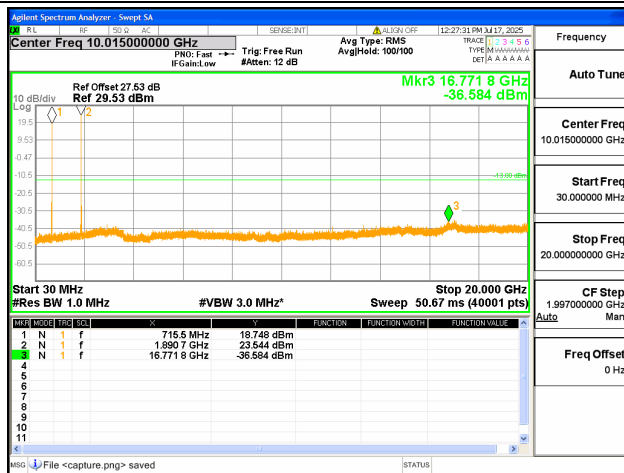
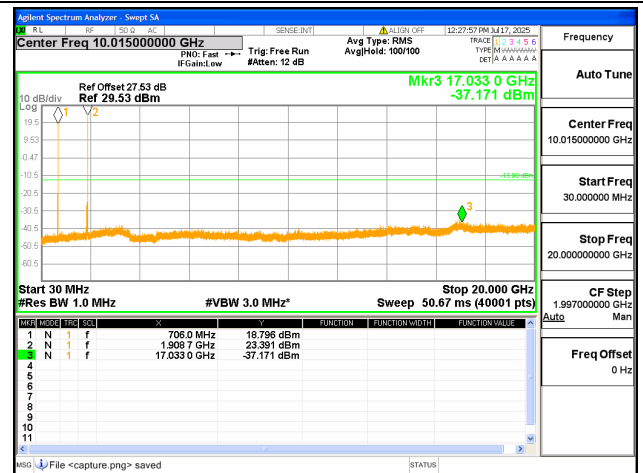
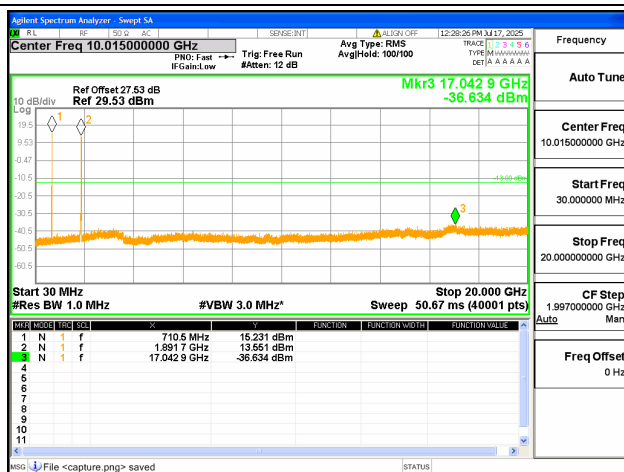


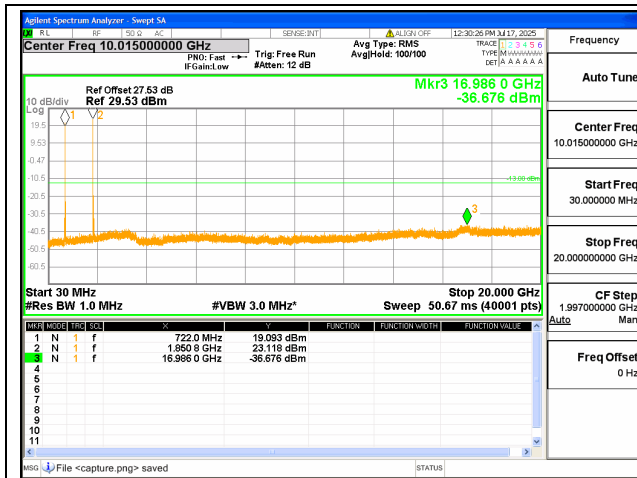
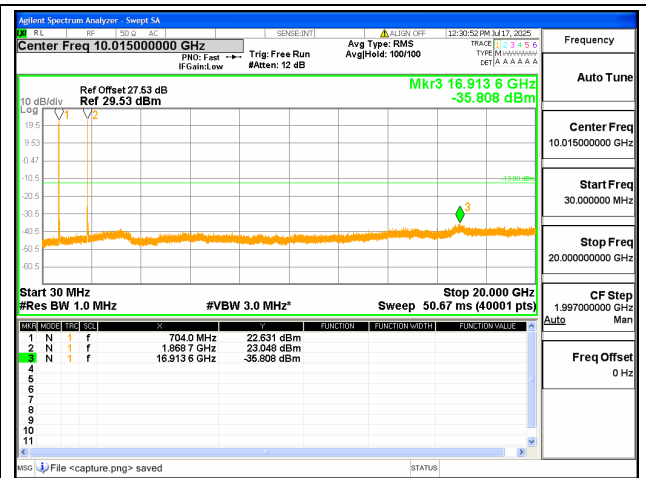
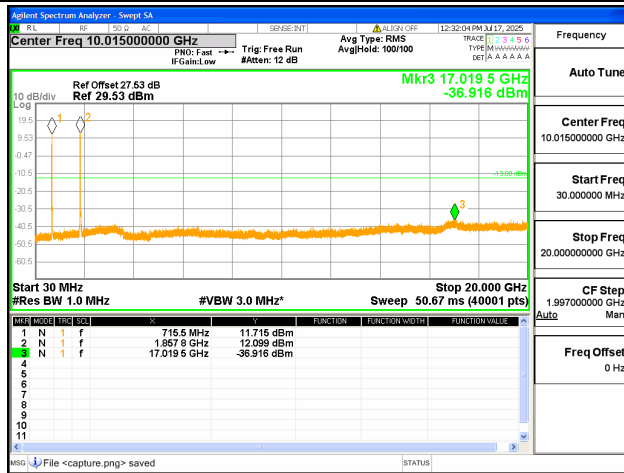
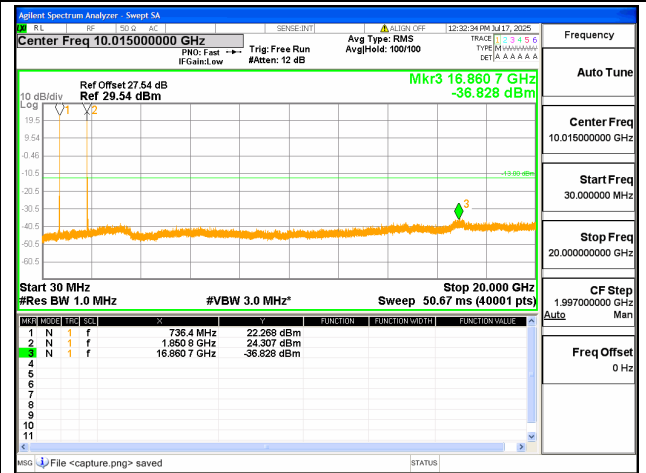
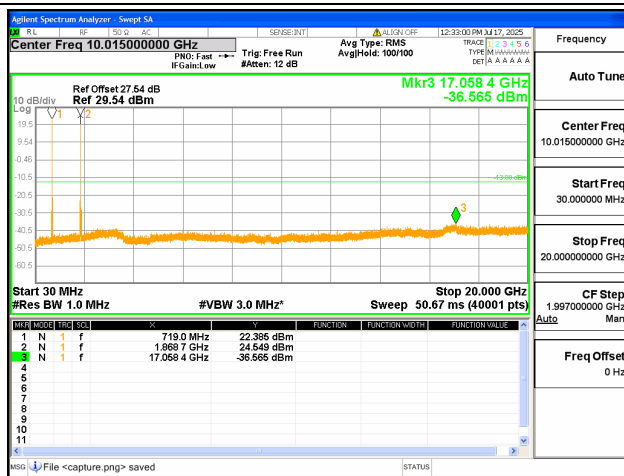
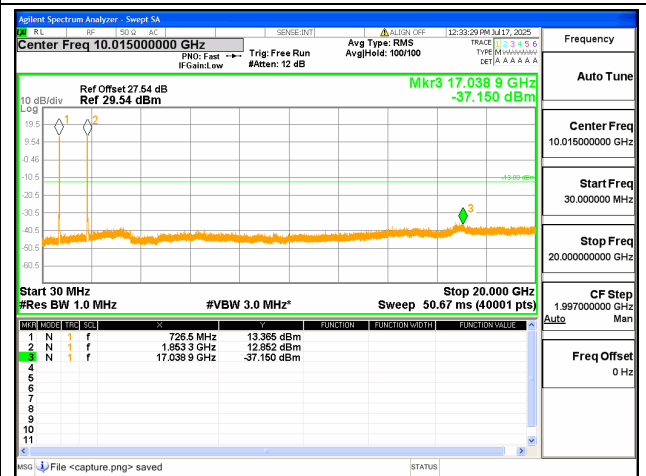
2A-12A / 20+10MHz / QPSK / Mid+Mid CH /
100@0-50@0



2A-12A / 20+10MHz / QPSK / Mid+High CH /
1@0-1@49

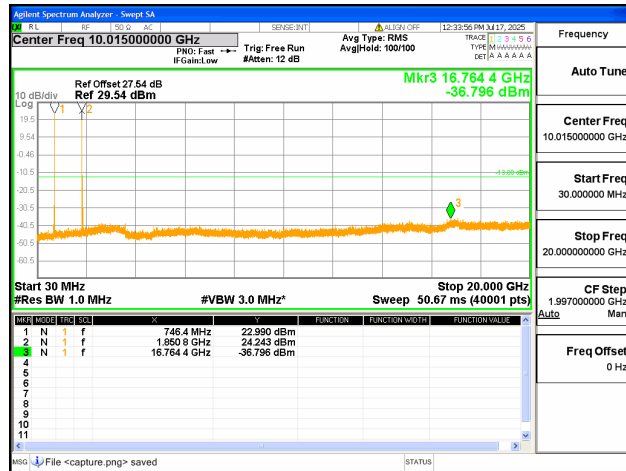
2A-12A / 20+10MHz / QPSK / Mid+High CH /
1@99-1@02A-12A / 20+10MHz / QPSK / Mid+High CH /
100@0-50@02A-12A / 20+10MHz / QPSK / High+Low CH /
1@0-1@492A-12A / 20+10MHz / QPSK / High+Low CH /
1@99-1@02A-12A / 20+10MHz / QPSK / High+Low CH /
100@0-50@02A-12A / 20+10MHz / QPSK / High+Mid CH /
1@0-1@49

2A-12A / 20+10MHz / QPSK / High+Mid CH /
1@99-1@02A-12A / 20+10MHz / QPSK / High+Mid CH /
100@0-50@02A-12A / 20+10MHz / QPSK / High+High CH /
1@0-1@492A-12A / 20+10MHz / QPSK / High+High CH /
1@99-1@02A-12A / 20+10MHz / QPSK / High+High CH /
100@0-50@0

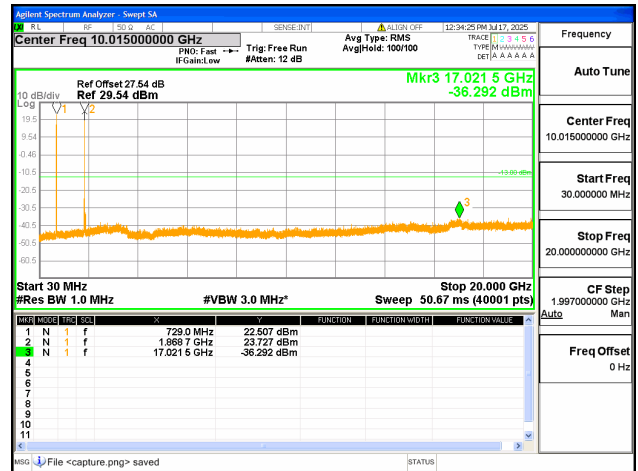
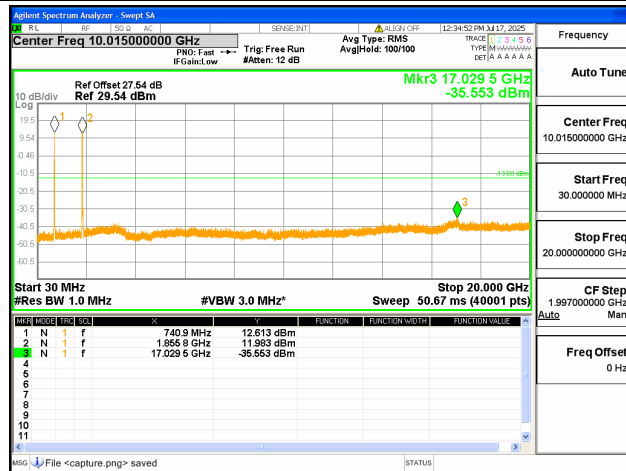
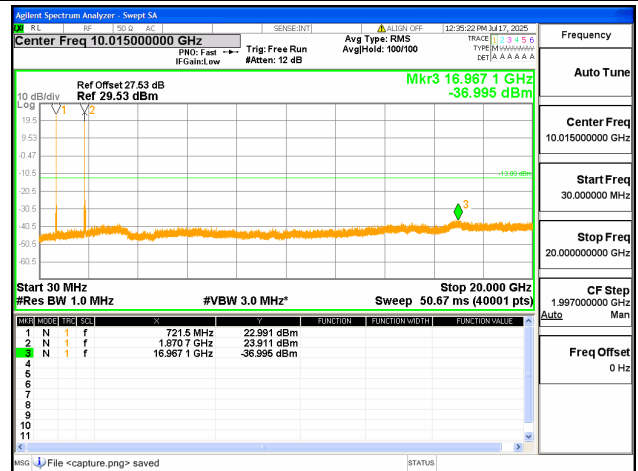
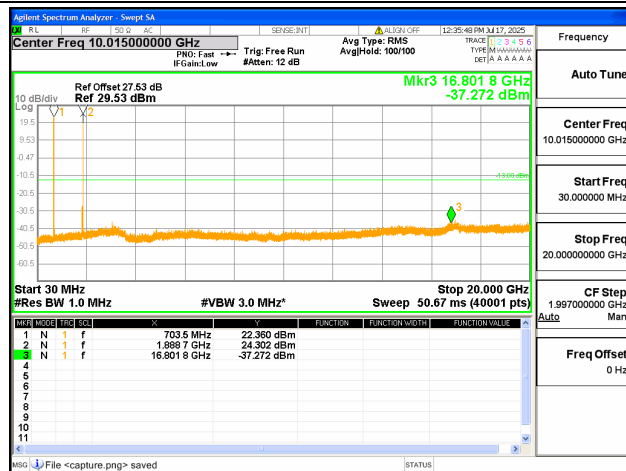
2A-28A / 20+20MHz / QPSK / Low+Low CH /
1@0-1@992A-28A / 20+20MHz / QPSK / Low+Low CH /
1@99-1@02A-28A / 20+20MHz / QPSK / Low+Low CH /
100@0-100@02A-28A / 20+20MHz / QPSK / Low+Mid CH /
1@0-1@992A-28A / 20+20MHz / QPSK / Low+Mid CH /
17@0-17@02A-28A / 20+20MHz / QPSK / Low+Mid CH /
17@0-17@0



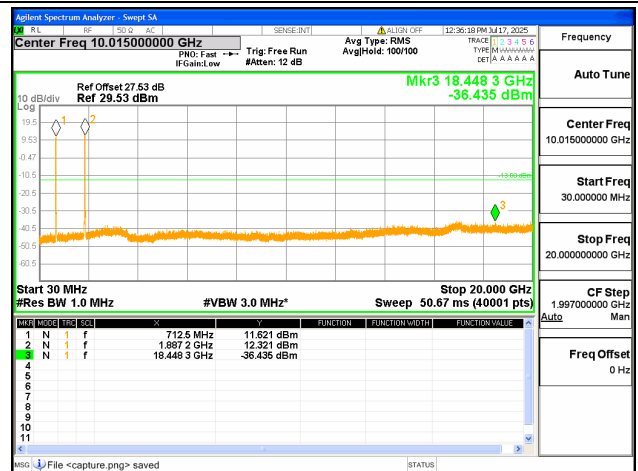
1@99-1@0

2A-28A / 20+20MHz / QPSK / Low+High CH /
1@0-1@99

100@0-100@0

2A-28A / 20+20MHz / QPSK / Low+High CH /
1@99-1@02A-28A / 20+20MHz / QPSK / Low+High CH /
100@0-100@02A-28A / 20+20MHz / QPSK / Mid+Low CH /
1@0-1@99

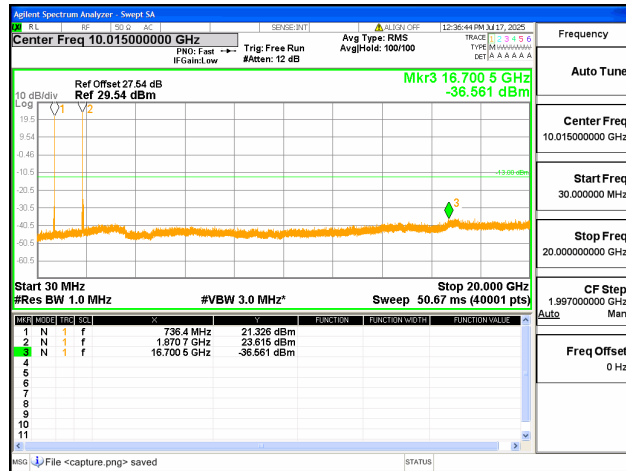
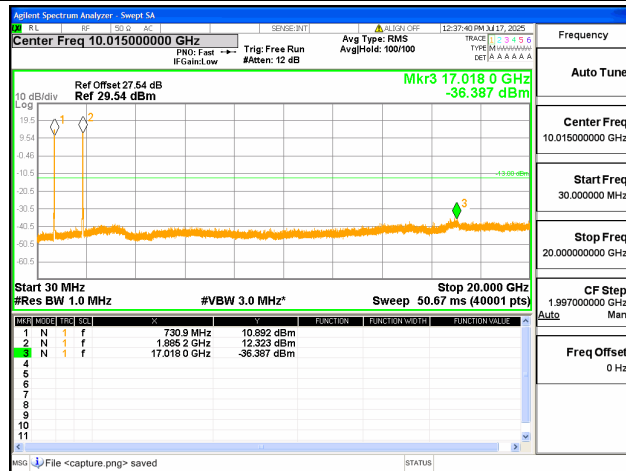
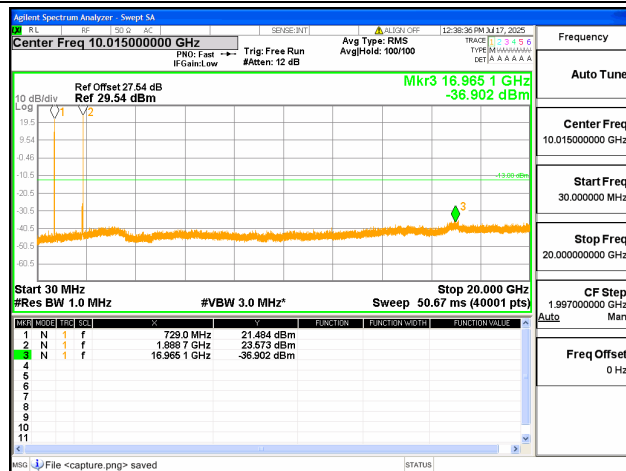
2A-28A / 20+20MHz / QPSK / Mid+Low CH /



2A-28A / 20+20MHz / QPSK / Mid+Low CH /

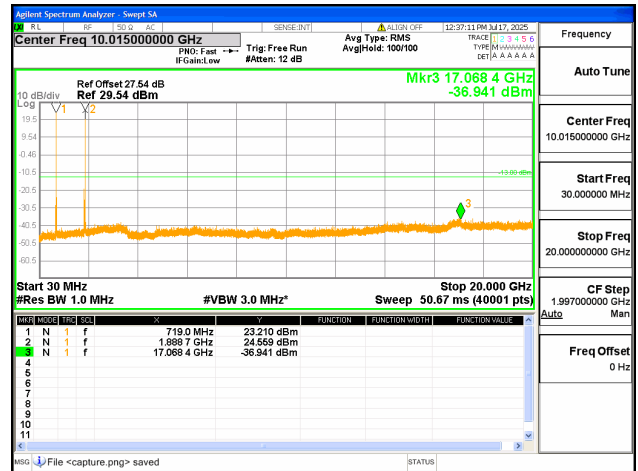
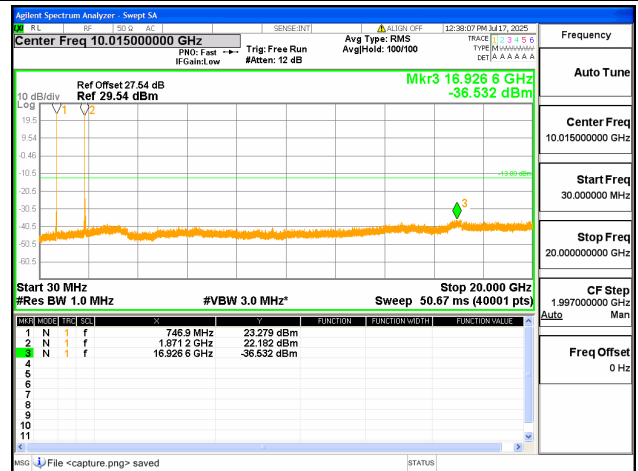
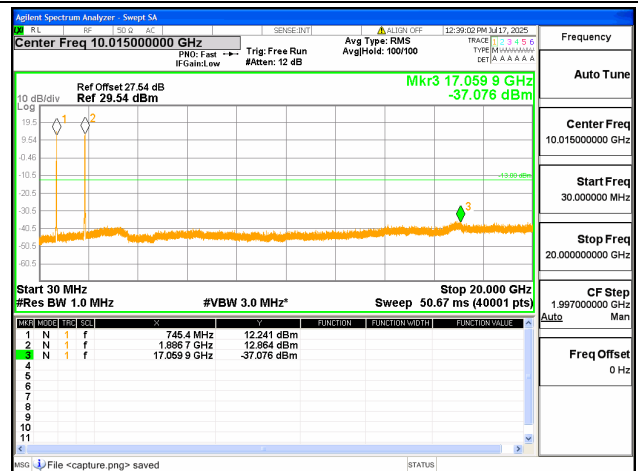


1@99-1@0

2A-28A / 20+20MHz / QPSK / Mid+Mid CH /
1@0-1@992A-28A / 20+20MHz / QPSK / Mid+Mid CH /
100@0-100@0

2A-28A / 20+20MHz / QPSK / Mid+High CH /

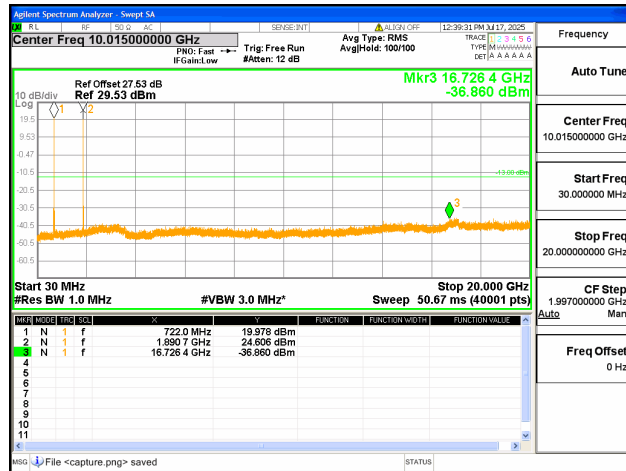
100@0-100@0

2A-28A / 20+20MHz / QPSK / Mid+Mid CH /
1@99-1@02A-28A / 20+20MHz / QPSK / Mid+High CH /
1@0-1@99

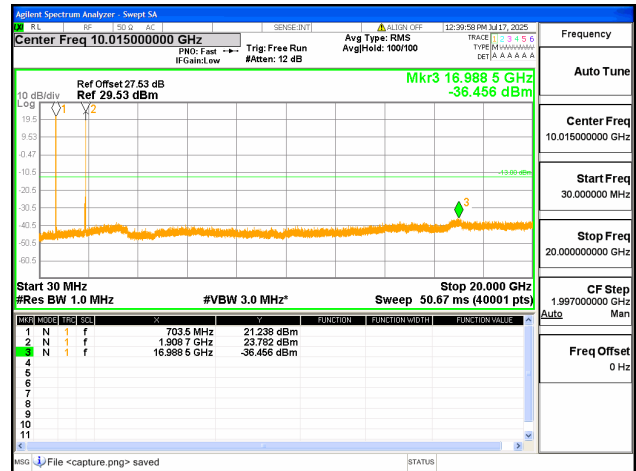
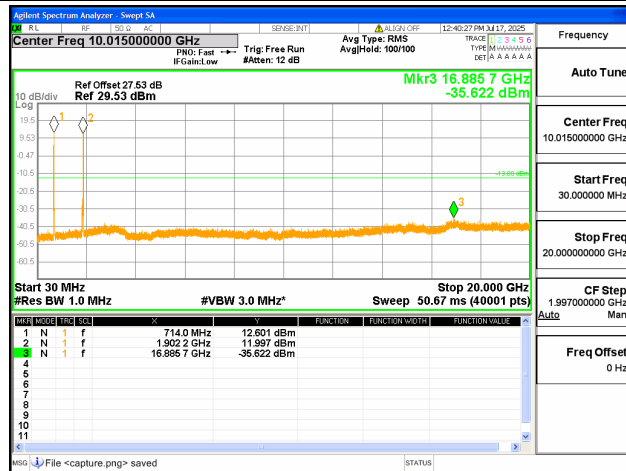
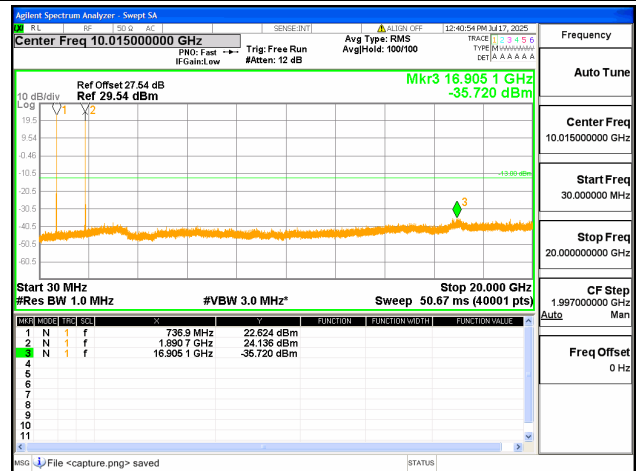
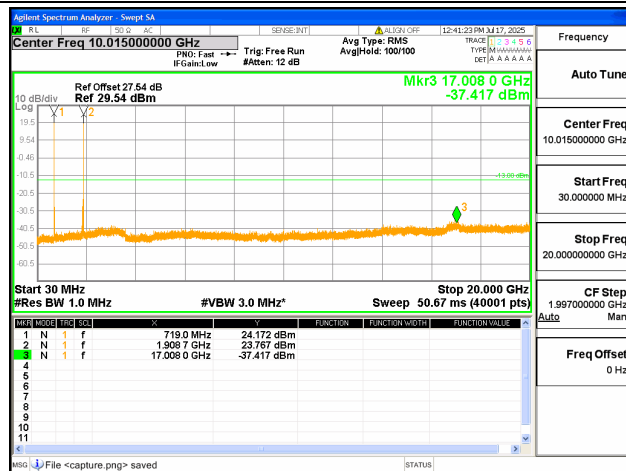
2A-28A / 20+20MHz / QPSK / Mid+High CH /



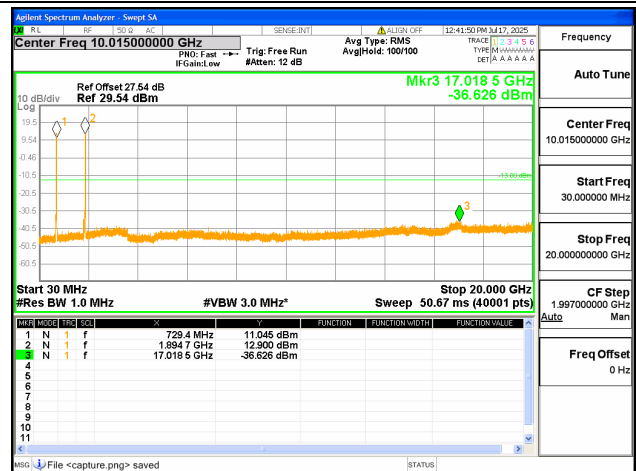
1@99-1@0

2A-28A / 20+20MHz / QPSK / High+Low CH /
1@0-1@99

100@0-100@0

2A-28A / 20+20MHz / QPSK / High+Low CH /
1@99-1@02A-28A / 20+20MHz / QPSK / High+Low CH /
100@0-100@02A-28A / 20+20MHz / QPSK / High+Mid CH /
1@0-1@99

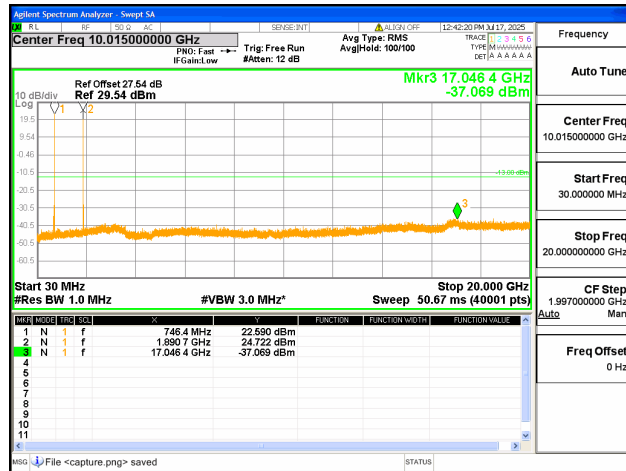
2A-28A / 20+20MHz / QPSK / High+Mid CH /



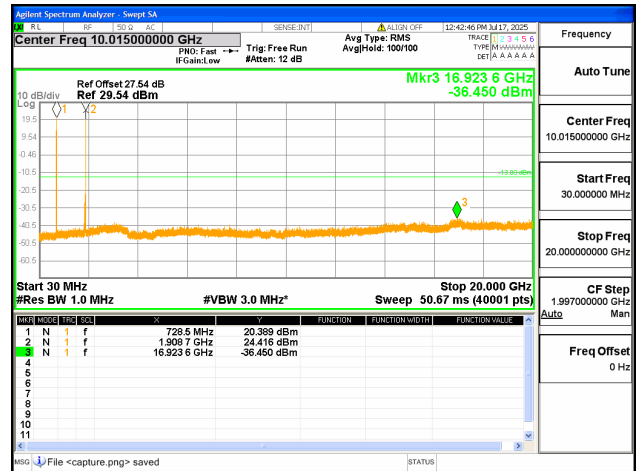
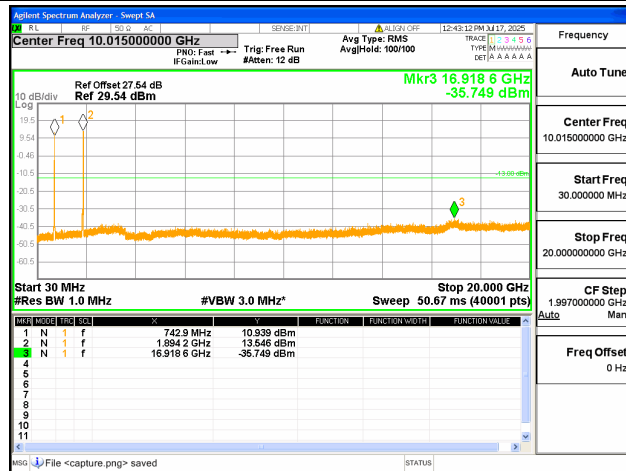
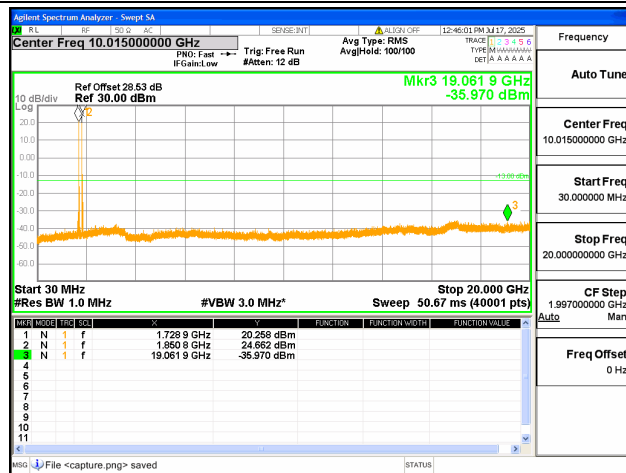
2A-28A / 20+20MHz / QPSK / High+Mid CH /



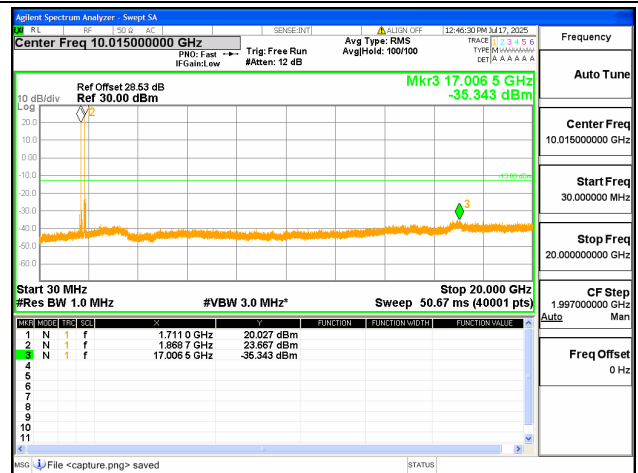
1@99-1@0

2A-28A / 20+20MHz / QPSK / High+High CH /
1@0-1@99

100@0-100@0

2A-28A / 20+20MHz / QPSK / High+High CH /
1@99-1@02A-28A / 20+20MHz / QPSK / High+High CH /
100@0-100@0

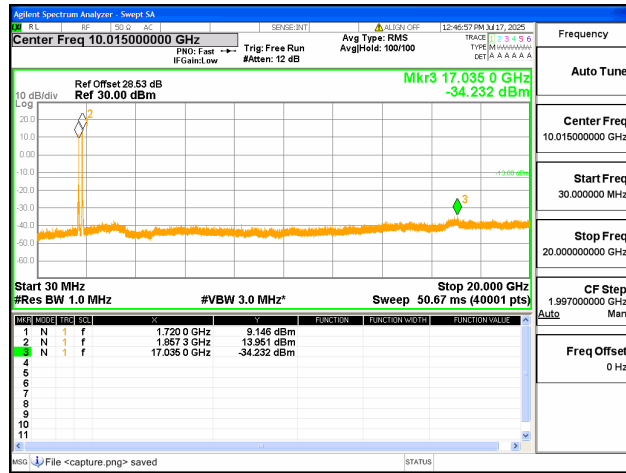
2A-66A / 20+20MHz / QPSK / Low+Low CH /



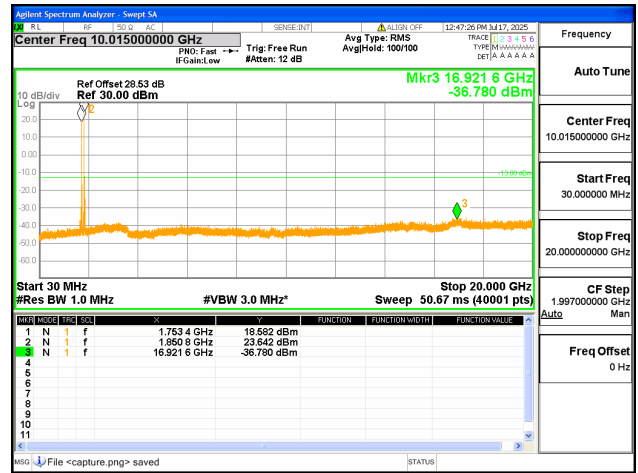
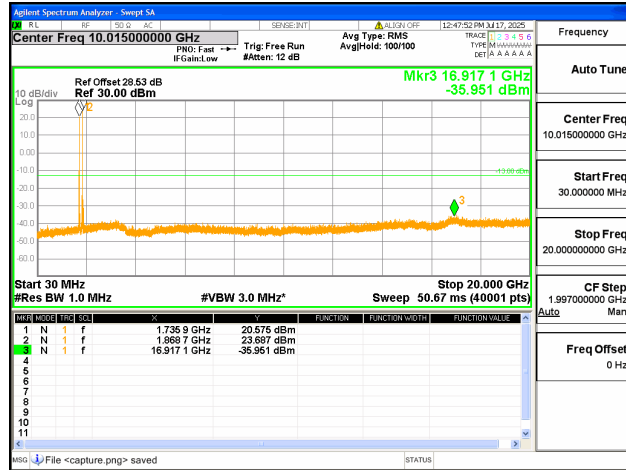
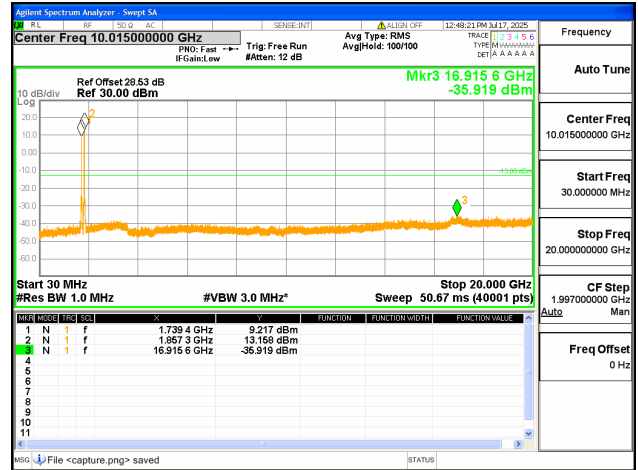
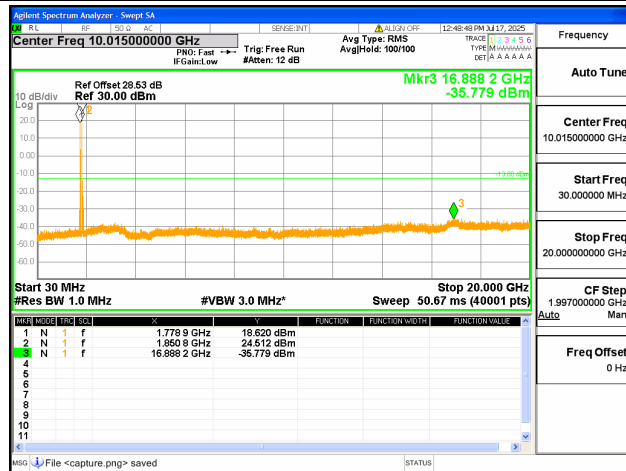
2A-66A / 20+20MHz / QPSK / Low+Low CH /



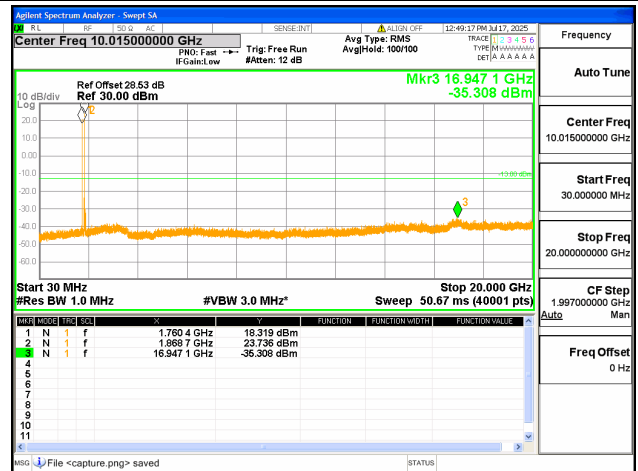
1@0-1@99

2A-66A / 20+20MHz / QPSK / Low+Low CH /
100@0-100@0

1@99-1@0

2A-66A / 20+20MHz / QPSK / Low+Mid CH /
1@0-1@992A-66A / 20+20MHz / QPSK / Low+Mid CH /
1@99-1@02A-66A / 20+20MHz / QPSK / Low+Mid CH /
100@0-100@0

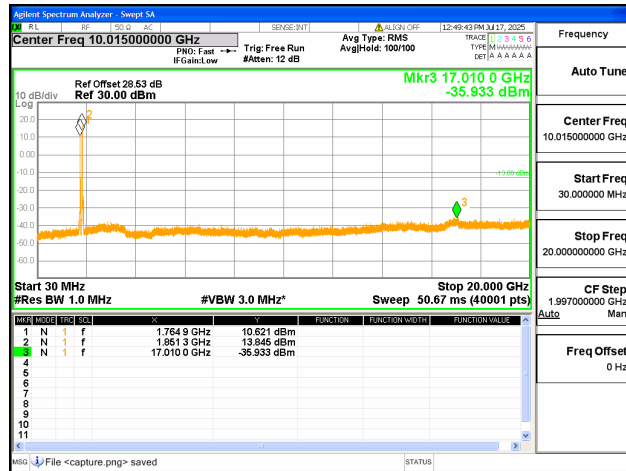
2A-66A / 20+20MHz / QPSK / Low+High CH /



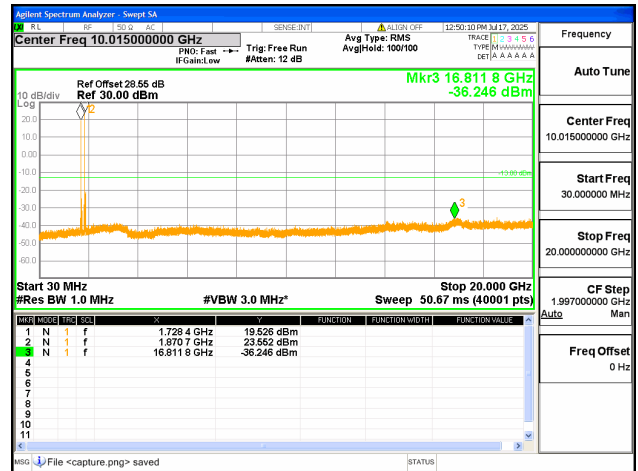
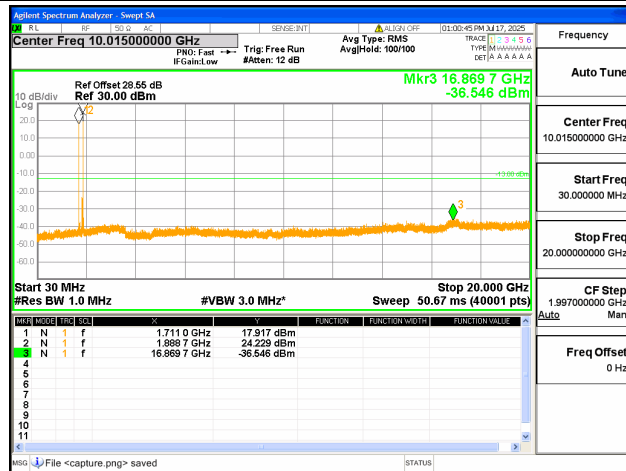
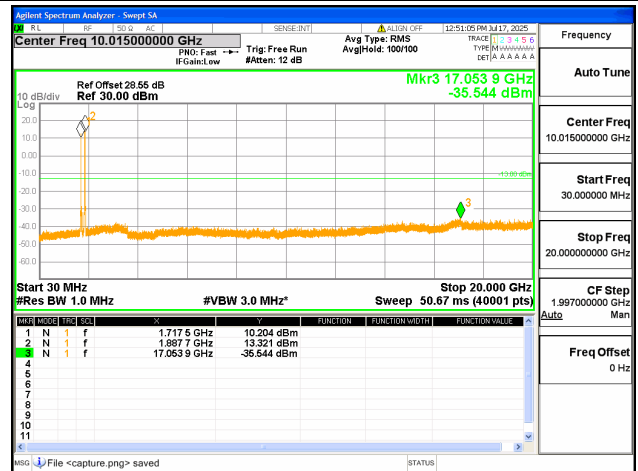
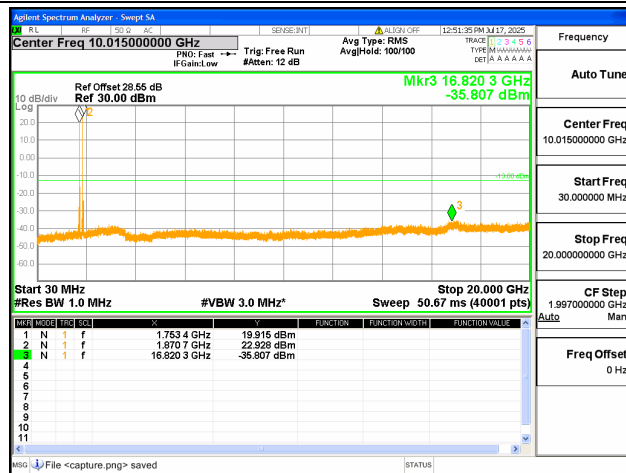
2A-66A / 20+20MHz / QPSK / Low+High CH /



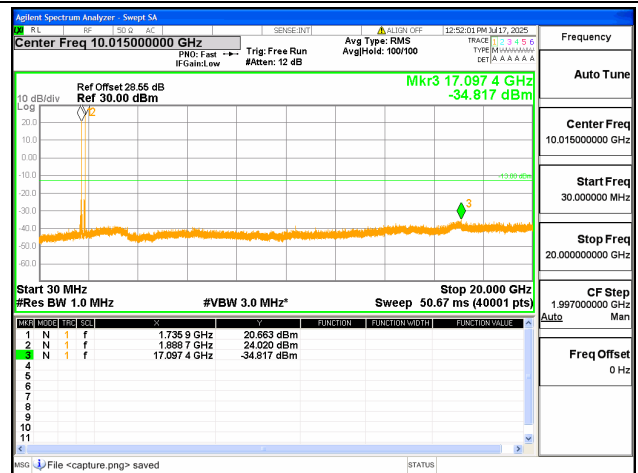
1@0-1@99

2A-66A / 20+20MHz / QPSK / Low+High CH /
100@0-100@0

1@99-1@0

2A-66A / 20+20MHz / QPSK / Mid+Low CH /
1@0-1@992A-66A / 20+20MHz / QPSK / Mid+Low CH /
1@99-1@02A-66A / 20+20MHz / QPSK / Mid+Low CH /
100@0-100@0

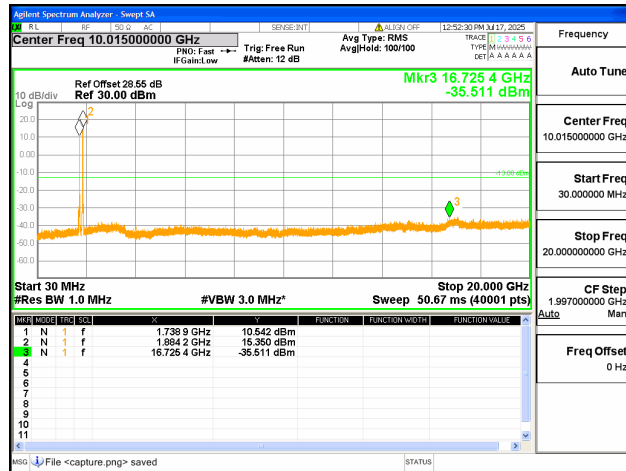
2A-66A / 20+20MHz / QPSK / Mid+Mid CH /



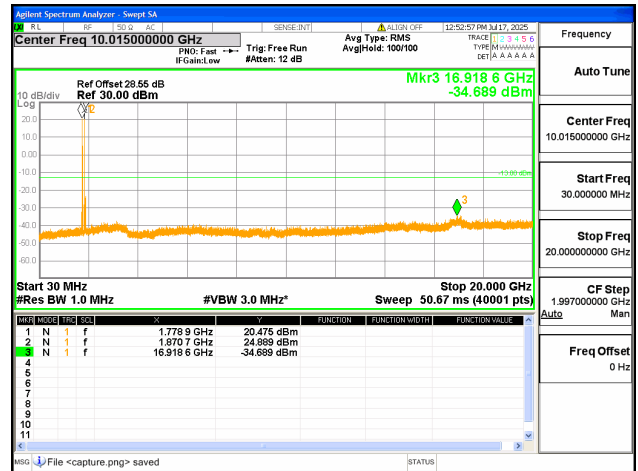
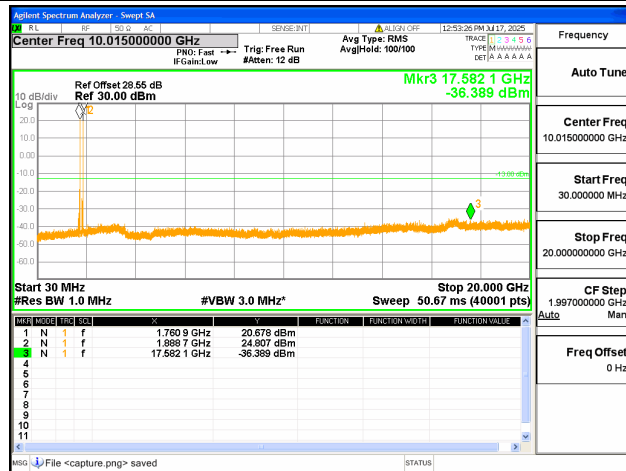
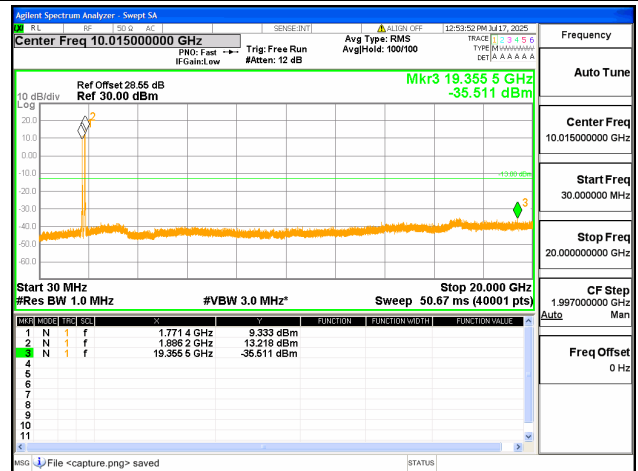
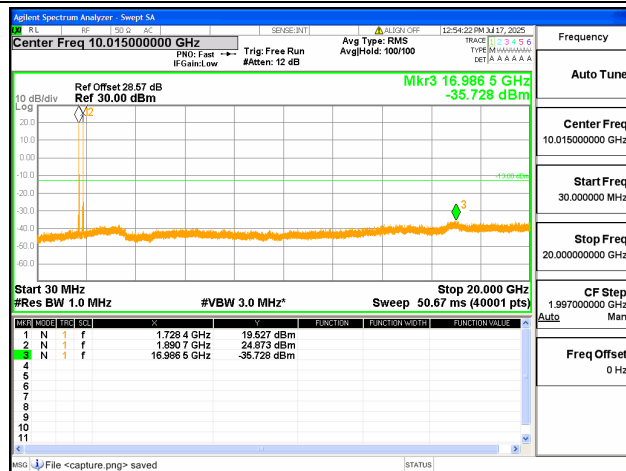
2A-66A / 20+20MHz / QPSK / Mid+Mid CH /



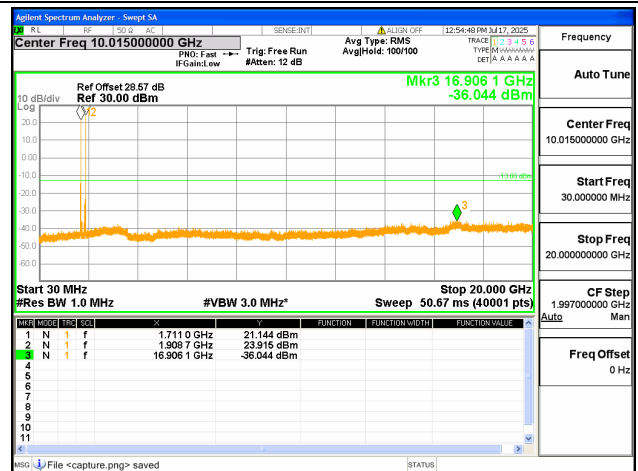
1@0-1@99

2A-66A / 20+20MHz / QPSK / Mid+Mid CH /
100@0-100@0

1@99-1@0

2A-66A / 20+20MHz / QPSK / Mid+High CH /
1@0-1@992A-66A / 20+20MHz / QPSK / Mid+High CH /
1@99-1@02A-66A / 20+20MHz / QPSK / Mid+High CH /
100@0-100@0

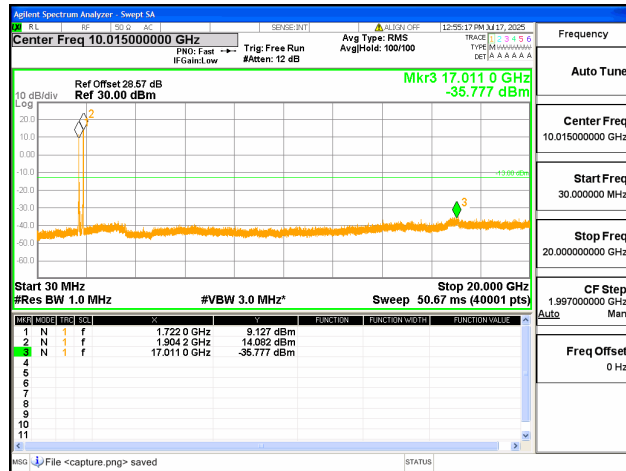
2A-66A / 20+20MHz / QPSK / High+Low CH /



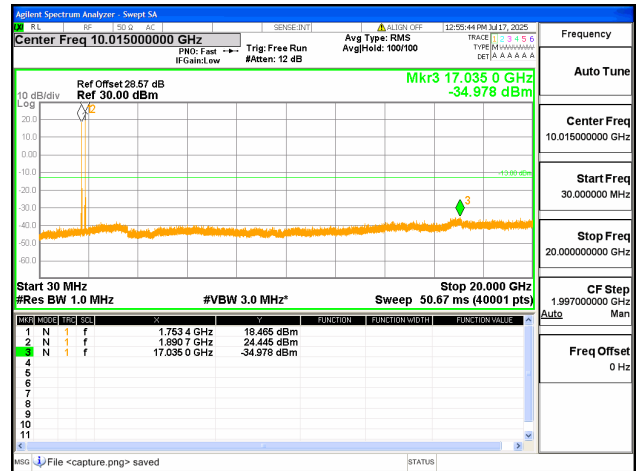
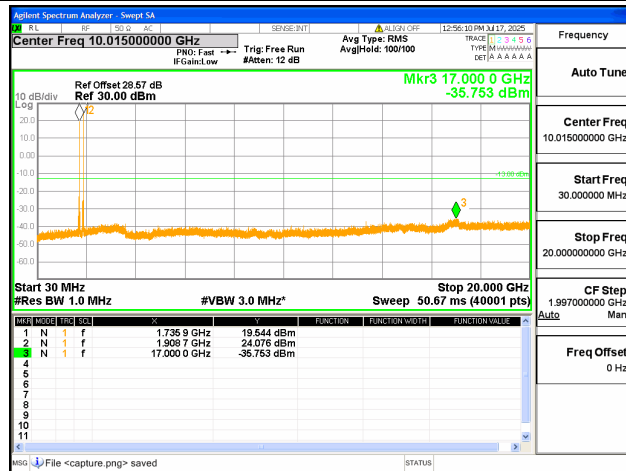
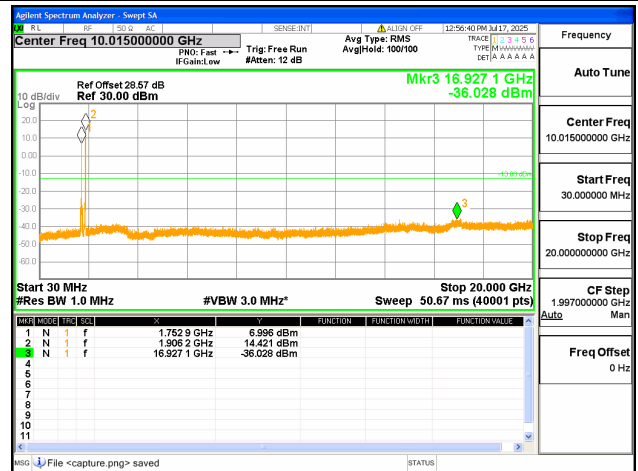
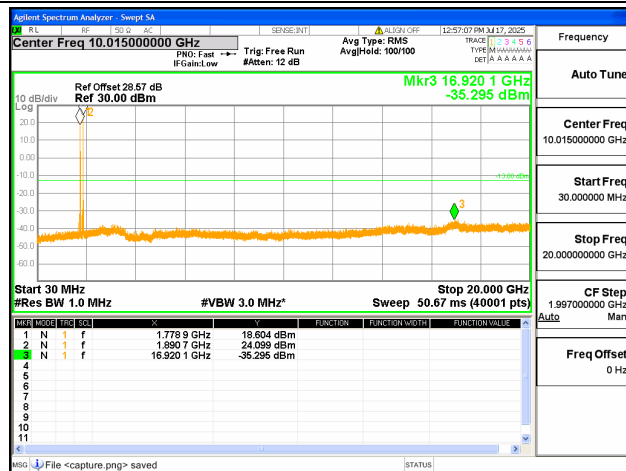
2A-66A / 20+20MHz / QPSK / High+Low CH /



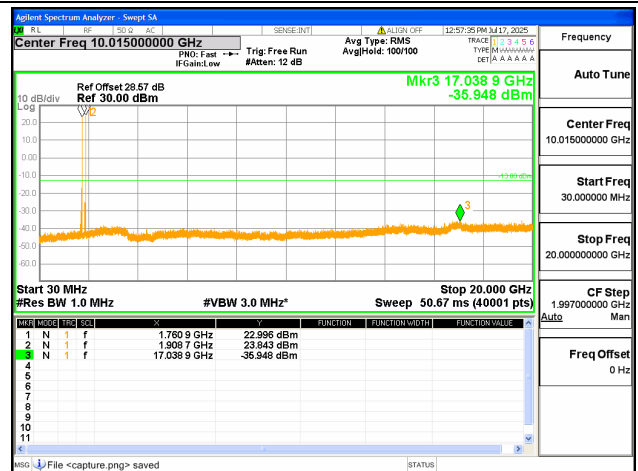
1@0-1@99

2A-66A / 20+20MHz / QPSK / High+Low CH /
100@0-100@0

1@99-1@0

2A-66A / 20+20MHz / QPSK / High+Mid CH /
1@0-1@992A-66A / 20+20MHz / QPSK / High+Mid CH /
1@99-1@02A-66A / 20+20MHz / QPSK / High+Mid CH /
100@0-100@0

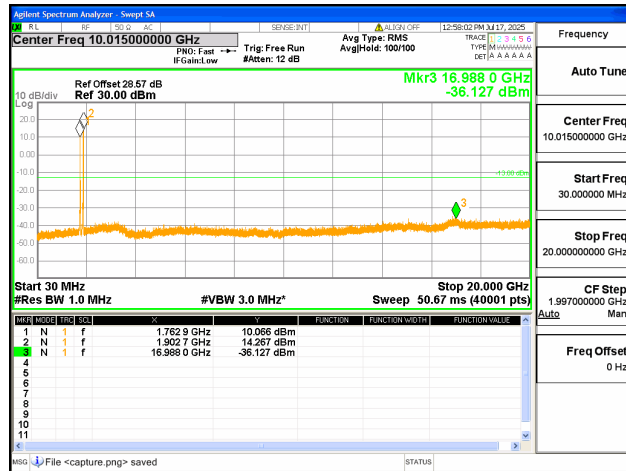
2A-66A / 20+20MHz / QPSK / High+High CH /



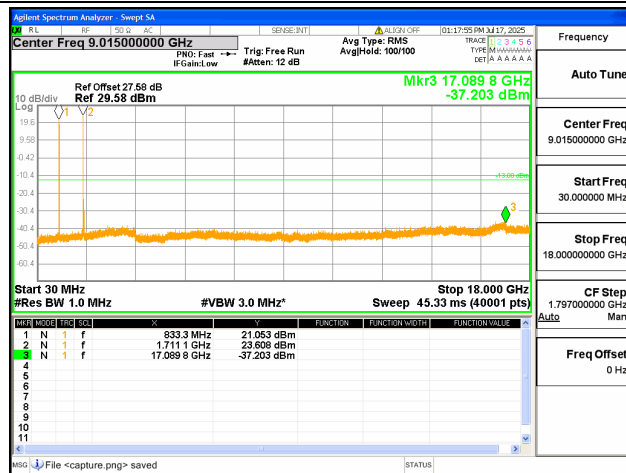
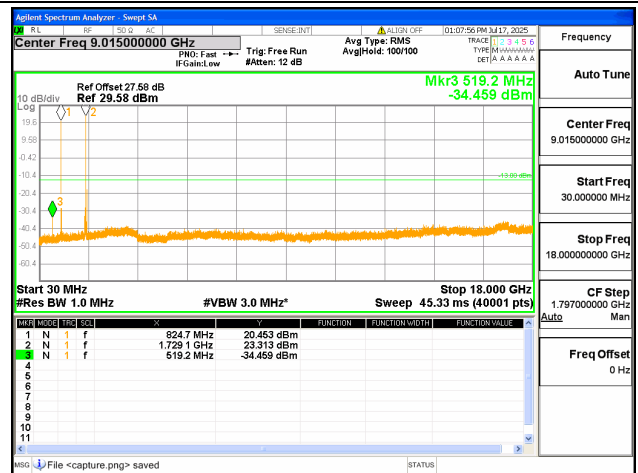
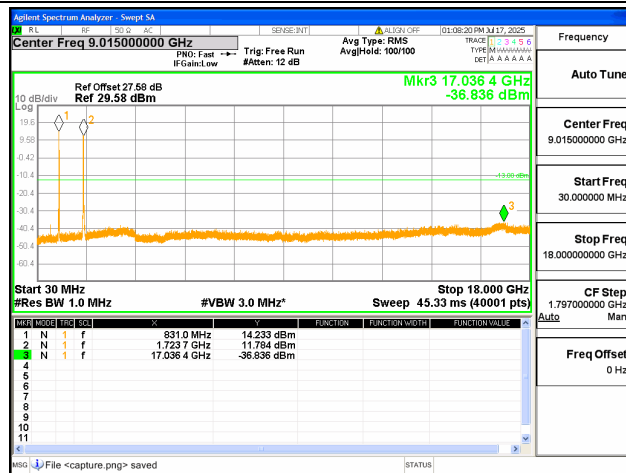
2A-66A / 20+20MHz / QPSK / High+High CH /



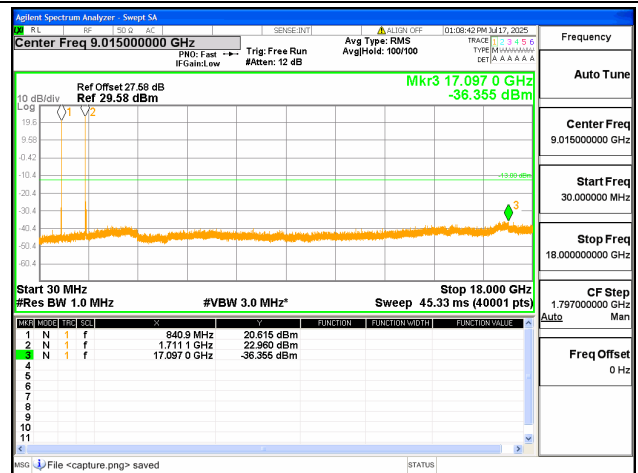
1@0-1@99

2A-66A / 20+20MHz / QPSK / High+High CH /
100@0-100@0

1@99-1@0

4A-5A / 20+10MHz / QPSK / Low+Low CH /
1@0-1@494A-5A / 20+10MHz / QPSK / Low+Low CH /
1@99-1@0

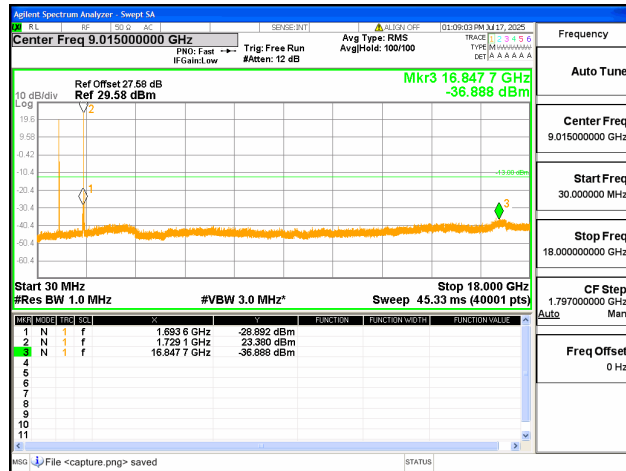
4A-5A / 20+10MHz / QPSK / Low+Low CH /



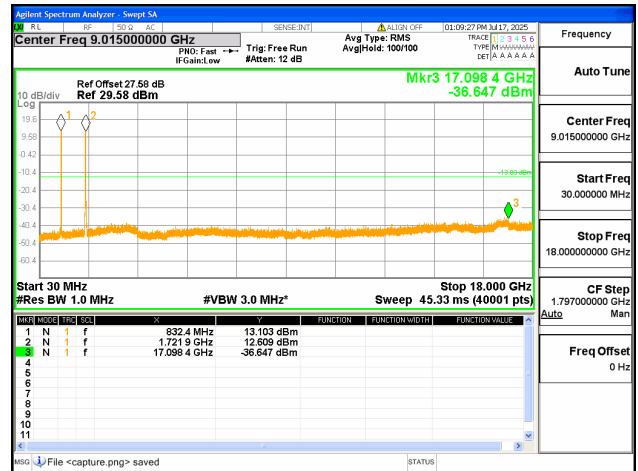
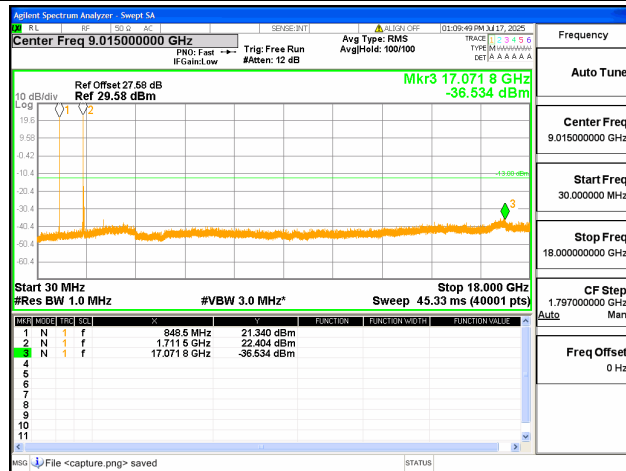
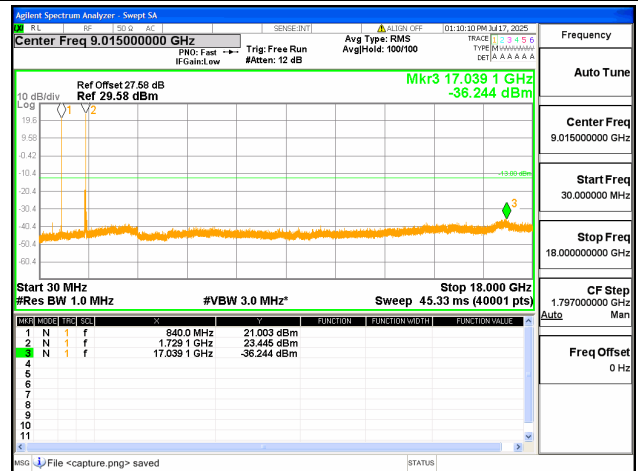
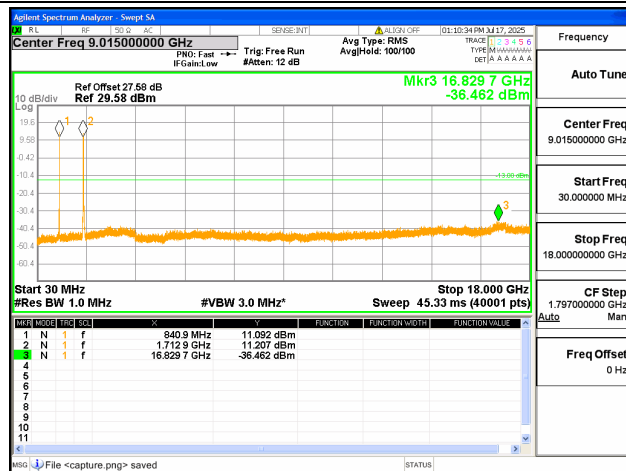
4A-5A / 20+10MHz / QPSK / Low+Mid CH /



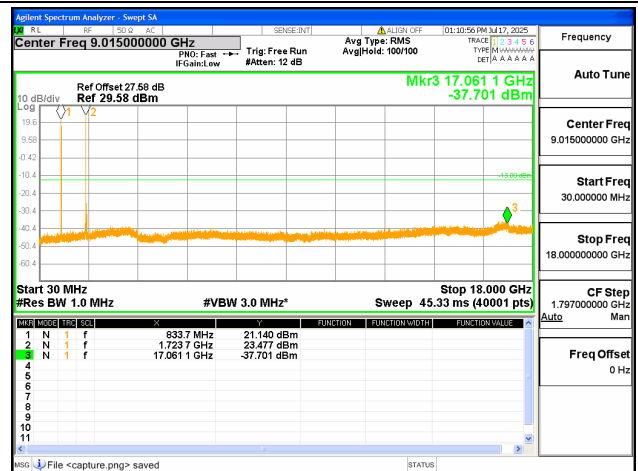
100@0-50@0

4A-5A / 20+10MHz / QPSK / Low+Mid CH /
1@99-1@0

1@0-1@49

4A-5A / 20+10MHz / QPSK / Low+Mid CH /
100@0-50@04A-5A / 20+10MHz / QPSK / Low+High CH /
1@0-1@494A-5A / 20+10MHz / QPSK / Low+High CH /
1@99-1@0

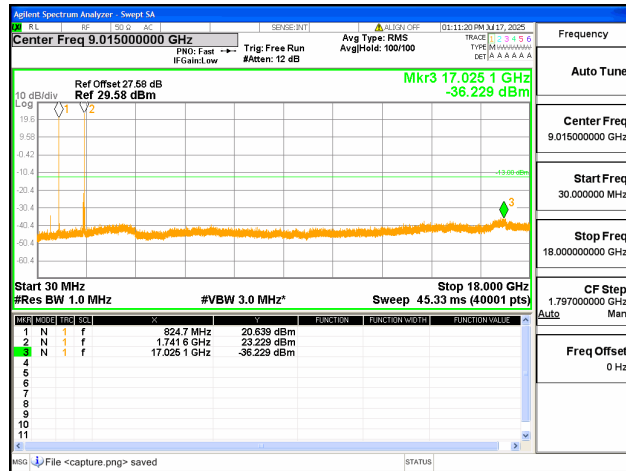
4A-5A / 20+10MHz / QPSK / Low+High CH /



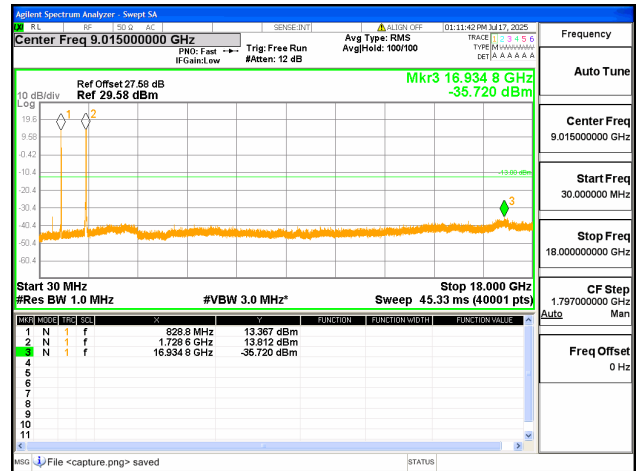
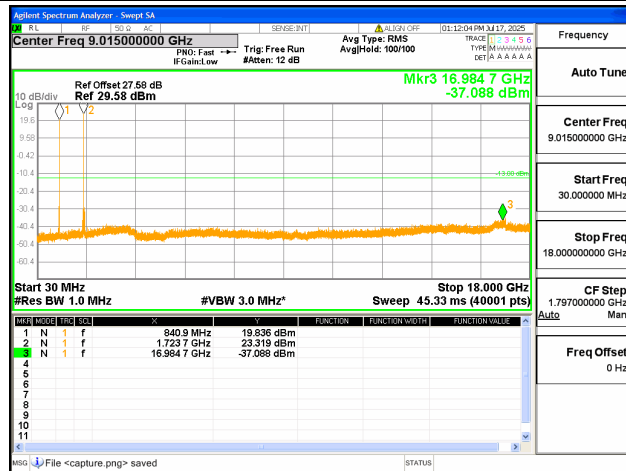
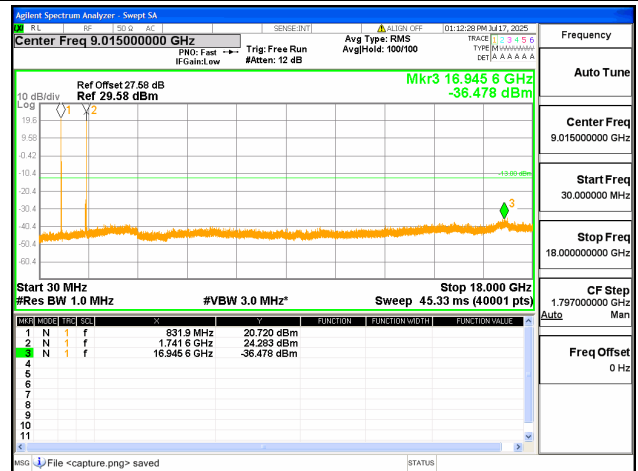
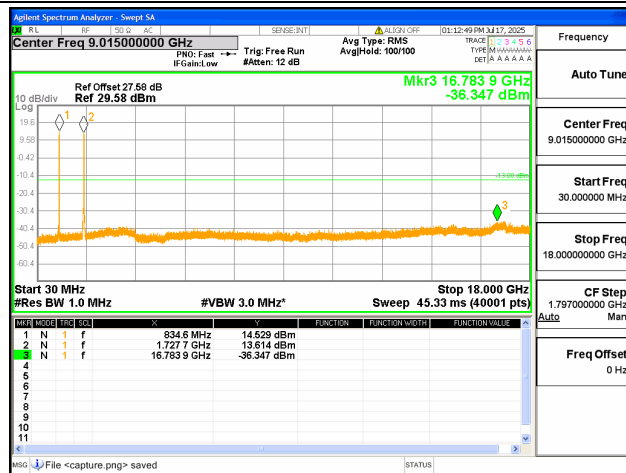
4A-5A / 20+10MHz / QPSK / Mid+Low CH /



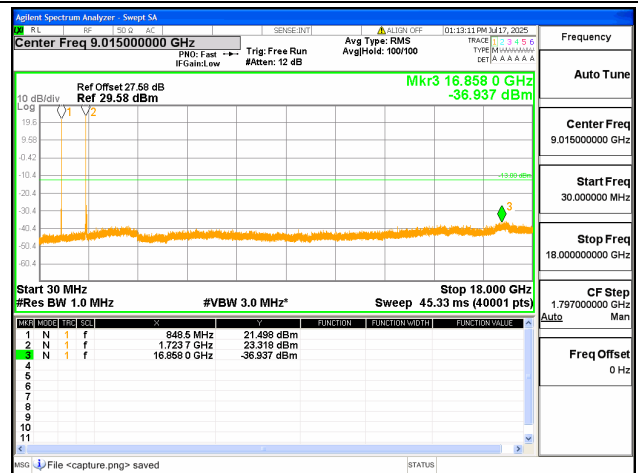
100@0-50@0

4A-5A / 20+10MHz / QPSK / Mid+Low CH /
1@99-1@0

1@0-1@49

4A-5A / 20+10MHz / QPSK / Mid+Low CH /
100@0-50@04A-5A / 20+10MHz / QPSK / Mid+Mid CH /
1@0-1@494A-5A / 20+10MHz / QPSK / Mid+Mid CH /
1@99-1@0

4A-5A / 20+10MHz / QPSK / Mid+Mid CH /



4A-5A / 20+10MHz / QPSK / Mid+High CH /