Extended Range Module

915 MHz ISM Band RF Transceiver Module

KEY FEATURES

- Receiver sensitivity: -140 dBm
- Transmit power: 400 mW
- TX Peak Current: 350 mA
- RX Peak Current: 10 mA
- Sleep current: 10 **µ**A
- UART interface

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SYSTEM OVERVIEW

Specifications

Frequency band	915 MHz ISM (902 MHz – 928 MHz)
On-air data rate	183 bps to 37.5 kbps
Output power	Programmable: +26 to +11 dBm in .6 dB steps
Supply Voltage	3.5 V to 5.5 V
I/O Voltage	3.3 V
Tx Current	350 mA
Rx Current	10 mA
Sleep Current	10 µA
Dimensions	16 mm x 29 mm (without on-board antenna) 25 mm x 29 mm (on-board antenna)
Operating Temperature	-40 to 85° C (industrial)
UART baud rate	Default: 115200 8n1

PINOUT

Pin #	Name	Type	Description
1	GND	Ground	
2	Reserved		
3	VBAT	Input voltage	Module supply voltage (3.5 V to 5.5V)
4	LDO_OUT	Output voltage	Power digital. 3.3 V output
5	Reserved	Reserved	
6	MD/FINED	IO	FINE interface
7	nRESET	I	External reset pin, active low
8	HOST_IO0	O	Status message indicator
9	GND	Ground	
10	SDA0	IO	I²C0: serial data
11	SCL0	IO	I²C0: serial clock
12	SMISO1/SSCL1	IO	SPI1: master in slave out
13	SMOSI1/SSDA1	IO	SPI1: master out slave in
14	Reserved		
15	SCK1	IO	SPI1: serial clock
16	Reserved		
17	BOOT	I	NC = Boot normally, GND = Bootloader
18	nSS1	IO	SPI1: slave select
19	WAKEUP/FINEC	IO	FINE interface
20	DP	IO	USB Data+ (D+)
21	DM	IO	USB Data- (D-)
22	HOST_IO1	I	Analog input. 0 – VCC
23	HOST_RXIN	I	UART interface: module Rx
24	HOST_TXOUT	O	UART interface: module Tx
25	GND	Ground	

Notes:

1. RF connection is via U.FL connector on upper left side of the module
2. RF output is 50 Ω, DC blocking cap not required
3. ALL I/O signals must be 3.3 V. If necessary, pin 4 (3.3 OUT) may be used to supply an off-module level shifter
4. Default UART baud rate is 115200 8n1

PCB LAYOUT REQUIREMENTS

Since this module and its associated set of approved antennas has been certified by the FCC and Industry Canada (IC) as a Modular Radio, the end user is authorized to integrate this module into an end-product, and is solely responsible for the Unintentional Emissions levels produced by the end-product.

In order to preserve the Modular Radio certifications, the integrator of the module must abide by the PCB layout recommendations outlined in the following paragraphs. Any divergence from these recommendations will invalidate the modular radio certifications and require the integrator to re-certify the module and/or end product.

The module must be used with one of the approved antennas:

- ½-wave center-fed omni antennas tuned for 915 MHz with a maximum gain of 1.9 dBi or less, such as ANT-916-CW-HWR-SMA
- On-board antenna

ANTENNA REFERENCE DESIGN

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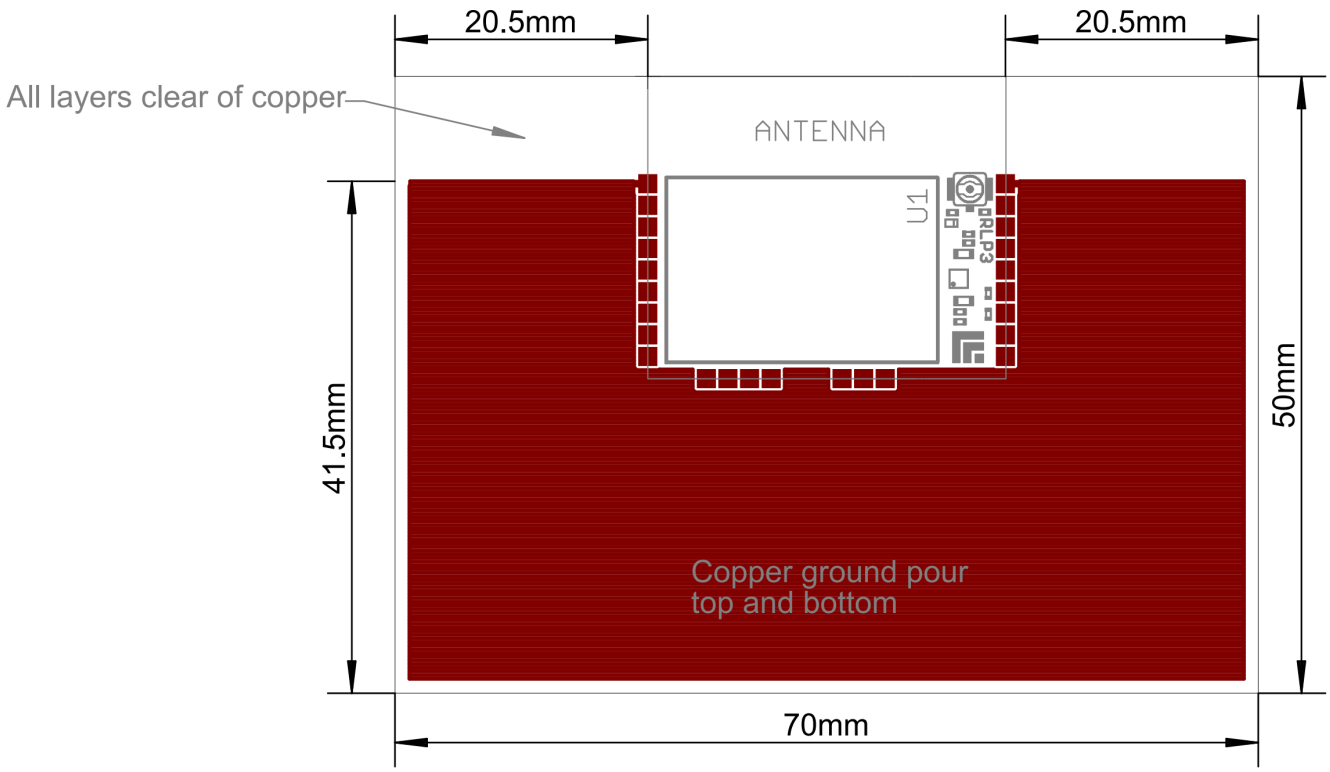
- ½-wave center-fed omni antennas tuned for 915 MHz with a maximum gain of 1.9 dBi or less, such as ANT-916-CW-HWR-SMA
- On-board antenna

ANTENNA REFERENCE DESIGN



Design Guidelines

When using the on-board antenna, this module can only be used with the layout design in strict compliance with the diagram above and the following design parameters:

Design Verification

1. Output Power Measurement
   a) Place module in test transmit mode (continuous transmit)
   b) Connect UFL on module to spectrum analyzer
   c) Set center frequency of spectrum analyzer to DUT frequency
   d) Place spectrum analyzer in zero-span mode, with RBW set to 1MHz
   e) Measure power level
   f) **Pass criteria**: Measured power level must be between +24dBm and +26dBm

2. EIRP Test
   a) In an environment suitably free of reflectors (e.g. anechoic chamber), position a transmit antenna and receive antenna (of known gain) at a fixed distance
   b) Connect a signal generator tuned to 915MHz to the transmit antenna and set output power to 0dBm
   c) Connect a spectrum analyzer to the receive antenna with the following settings: CF = 915MHz, SPAN = 0Hz, RBW = 500kHz
   d) Measure received power
   e) Determine path loss with the following equation:

$$PL = P_{tx} - P_{rx} = (0 - L_{cable} + G_{tx\ antenna}) - (P_{rx_{meas}} - L_{cable} + G_{rx\ antenna})$$

   f) Replace TX antenna/source with DUT in continuous TX mode
   g) Measure received power
   h) Determine EIRP with the following equation:

$$EIRP = PL + P_{rx} = PL + (P_{rx_{meas}} - L_{cable} + G_{rx\ antenna})$$

   i) **Pass criteria**: EIRP between +25.9dBm and +27.9dBm

Production Verification

Production verification consists of verifying the output power of the module. The power level of the module is pre-set during manufacturing. The absolute maximum output target level is +26 dBm. Due to manufacturing variability, a typical module transmits with a power of 25.0 dBm +/- 0.5 dBm.

1. Output Power Measurement
   a) Place module in test transmit mode (continuous transmit)
   b) Connect UFL on module to spectrum analyzer
   c) Set center frequency of spectrum analyzer to DUT frequency
   d) Place spectrum analyzer in zero-span mode, with RBW set to 1MHz
   e) Measure power level
   f) **Pass criteria**: Measured power level must be between +24dBm and +26dBm

HOST INTERFACE

The communication between the Link Labs module and an external host controller is a point-to-point Master/Slave communications protocol using a UART connection. The following chapters describe the message flow and message format.

Message Flow

The host interface defines two different message types that are exchanged between the host controller and the radio module:

1. Command packet
2. Response packet

The host initiates communication (protocol master) by sending 1 command packet, and waiting for a response before sending another command packet. The slave responds immediately to 1 command packet with 1 response packet.

Any data that flows from the slave to the master must be done in the form of a polled request from the master to the slave. To allow devices to be put into low power sleep modes, an interrupt should be provisioned from the slave device to the host device, after which the master should poll the slave device.

Message Format

Command Packet

Byte Number	Description	Comments
0	Start of Frame	Always equal to 0xC4
1	Command Byte	Specifies which master command is contained in this message.
2	Message number	An 8-bit number incremented by the master device. Expected to rollover: 253, 254, 255, 0, 1, 2, …
3	Payload Length	Total number of bytes in the payload. Valid range is 0-255.
	Payload Byte 0	
	Payload Byte 1	
	Payload Byte 2	
…	…	
Payload Length +5	Checksum Byte	An 8-bit checksum. See the next section for details on computing the checksum.

Response Packet

Byte Number	Description	Comments
0	Start of Frame	Always equal to 0xC4
1	Command Byte	Specifies which master command is being replied to.
2	Message number	Specifies the message number that is being replied to.
3	ACK Byte	00 = ACK: Command Acknowledged 01 = NACK: Command not supported 02 = NACK: Incorrect Checksum 03 = NACK: Payload length out of range 04 = NACK: Payload value out of range 05: …
4	Payload Length (MSB)	MSB of the total number of bytes in the payload.
5	Payload Length (LSB)	LSB of the total number of bytes in the payload
	Payload Byte 0	
	Payload Byte 1	
	Payload Byte 2	
…	…	
Payload Length + 6	Checksum Byte (MSB)	The MSB of a 16-bit CRC. See the “Checksum” section for details.
Payload Length + 7	Checksum Byte (LSB)	The LSB of a 16-bit CRC.

Checksum

The CRC will be computed on all bytes that precede the two CRC bytes. The CRC is a CRC-16-CCITT with initializer 0, and is computed as follows.

```
/**
 * @brief
 *   compute_checksum
 *
 * @param[in] buf
 *   byte array to compute checksum on
 *
 * @param[in] len
 *   size of the byte array in bytes
 *
 * @return
 *   The 16-bit checksum
 */
static uint16_t compute_checksum(uint8_t* buf, uint16_t len)
{
    uint16_t i;
    uint16_t crc = 0;

    for(i = 0; i < len; i++)
    {
        crc  = (crc >> 8) | (crc << 8);
        crc ^= buf[i];
        crc ^= (crc & 0xff) >> 4;
        crc ^= crc << 12;
        crc ^= (crc & 0xff) << 5;
    }

    return crc;
}
```

HOST APPLICATION INTERFACE

Reset

The module reset is controlled via pin 6. To apply an external reset source to this pin, it is required to only drive this pin low during reset, and let the internal pull-up en- sure that reset is released.

Wakeup

The module will wakeup automatically in response to UART traffic.

Firmware Version

Command Byte: 0x00 (0)

Command Payload:

None

Response Payload:

Payload Length	4 bytes
Payload Byte 0	Major version
Payload Byte 1	Minor version
Payload Byte 2	Tag most significant byte
Payload Byte 3	Tag least significant byte

Host Interface Version

Command Byte: 0x01 (1)

Command Payload:

None

Response Payload:

Payload Length	4 bytes
Payload Byte 0	Major version
Payload Byte 1	Minor version
Payload Byte 2	Tag most significant byte
Payload Byte 3	Tag least significant byte

Software Reset

Command Byte: 0x3C (60)

Command Payload:

None

Response Payload:

None

Boot Loader Mode

Command Byte: 0x3D (61)

Command Payload:

None

Response Payload:

None

Note: Module will reset to enter boot loader mode

Module ID

Command Byte: 0x32 (50)

Command Payload:

None

Response Payload:

Payload Length	8 bytes
Payload Byte 0	MSB of 64-bit unique identifier
…	…
Payload Byte 7	LSB of 64-bit unique identifier

MAC Mode Get

Command Byte: 0x47 (71)

Command Payload:

None

Response Payload:

Payload Length	1 bytes
Payload Byte 0	MAC mode [0 – No MAC, 3 – Symphony/WALoP]

MAC Mode Set

Command Byte: 0x46 (70)

Command Payload:

Payload Length	1 bytes
Payload Byte 0	MAC mode [0 – No MAC, 3 – Symphony/WALoP]

Response Payload:

None

Interrupt Flags

Command Byte: 0x0F (15)

Command Payload:

Payload Length	4 bytes
Payload Byte 0	Flags_to_clear (Bits 31:24)
Payload Byte 1	Flags_to_clear (Bits 23:16)
Payload Byte 2	Flags_to_clear (Bits 15:8)
Payload Byte 3	Flags_to_clear (Bits 78:0)

Response Payload:

Payload Length	4 bytes
Payload Byte 0	Flags (Bits 31:24)
Payload Byte 1	Flags (Bits 23:16)
Payload Byte 2	Flags (Bits 15:8)
Payload Byte 3	Flags (Bits 7:0)

Comments:

This function allows the host processor to check whether an event has occurred in the module that has latched a bit in the "IRQ Flags" vector.

“Flags_to_clear” is a 32-bit vector containing flags that should be cleared if they are set. This can be 0 if the host processor just wants to read without clearing. If a bit is set, this function performs a clear-on-read of the irq_flags bits passed in.

“Flags” is a 32-bit vector containing the value of the irq_flags in the module. Note that if the “Flags_to_clear” argument is non-zero, then “Flags” is the value of the flags **before** the clear operation.

Interrupt Bits:

0x00000001 – Watchdog Reset Occurred
0x00000010 – Tx Done
0x00000020 – Tx Error
0x00000100 – Rx Done
0x00001000 – Gateway Connection Established
0x00002000 – Gateway Connection Lost

Symphony Packet Send Unacknowledged

Command Byte: 0x5B (91)

Command Payload:

Payload Length	n bytes
Payload Byte 0	User specified payload
Payload Byte …	“
Payload Byte …	“
Payload Byte n-1	“

Response Payload:

None

Note: The maximum allowed packet size is 256 bytes

Symphony Packet Send Acknowledged

Command Byte: 0x5A (90)

Command Payload:

Payload Length	n bytes
Payload Byte 0	User specified payload
Payload Byte …	“
Payload Byte …	“
Payload Byte n-1	“

Response Payload:

None

Symphony Receive Mode Set

Command Byte: 0x6E (110)

Command Payload:

Payload Length	1 bytes
Payload Byte 0	Receive mode • 0 – Downlink off • 1 – Always On • 2 – Mailbox • 3 – Periodic
Payload Byte 1	(optional) Periodic mode only – frame quanta

Response Payload:

None

Symphony Receive Mode Get

Command Byte: 0x6F (111)

Command Payload:

None

Response Payload:

Payload Length	1 bytes
Payload Byte 0	Receive mode • 0 – Downlink off • 1 – Always On • 2 – Mailbox • 3 – Periodic

Symphony Packet Receive

Command Byte: 0x28 (40)

Command Payload:

Timeout Symbols	Number of timeout symbols

Response Payload:

Payload Length	n bytes
Payload Byte 0	Received payload
Payload Byte …	“
Payload Byte …	“
Payload Byte n-1	“

Symphony Packet Receive with RSSI

Command Byte: 0x29 (41)

Command Payload:

Timeout Symbols	Number of timeout symbols

Response Payload:

Payload Length	n + 2 bytes
Payload Byte 0,1	RSSI (16-bit signed)
Payload Byte 2	SnR
Payload Byte 3	Received payload
Payload Byte …	“
Payload Byte …	“

Payload Byte n-2	“

SnR[dB] = (8-bit signed integer) SnR / 4

SYSTEM OPERATION MODES

The module can operate in one of four modes: End-node, Coordinator, Gateway, and Peer-to-peer. When choosing a high-level role, the module will manage the MAC layer to optimize transmit power, data rate, and current consumption.

End-node

An end-node is the most basic operating mode in the network. This mode is optimized for low-power long-range two-way communication. In this mode, the module stays in a deep sleep mode the majority of the time. The module only wakes up for the minimum amount of time required to maintain connectivity.

Coordinator

The coordinator operating-mode is an extended mode where an end-node can also act as relay between other nodes and the gateway. This allows for a star of stars or clustered-tree topology to extend the overall wireless range of the network.

Gateway

The gateway operating-mode serves as the center node in the star network. It sends periodic beacons and aggregates messages from the nodes in the network. This mode is typically used for a concentrator or gateway in the system.

Peer-to-Peer

The peer-to-peer operating-mode is used when there are only 2 nodes in the network.

REGULATORY COMPLIANCE INFORMATION

To satisfy FCC RF Exposure requirements for mobile and base station transmission devices, a separation distance of 20cm or more should be maintained between the antenna of this device and persons during operation. To ensure compliance, operation at closer than this distance is not recommended.

The antenna(s) used for this transmitter must not be co-located or operating in conjunction with any other antenna or transmitter.

OEM Labeling Requirements for End-Product

The module is labeled with its own FCC ID and IC Certification Number. The FCC ID and IC certification numbers are not visible when the module is installed inside another device, as such the end device into which the module is installed must display a label referring to the enclosed module. The final end product must be labeled in a visible area with the following:

"Contains Transmitter Module FCC ID: 2ACT6LLRXR27
"Contains Transmitter Module IC: 12201A-LLRXR27

or

"Contains FCC ID: 2ACT6LLRXR27
"Contains IC: 12201A-LLRXR27

The OEM of the module must only use the approved antenna(s) listed above, which have been certified with this module.

OEM End Product User Manual Statements

The OEM integrator should not provide information to the end user regarding how to install or remove this RF module or change RF Related parameters in the user manual of the end product.

Other use manual statements may apply.

Federal Communication Commission Interference Statement

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

FCC CAUTION: Any changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate this equipment.

Conformité aux normes FCC Cet équipement a été testé et trouvé conforme aux limites pour un dispositif numérique de classe B, conformément à la Partie 15 des règlements de la FCC. Ces limites sont conçues pour fournir une protection raisonnable contre les interférences nuisibles dans une installation résidentielle.

Cet équipement génère, utilise et peut émettre des fréquences radio et, s'il n'est pas installé et utilisé conformément ment aux instructions du fabricant, peut causer des interferences nuisibles aux communications radio.

Rien ne garantit cependant que l'interférence ne se produira pas dans une installation particulière. Si cet équipement provoque des interférences nuisibles à la réception radio ou de télévision, qui peut être déterminé en comparant et en l'éteignant, l'utilisateur est encouragé à essayer de corriger les interférence par une ou plusieurs des mesures suivantes:

- Réorienter ou déplacer l'antenne de réception.
- Augmenter la distance entre l'équipement et le récepteur.
- Branchez l'appareil dans une prise sur un circuit différent de celui auquel le récepteur est connecté.
- Consultez votre revendeur ou un technicien radio / TV pour assistance.Avertissement

Les changements ou modifications à cet appareil sans expressément approuvée par la partie responsable de conformité pourraient annuler l'autorité de l'utilisateur de faire fonctionner cet équipement.

Industry Canada Statements

This device complies with Industry Canada license-exempt RSS standard(s). Operation is subject to the following two conditions: (1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that permitted for successful communication.

This device has been designed to operate with the antennas listed in the filing, and having a maximum gain of 1.9 dBi. Antennas not included in this list or having a gain greater than 1.9 dBi are strictly prohibited for use with this device. The required antenna impedance is 50 ohms.

Cet appareil est conforme avec Industrie Canada exempt de licence Rss standard(s). Son fonctionnement est soumis aux deux conditions suivantes : (1) cet appareil ne peut causer d'interférences, et (2) cet appareil doit accepter toute interférence, y compris des interférences qui peuvent provoquer un fonctionnement indésirable du périphérique.

Ce dispositif est conforme à la norme CNR-210 d'Industrie Canada applicable aux appareils radio exempts de licence. Son fonctionnement est sujet aux deux conditions suivantes: (1) le dispositif ne doit pas produire de brouillage préjudiciable, et (2) ce dispositif doit accepter tout brouillage reçu, y compris un brouillage susceptible de provoquer un fonctionnement indésirable.

Pour réduire le risque d'interférence aux autres utilisateurs, le type d'antenne et son gain doivent être choisies de façon que la puissance isotrope rayonnée équivalente (e.i.r.p) ne dépasse pas celle admise pour une communication réussie.

Cet appareil a été conçu pour fonctionner avec les antennes énumérées ci-dessous, et ayant un gain maximum de 6,0 dB. Antennes pas inclus dans cette liste ou ayant un gain supérieur à 6,0 dB sont strictement interdites pour une utilisation avec cet appareil. L'impédance d'antenne requise est de 50 ohms.

IMPORTANT NOTICE

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