



DIGITAL LOGGERS, INC./ CSC

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To Whom it May Concern:

The Digital Loggers Ethernet Power Controller P/N EPCR5 contains a WiFi transceiver SoC P/N AR9331 built by Atheros / Qualcomm.

Although this part can support many RF modulation modes, frequencies, export spectral masks, and power settings, it is limited to the following characteristics when used in the EPCR5 design:

Modulation	IEEE 801.11N 20MHz mode (no other modes supported)
TX power	Set to -10dBm peak, regulated by Atheros hardware
Spectral Mask	USA HAL modulation mask

The Atheros Hardware Abstraction Layer regulates output power. There is no way to adjust this in software. This is not a high power design. The WiFi feature is intended only to establish a low power link to a local client or router.

The components used in the design are based on the Atheros AP-121 reference design. The PA output filter network has been built with selected low-tolerance components, and we find consistent RF performance on production boards.

Although we are a manufacturer, not a certified test lab, we have internally tested these boards with spectrum analyzers and a Agilent WiFi analyzer to ensure they fall within the permissible spectral mask.



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Please feel free to contact us with any questions.

Sincerely,

A handwritten signature in blue ink, which appears to read 'Martin Bodo'.

Martin Bodo

CEO

Digital Loggers, Inc.

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