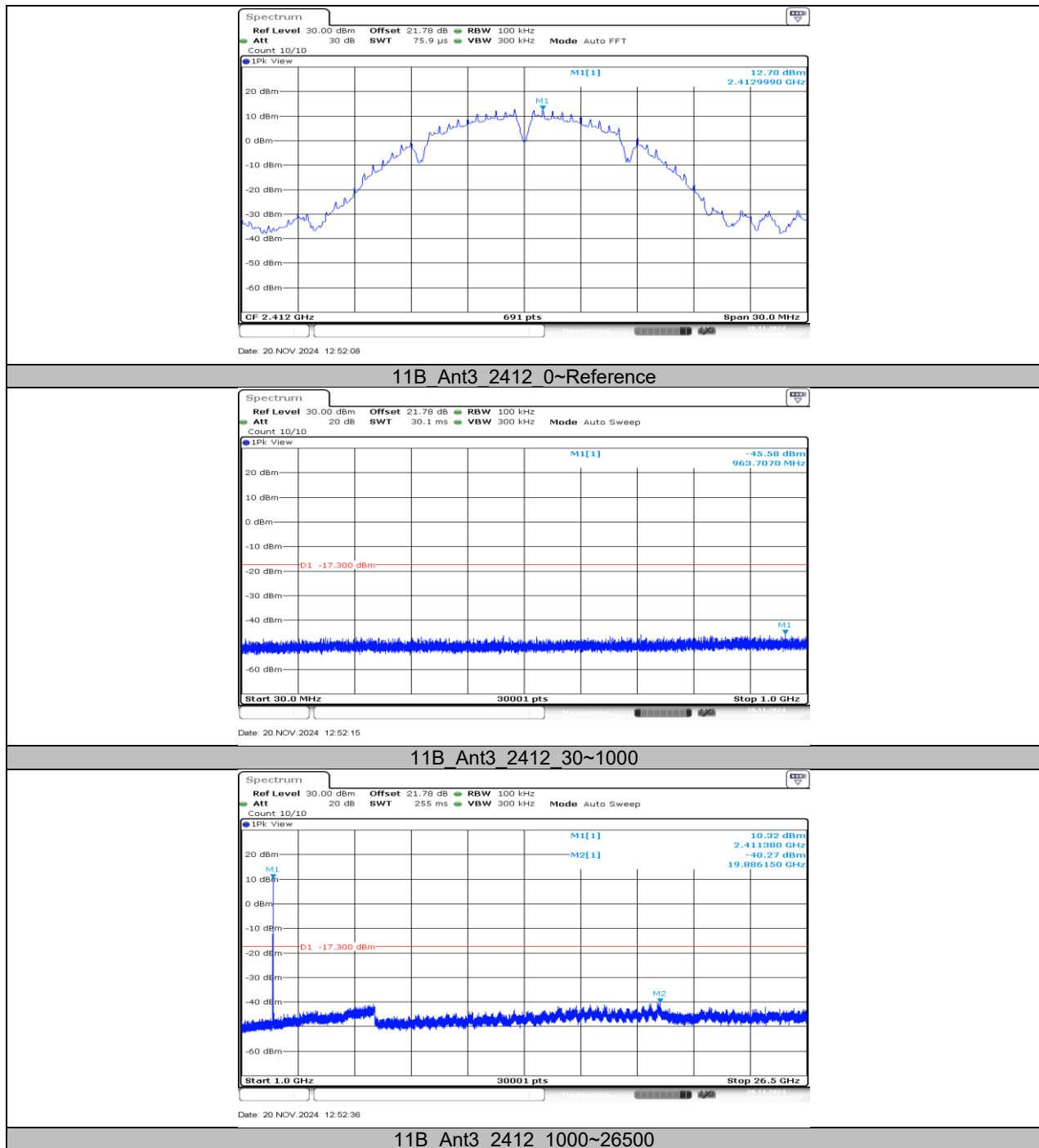
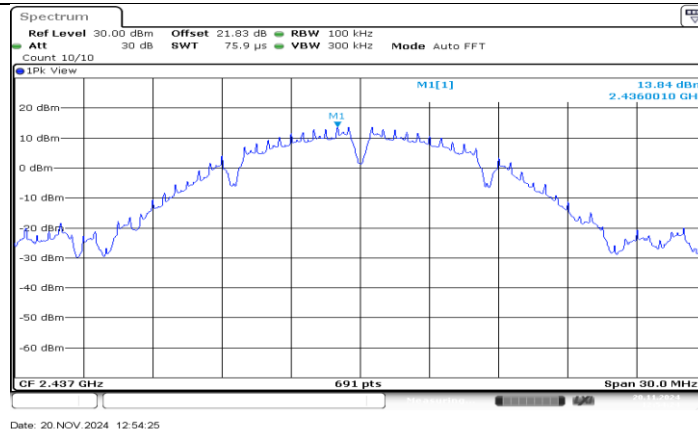
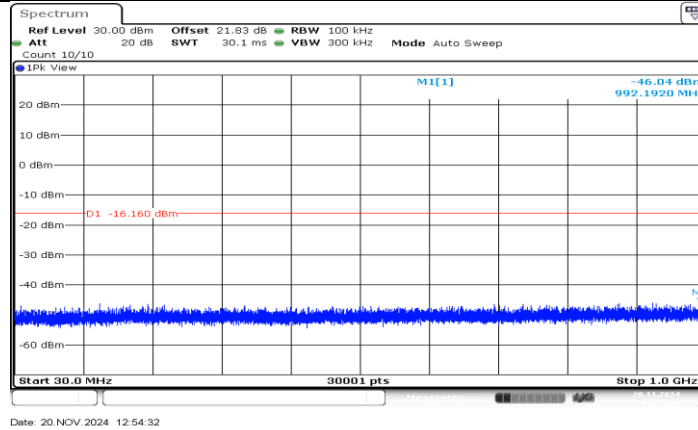


11.6.2. Test Graphs

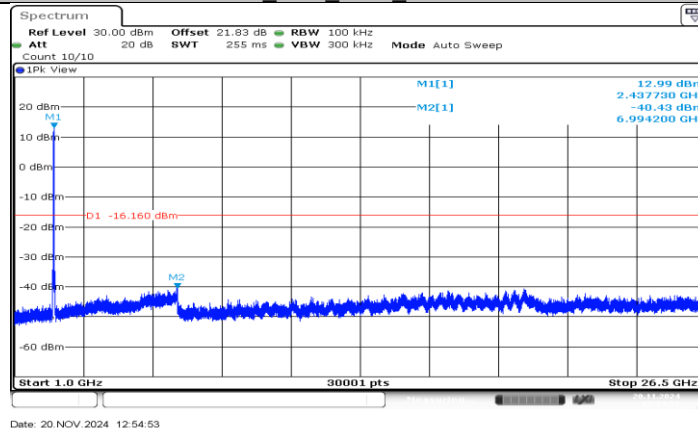




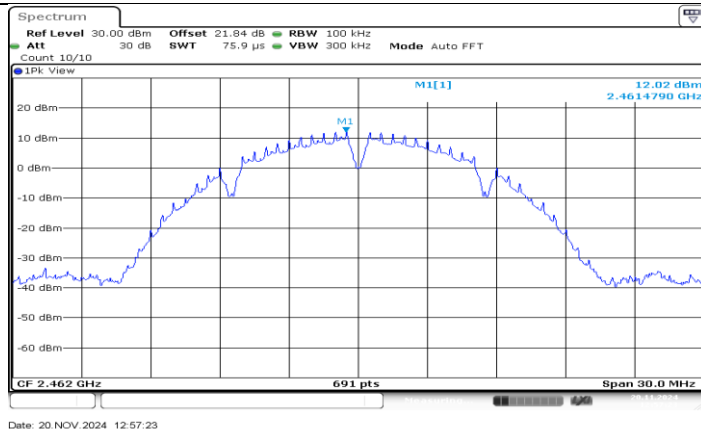
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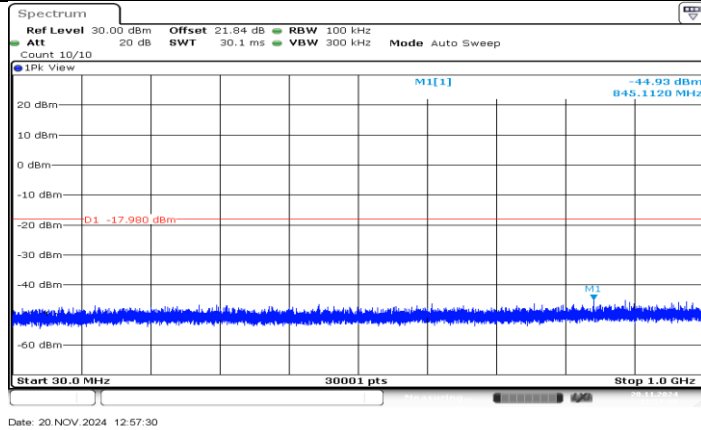
11B_Ant3_2437_30~1000



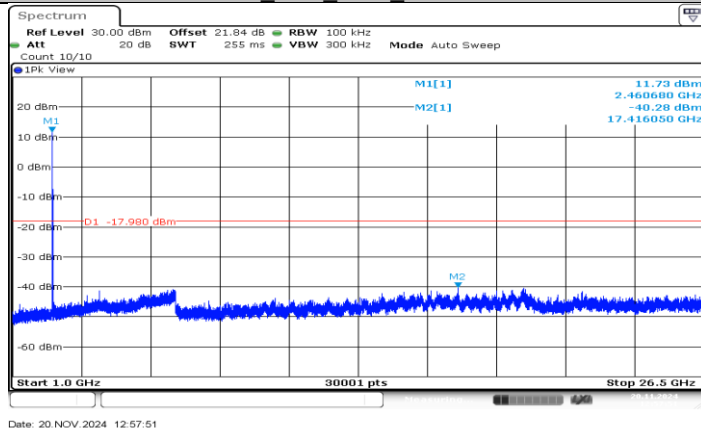
11B_Ant3_2437_1000~26500



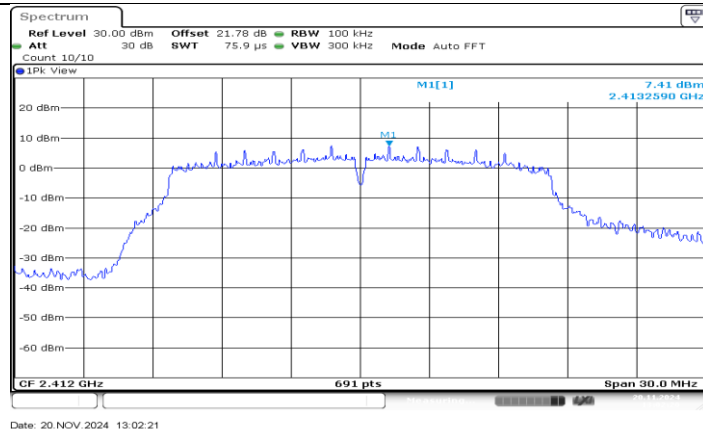
11B_Ant3_2462_0~Reference



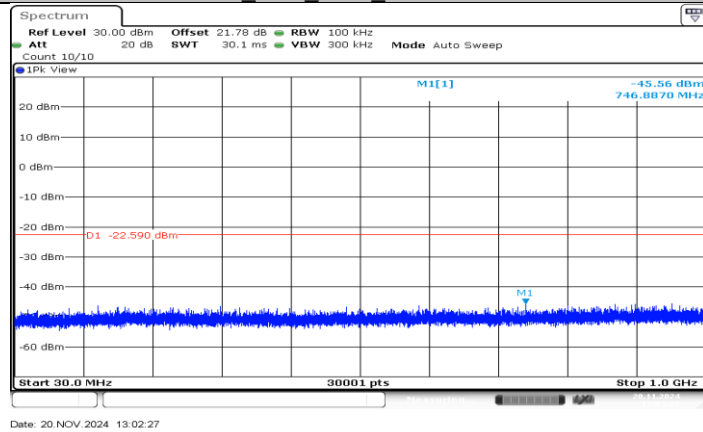
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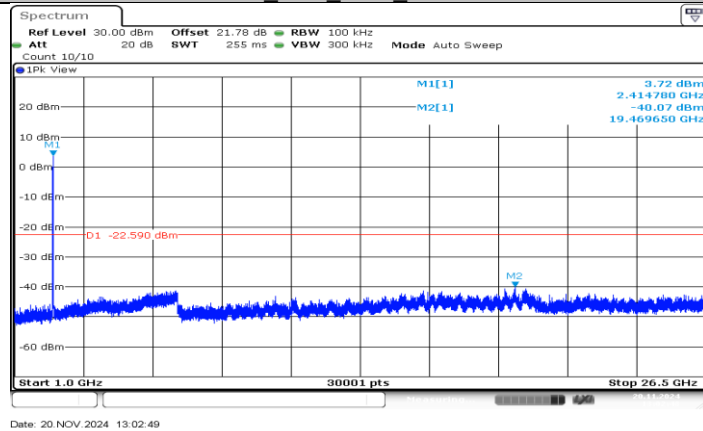
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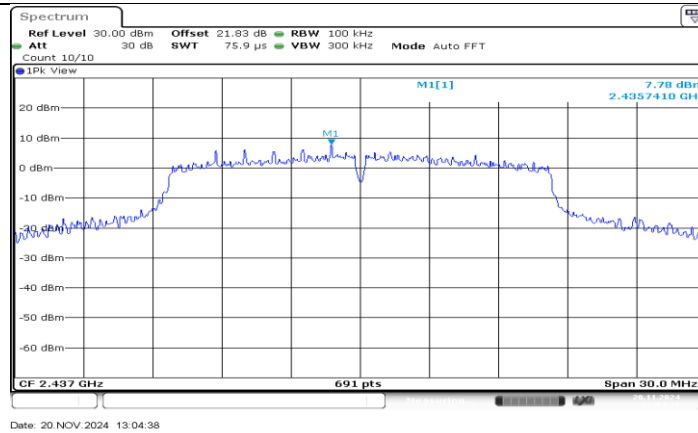
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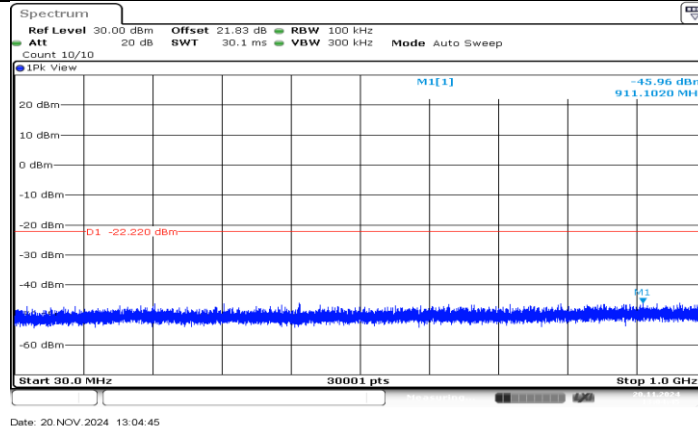
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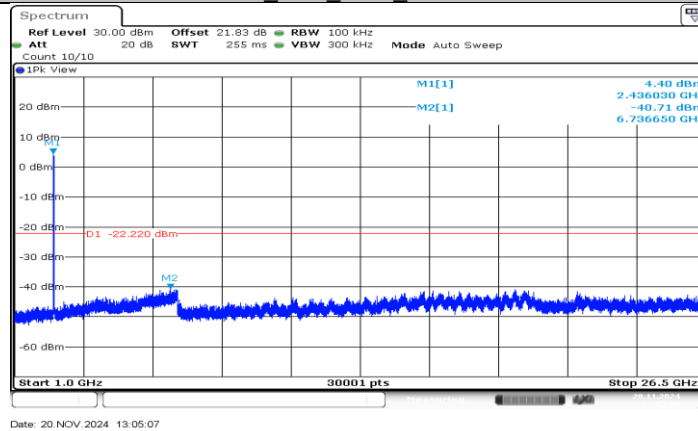
11G_Ant3_2412_1000~26500



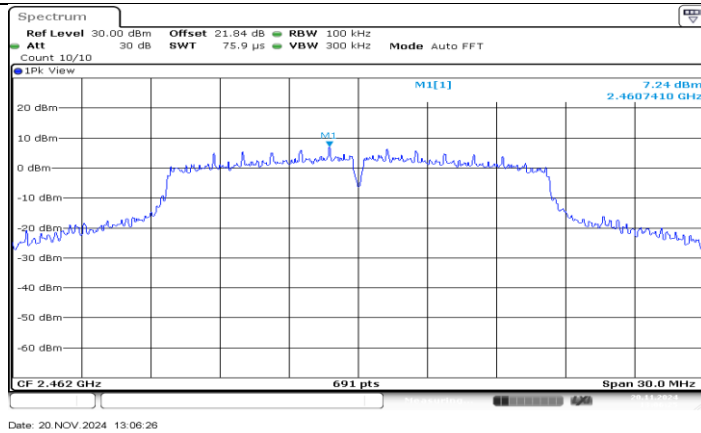
11G_Ant3_2437_0~Reference



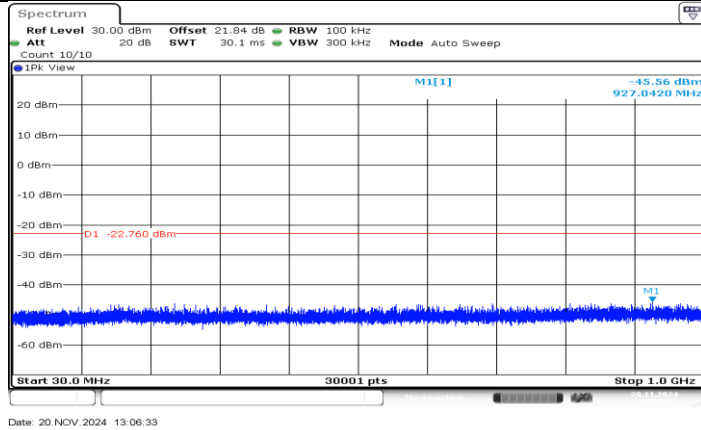
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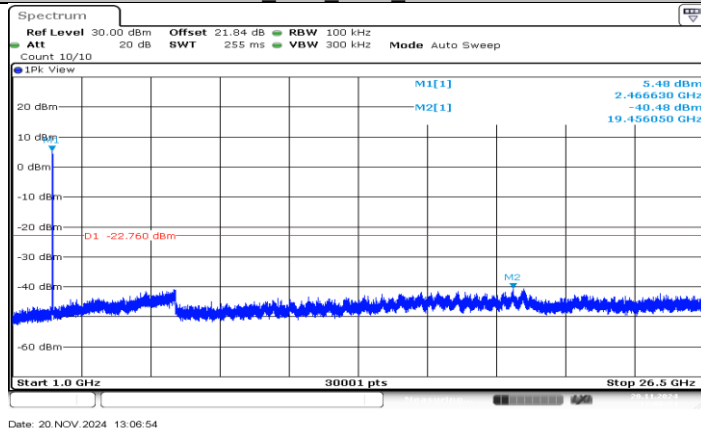
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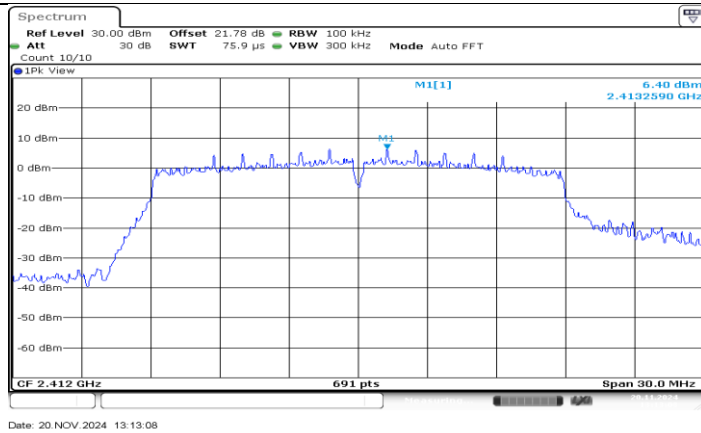
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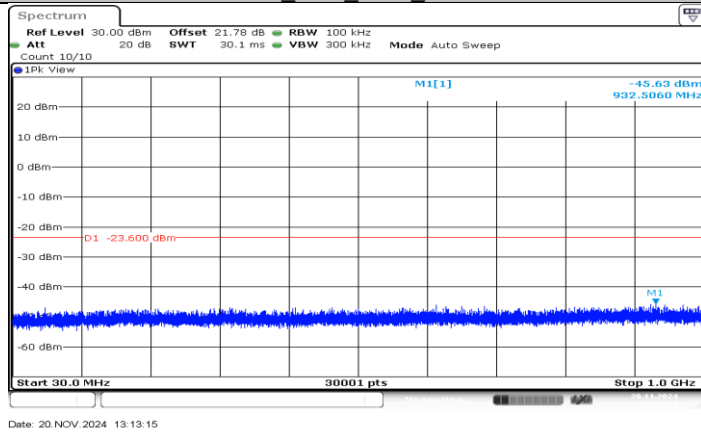
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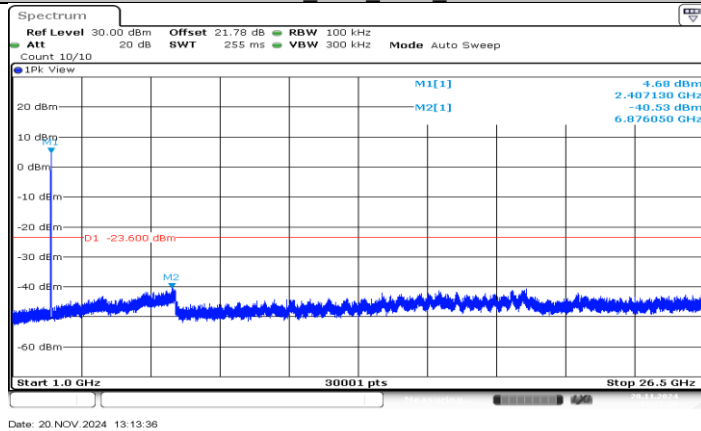
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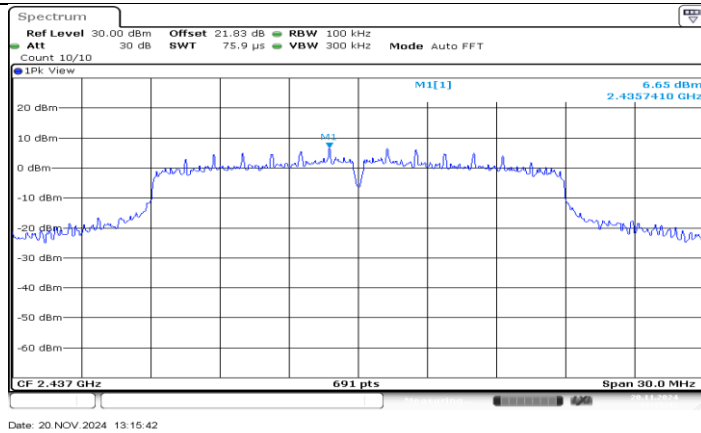
11N20SISO_Ant3_2412_0~Reference



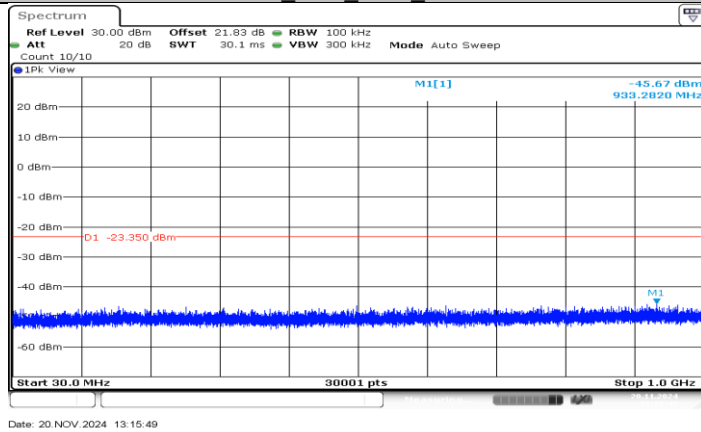
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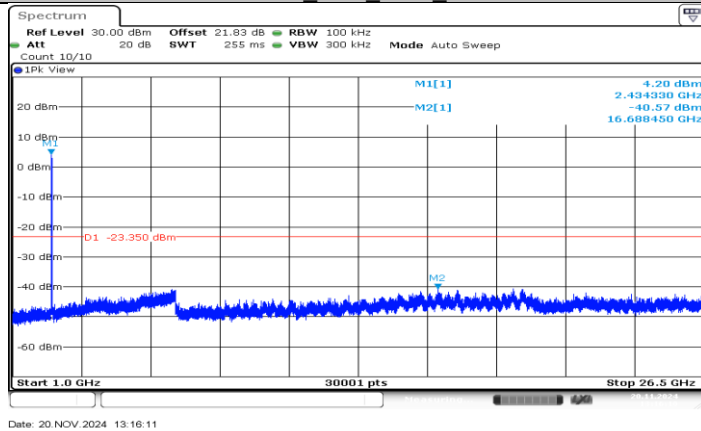
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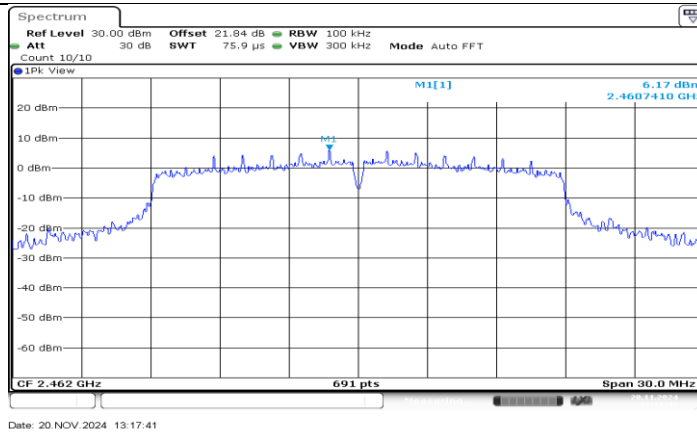
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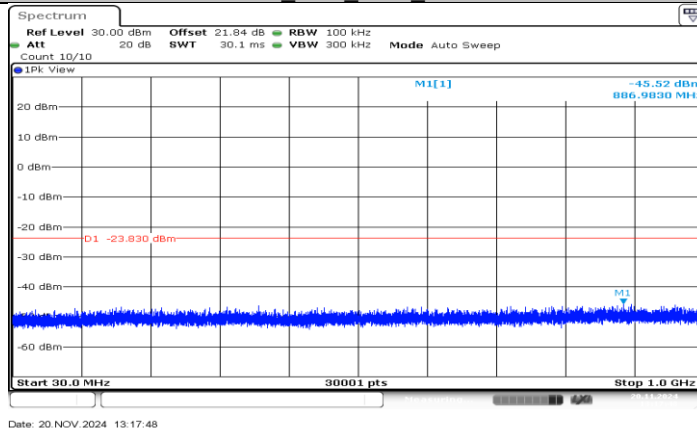
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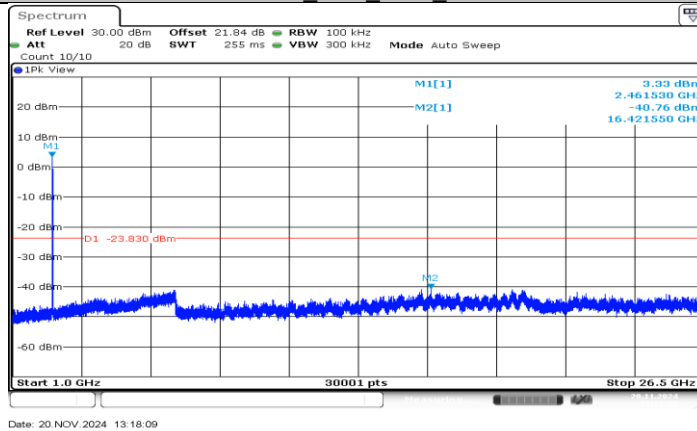
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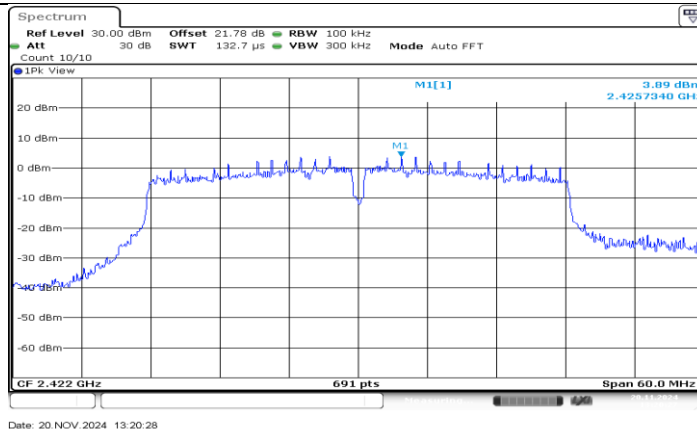
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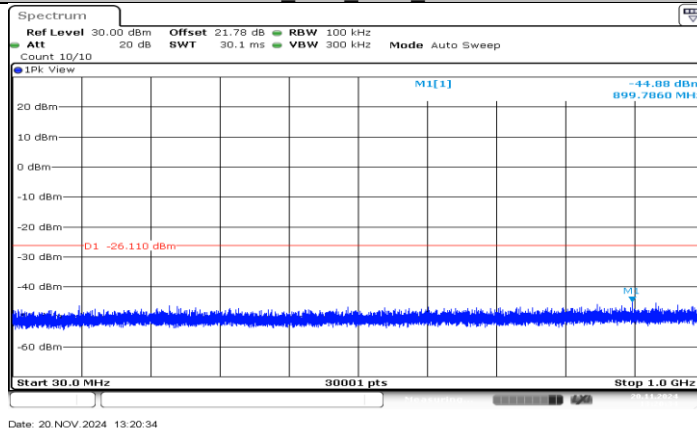
11N20SISO_Ant3_2462_30~1000



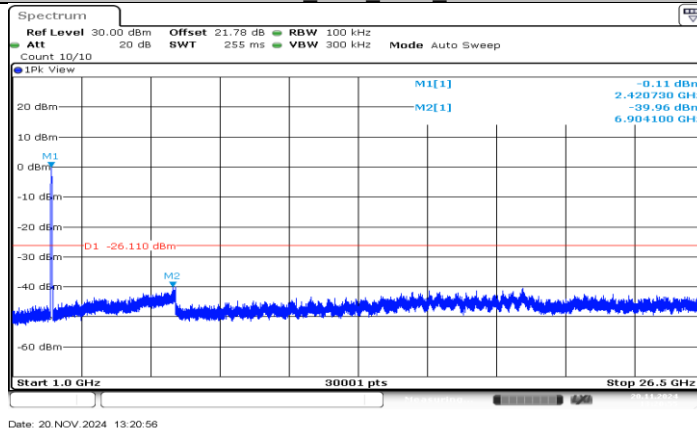
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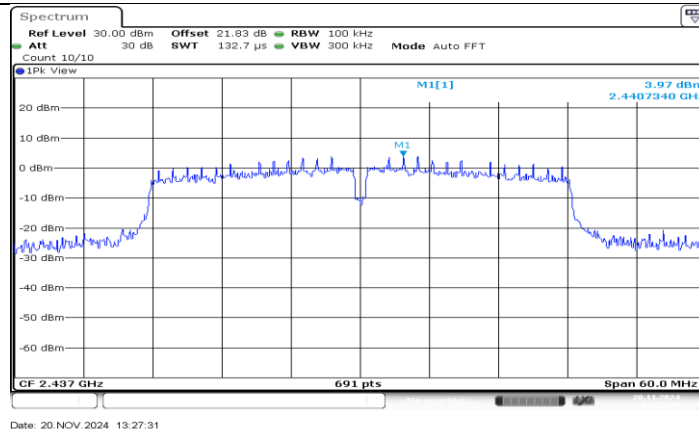
11N40SISO_Ant3_2422_0~Reference



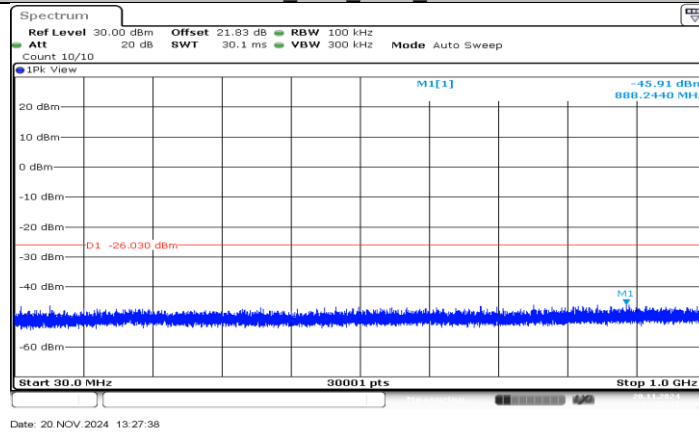
11N40SISO_Ant3_2422_30~1000



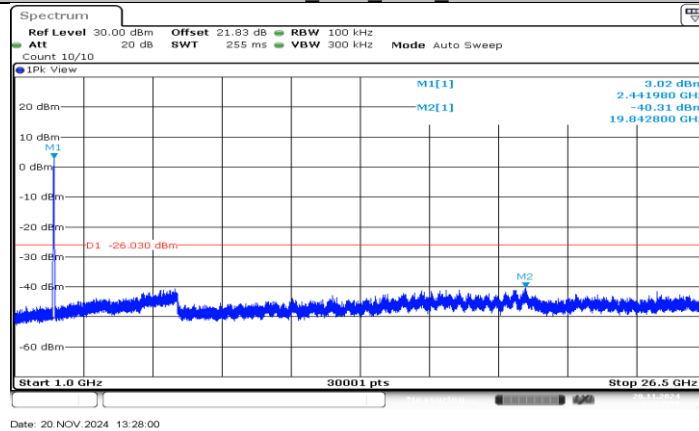
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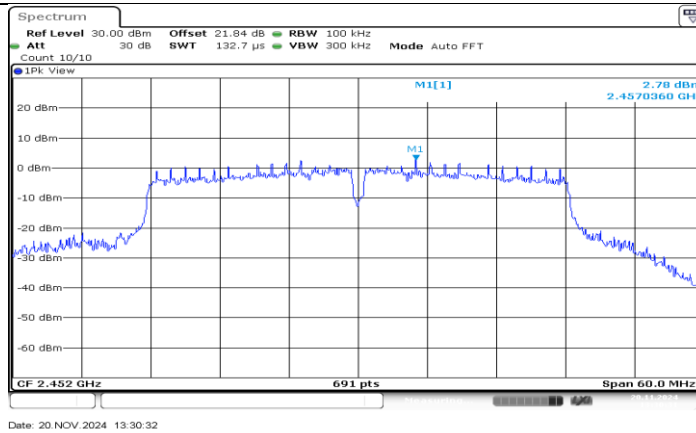
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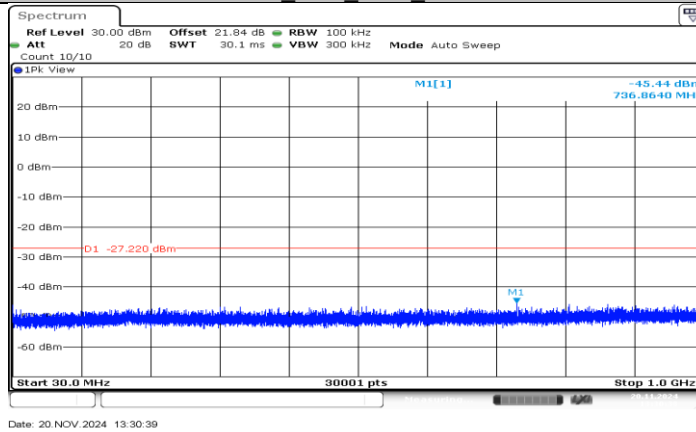
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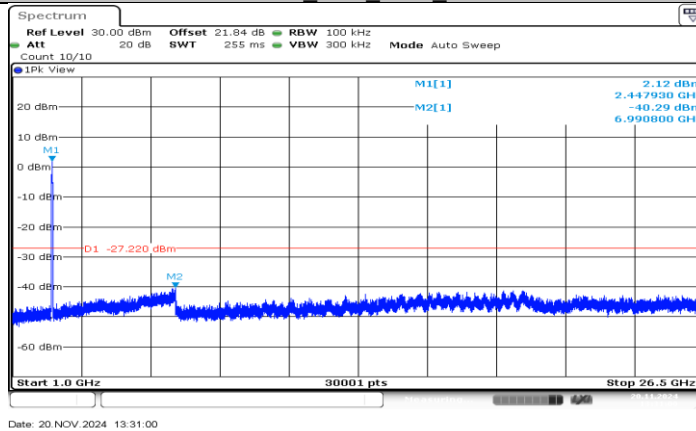
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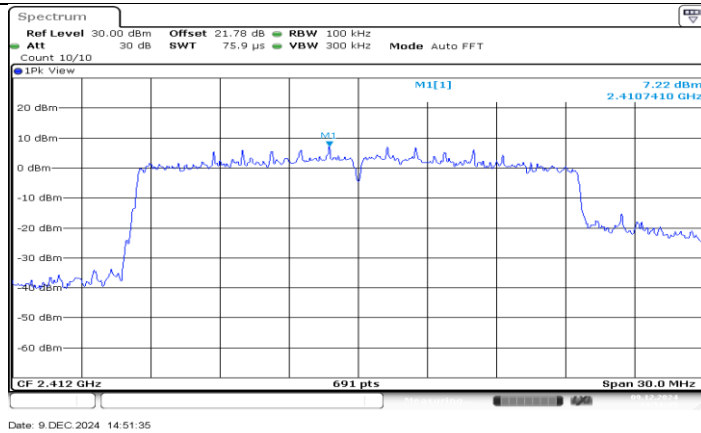
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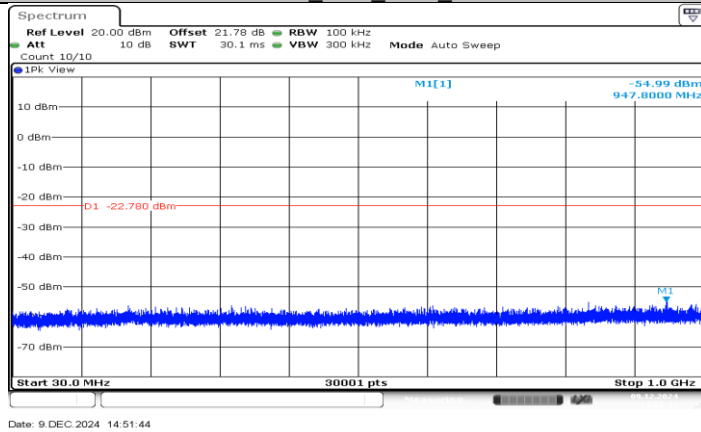
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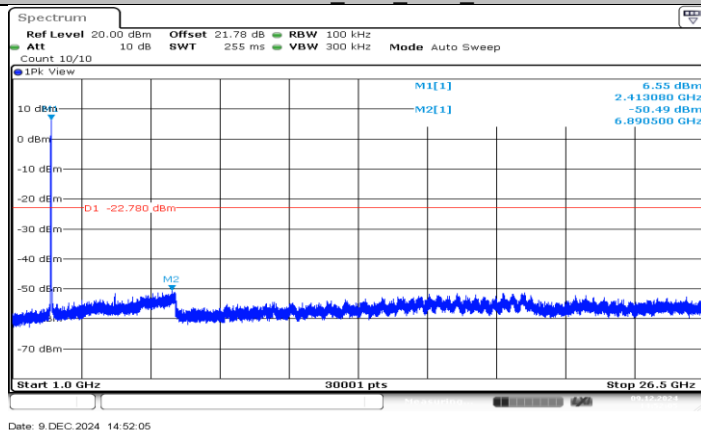
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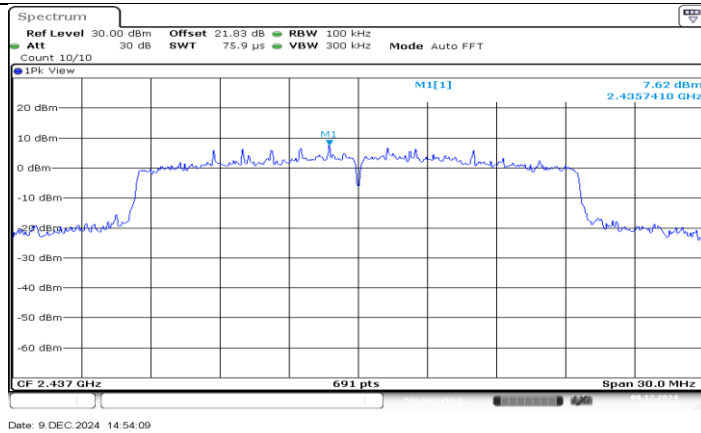
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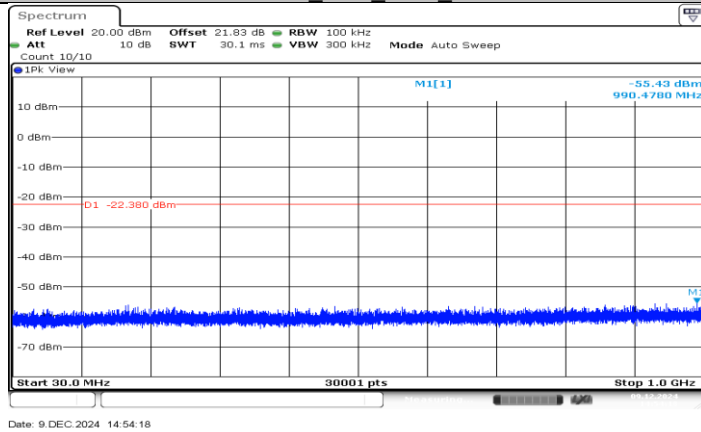
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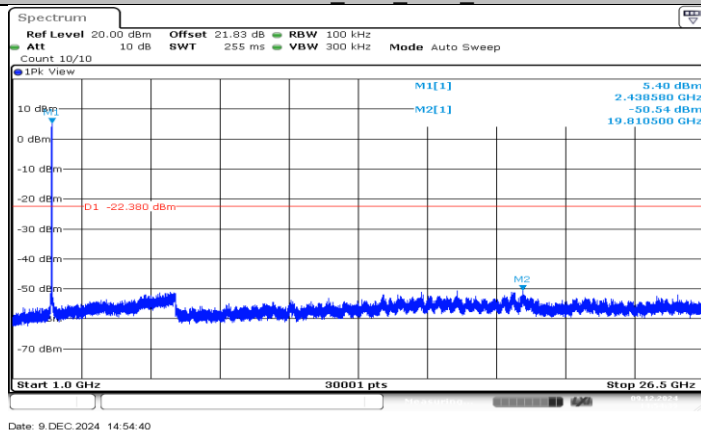
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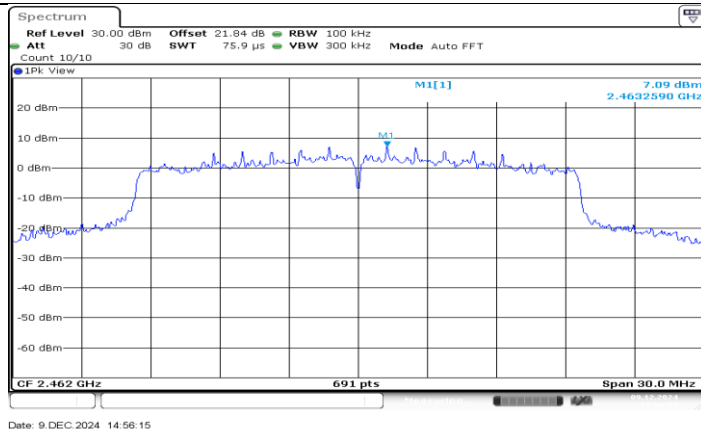
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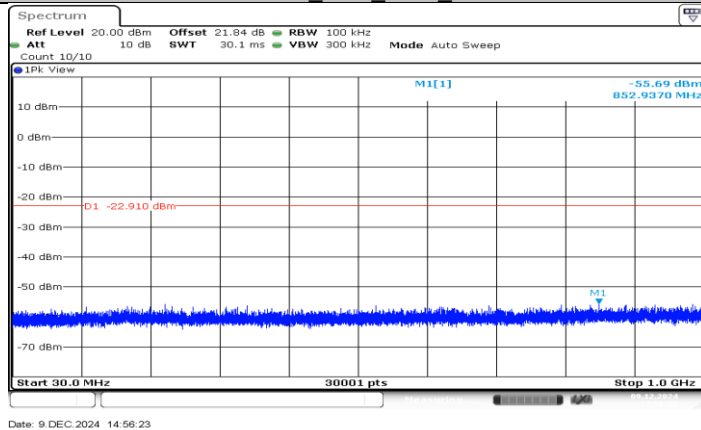
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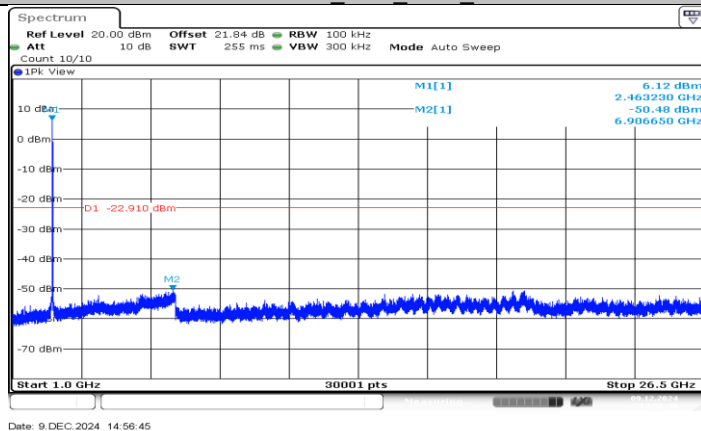
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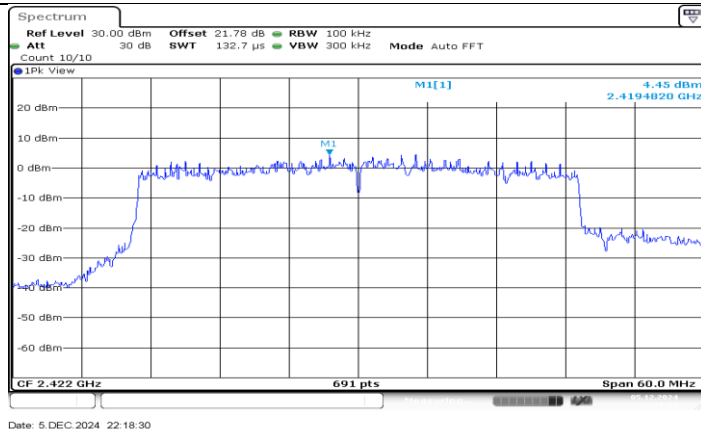
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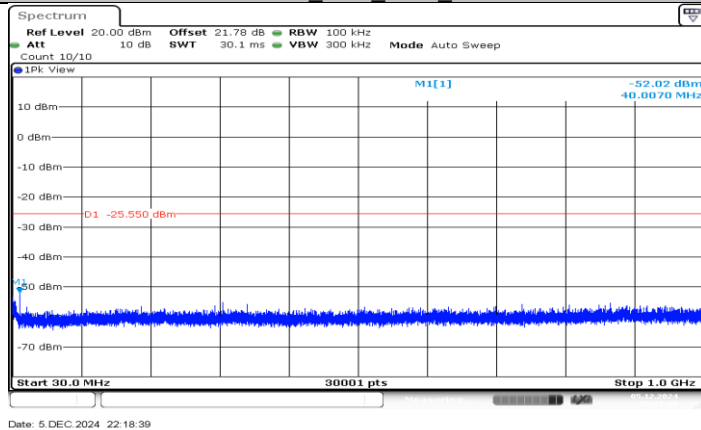
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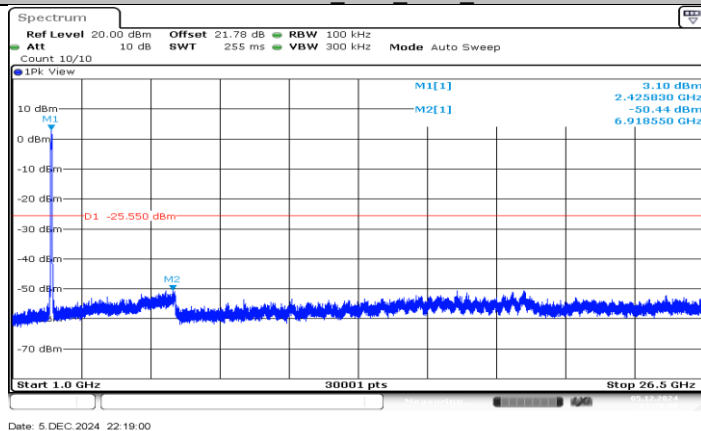
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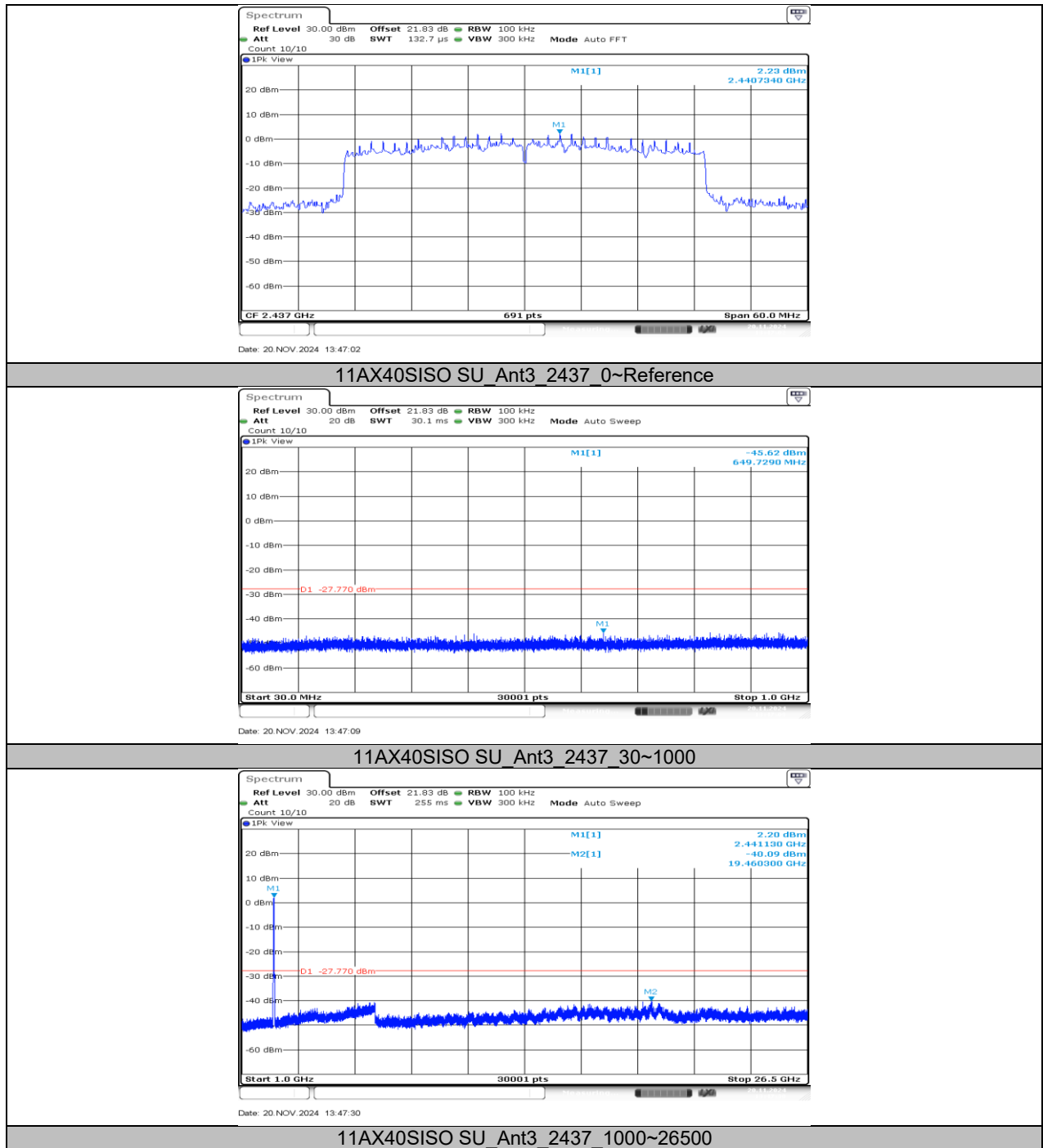
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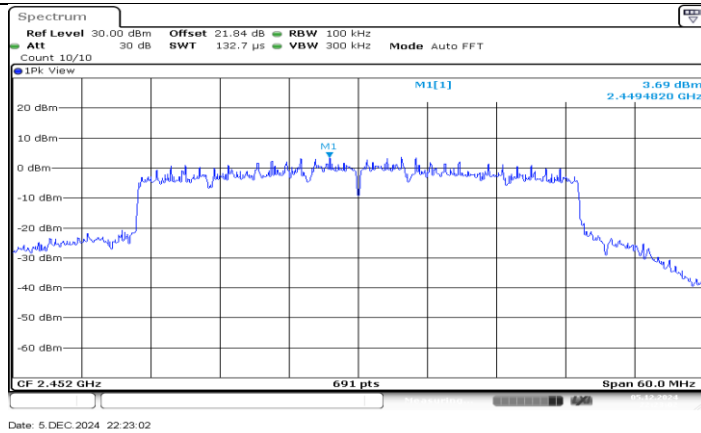


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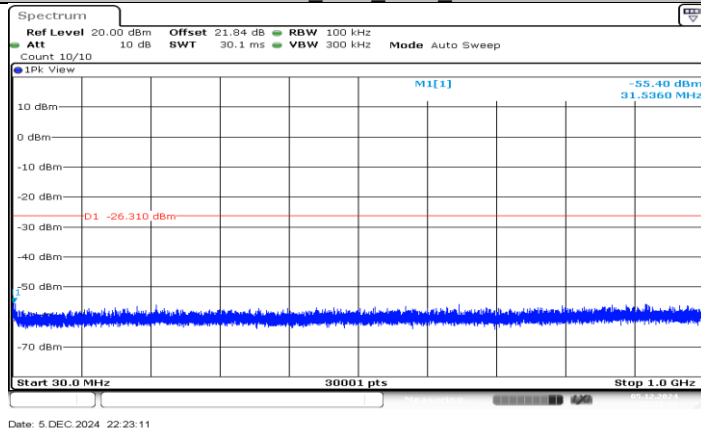


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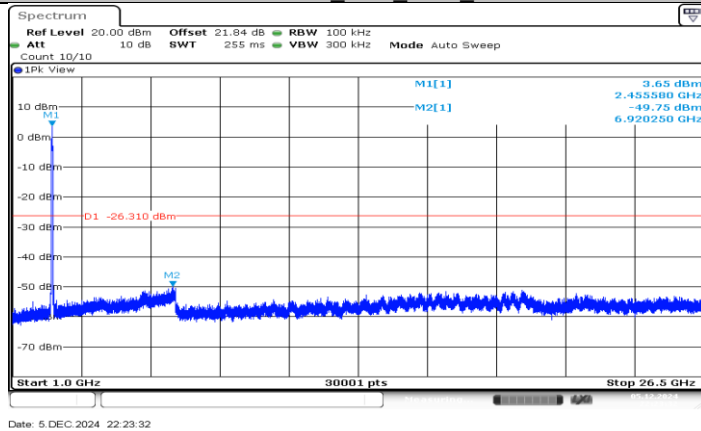




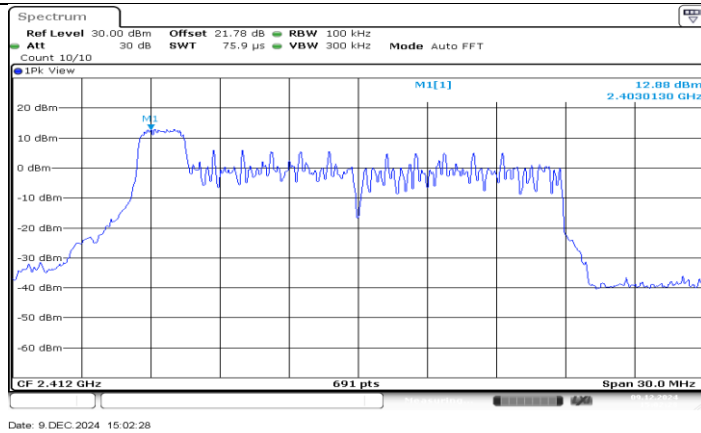
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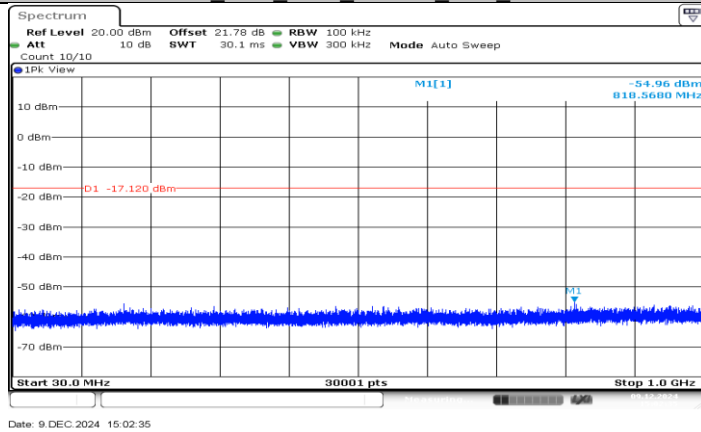
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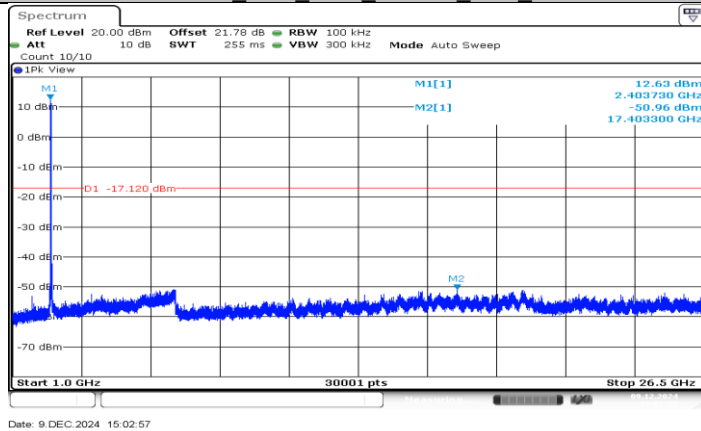
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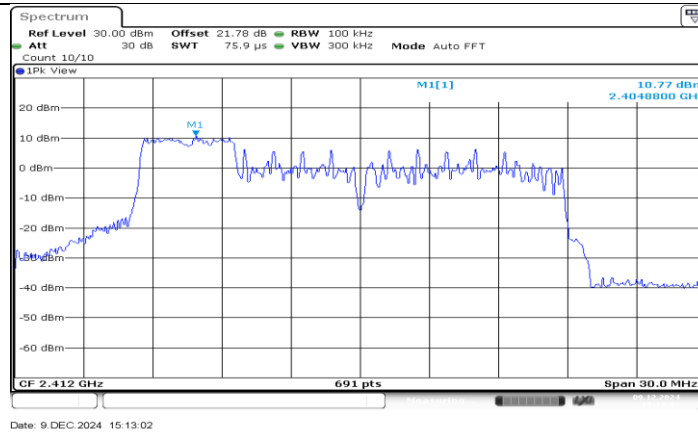
11AX20SISO_Ant3_2412_26Tone_RU0_0~Reference



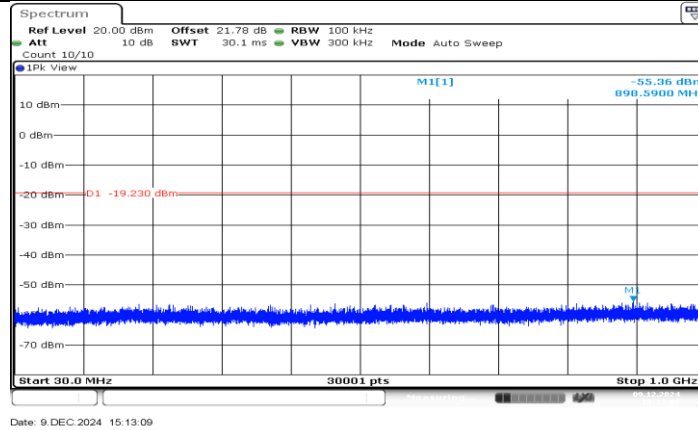
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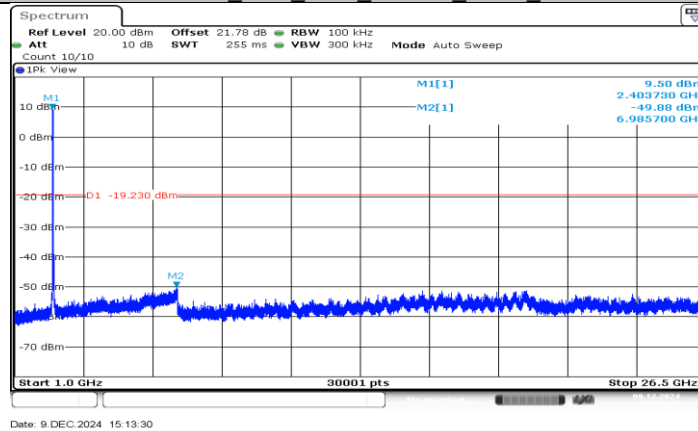
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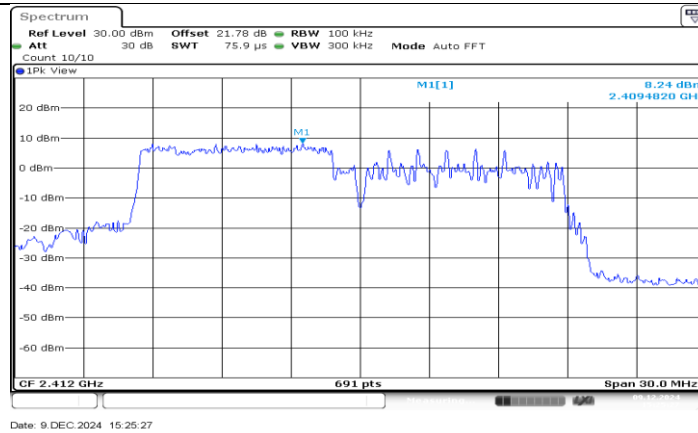
11AX20SISO_Ant3_2412_52Tone_RU37_0~Reference



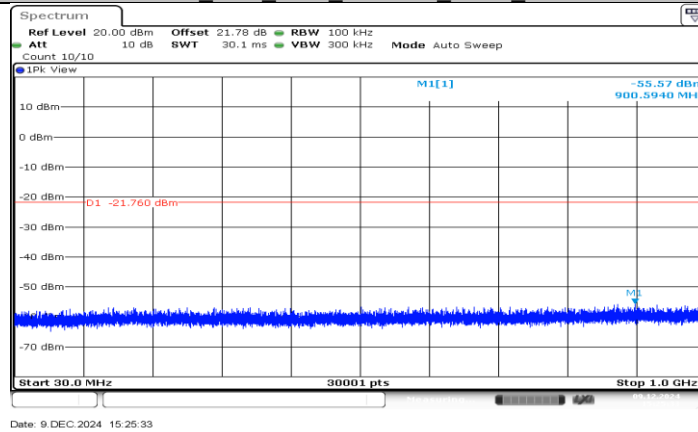
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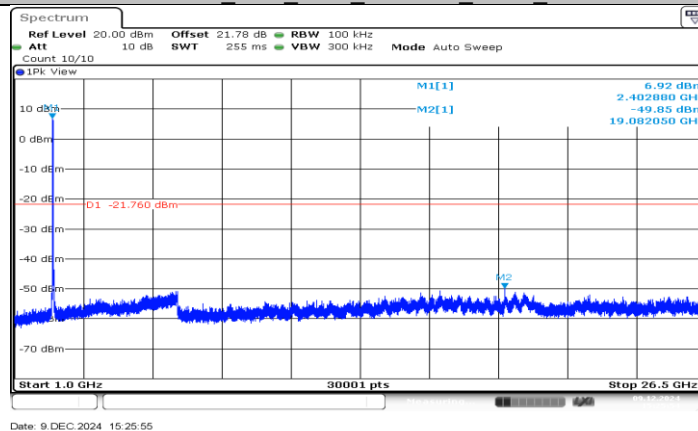
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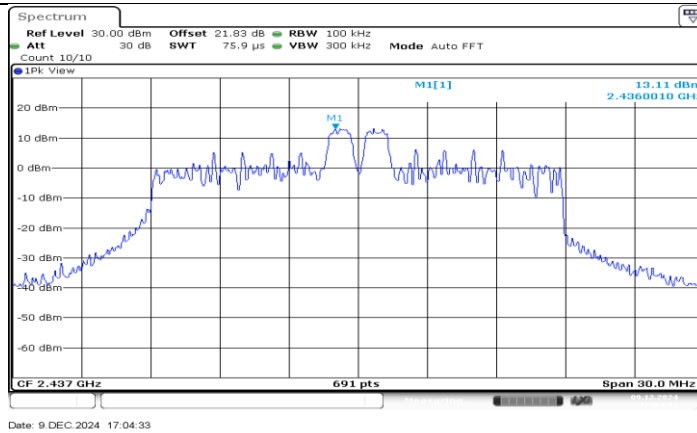
11AX20SISO_Ant3_2412_106Tone_RU53_0~Reference



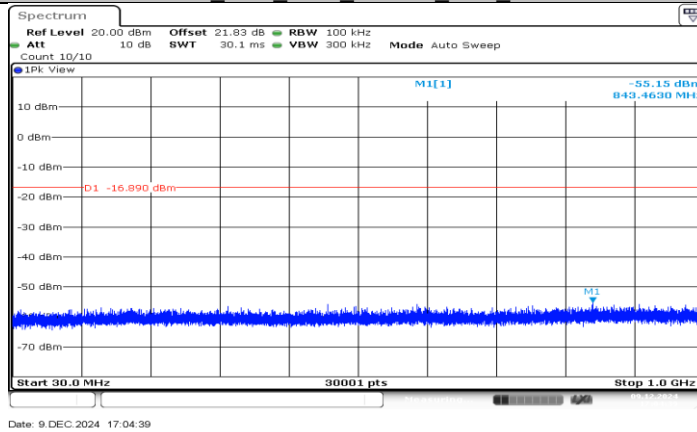
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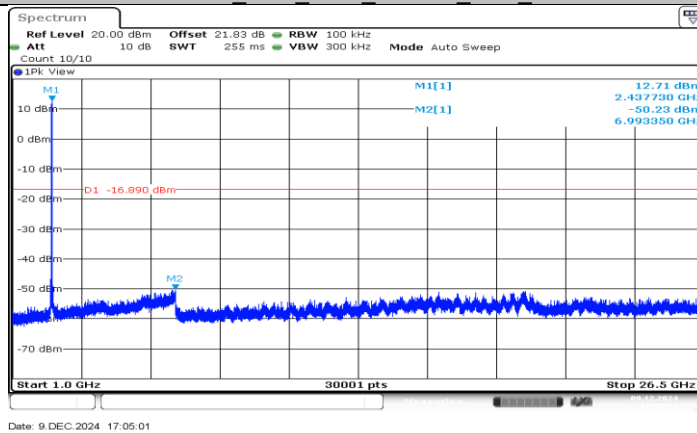
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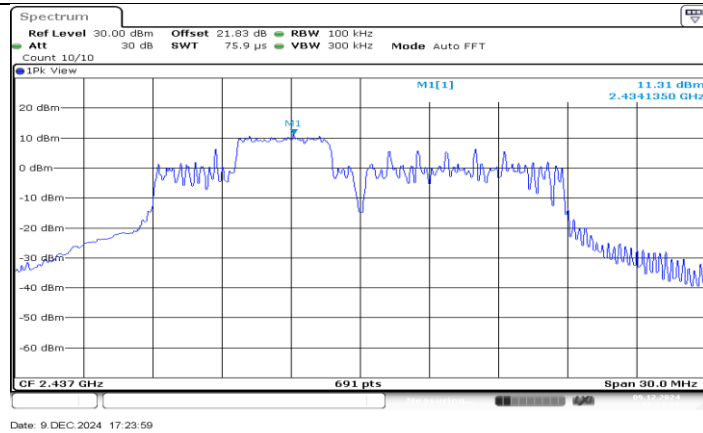
11AX20SISO_Ant3_2437_26Tone_RU4_0~Reference



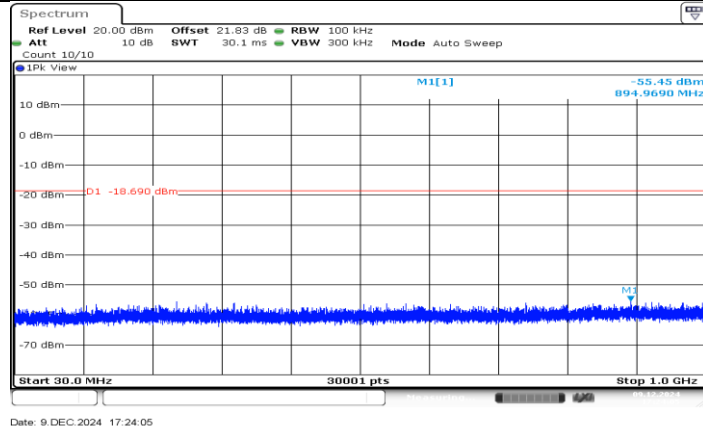
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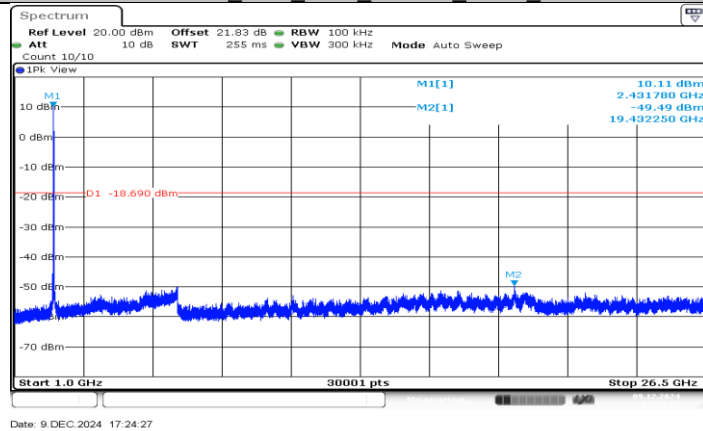
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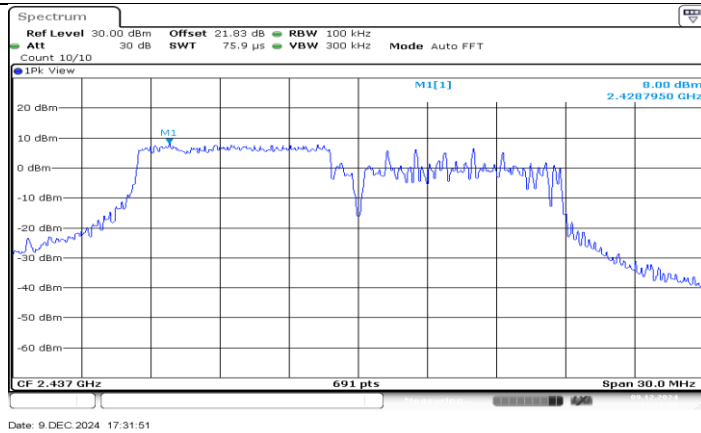
11AX20SISO_Ant3_2437_52Tone_RU38_0~Reference



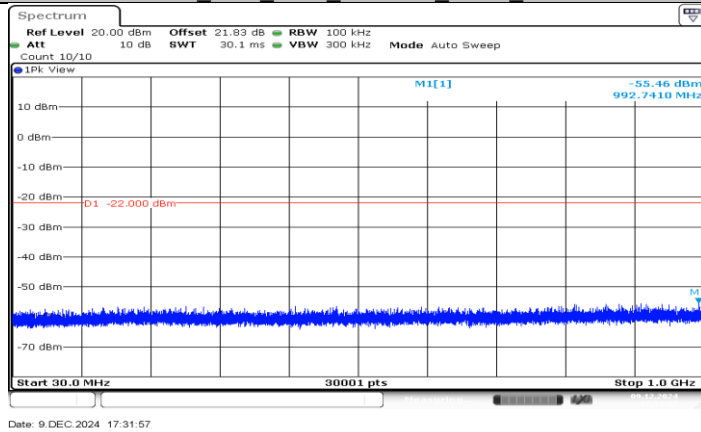
11AX20SISO_Ant3_2437_52Tone_RU38_30~1000



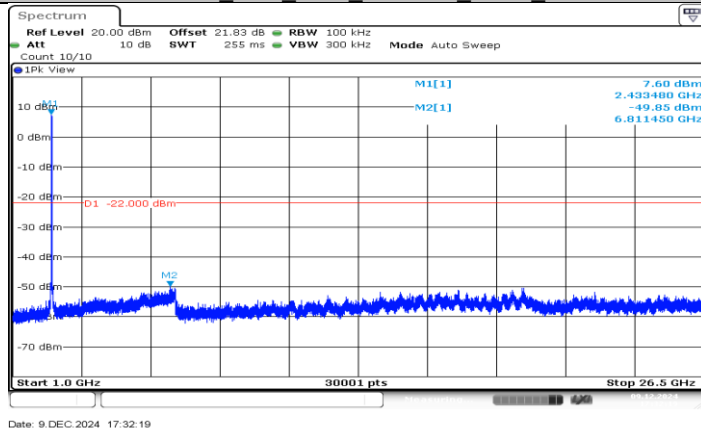
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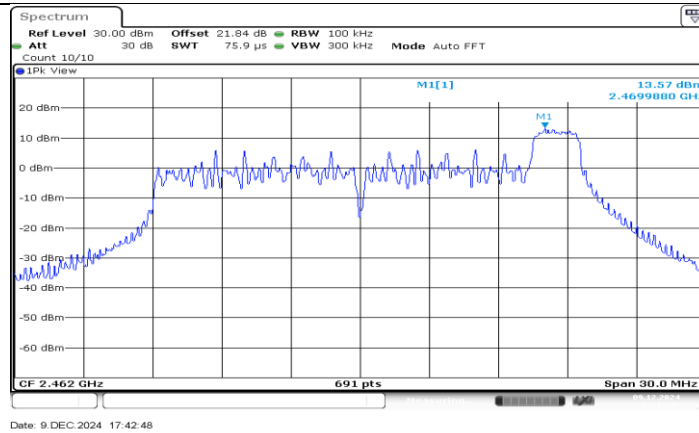
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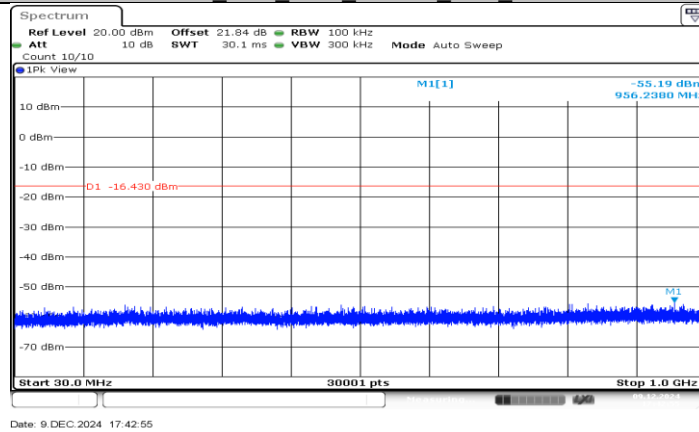
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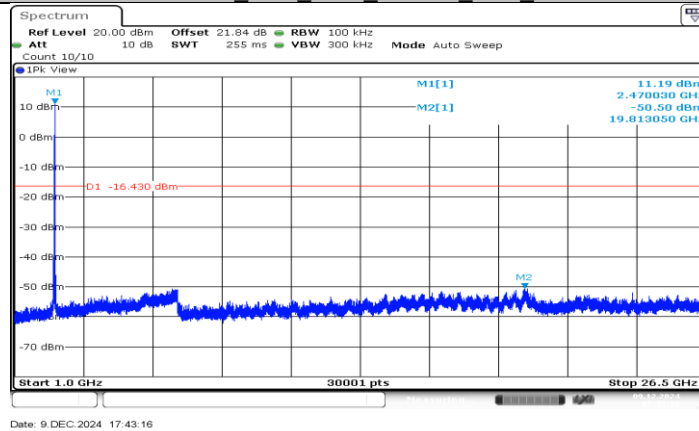
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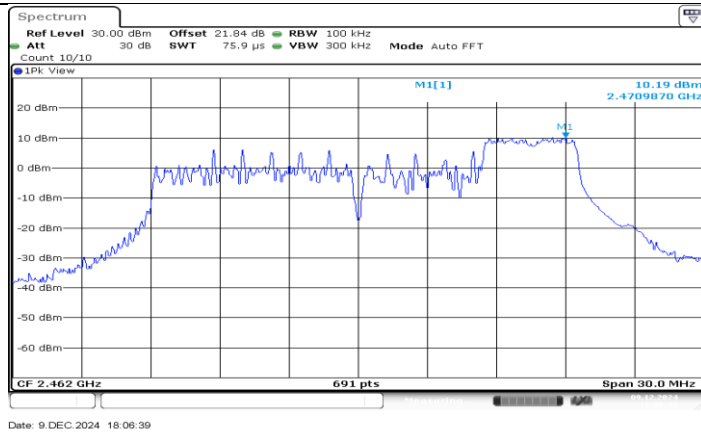
11AX20SISO_Ant3_2462_26Tone_RU8_0~Reference



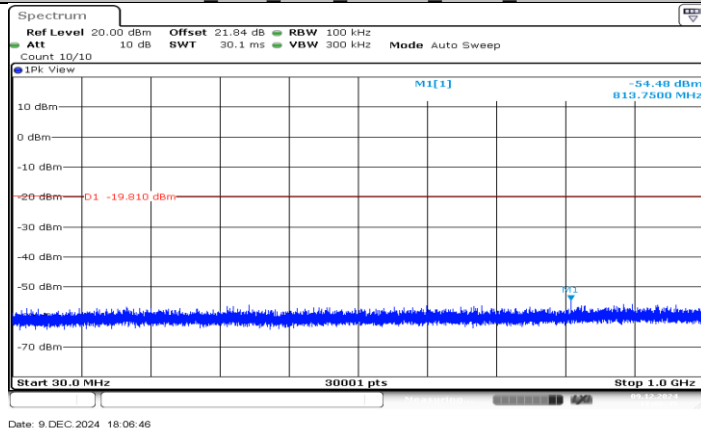
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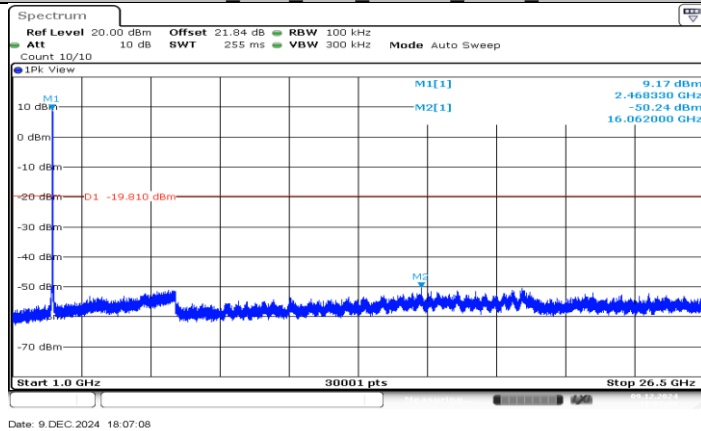
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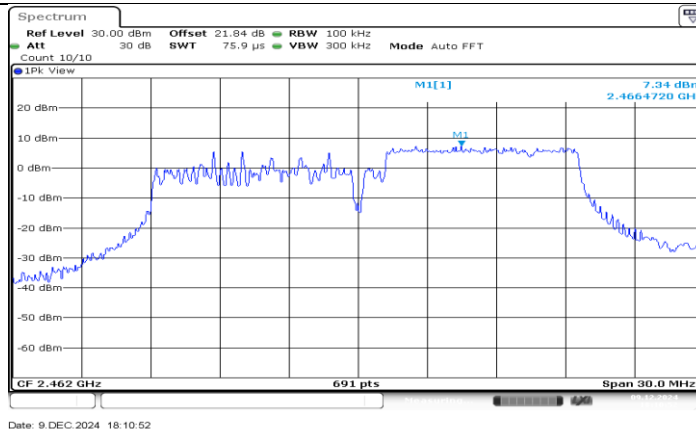
11AX20SISO_Ant3_2462_52Tone_RU40_0~Reference



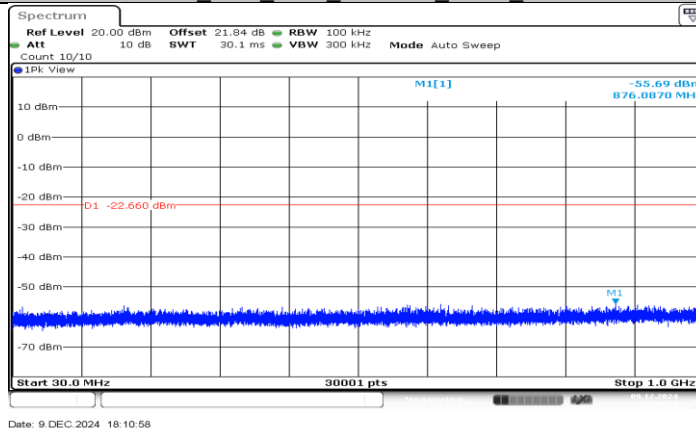
11AX20SISO_Ant3_2462_52Tone_RU40_30~1000



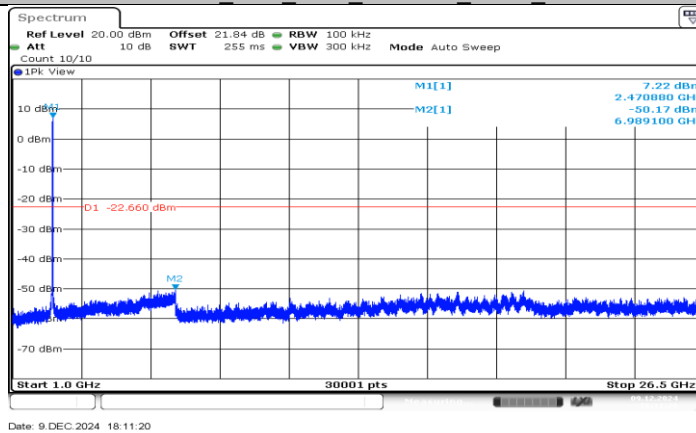
11AX20SISO_Ant3_2462_52Tone_RU40_1000~26500



11AX20SISO_Ant3_2462_106Tone_RU54_0~Reference



11AX20SISO_Ant3_2462_106Tone_RU54_30~1000



11AX20SISO_Ant3_2462_106Tone_RU54_1000~26500

11.7. APPENDIX G: DUTY CYCLE

11.7.1. Test Result

Test Mode	On Time (msec)	Period (msec)	Duty Cycle x (Linear)	Duty Cycle (%)	Duty Cycle Correction Factor (dB)	1/T Minimum VBW (kHz)	Final setting For VBW (kHz)
11B	8.38	8.74	0.9588	95.88	0.18	0.12	1
11G	1.39	1.78	0.7809	78.09	1.07	0.72	1
11N20SISO	1.3	1.67	0.7784	77.84	1.09	0.77	1
11N40SISO	0.65	1.02	0.6373	63.73	1.96	1.54	2
11AX20SISO SU	1.02	1.39	0.7338	73.38	1.34	0.98	1
11AX40SISO SU	0.19	0.39	0.4872	48.72	3.12	5.26	6

Test Mode	RuSize	RuIndex	On Time (msec)	Period (msec)	Duty Cycle x (Linear)	Duty Cycle (%)	Duty Cycle Correction Factor (dB)	1/T Minimum VBW (kHz)	Final setting For VBW (kHz)
11AX20SISO	26Tone	RU0	1.6	1.95	0.8205	82.05	0.86	0.63	1
	52Tone	RU38	1.51	1.85	0.8162	81.62	0.88	0.66	1
	106Tone	RU54	0.44	0.79	0.5570	55.70	2.54	2.27	3

Note:

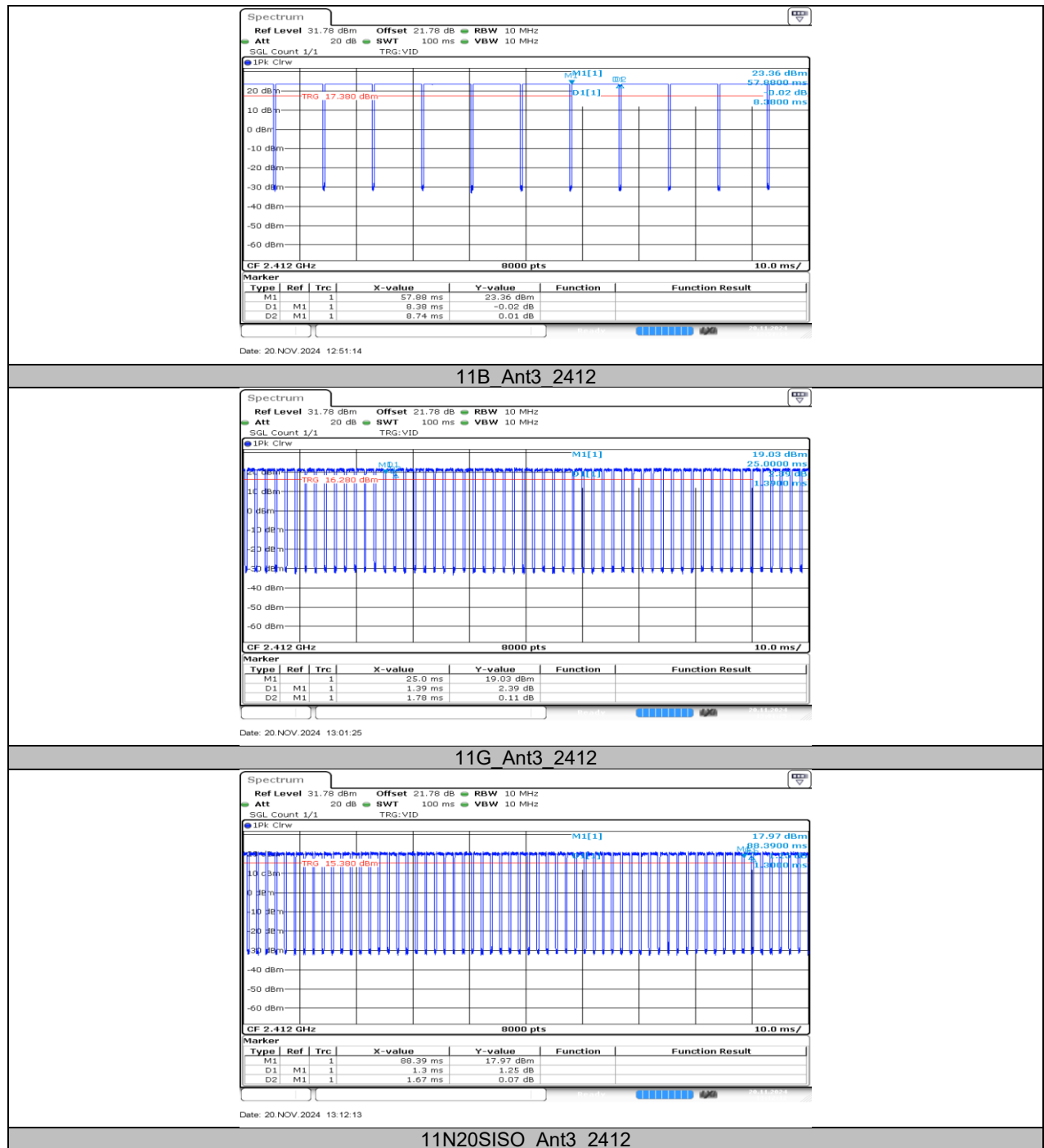
Duty Cycle Correction Factor=10log (1/x).

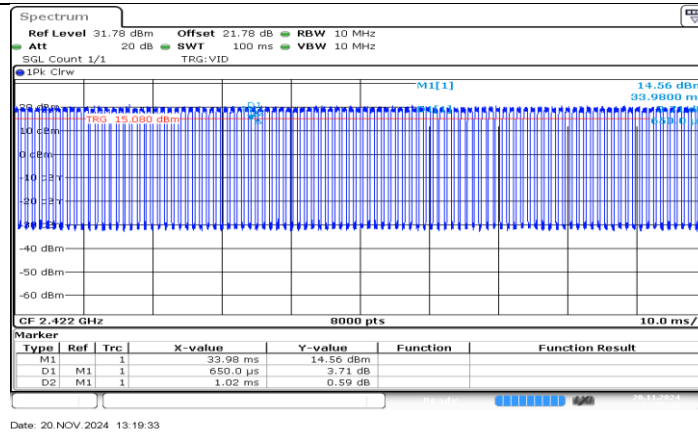
Where: x is Duty Cycle (Linear)

Where: T is On Time

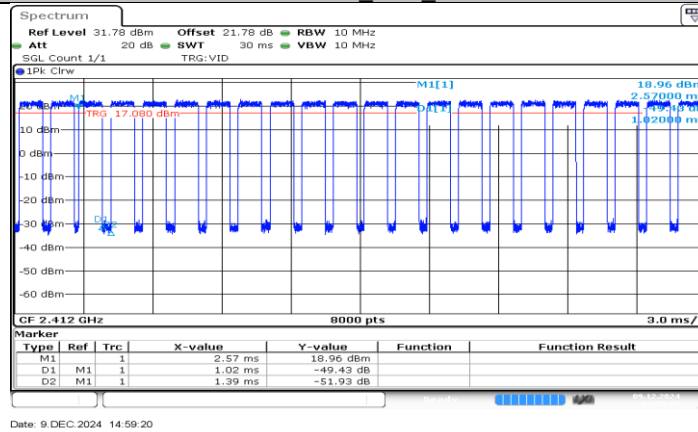
If that calculated VBW is not available on the analyzer then the next higher value should be used.

11.7.2. Test Graphs

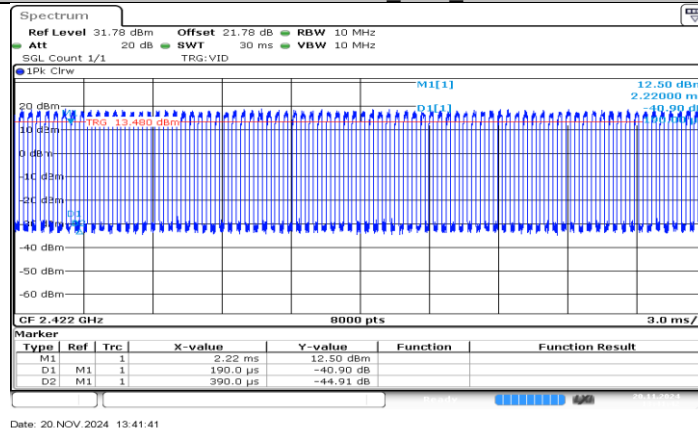




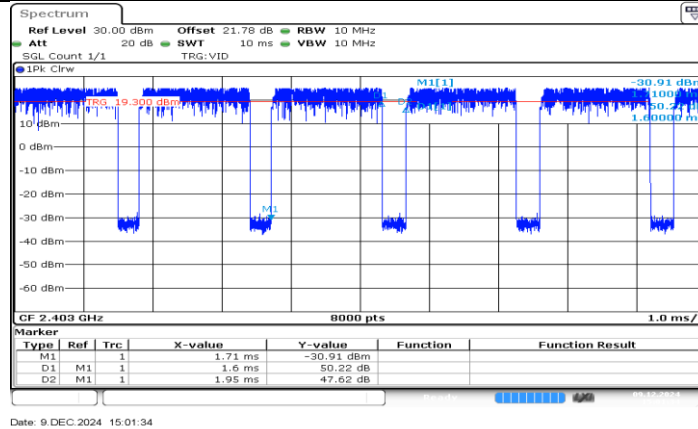
11N40SISO Ant3_2422



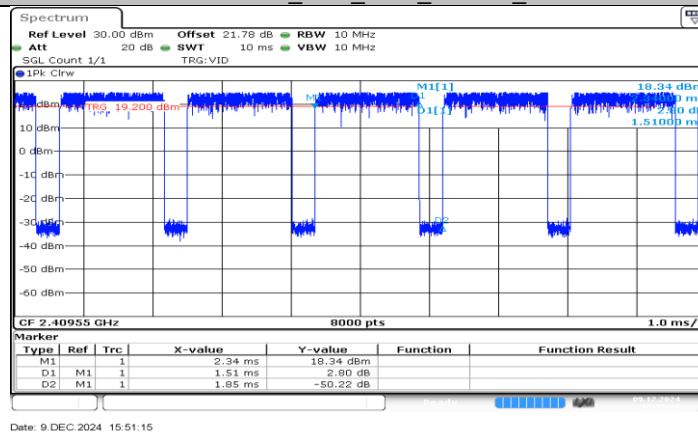
11AX20SISO SU_Ant3_2412



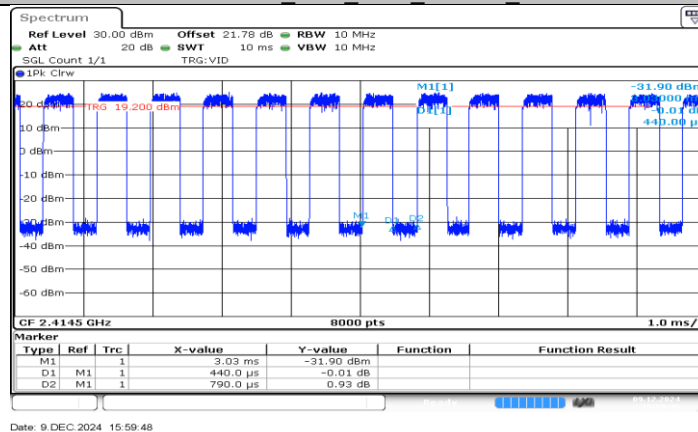
11AX40SISO SU_Ant3_2422



11AX20SISO_Ant3_2412_26Tone_RU0



11AX20SISO_Ant3_2412_52Tone_RU38



11AX20SISO_Ant3_2412_106Tone_RU54

END OF REPORT