

11.4. APPENDIX D: MAXIMUM CONDUCTED OUTPUT POWER

11.4.1. Test Result

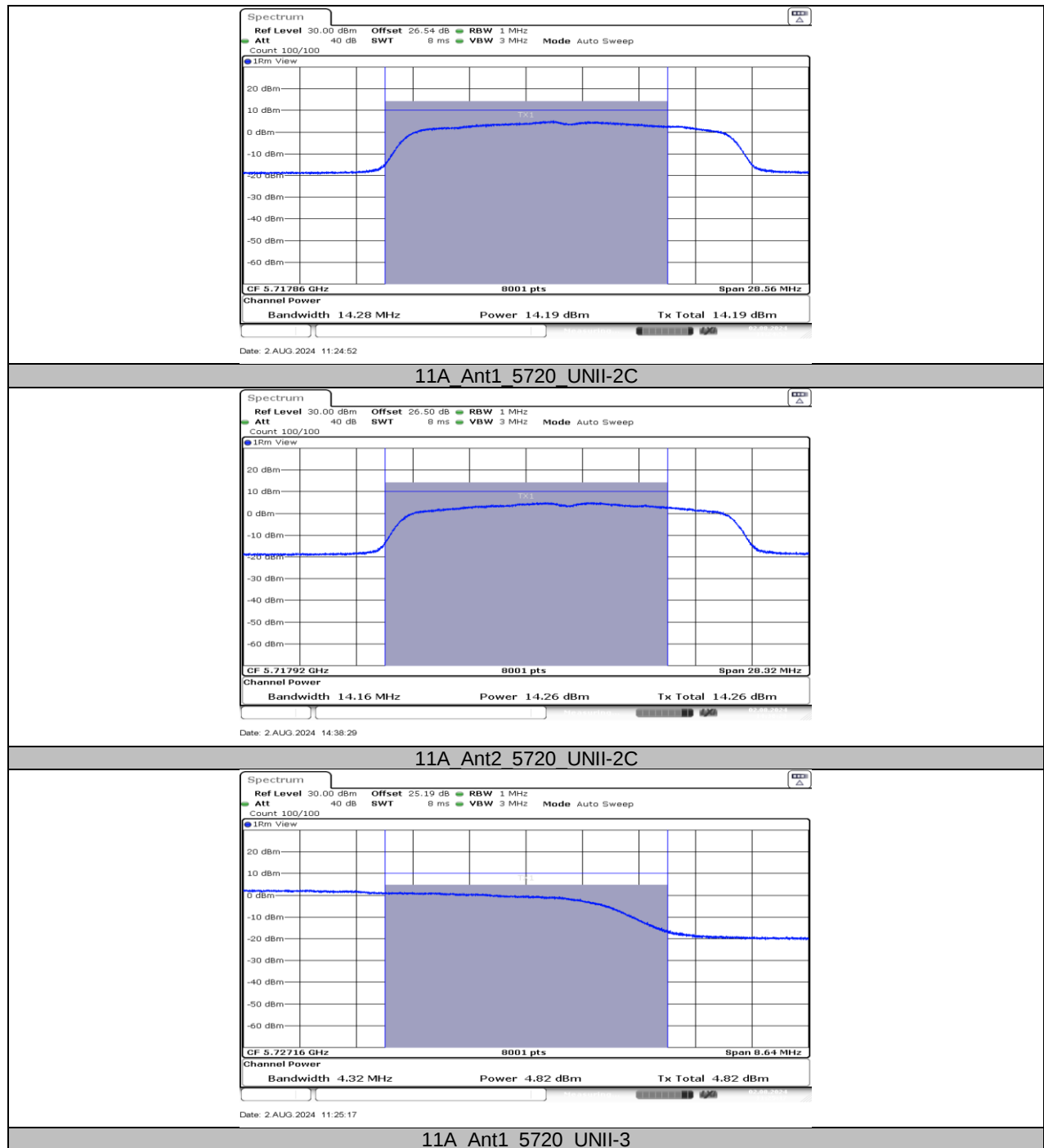
Test Mode	Antenna	Frequency[MHz]	Power [dBm]	FCC Limit [dBm]	ISED Limit [dBm]	EIRP [dBm]	Limit [dBm]	Verdict
11A	Ant1	5180	12.44	≤23.39	---	19.03	≤21.64	PASS
	Ant2	5180	14.25	≤23.98	---	17.05	≤22.22	PASS
	Ant1	5200	12.05	≤23.39	---	18.64	≤21.64	PASS
	Ant2	5200	14.33	≤23.98	---	17.13	≤22.23	PASS
	Ant1	5240	12.18	≤23.39	---	18.77	≤21.65	PASS
	Ant2	5240	14.35	≤23.98	---	17.15	≤22.24	PASS
	Ant1	5260	13.98	≤23.13	≤22.63	20.57	≤28.63	PASS
	Ant2	5260	14.45	≤23.86	≤23.23	17.25	≤29.23	PASS
	Ant1	5280	14.11	≤23.17	≤22.65	20.70	≤28.65	PASS
	Ant2	5280	14.11	≤23.73	≤23.24	16.91	≤29.24	PASS
	Ant1	5320	14.09	≤23.23	≤22.65	20.68	≤28.65	PASS
	Ant2	5320	14.25	≤23.89	≤23.23	17.05	≤29.23	PASS
	Ant1	5500	13.91	≤23.19	≤22.65	20.50	≤28.65	PASS
	Ant2	5500	13.96	≤23.79	≤23.24	16.76	≤29.24	PASS
	Ant1	5580	14.17	≤23.35	≤22.65	20.76	≤28.65	PASS
	Ant2	5580	13.98	≤23.73	≤23.23	16.78	≤29.23	PASS
	Ant1	5700	14.15	≤23.17	≤22.65	20.74	≤28.65	PASS
	Ant2	5700	14.16	≤23.78	≤23.23	16.96	≤29.23	PASS
	Ant1	5720 UNII-2C	14.19	≤21.96	≤21.65	20.78	≤27.65	PASS
	Ant2	5720 UNII-2C	14.26	≤22.51	≤22.24	17.06	≤28.24	PASS
	Ant1	5720 UNII-3	4.82	≤29.41	≤29.41	11.41	---	PASS
	Ant2	5720 UNII-3	4.90	≤30.00	≤30.00	7.70	---	PASS
	Ant1	5745	14.12	≤29.41	≤29.41	20.71	---	PASS
	Ant2	5745	14.32	≤30.00	≤30.00	17.12	---	PASS
	Ant1	5785	14.18	≤29.41	≤29.41	20.77	---	PASS
	Ant2	5785	14.01	≤30.00	≤30.00	16.81	---	PASS
	Ant1	5825	14.13	≤29.41	≤29.41	20.72	---	PASS
	Ant2	5825	13.92	≤30.00	≤30.00	16.72	---	PASS
11N20SISO	Ant1	5180	12.39	≤22.80	---	18.98	≤21.89	PASS
	Ant2	5180	14.35	≤23.98	---	17.15	≤22.49	PASS
	Ant1	5200	12.37	≤22.80	---	18.96	≤21.90	PASS
	Ant2	5200	13.99	≤23.98	---	16.79	≤22.49	PASS
	Ant1	5240	12.05	≤22.80	---	18.64	≤21.90	PASS
	Ant2	5240	14.37	≤23.98	---	17.17	≤22.49	PASS
	Ant1	5260	13.94	≤23.31	≤22.89	20.53	≤28.89	PASS
	Ant2	5260	13.96	≤23.98	≤23.49	16.76	≤29.49	PASS
	Ant1	5280	14.08	≤23.33	≤22.90	20.67	≤28.90	PASS
	Ant2	5280	14.13	≤23.90	≤23.48	16.93	≤29.48	PASS
	Ant1	5320	14.09	≤23.39	≤22.90	20.68	≤28.90	PASS
	Ant2	5320	14.17	≤23.98	≤23.48	16.97	≤29.48	PASS
	Ant1	5500	13.93	≤23.33	≤22.91	20.52	≤28.91	PASS
	Ant2	5500	13.92	≤23.98	≤23.51	16.72	≤29.51	PASS
	Ant1	5580	14.16	≤23.39	≤22.90	20.75	≤28.90	PASS
	Ant2	5580	13.99	≤23.94	≤23.49	16.79	≤29.49	PASS
	Ant1	5700	14.14	≤23.39	≤22.90	20.73	≤28.90	PASS
	Ant2	5700	14.07	≤23.85	≤23.48	16.87	≤29.48	PASS
	Ant1	5720 UNII-2C	13.69	≤22.11	≤21.83	20.28	≤27.83	PASS
	Ant2	5720 UNII-2C	14.12	≤22.76	≤22.42	16.92	≤28.42	PASS
	Ant1	5720 UNII-3	4.61	≤29.41	≤29.41	11.20	---	PASS
	Ant2	5720 UNII-3	5.15	≤30.00	≤30.00	7.95	---	PASS
	Ant1	5745	14.29	≤29.41	≤29.41	20.88	---	PASS
	Ant2	5745	14.30	≤30.00	≤30.00	17.10	---	PASS
	Ant1	5785	13.86	≤29.41	≤29.41	20.45	---	PASS
	Ant2	5785	13.90	≤30.00	≤30.00	16.70	---	PASS

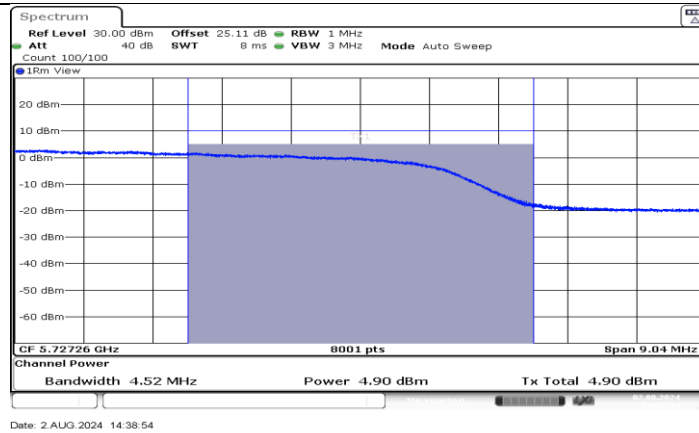
	Ant1	5825	13.76	≤29.41	≤29.41	20.35	---	PASS
	Ant2	5825	13.79	≤30.00	≤30.00	16.59	---	PASS
11N40SISO	Ant1	5190	14.14	≤23.39	---	20.73	≤22.41	PASS
	Ant2	5190	14.06	≤23.98	---	16.86	≤23.00	PASS
	Ant1	5230	13.93	≤23.39	---	20.52	≤22.41	PASS
	Ant2	5230	14.05	≤23.98	---	16.85	≤23.00	PASS
	Ant1	5270	13.86	≤23.39	≤23.39	20.45	≤29.41	PASS
	Ant2	5270	14.03	≤23.98	≤23.98	16.83	≤30.00	PASS
	Ant1	5310	14.03	≤23.39	≤23.39	20.62	≤29.41	PASS
	Ant2	5310	13.80	≤23.98	≤23.98	16.60	≤30.00	PASS
	Ant1	5510	13.84	≤23.39	≤23.39	20.43	≤29.41	PASS
	Ant2	5510	14.10	≤23.98	≤23.98	16.90	≤30.00	PASS
	Ant1	5550	14.21	≤23.39	≤23.39	20.80	≤29.41	PASS
	Ant2	5550	14.00	≤23.98	≤23.98	16.80	≤30.00	PASS
	Ant1	5670	14.18	≤23.39	≤23.39	20.77	≤29.41	PASS
	Ant2	5670	14.23	≤23.98	≤23.98	17.03	≤30.00	PASS
	Ant1	5710 UNII-2C	13.81	≤23.39	≤23.39	20.40	≤29.41	PASS
	Ant2	5710 UNII-2C	14.11	≤23.98	≤23.98	16.91	≤30.00	PASS
	Ant1	5710 UNII-3	-2.52	≤29.41	≤30.00	4.07	---	PASS
	Ant2	5710 UNII-3	-2.14	≤30.00	≤30.00	0.66	---	PASS
	Ant1	5755	14.05	≤29.41	≤30.00	20.64	---	PASS
	Ant2	5755	13.90	≤30.00	≤30.00	16.70	---	PASS
	Ant1	5795	13.62	≤29.41	≤30.00	20.21	---	PASS
	Ant2	5795	13.85	≤30.00	≤30.00	16.65	---	PASS

Note: 1. Conducted Power=Meas. Level+ Correction Factor

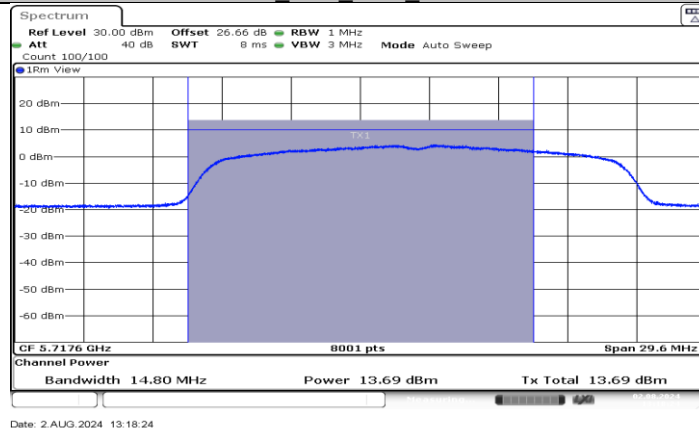
2. The Duty Cycle Factor (refer to section 7.1) had already compensated to the test data.

11.4.2. Test Graphs

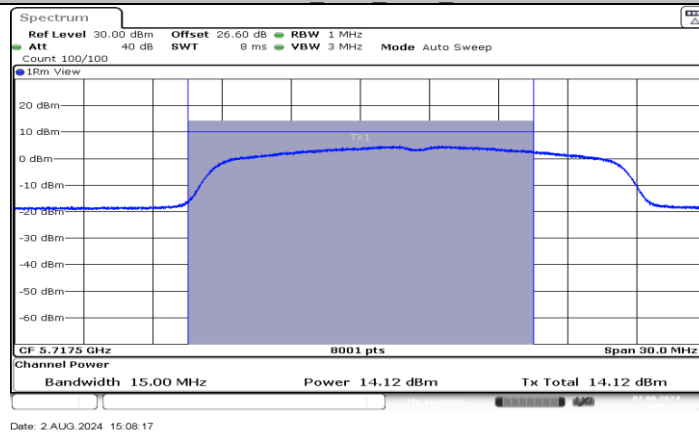




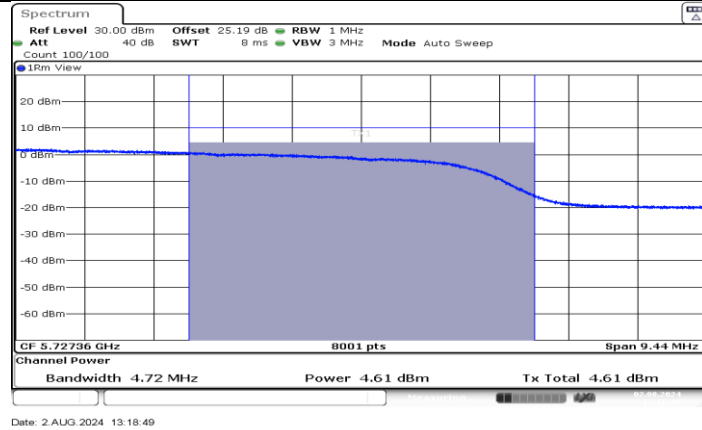
11A_Ant2_5720_UNII-3



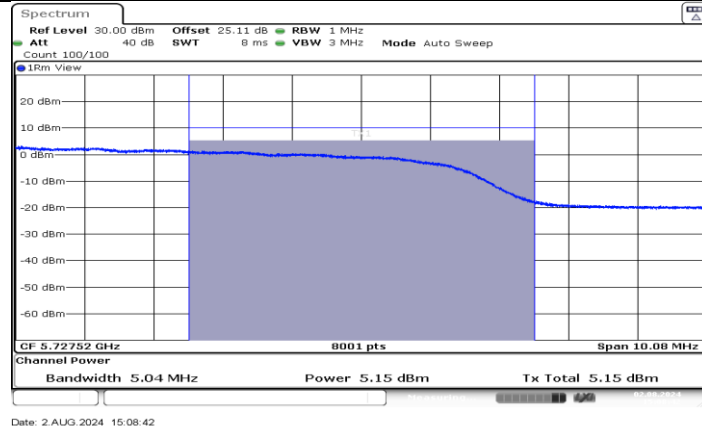
11N20SISO_Ant1_5720_UNII-2C



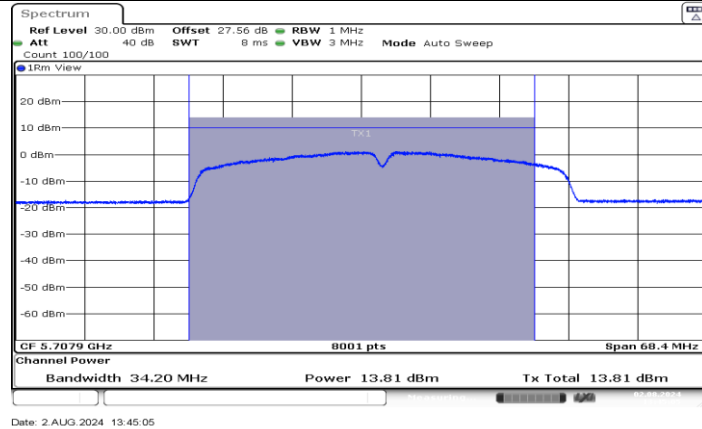
11N20SISO_Ant2_5720_UNII-2C



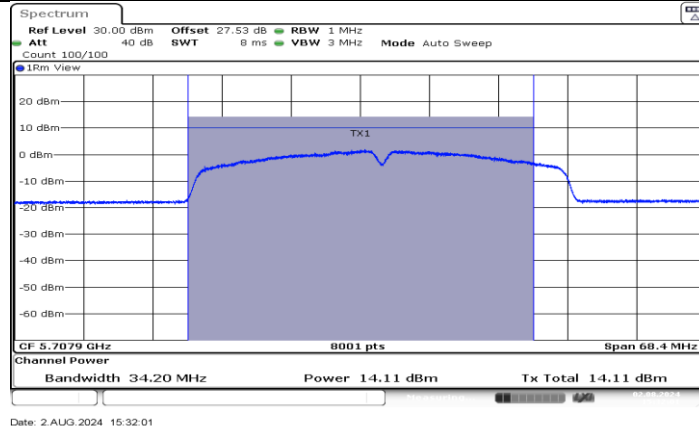
11N20SISO_Ant1_5720_UNII-3



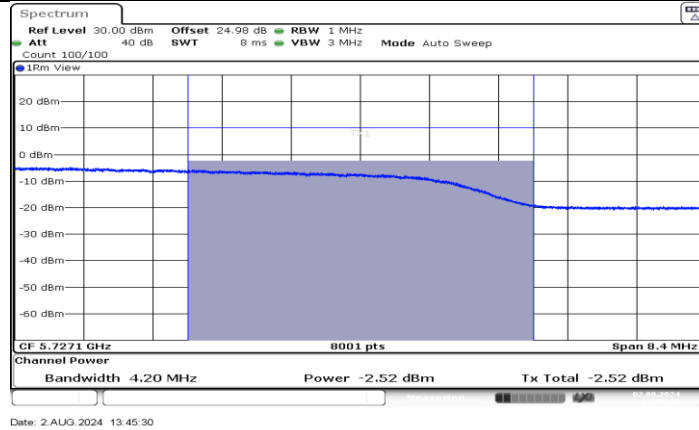
11N20SISO_Ant2_5720_UNII-3



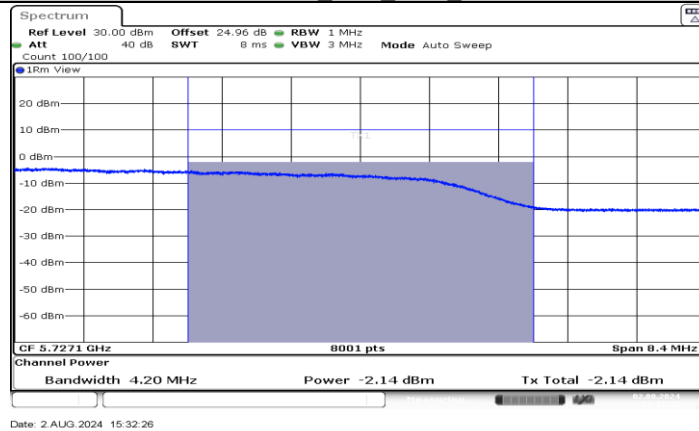
11N40SISO_Ant1_5710_UNII-2C



11N40SISO_Ant2_5710_UNII-2C



11N40SISO_Ant1_5710_UNII-3



11N40SISO_Ant2_5710_UNII-3

11.5. APPENDIX E: MAXIMUM POWER SPECTRAL DENSITY

11.5.1. Test Result

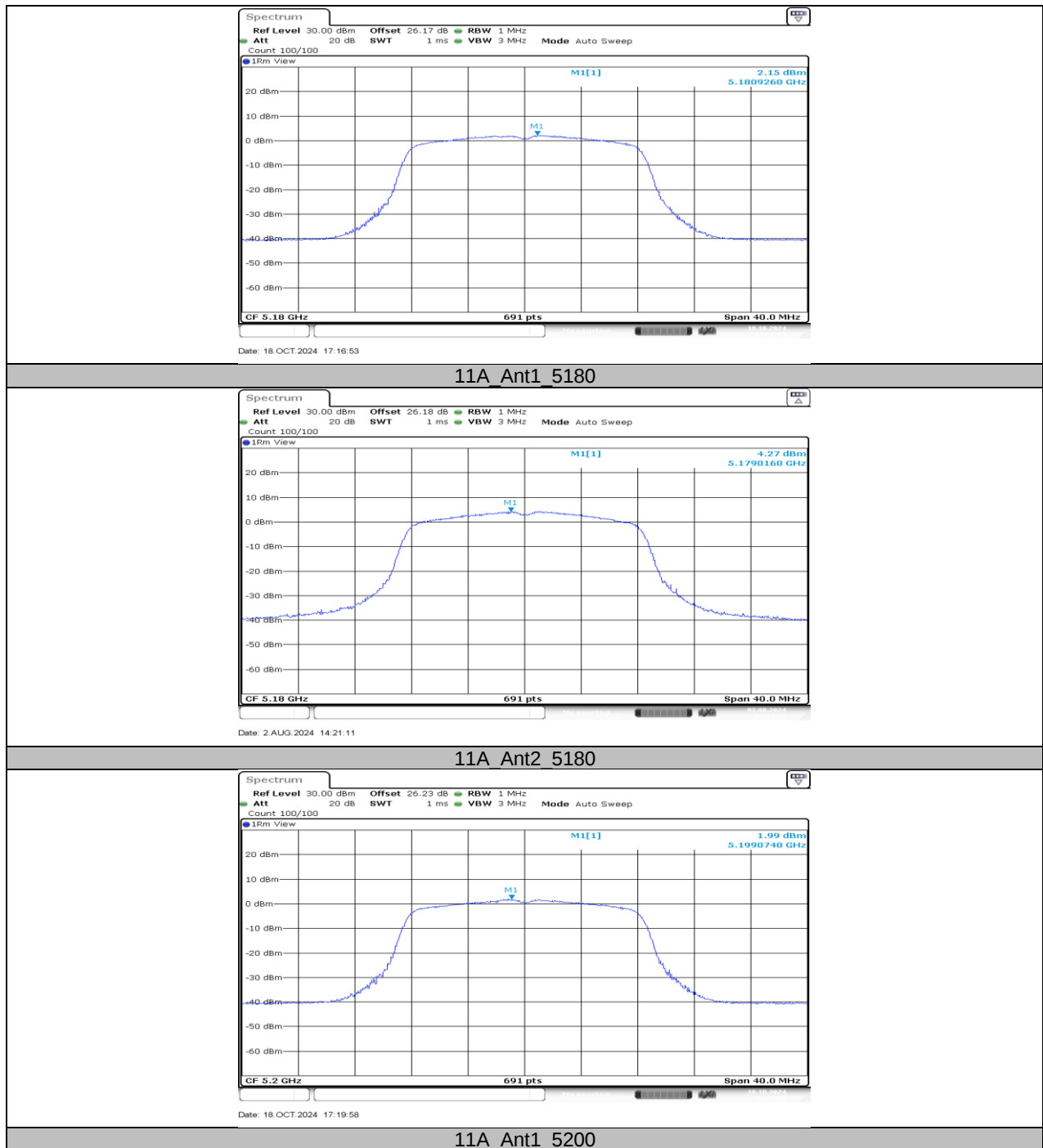
Test Mode	Antenna	Frequency[MHz]	Power [dBm/MHz]	Limit [dBm/MHz]	EIRP [dBm/MHz]	Limit [dBm/MHz]	Verdict
11A	Ant1	5180	2.15	≤10.41	8.74	≤9.41	PASS
	Ant2	5180	4.27	≤11.00	7.07	≤10.00	PASS
	Ant1	5200	1.99	≤10.41	8.58	≤9.41	PASS
	Ant2	5200	4.39	≤11.00	7.19	≤10.00	PASS
	Ant1	5240	2.09	≤10.41	8.68	≤9.41	PASS
	Ant2	5240	4.12	≤11.00	6.92	≤10.00	PASS
	Ant1	5260	4.11	≤10.41	10.70	---	PASS
	Ant2	5260	4.40	≤11.00	7.20	---	PASS
	Ant1	5280	3.96	≤10.41	10.55	---	PASS
	Ant2	5280	4.05	≤11.00	6.85	---	PASS
	Ant1	5320	3.90	≤10.41	10.49	---	PASS
	Ant2	5320	4.16	≤11.00	6.96	---	PASS
	Ant1	5500	3.76	≤10.41	10.35	---	PASS
	Ant2	5500	4.09	≤11.00	6.89	---	PASS
	Ant1	5580	4.10	≤10.41	10.69	---	PASS
	Ant2	5580	3.81	≤11.00	6.61	---	PASS
	Ant1	5700	4.02	≤10.41	10.61	---	PASS
	Ant2	5700	4.02	≤11.00	6.82	---	PASS
	Ant1	5720 UNII-2C	4.66	≤10.41	11.25	---	PASS
	Ant2	5720 UNII-2C	4.92	≤11.00	7.72	---	PASS
	Ant1	5720 UNII-3	-0.25	≤29.41	6.34	---	PASS
	Ant2	5720 UNII-3	-0.38	≤30.00	2.42	---	PASS
	Ant1	5745	1.07	≤29.41	7.66	---	PASS
	Ant2	5745	1.27	≤30.00	4.07	---	PASS
	Ant1	5785	1.35	≤29.41	7.94	---	PASS
	Ant2	5785	1.09	≤30.00	3.89	---	PASS
	Ant1	5825	1.07	≤29.41	7.66	---	PASS
	Ant2	5825	0.86	≤30.00	3.66	---	PASS
11N20SISO	Ant1	5180	1.85	≤10.41	8.44	≤9.41	PASS
	Ant2	5180	3.93	≤11.00	6.73	≤10.00	PASS
	Ant1	5200	2.14	≤10.41	8.73	≤9.41	PASS
	Ant2	5200	3.85	≤11.00	6.65	≤10.00	PASS
	Ant1	5240	1.73	≤10.41	8.32	≤9.41	PASS
	Ant2	5240	4.22	≤11.00	7.02	≤10.00	PASS
	Ant1	5260	3.88	≤10.41	10.47	---	PASS
	Ant2	5260	3.79	≤11.00	6.59	---	PASS
	Ant1	5280	4.11	≤10.41	10.70	---	PASS
	Ant2	5280	3.77	≤11.00	6.57	---	PASS
	Ant1	5320	3.87	≤10.41	10.46	---	PASS
	Ant2	5320	4.02	≤11.00	6.82	---	PASS
	Ant1	5500	3.60	≤10.41	10.19	---	PASS
	Ant2	5500	3.73	≤11.00	6.53	---	PASS
	Ant1	5580	3.85	≤10.41	10.44	---	PASS
	Ant2	5580	3.58	≤11.00	6.38	---	PASS
	Ant1	5700	4.03	≤10.41	10.62	---	PASS
	Ant2	5700	3.87	≤11.00	6.67	---	PASS
	Ant1	5720 UNII-2C	4.08	≤10.41	10.67	---	PASS
	Ant2	5720 UNII-2C	4.70	≤11.00	7.50	---	PASS
	Ant1	5720 UNII-3	-0.90	≤29.41	5.69	---	PASS
	Ant2	5720 UNII-3	-0.39	≤30.00	2.41	---	PASS
	Ant1	5745	1.11	≤29.41	7.70	---	PASS
	Ant2	5745	1.09	≤30.00	3.89	---	PASS
	Ant1	5785	0.81	≤29.41	7.40	---	PASS
	Ant2	5785	0.95	≤30.00	3.75	---	PASS
	Ant1	5825	0.77	≤29.41	7.36	---	PASS

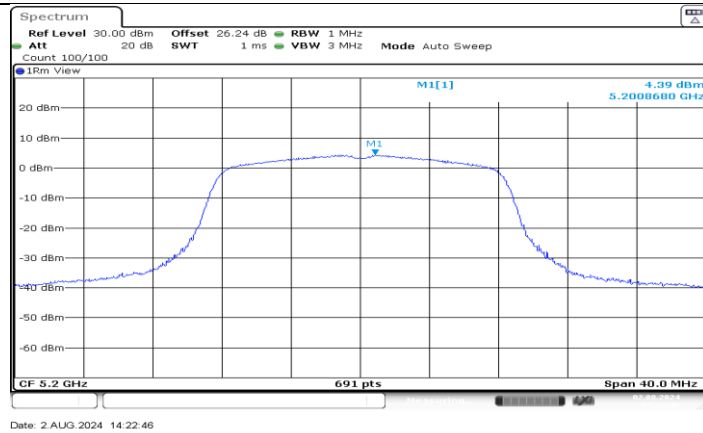
	Ant2	5825	0.64	≤30.00	3.44	---	PASS
11N40SISO	Ant1	5190	0.64	≤10.41	7.23	≤9.41	PASS
	Ant2	5190	1.20	≤11.00	4.00	≤10.00	PASS
	Ant1	5230	0.86	≤10.41	7.45	≤9.41	PASS
	Ant2	5230	0.46	≤11.00	3.26	≤10.00	PASS
	Ant1	5270	1.16	≤10.41	7.75	---	PASS
	Ant2	5270	0.93	≤11.00	3.73	---	PASS
	Ant1	5310	1.08	≤10.41	7.67	---	PASS
	Ant2	5310	0.78	≤11.00	3.58	---	PASS
	Ant1	5510	0.89	≤10.41	7.48	---	PASS
	Ant2	5510	1.29	≤11.00	4.09	---	PASS
	Ant1	5550	0.96	≤10.41	7.55	---	PASS
	Ant2	5550	1.16	≤11.00	3.96	---	PASS
	Ant1	5670	1.11	≤10.41	7.70	---	PASS
	Ant2	5670	1.13	≤11.00	3.93	---	PASS
	Ant1	5710 UNII-2C	1.10	≤10.41	7.69	---	PASS
	Ant2	5710 UNII-2C	1.40	≤11.00	4.20	---	PASS
	Ant1	5710 UNII-3	-6.25	≤29.41	0.34	---	PASS
	Ant2	5710 UNII-3	-6.52	≤30.00	-3.72	---	PASS
	Ant1	5755	-1.87	≤29.41	4.72	---	PASS
	Ant2	5755	-1.95	≤30.00	0.85	---	PASS
	Ant1	5795	-2.21	≤29.41	4.38	---	PASS
	Ant2	5795	-1.84	≤30.00	0.96	---	PASS

Note: 1.The Result and Limit Unit is dBm/500 kHz in the band 5.725–5.85 GHz.

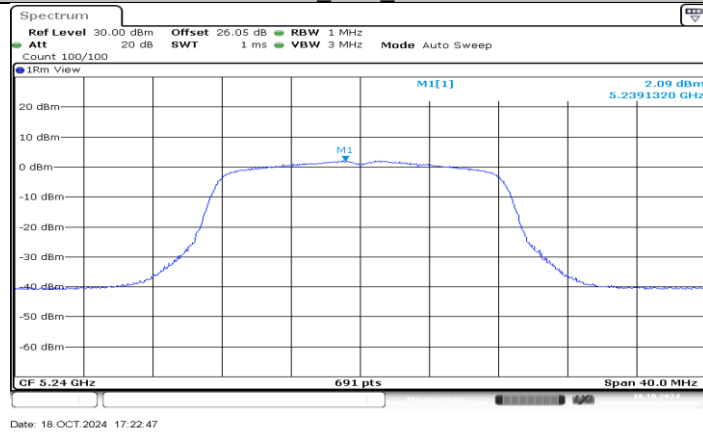
2.The Duty Cycle Factor and RBW Factor is compensated in the graph.

11.5.2. Test Graphs

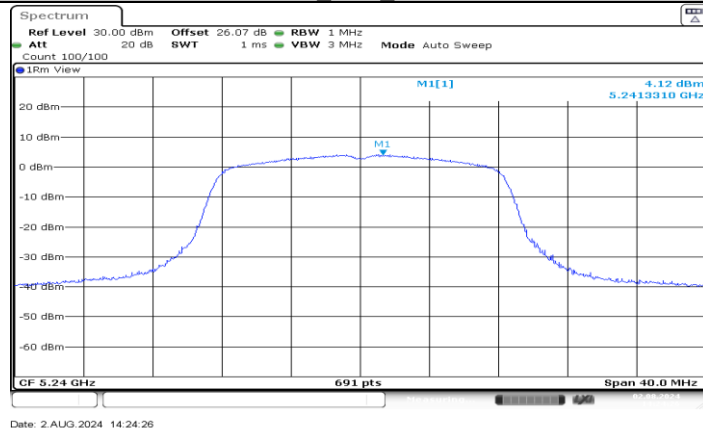




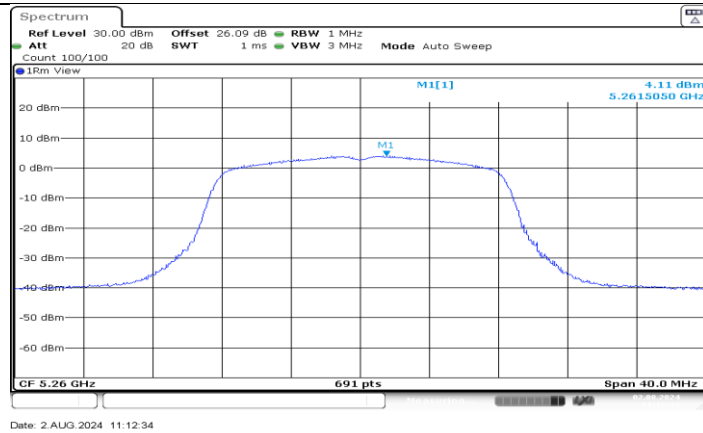
11A_Ant2_5200



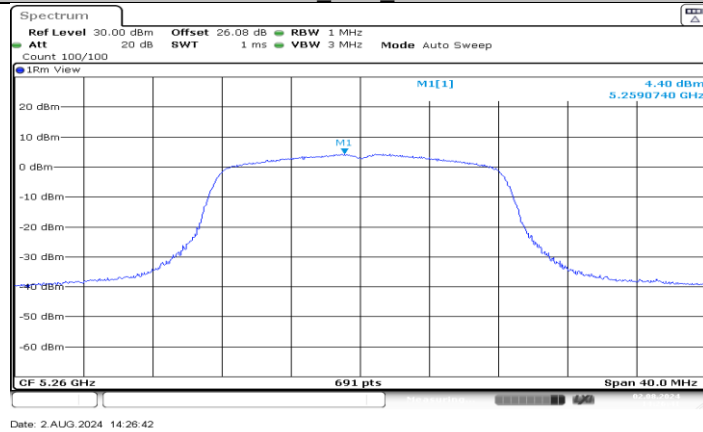
11A_Ant1_5240



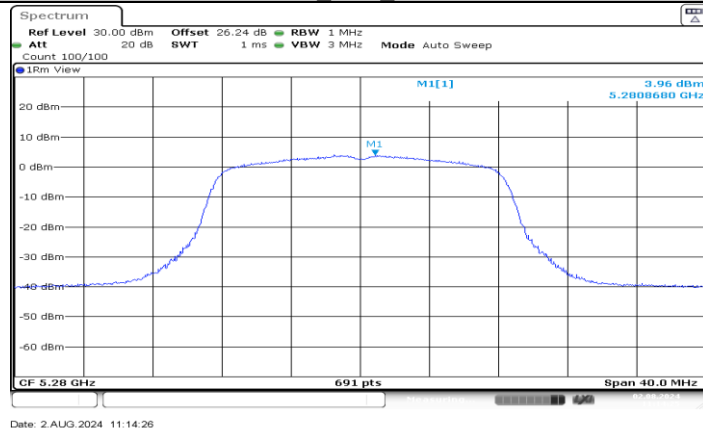
11A_Ant2_5240



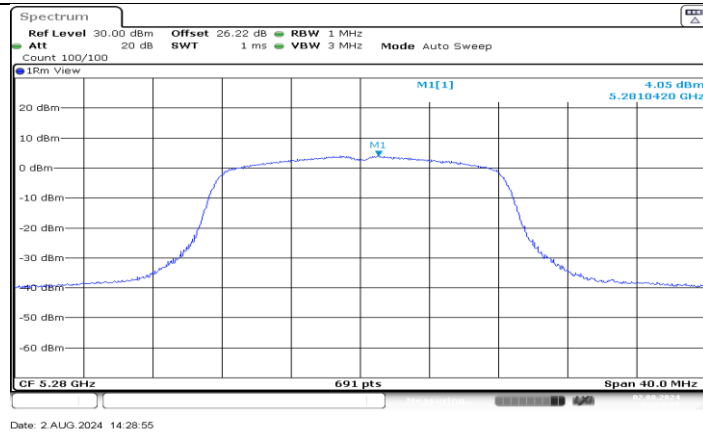
11A_Ant1_5260



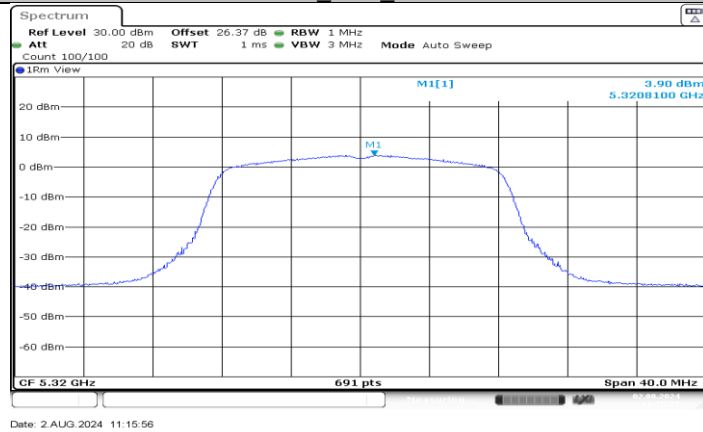
11A_Ant2_5260



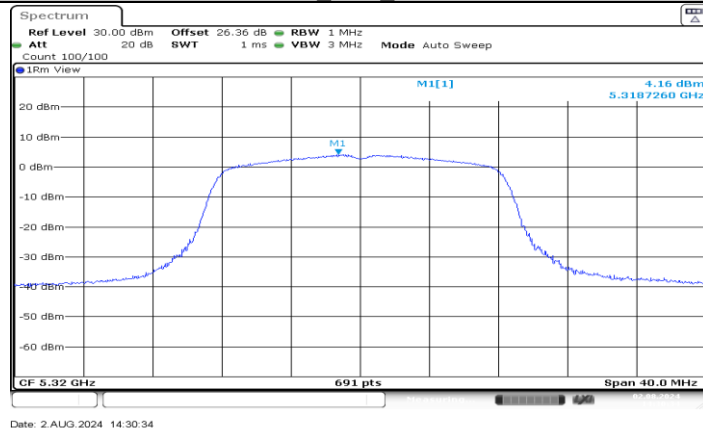
11A_Ant1_5280



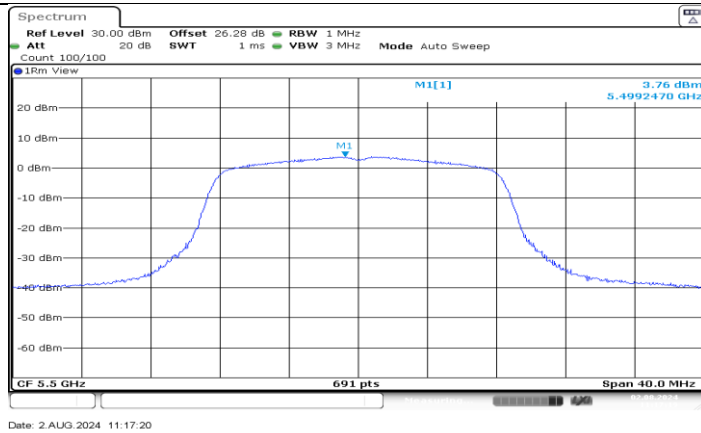
11A_Ant2_5280



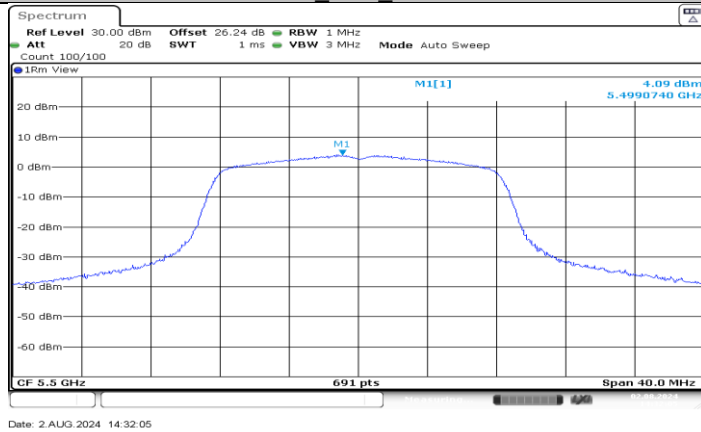
11A_Ant1_5320



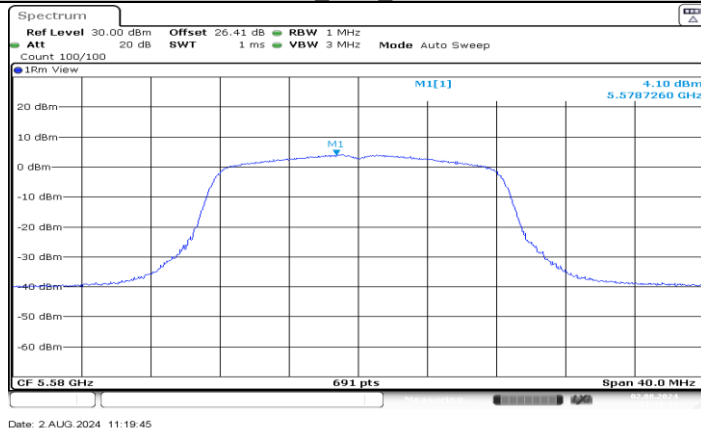
11A_Ant2_5320



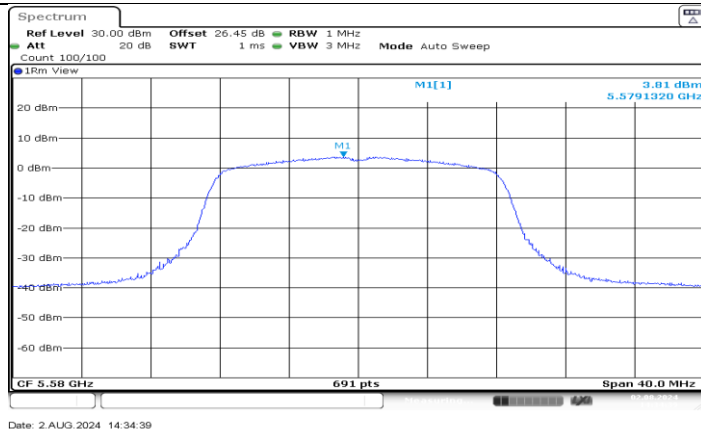
11A_Ant1_5500



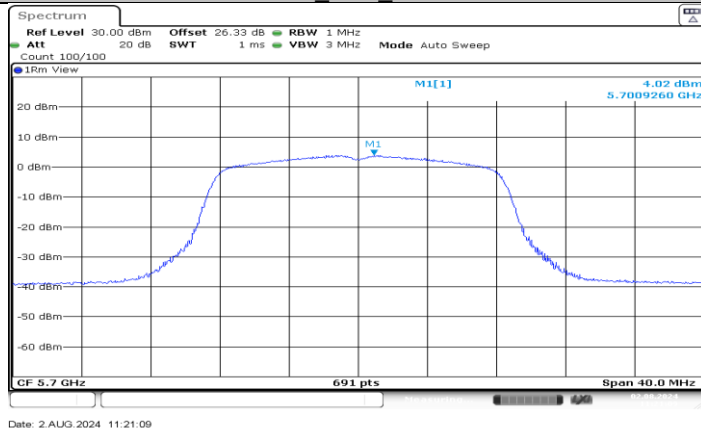
11A_Ant2_5500



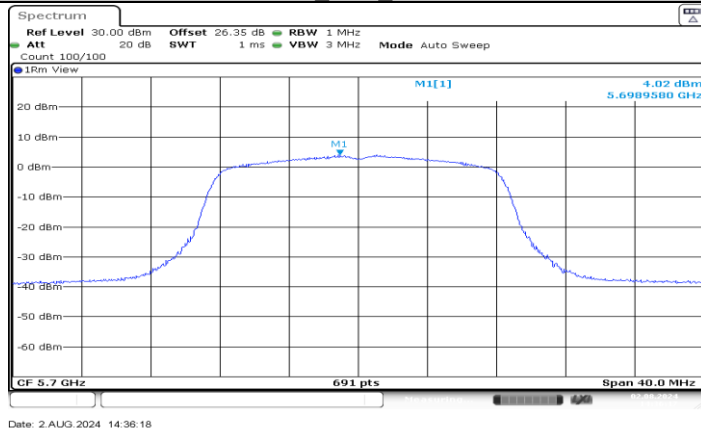
11A_Ant1_5580



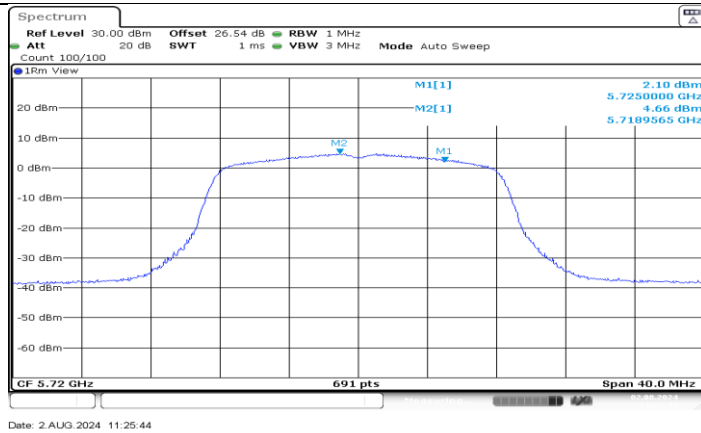
11A_Ant2_5580



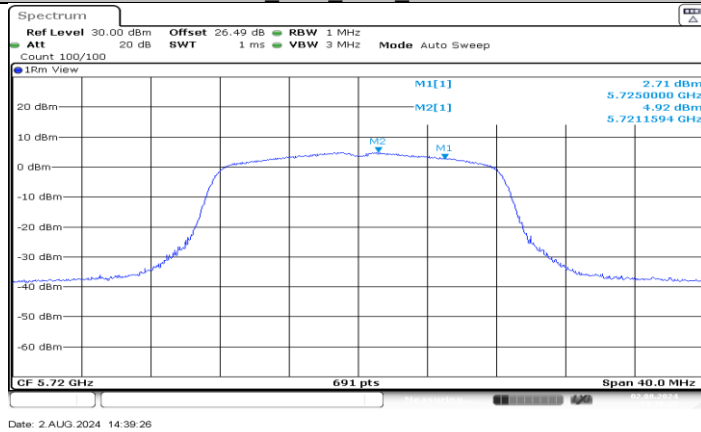
11A_Ant1_5700



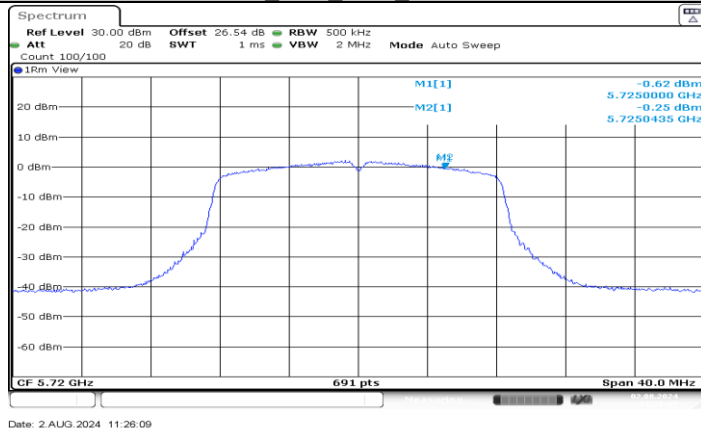
11A_Ant2_5700



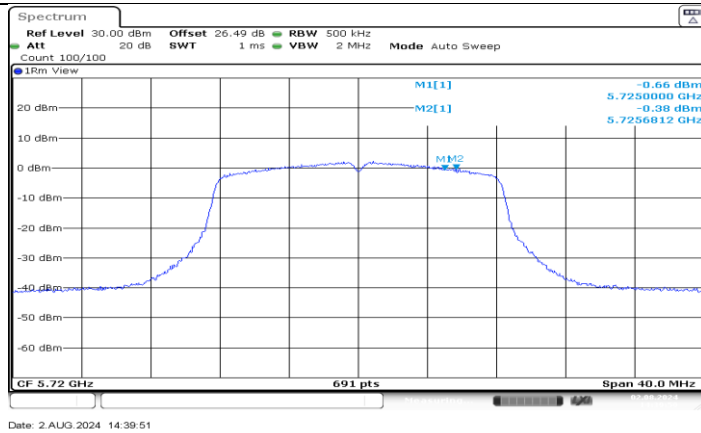
11A Ant1 5720 UNII-2C



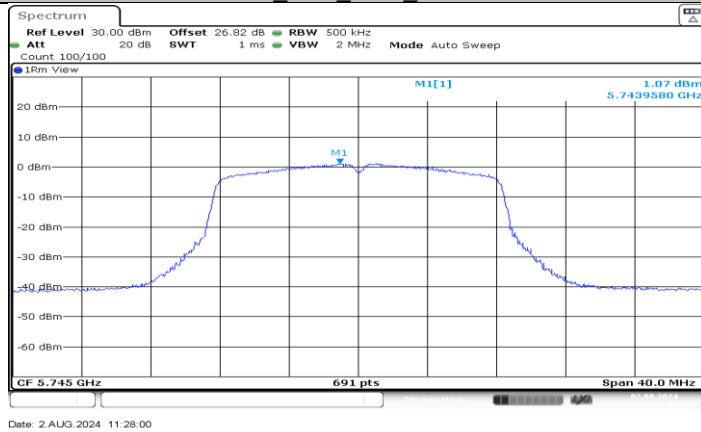
11A Ant2 5720 UNII-2C



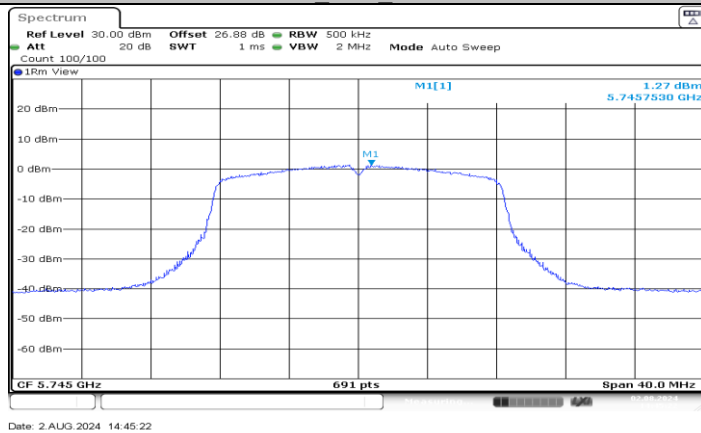
11A Ant1 5720 UNII-3



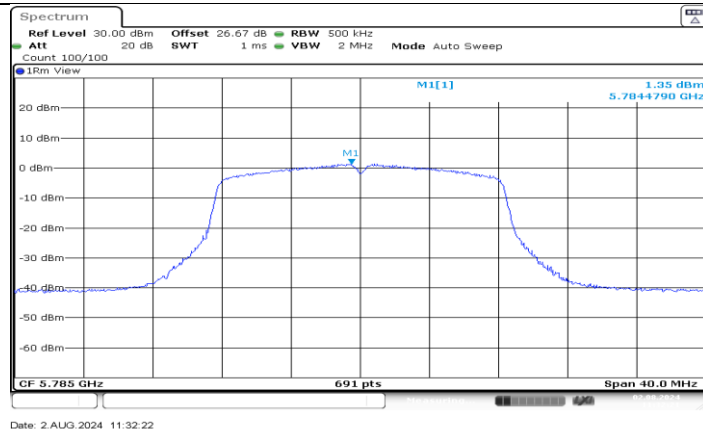
11A_Ant2_5720_UNII-3



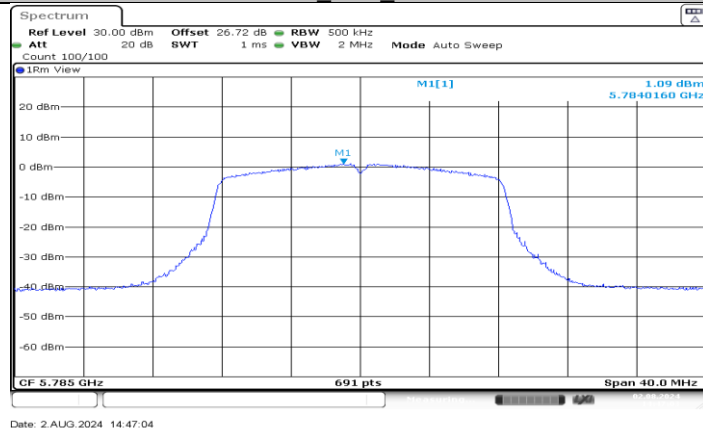
11A_Ant1_5745



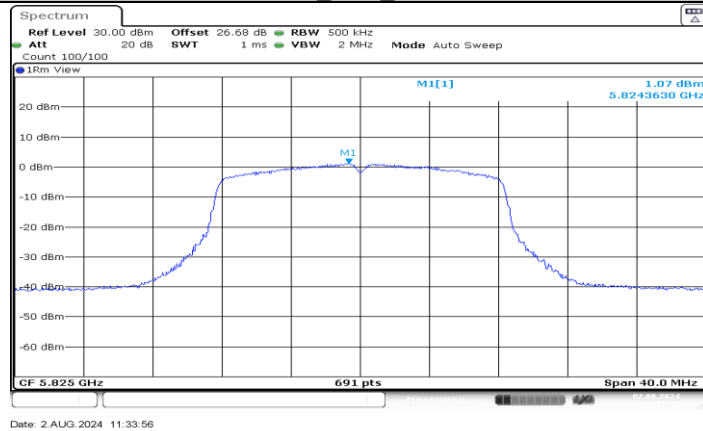
11A_Ant2_5745



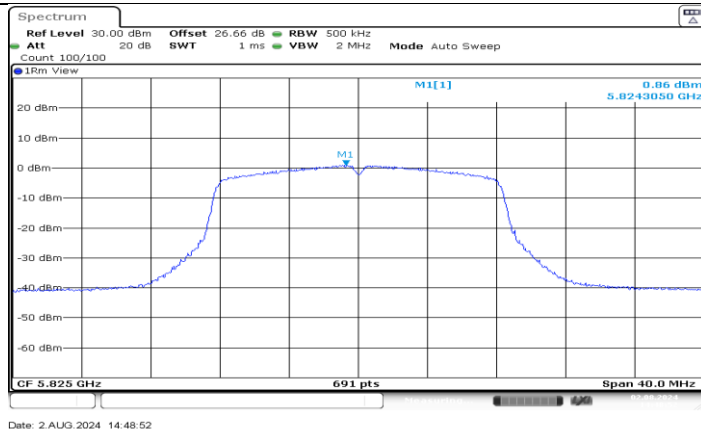
11A Ant1_5785



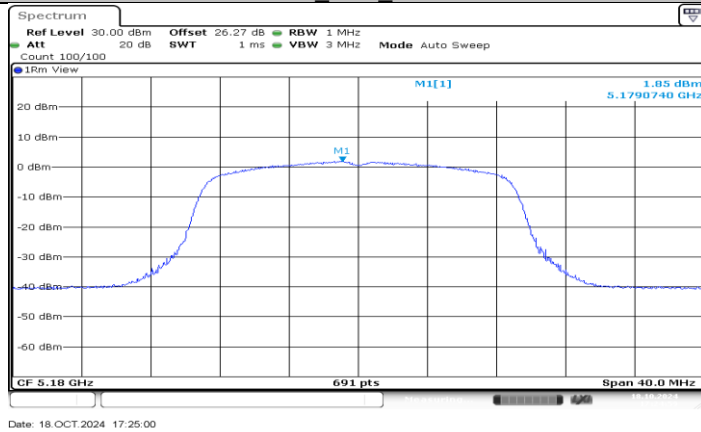
11A Ant2_5785



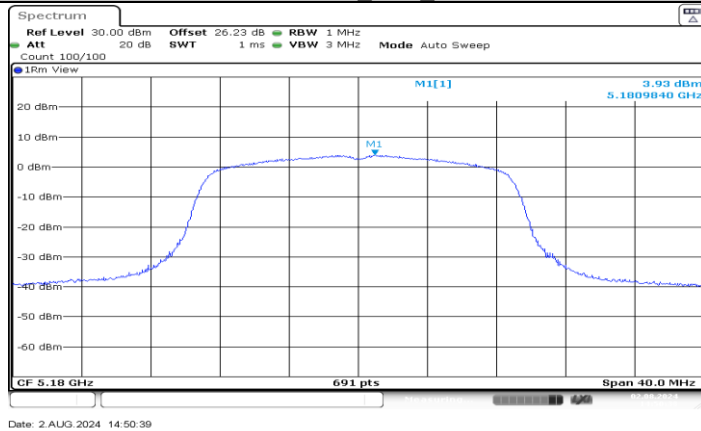
11A Ant1_5825



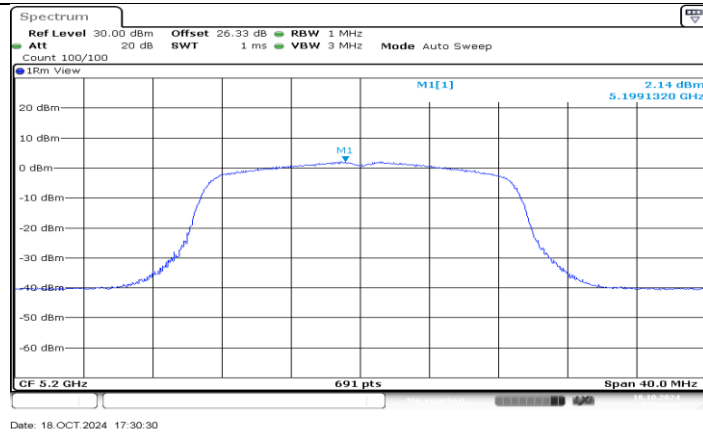
11A_Ant2_5825



11N20SISO_Ant1_5180



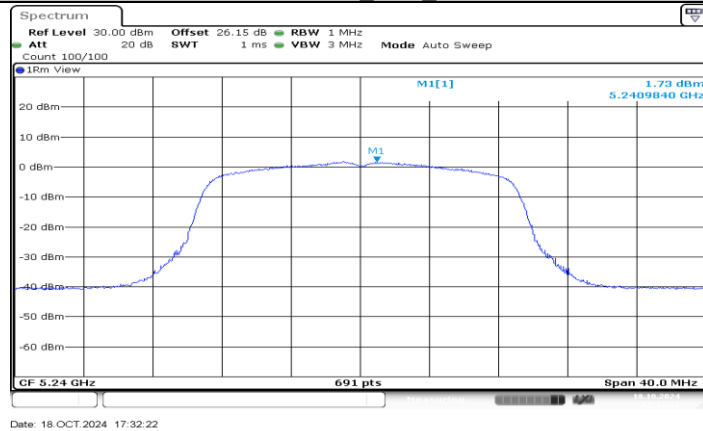
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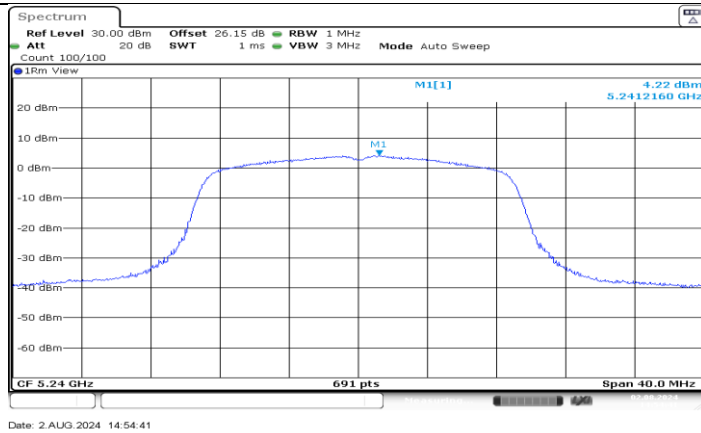
11N20SISO Ant1 5200



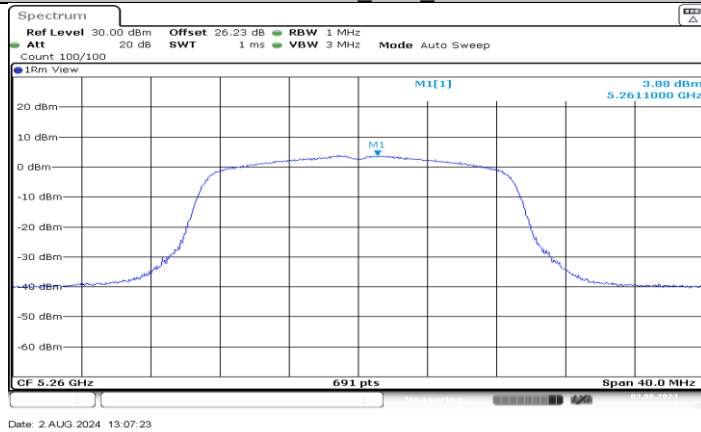
11N20SISO Ant2 5200



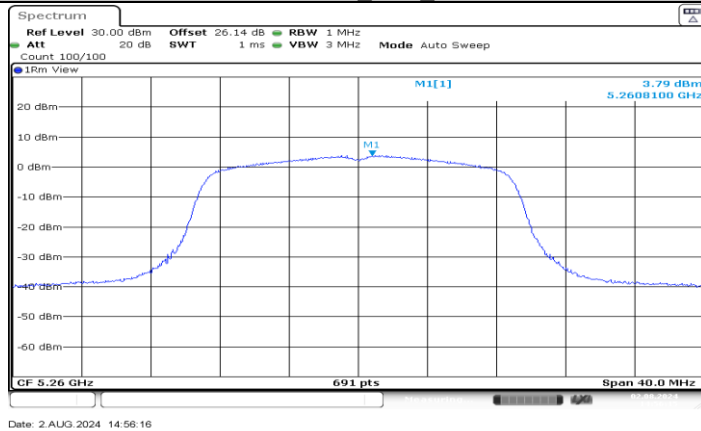
11N20SISO Ant1 5240



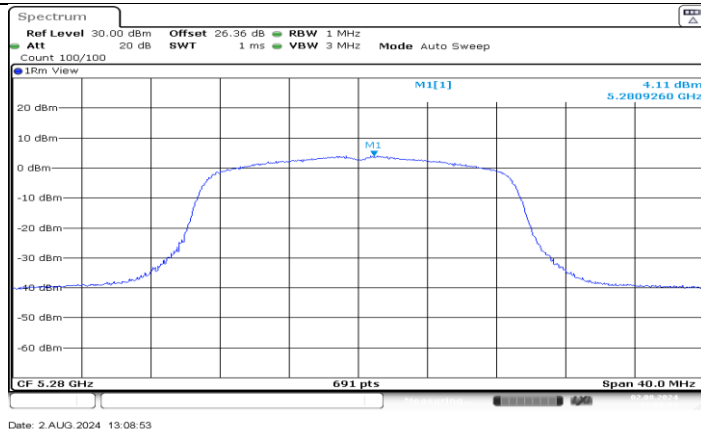
11N20SISO Ant2 5240



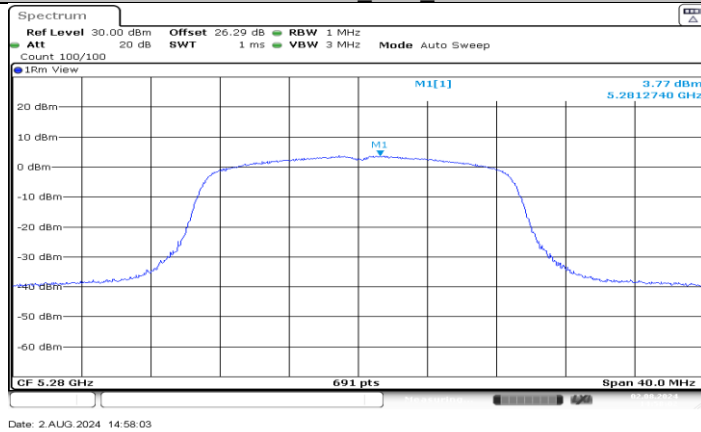
11N20SISO Ant1 5260



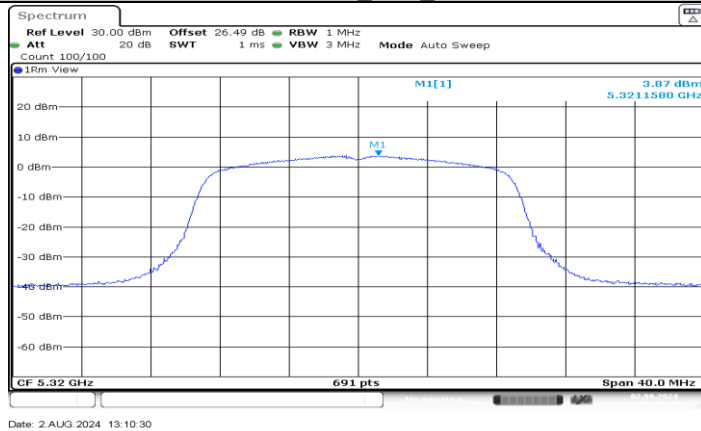
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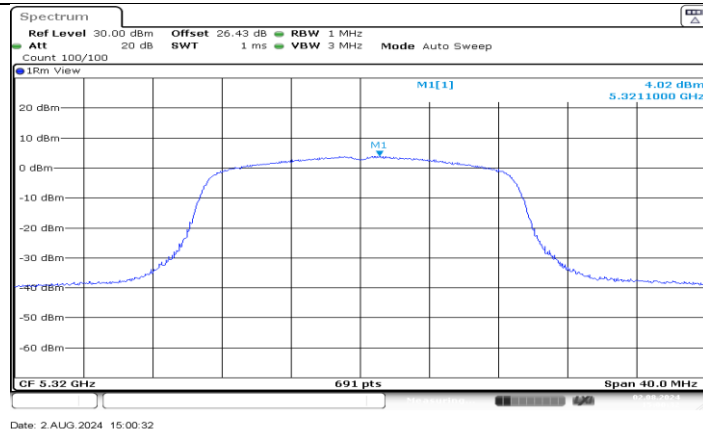
11N20SISO Ant1 5280



11N20SISO Ant2 5280



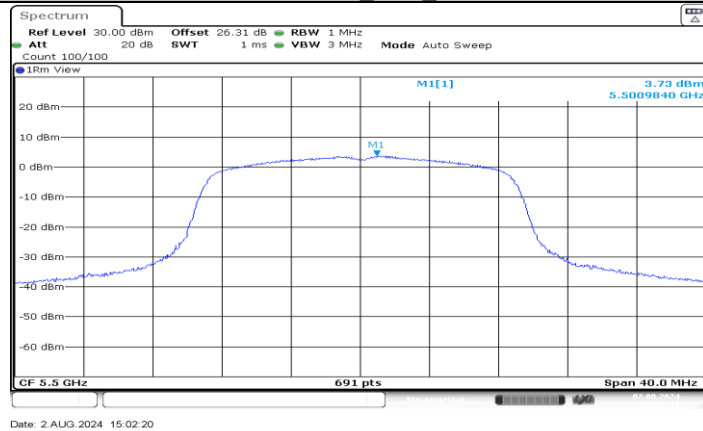
11N20SISO Ant1 5320



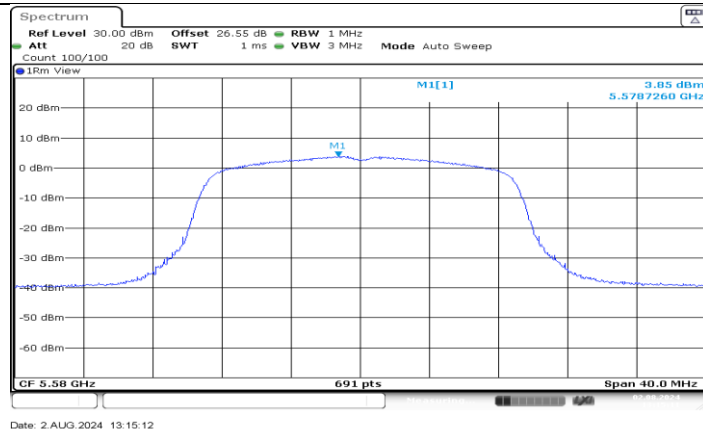
11N20SISO Ant2 5320



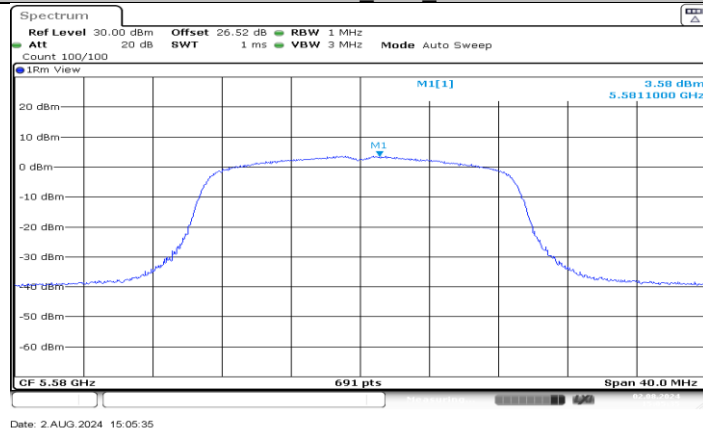
11N20SISO Ant1 5500



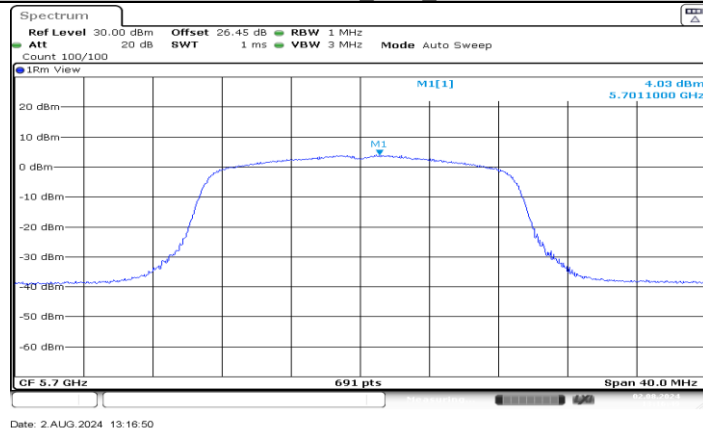
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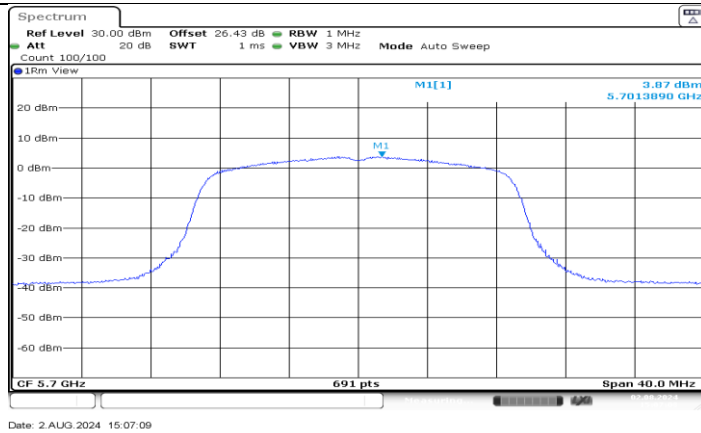
11N20SISO Ant1 5580



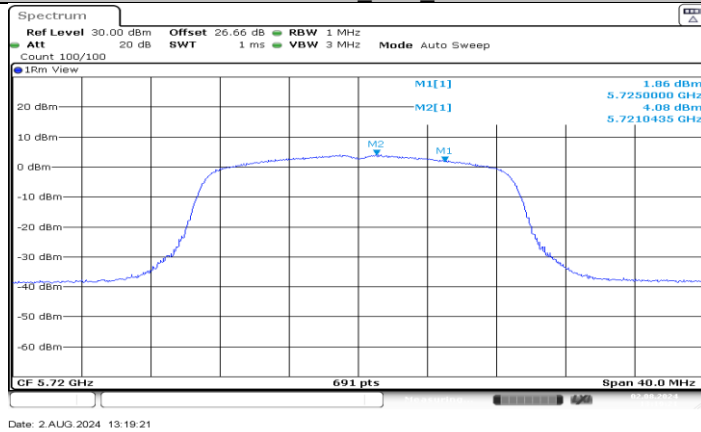
11N20SISO Ant2 5580



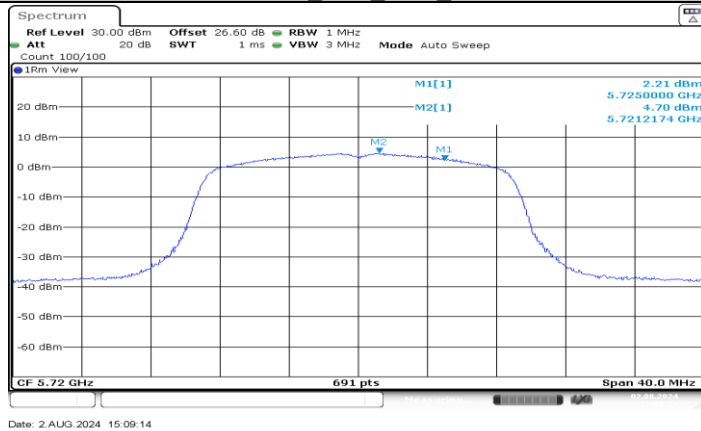
11N20SISO Ant1 5700



11N20SISO Ant2 5700



11N20SISO Ant1 5720 UNII-2C



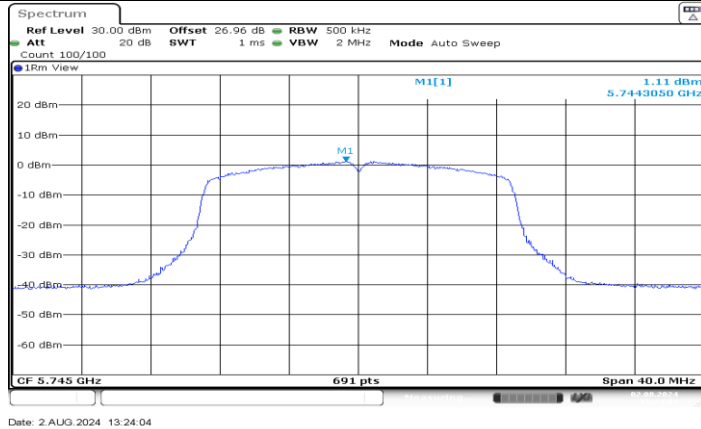
11N20SISO Ant2 5720 UNII-2C



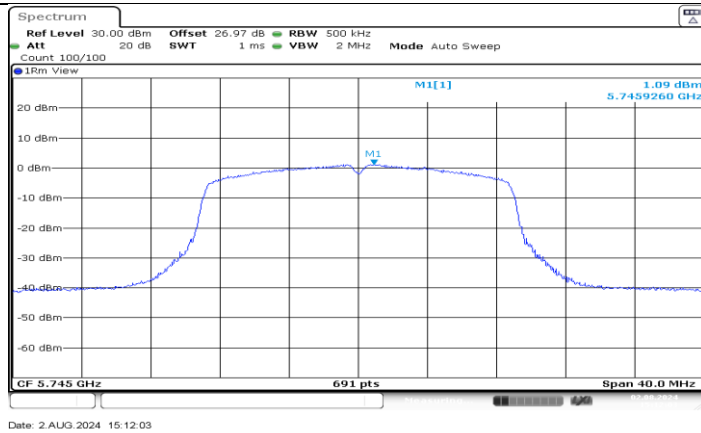
11N20SISO Ant1_5720_UNII-3



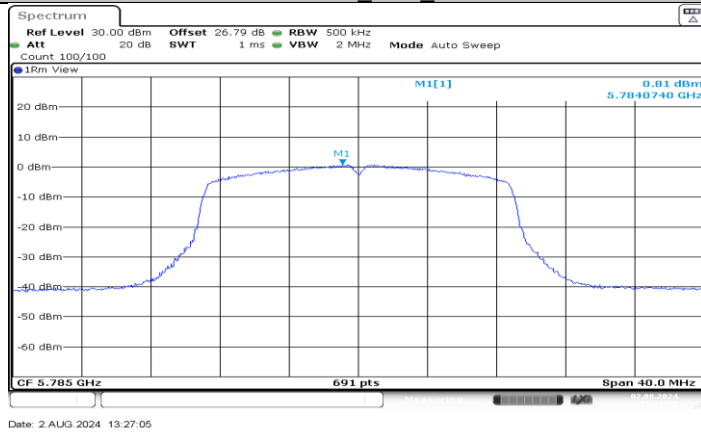
11N20SISO Ant2_5720_UNII-3



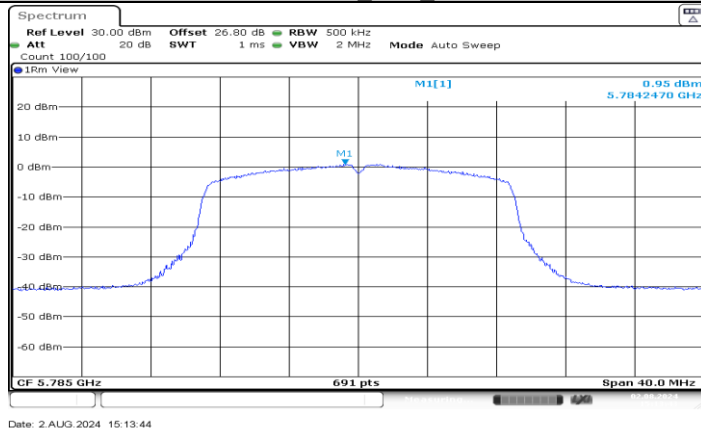
11N20SISO Ant1_5745



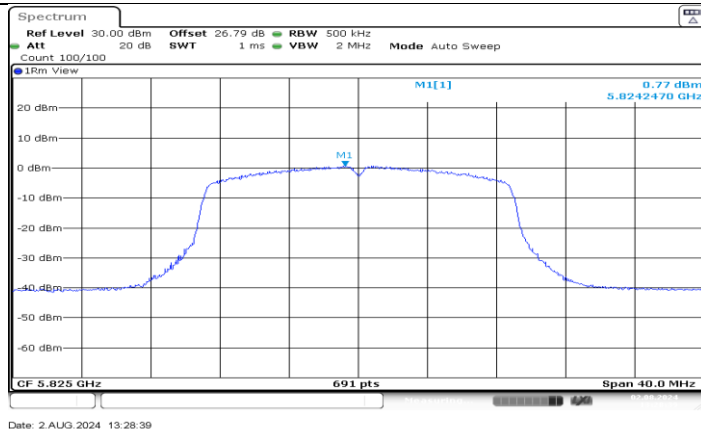
11N20SISO Ant2 5745



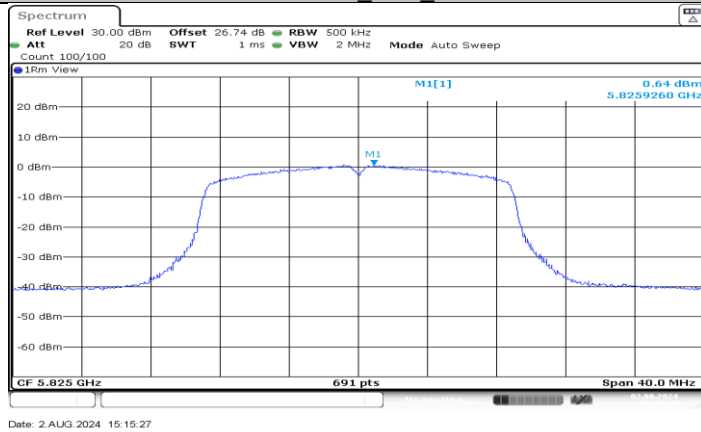
11N20SISO Ant1 5785



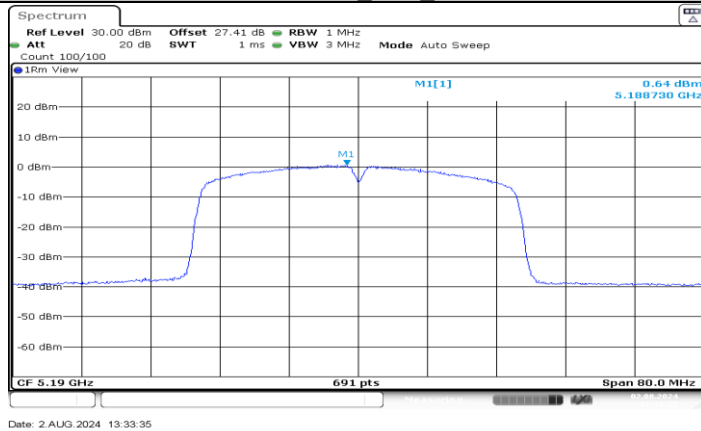
11N20SISO Ant2 5785



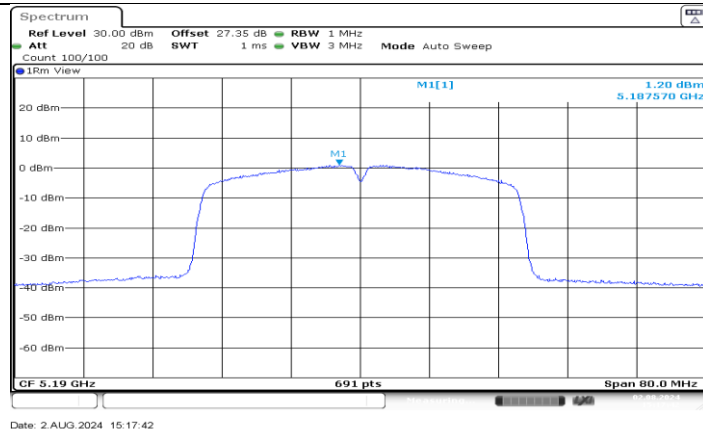
11N20SISO Ant1 5825



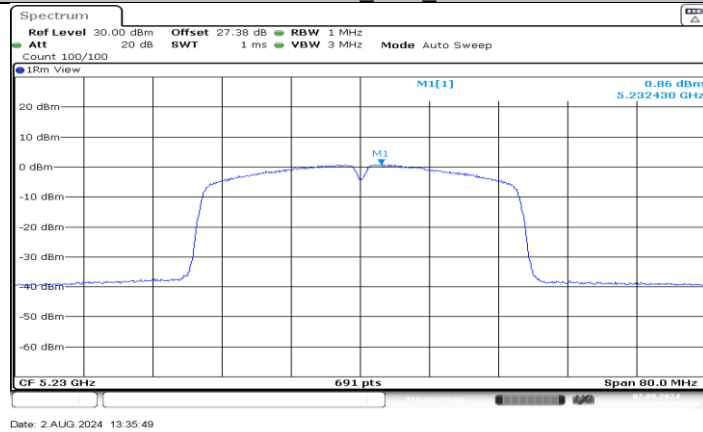
11N20SISO Ant2 5825



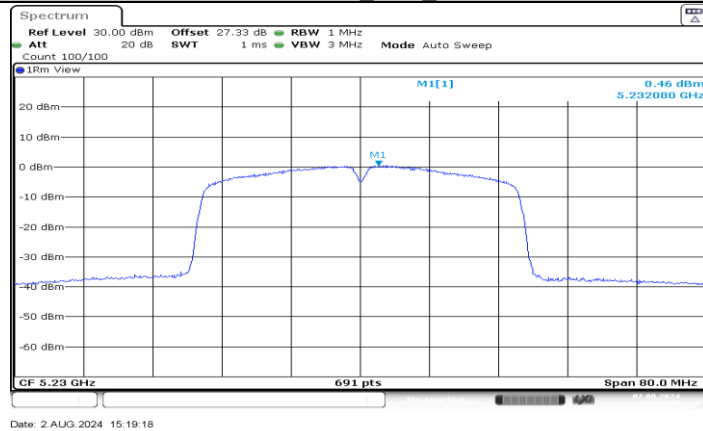
11N40SISO Ant1 5190



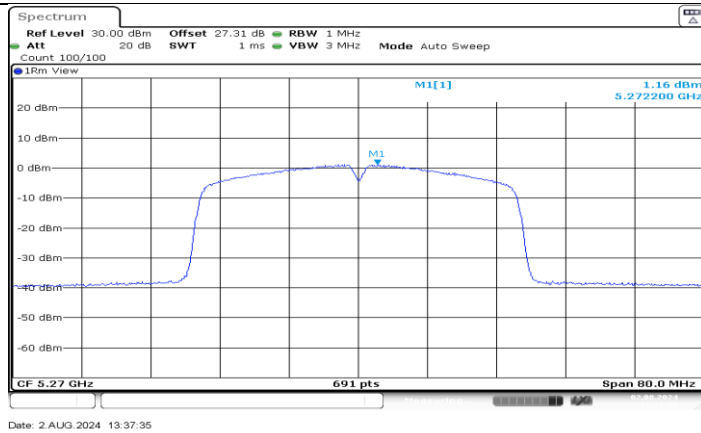
11N40SISO Ant2 5190



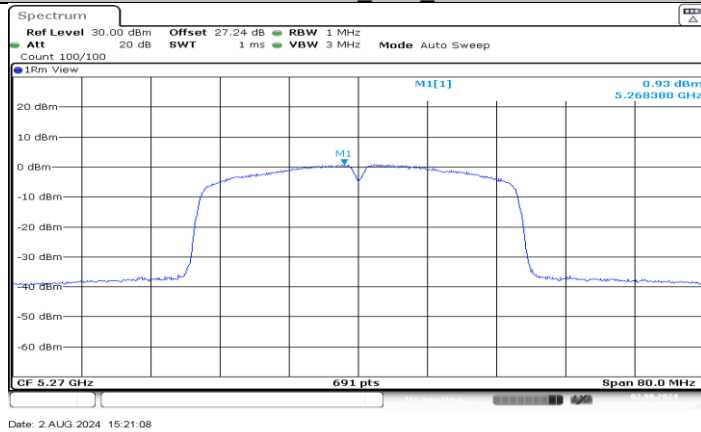
11N40SISO Ant1 5230



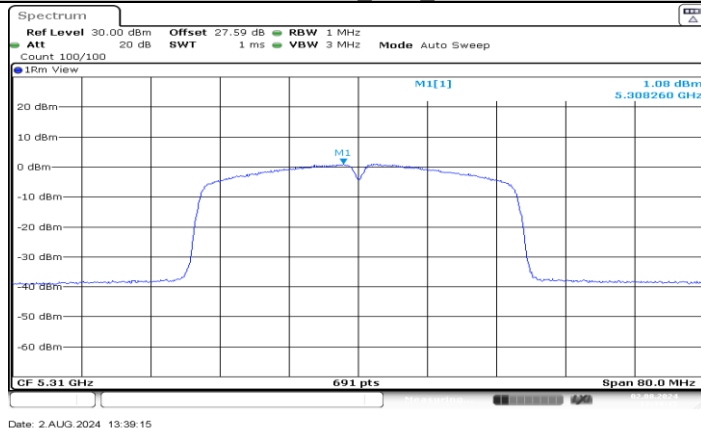
11N40SISO Ant2 5230



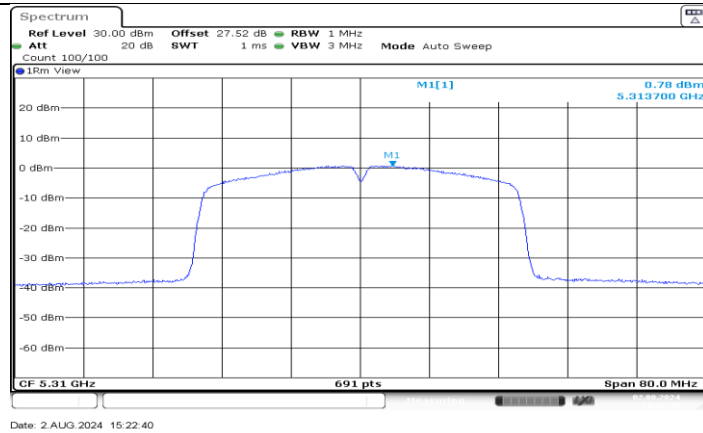
11N40SISO Ant1 5270



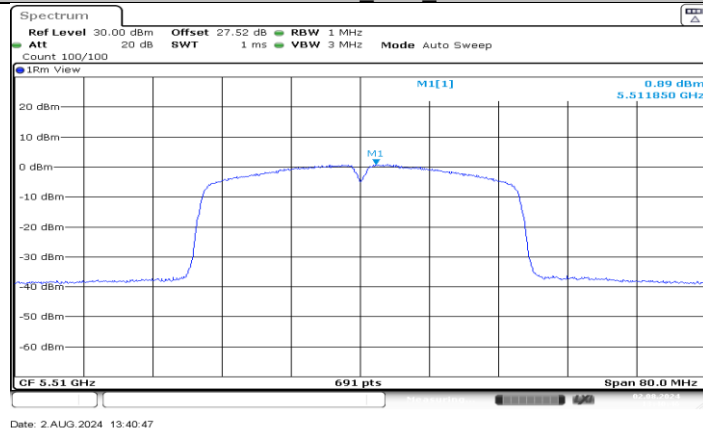
11N40SISO Ant2 5270



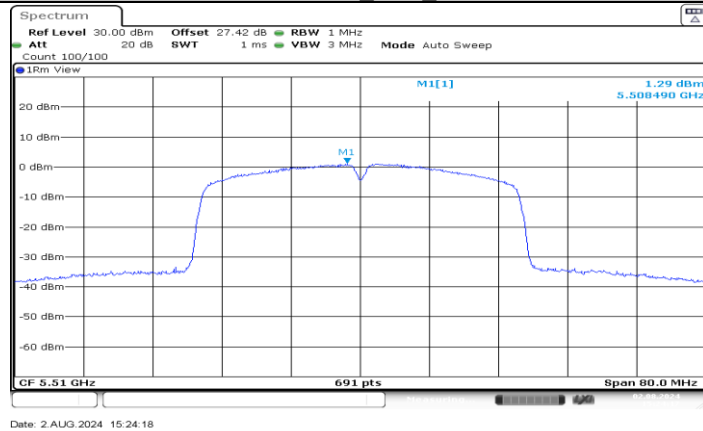
11N40SISO Ant1 5310



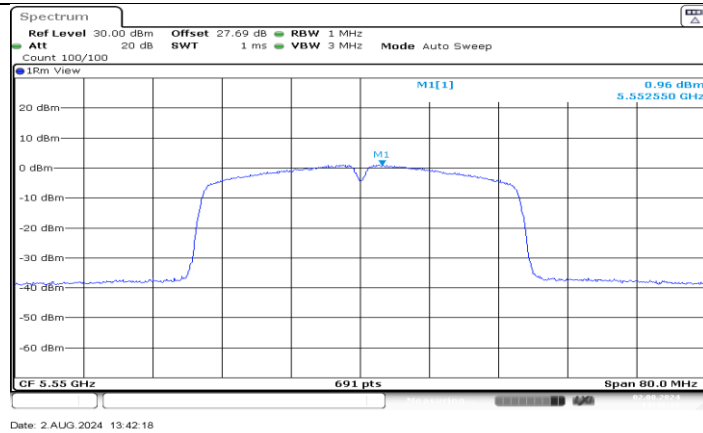
11N40SISO Ant2 5310



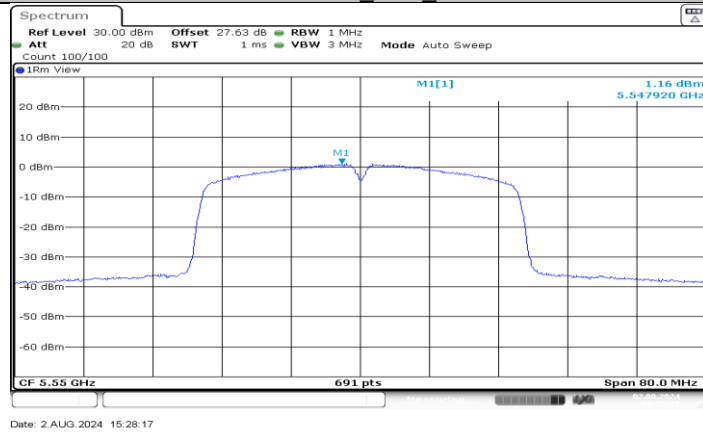
11N40SISO Ant1 5510



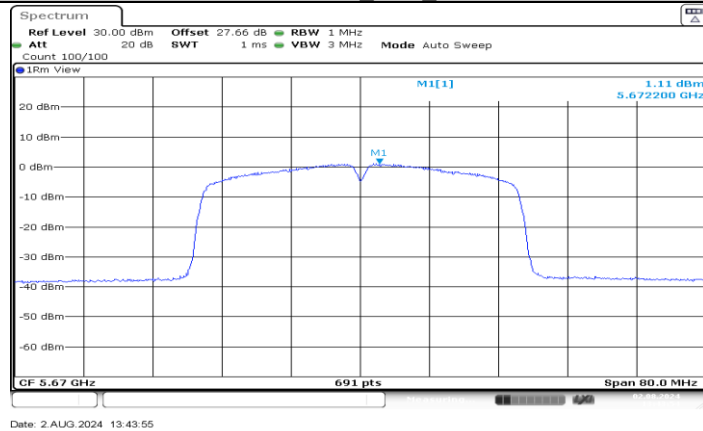
11N40SISO Ant2 5510



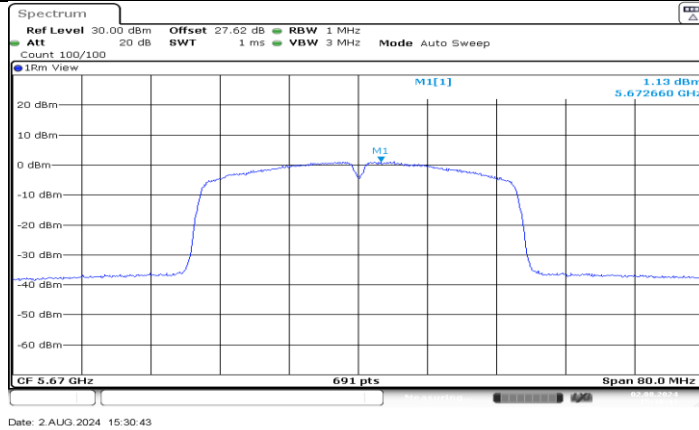
11N40SISO Ant1 5550



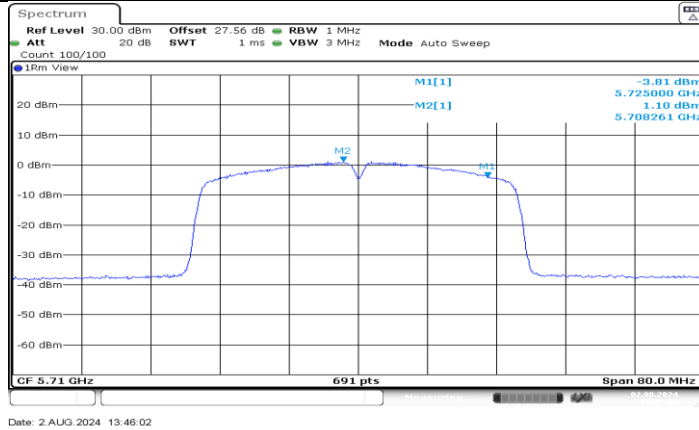
11N40SISO Ant2 5550



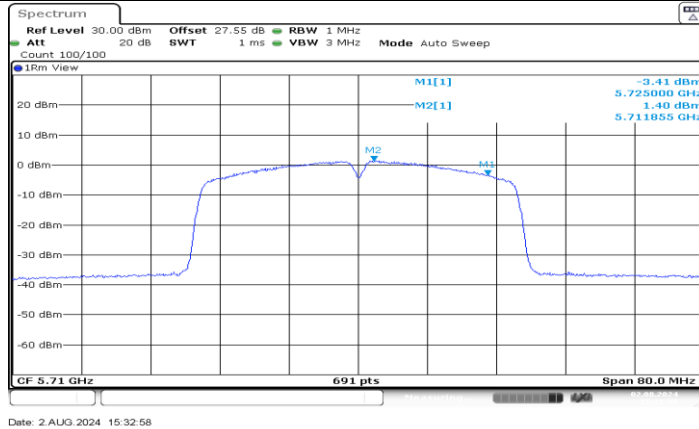
11N40SISO Ant1 5670



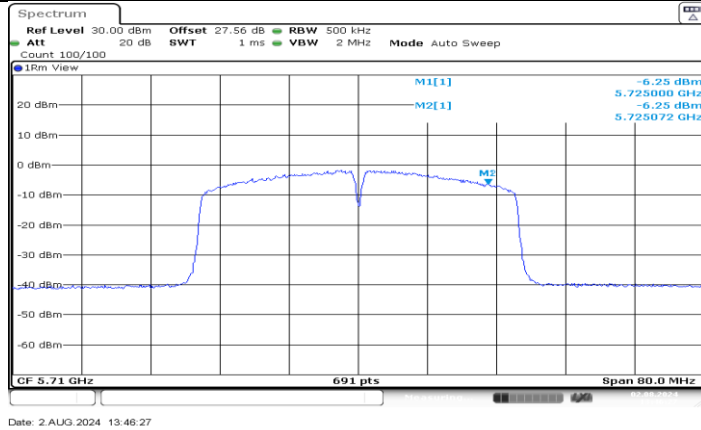
11N40SISO Ant2 5670



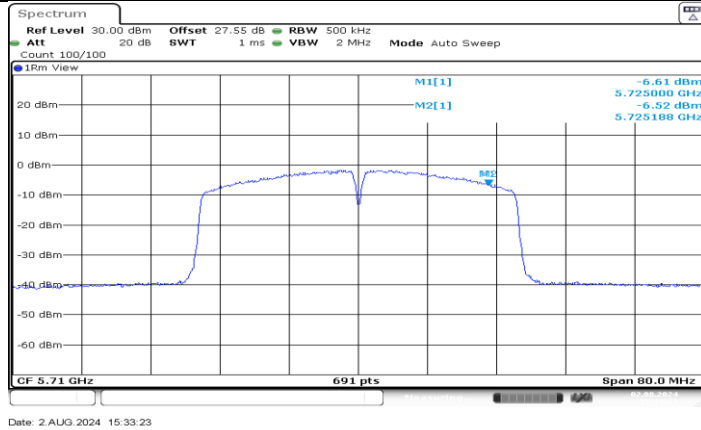
11N40SISO Ant1 5710 UNII-2C



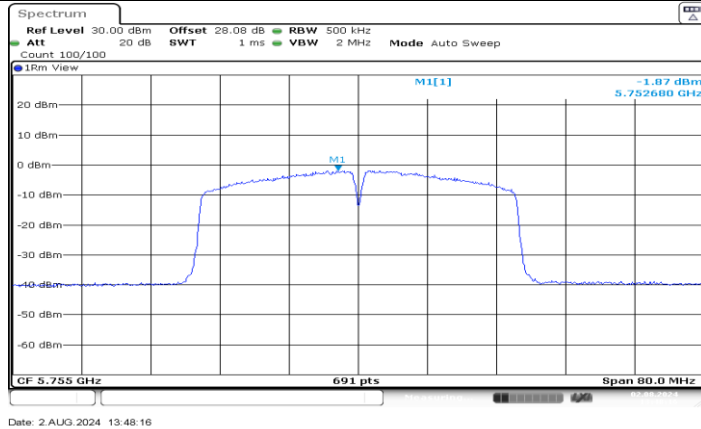
11N40SISO Ant2 5710 UNII-2C



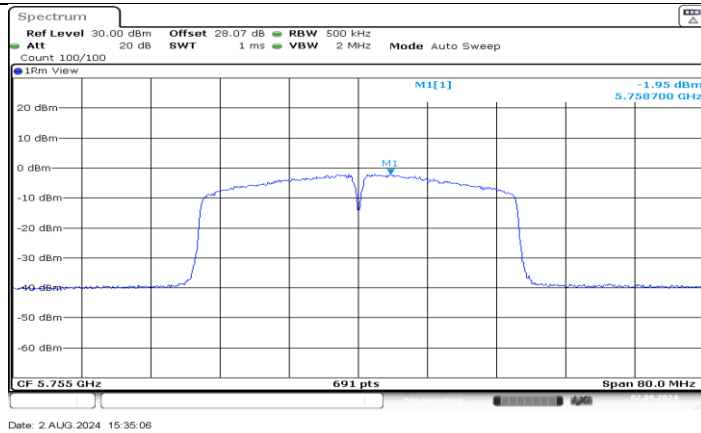
11N40SISO Ant1_5710_UNII-3



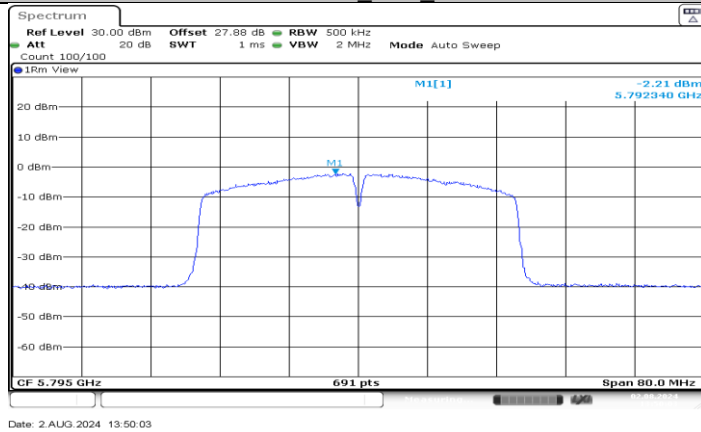
11N40SISO Ant2_5710_UNII-3



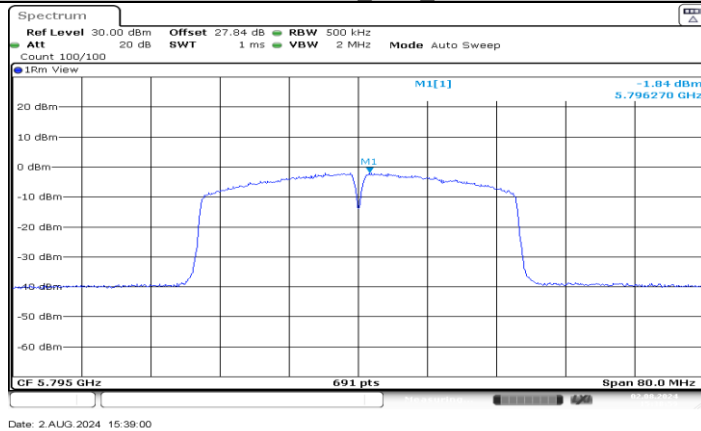
11N40SISO Ant1_5755



11N40SISO Ant2 5755



11N40SISO Ant1 5795



11N40SISO Ant2 5795

11.6. APPENDIX F: FREQUENCY STABILITY

11.6.1. Test Result

Frequency Error vs. Voltage									
802.11a:5200MHz									
Temp.	Volt.	0 Minute		2 Minute		5 Minute		10 Minute	
		Freq.Error (MHz)	Tolerance (ppm)	Freq.Error (MHz)	Tolerance (ppm)	Freq.Error (MHz)	Tolerance (ppm)	Freq.Error (MHz)	Tolerance (ppm)
TN	VL	5200.0090	1.72	5199.9785	-4.14	5199.9842	-3.04	5199.9870	-2.51
TN	VN	5199.9795	-3.95	5199.9836	-3.15	5199.9880	-2.31	5199.9817	-3.52
TN	VH	5200.0222	4.28	5200.0165	3.17	5200.0208	4.00	5199.9851	-2.86
Frequency Error vs. Temperature									
802.11a:5200MHz									
Temp.	Volt.	0 Minute		2 Minute		5 Minute		10 Minute	
		Freq.Error (MHz)	Tolerance (ppm)	Freq.Error (MHz)	Tolerance (ppm)	Freq.Error (MHz)	Tolerance (ppm)	Freq.Error (MHz)	Tolerance (ppm)
70	VN	5199.9959	-0.79	5199.9886	-2.19	5200.0062	1.20	5200.0185	3.55
60	VN	5199.9815	-3.55	5199.9839	-3.10	5200.0206	3.96	5199.9827	-3.32
50	VN	5200.0207	3.98	5200.0080	1.54	5200.0061	1.17	5199.9776	-4.31
40	VN	5199.9862	-2.65	5199.9904	-1.84	5200.0116	2.23	5200.0213	4.09
30	VN	5199.9898	-1.96	5199.9880	-2.31	5200.0147	2.83	5200.0125	2.40
20	VN	5199.9945	-1.05	5199.9979	-0.41	5200.0202	3.89	5200.0092	1.77
10	VN	5200.0041	0.80	5199.9884	-2.23	5200.0168	3.24	5199.9800	-3.85
0	VN	5200.0161	3.09	5200.0185	3.55	5199.9933	-1.29	5200.0092	1.77
-10	VN	5200.0182	3.50	5200.0222	4.27	5199.9933	-1.29	5200.0183	3.52

Note:

1. All antennas, test modes and test channels have been tested, only the worst data record in the report.
2. For the detail Test Conditions, please refer to section 7.5 TEST ENVIRONMENT.

11.7. APPENDIX G: DUTY CYCLE**11.7.1. Test Result**

Test Mode	On Time (msec)	Period (msec)	Duty Cycle x (Linear)	Duty Cycle (%)	Duty Cycle Correction Factor (dB)	1/T Minimum VBW (kHz)	Final setting For VBW (kHz)
11A	1.34	1.83	0.7322	73.22	1.35	0.75	1
11N20SISO	1.26	1.76	0.7159	71.59	1.45	0.79	1
11N40SISO	0.63	1.12	0.5625	56.25	2.50	1.59	2

Note:

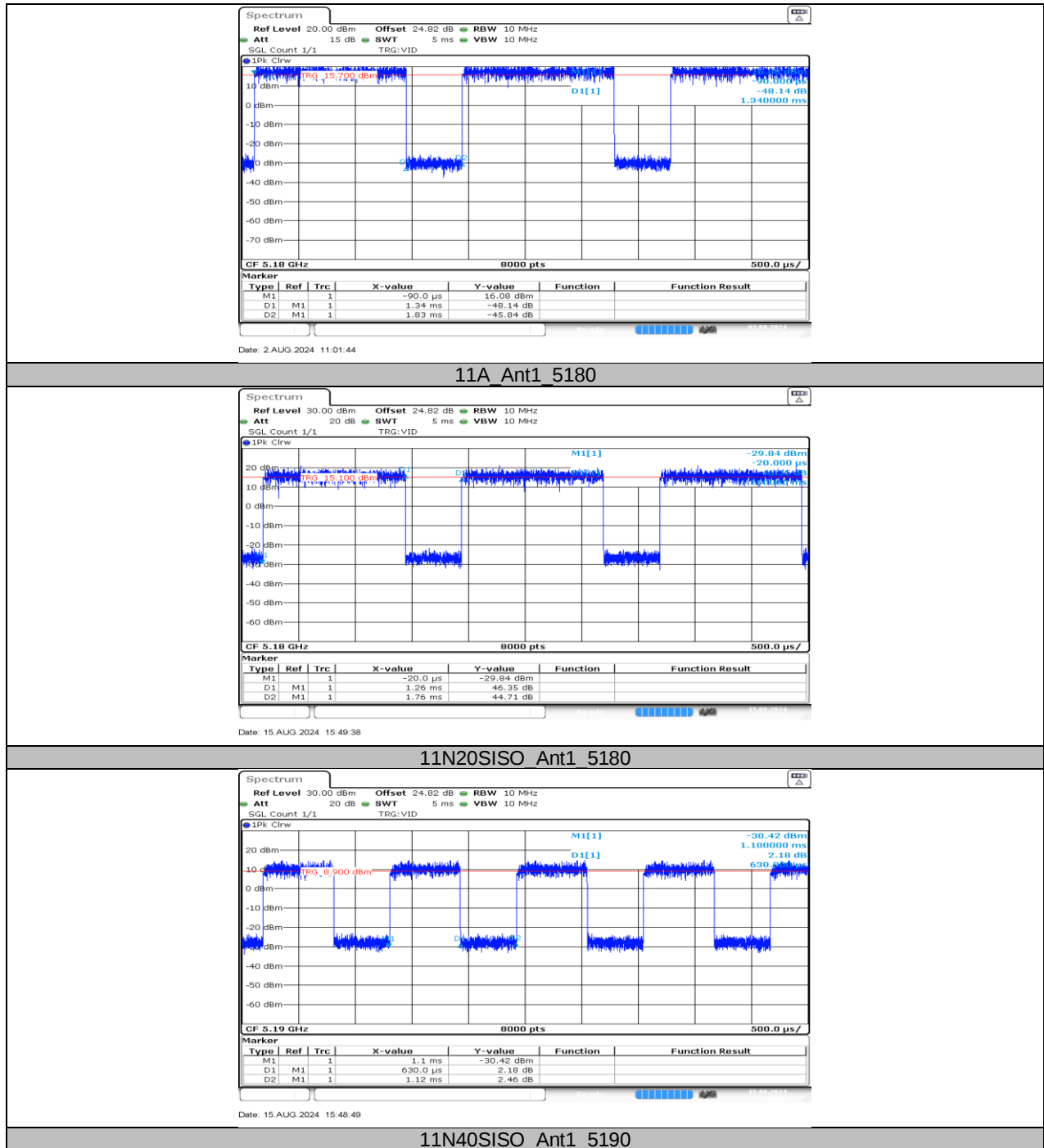
Duty Cycle Correction Factor= $10\log(1/x)$.

Where: x is Duty Cycle (Linear)

Where: T is On Time

If that calculated VBW is not available on the analyzer then the next higher value should be used.

11.7.2. Test Graphs

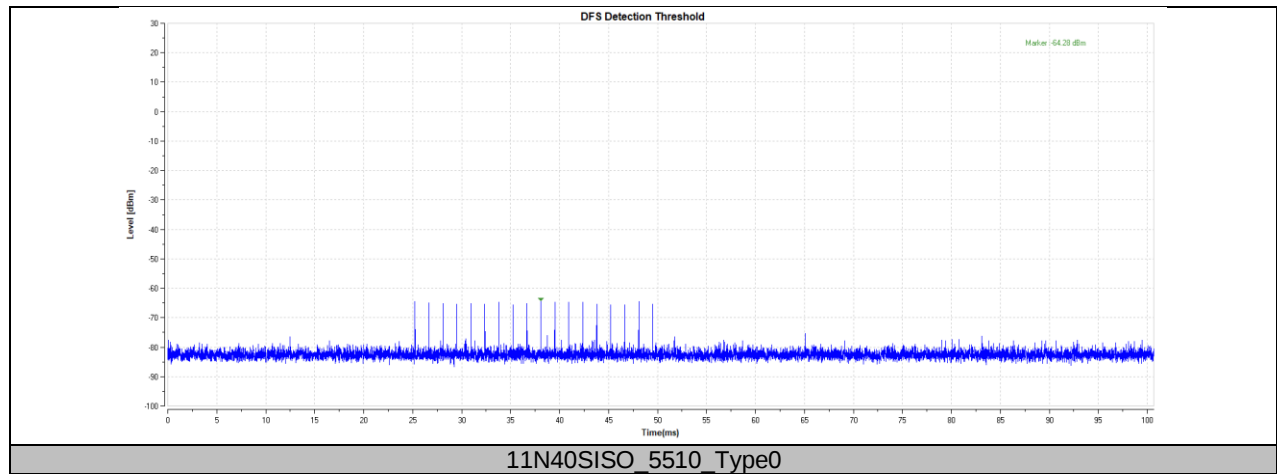


11.8. APPENDIX H: DFS DETECTION THRESHOLDS

11.8.1. Test Result

Test Mode	Frequency[MHz]	Radar Type	Result	Limit[dbm]	Verdict
11N40SISO	5510	Type0	-64.28	-59.00	PASS

11.8.2. Test Graphs

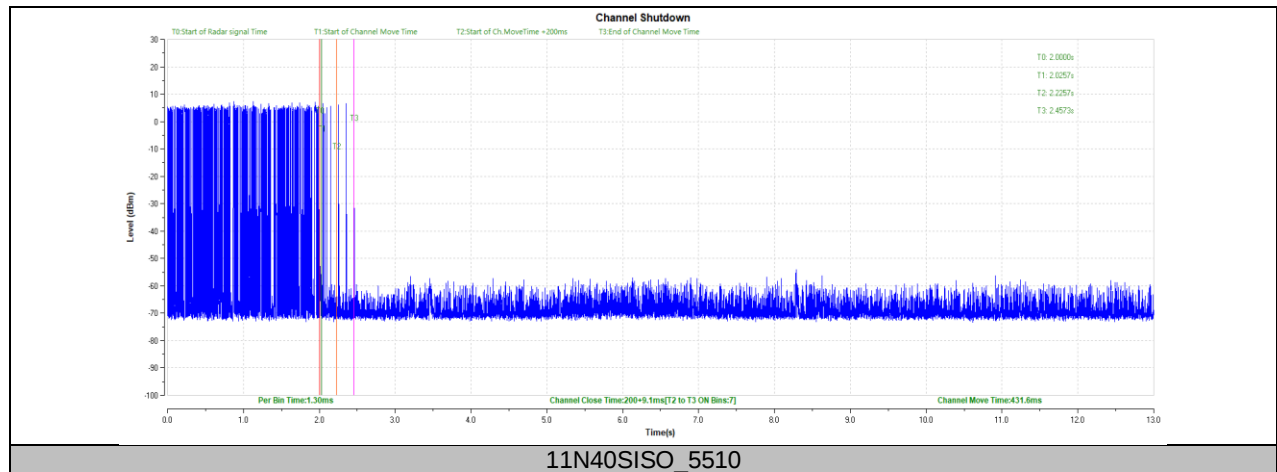


11.9. APPENDIX I: CHANNEL MOVE TIME AND CHANNEL CLOSING TRANSMISSION TIME

11.9.1. Test Result

Test Mode	Frequency[MHz]	CCT[ms]	Limit[ms]	CMT[ms]	Limit[ms]	Verdict
11N40SISO	5510	200+9.1	200+60	431.6	10000	PASS

11.9.2. Test Graphs

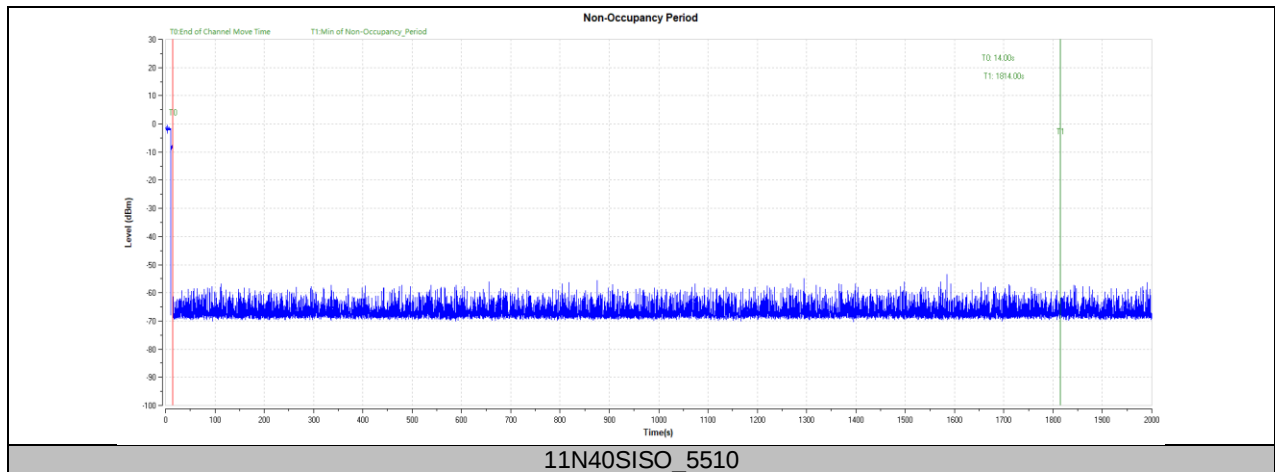


11.10. APPENDIX J: NON-OCCUPANCY PERIOD

Test Result

Test Mode	Channel	Result	Limit[s]	Verdict
11N40SISO	5510	see test graph	≥ 1800	PASS

11.10.1. Test Graphs



END OF REPORT