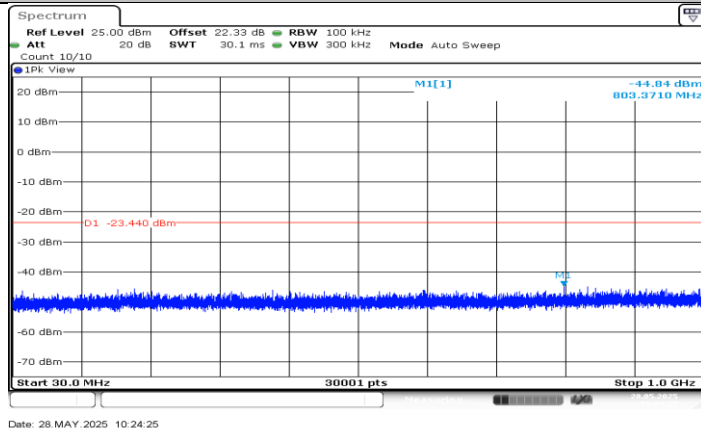
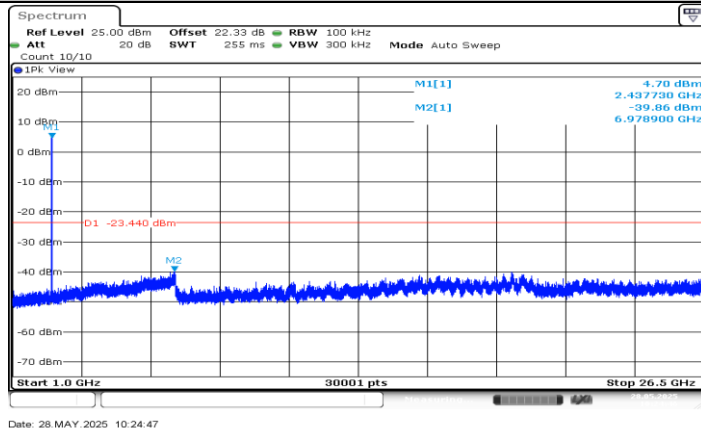


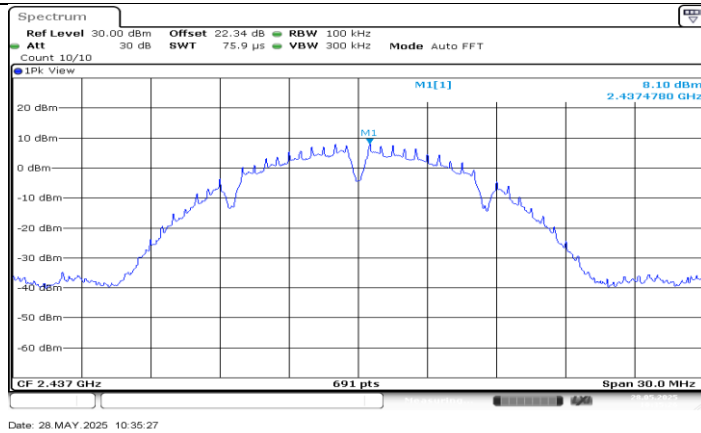
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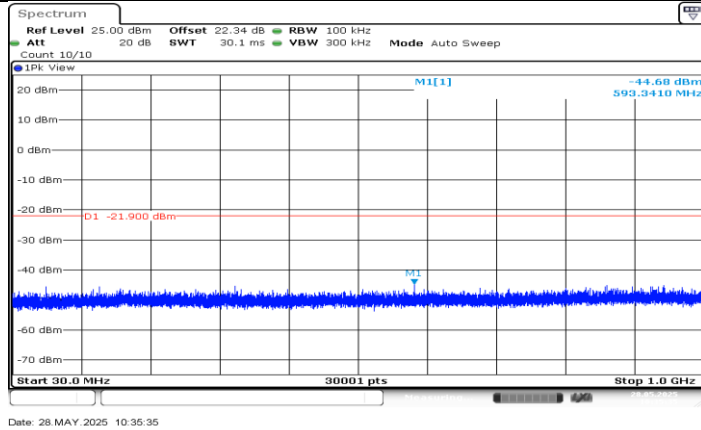
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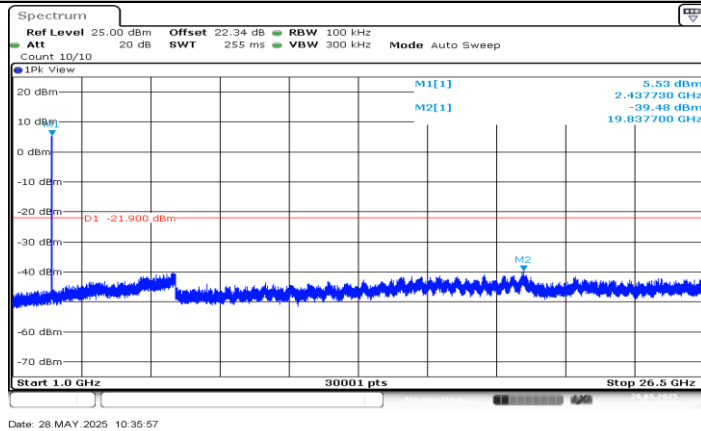
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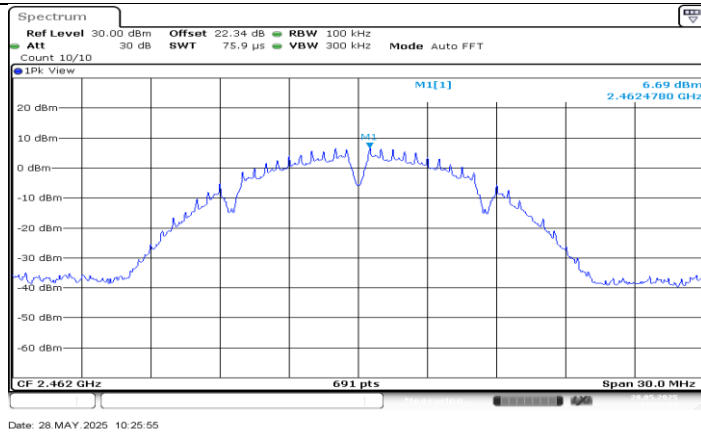
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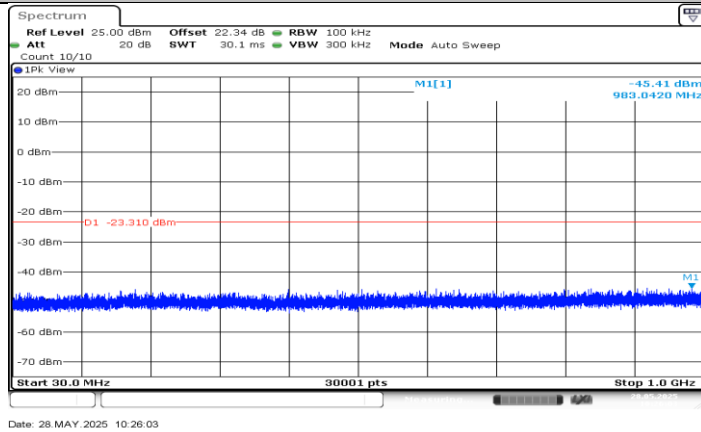
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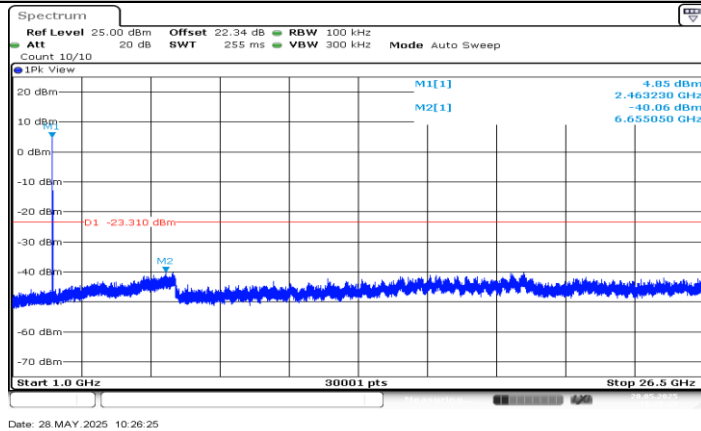
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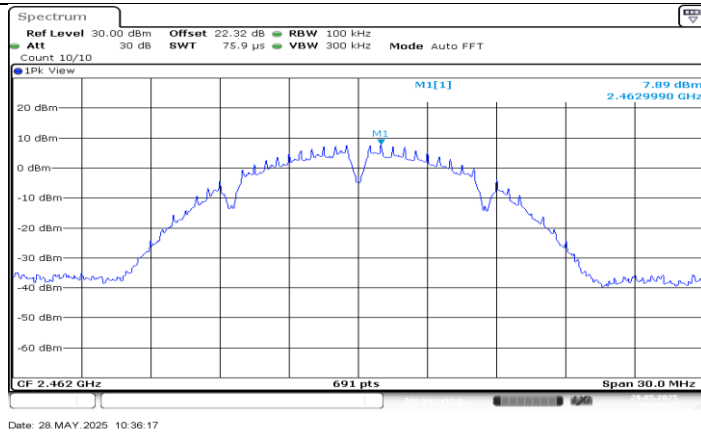
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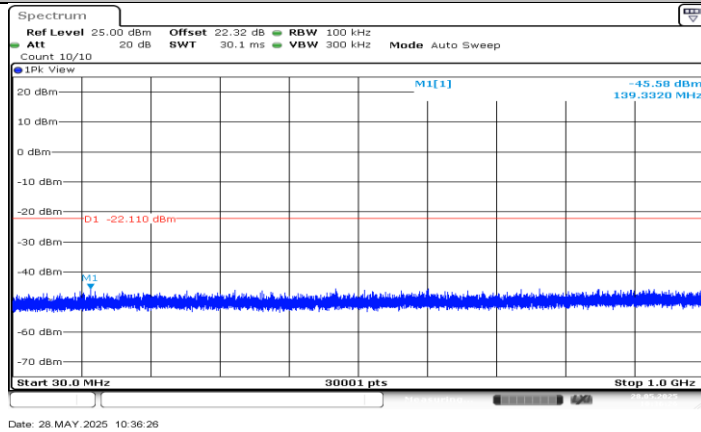
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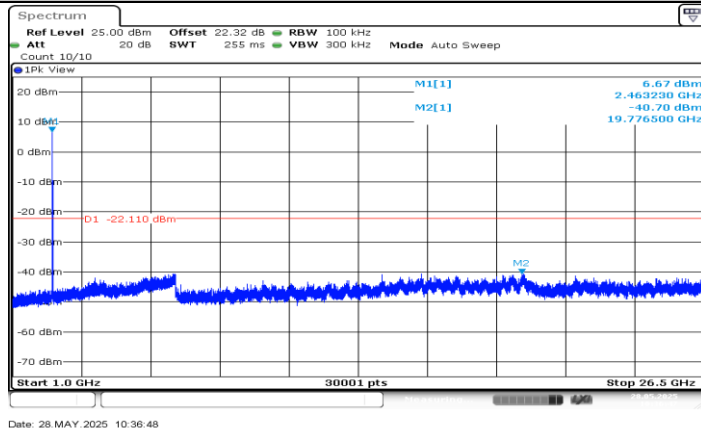
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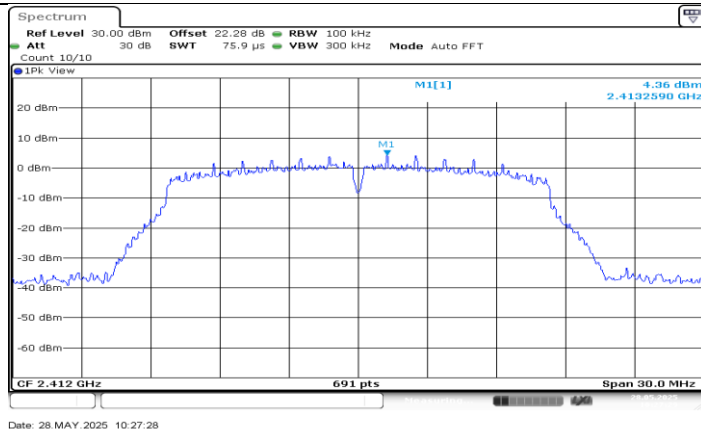
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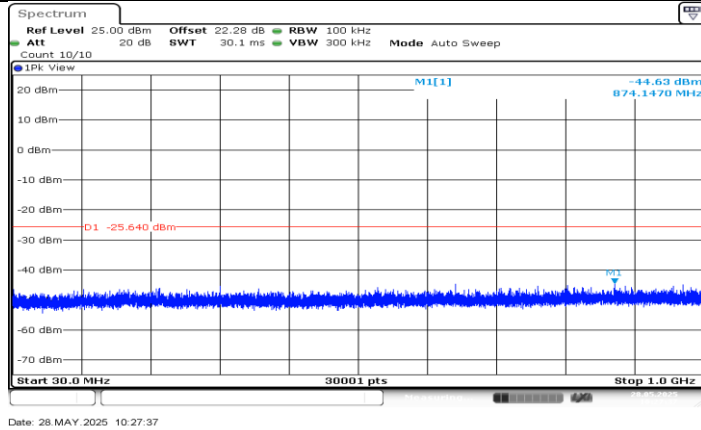
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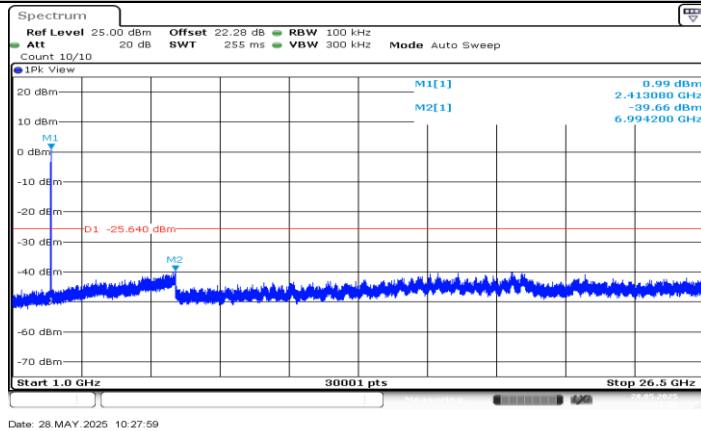
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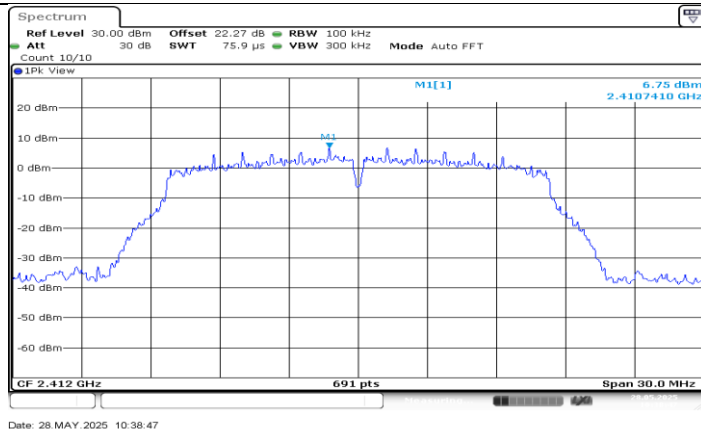
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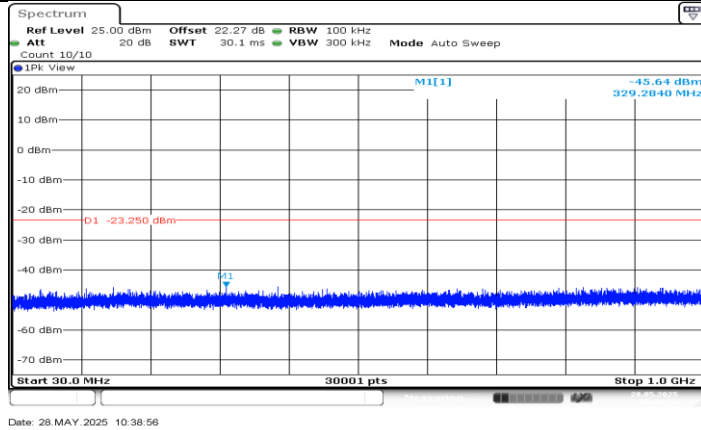
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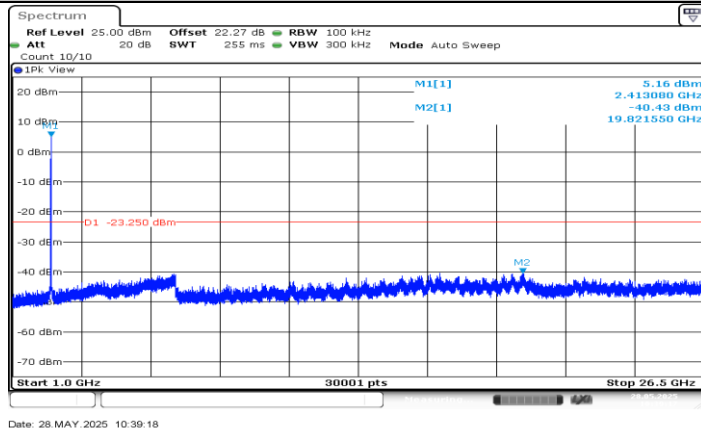
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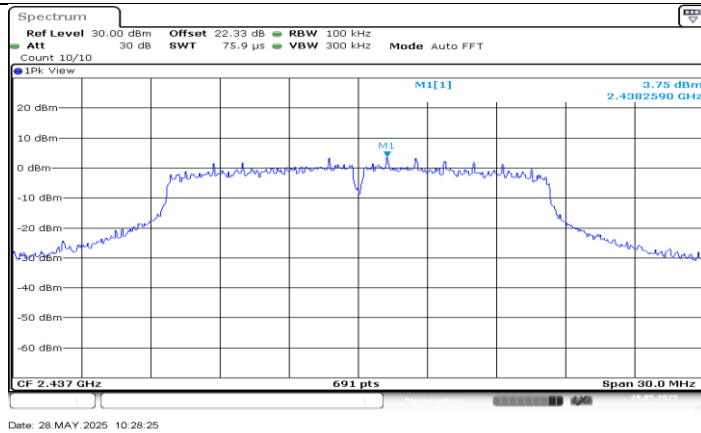
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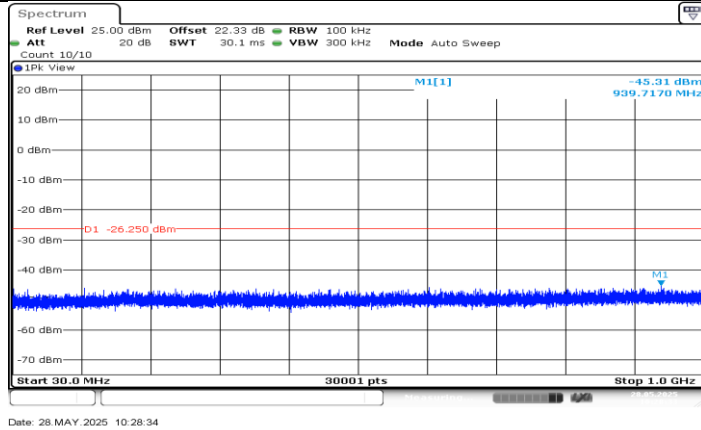
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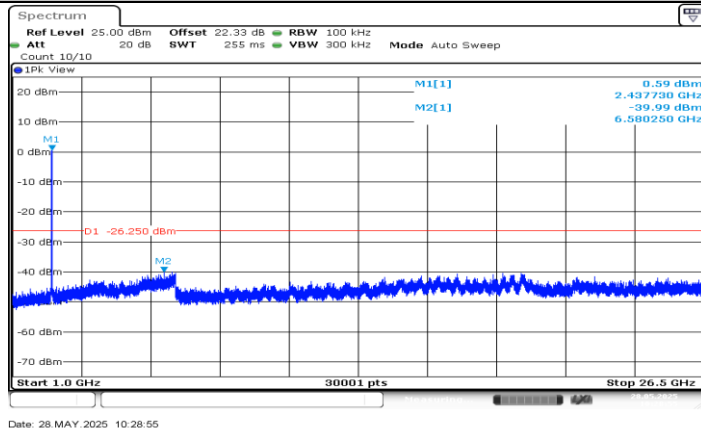
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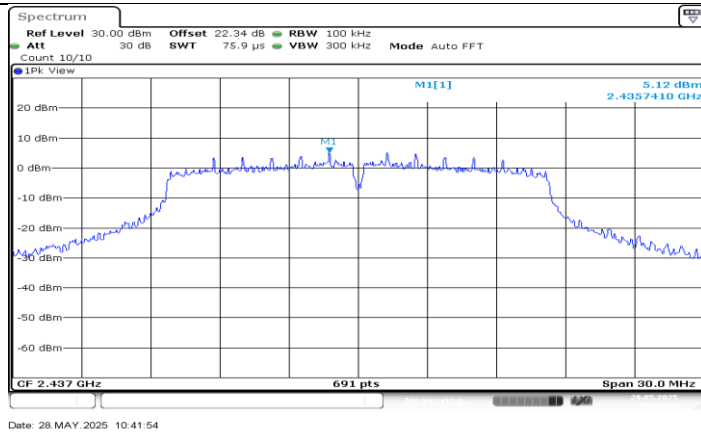
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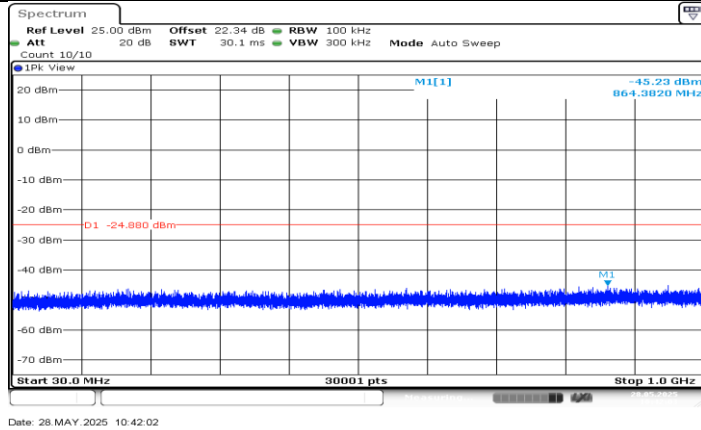
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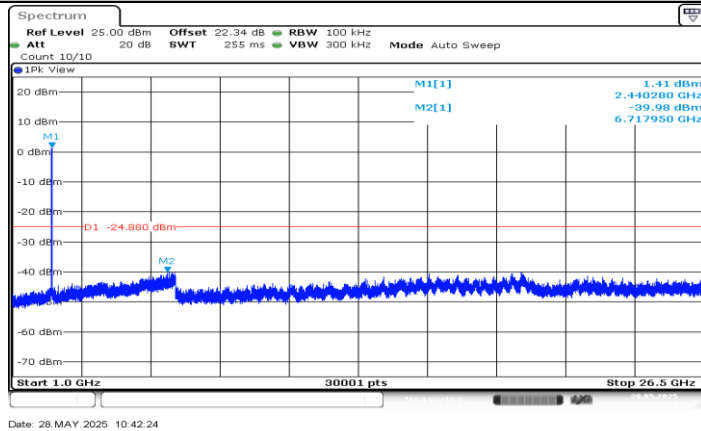
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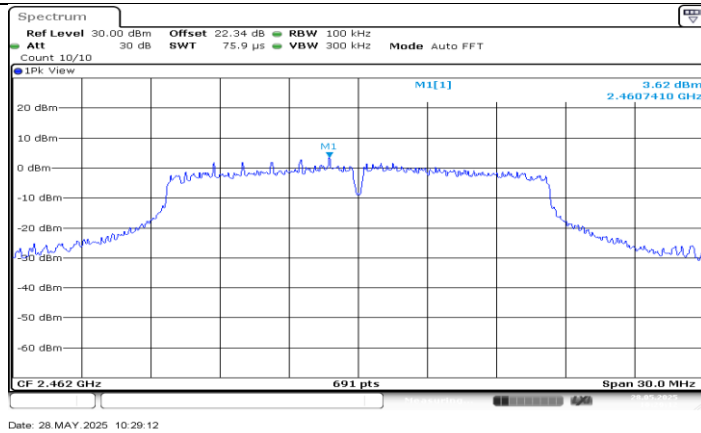
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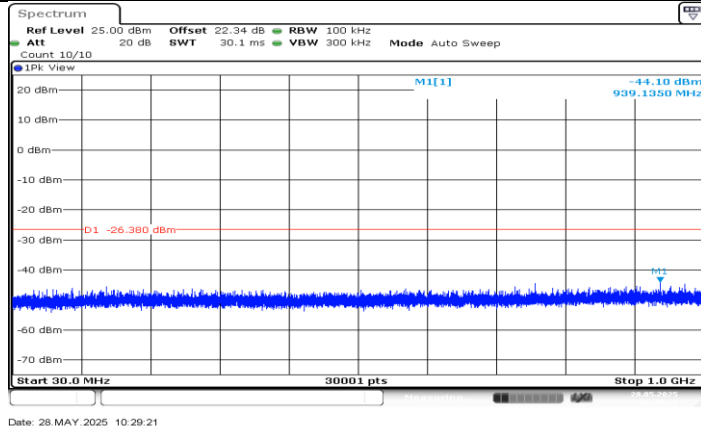
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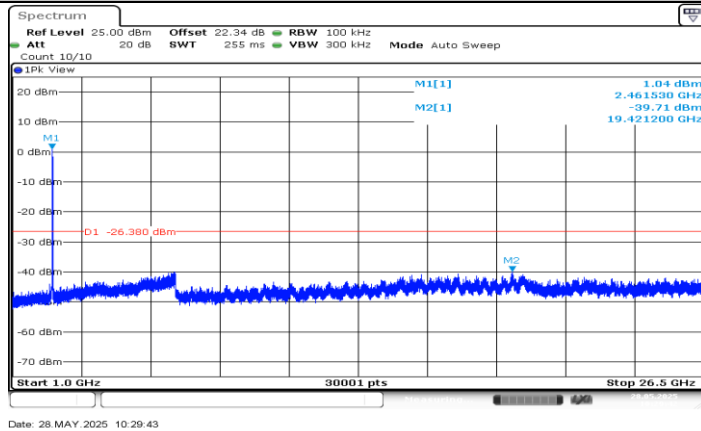
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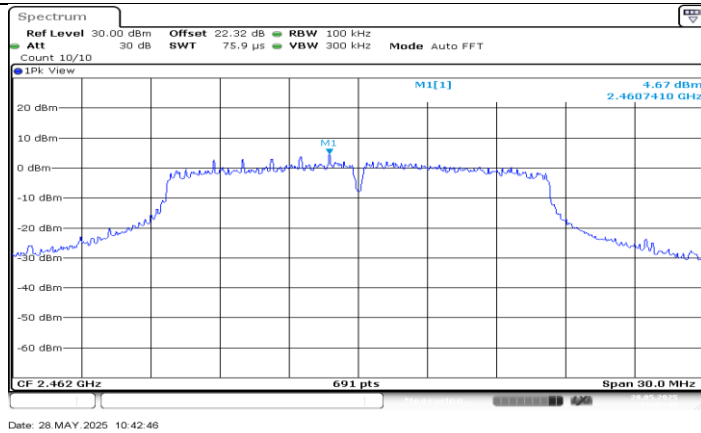
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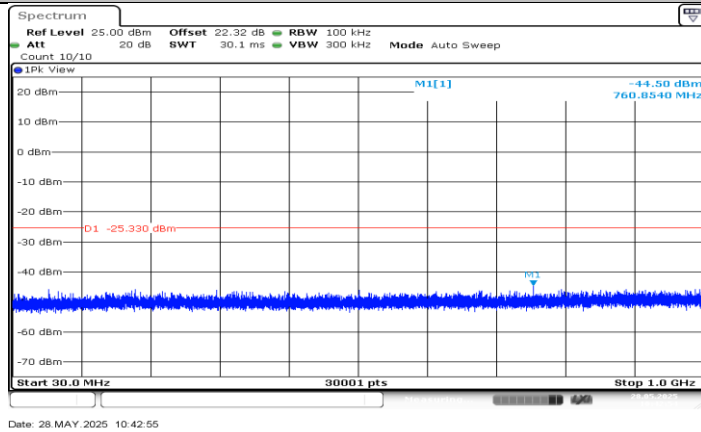
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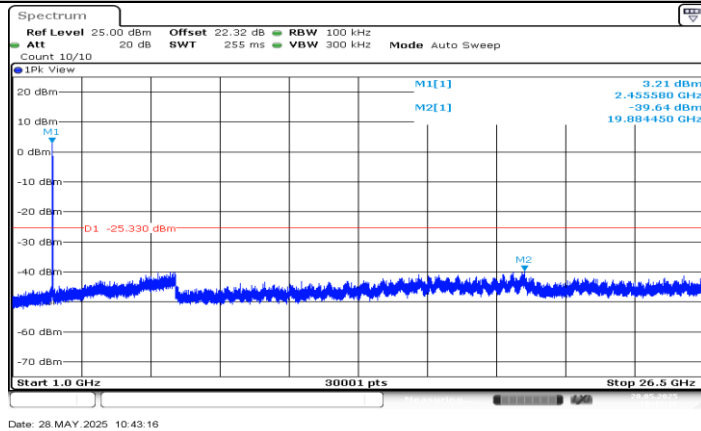
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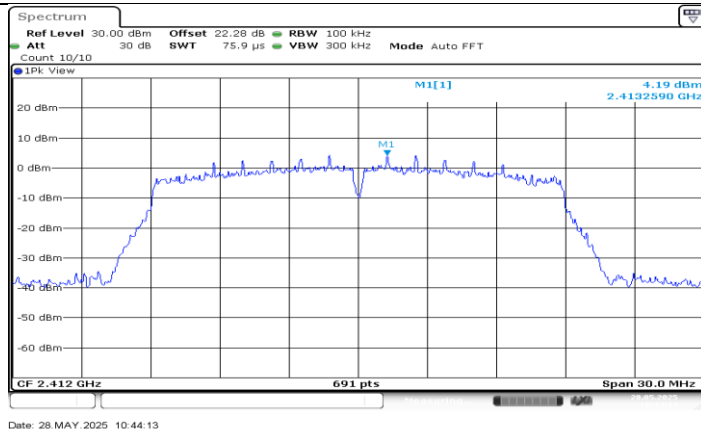
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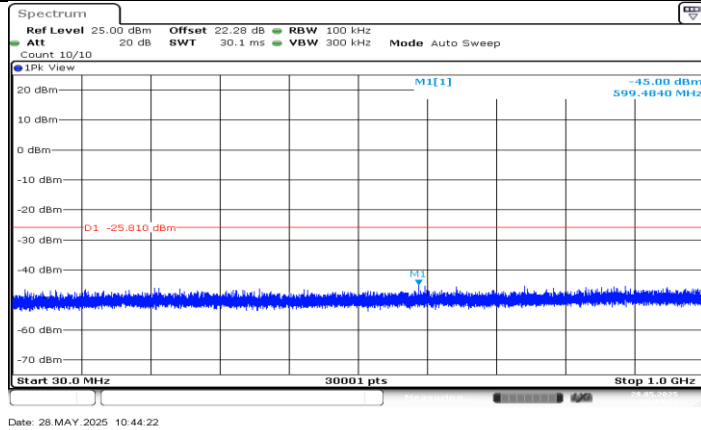
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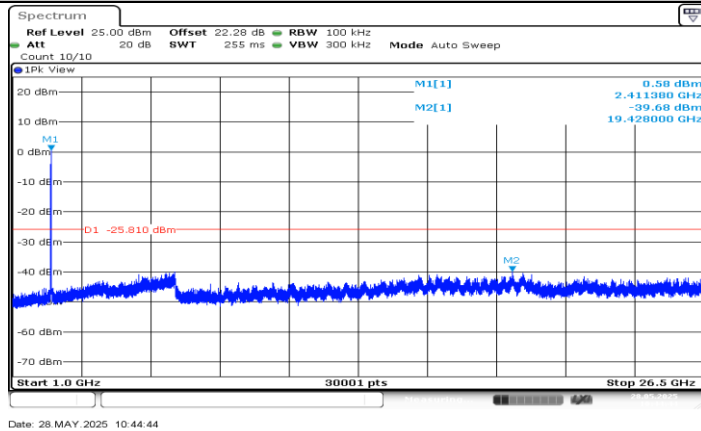
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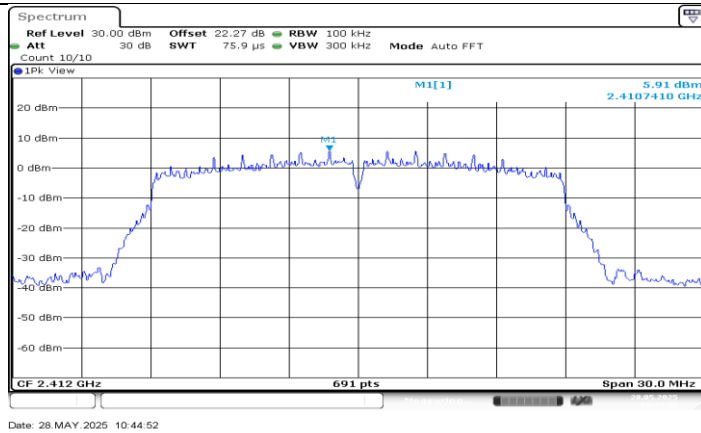
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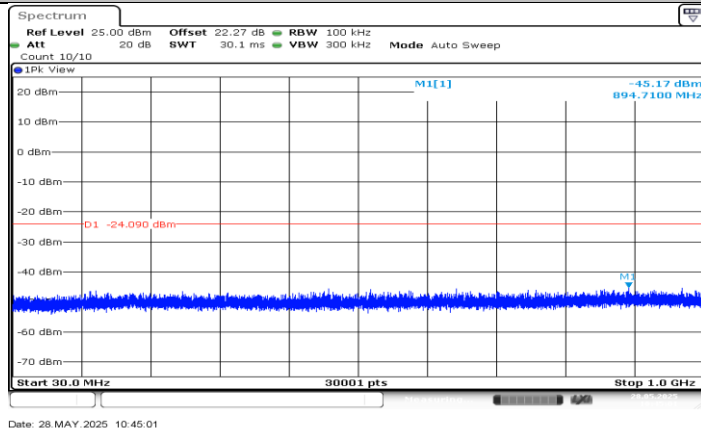
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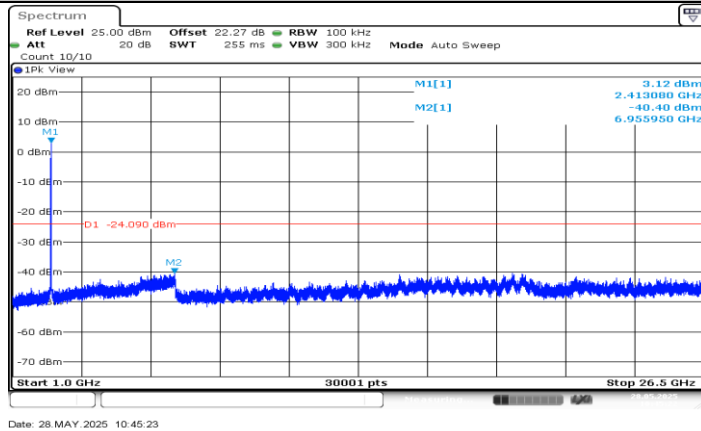
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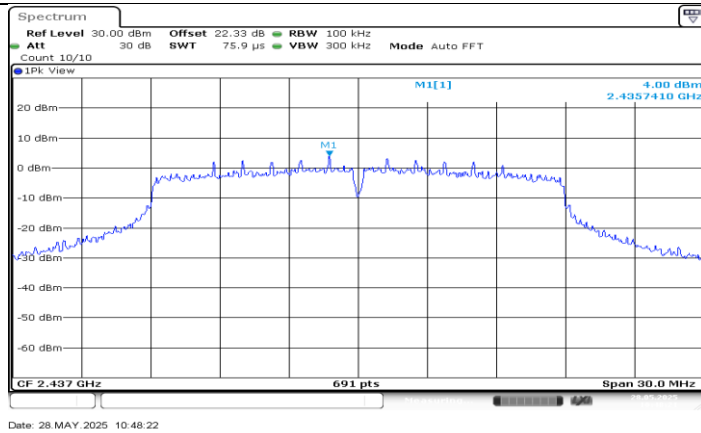
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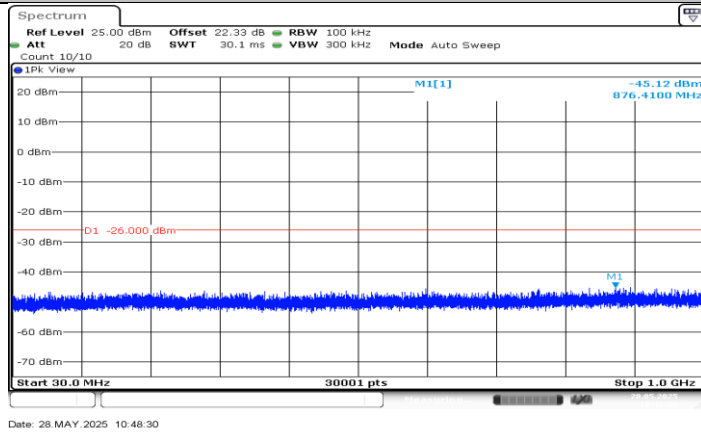
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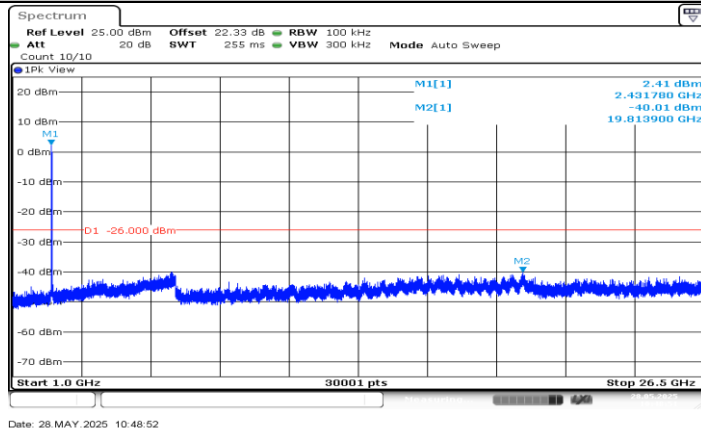
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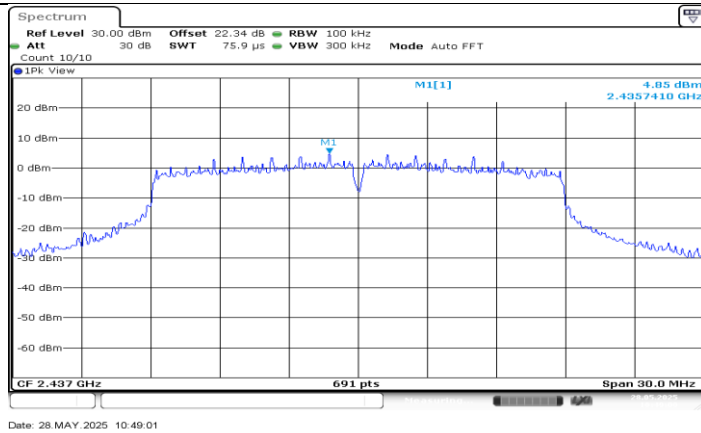
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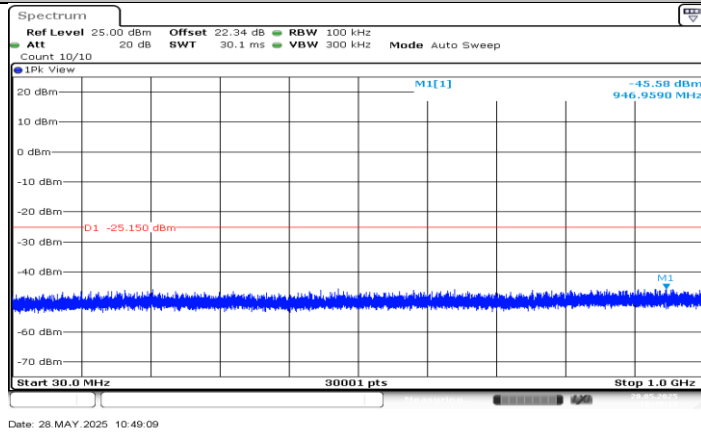
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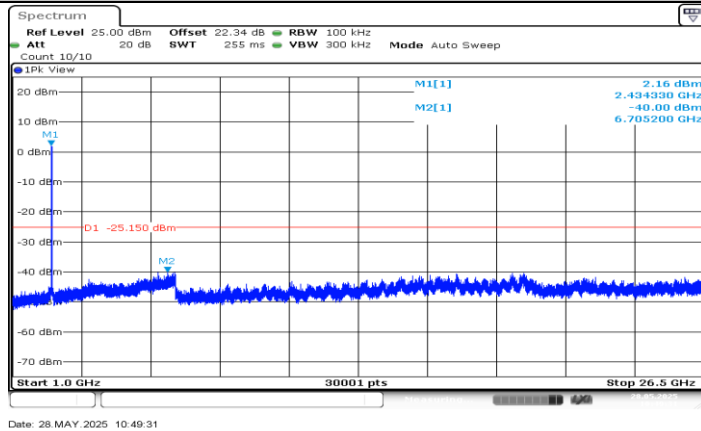
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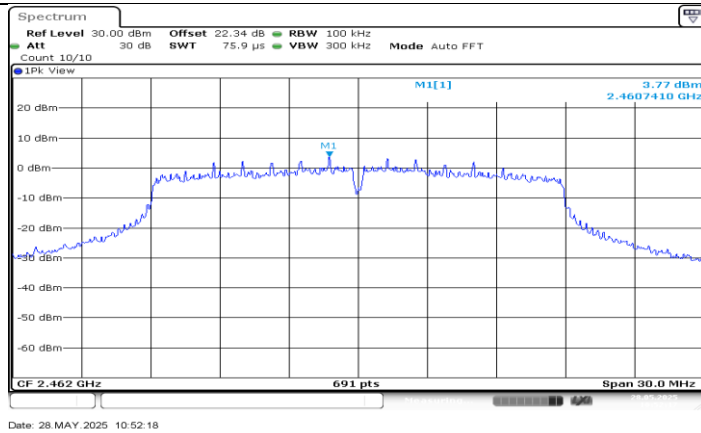
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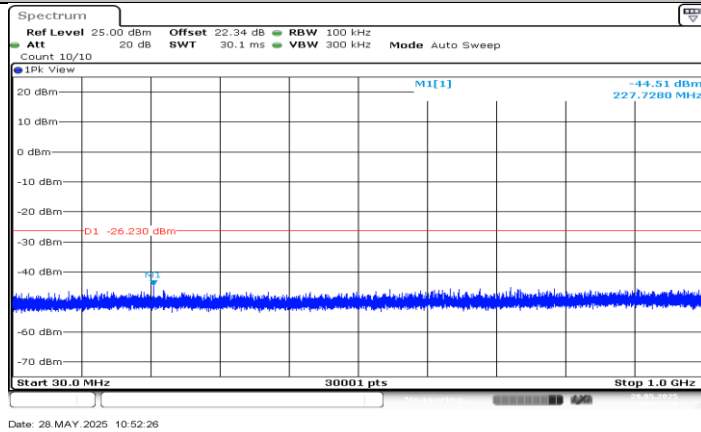
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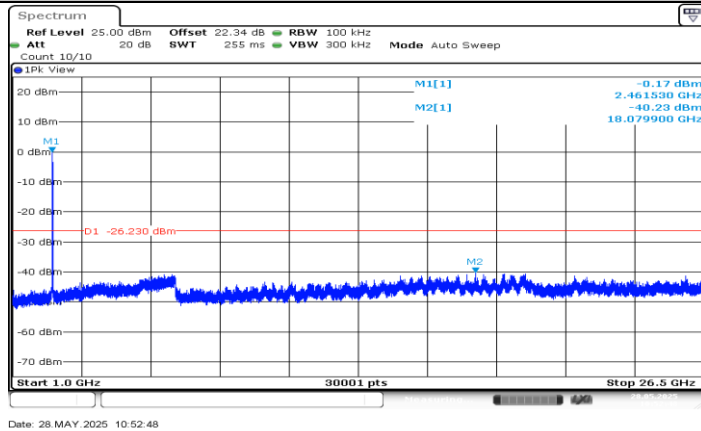
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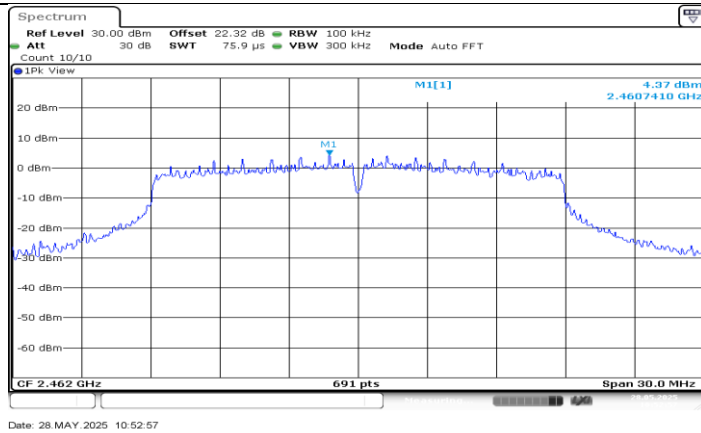
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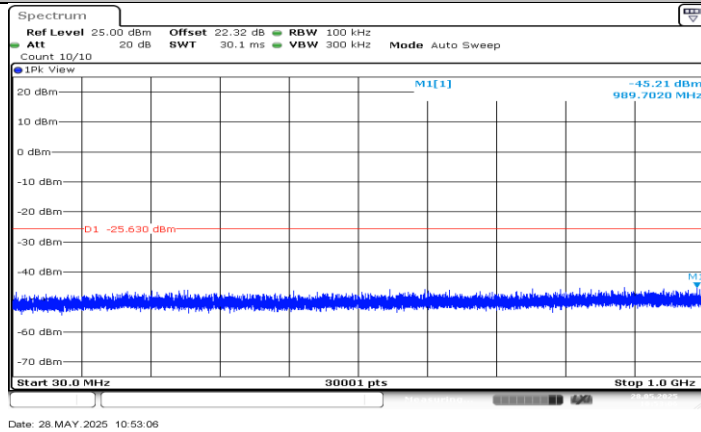
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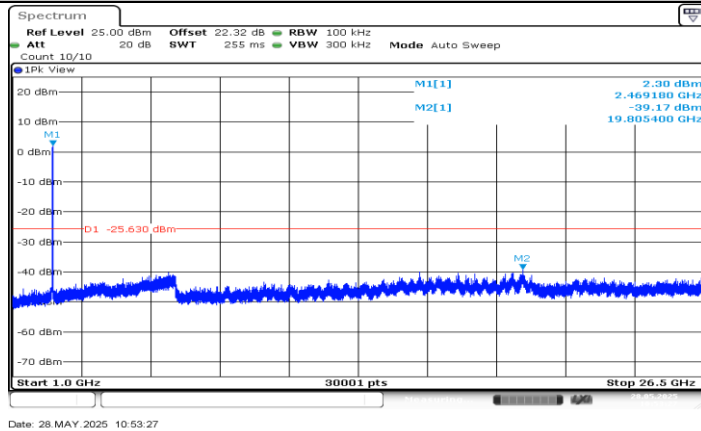
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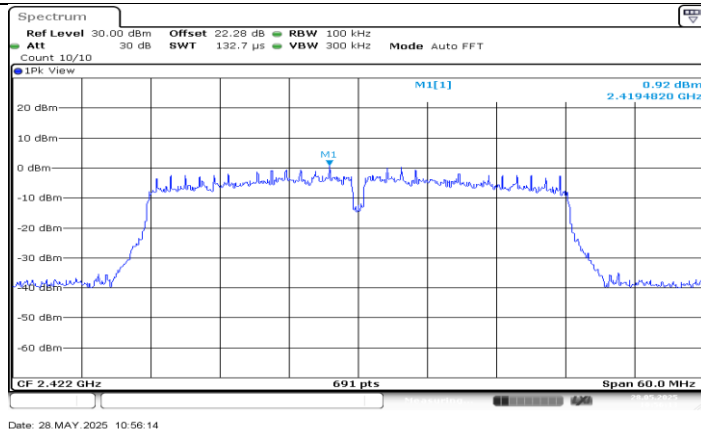
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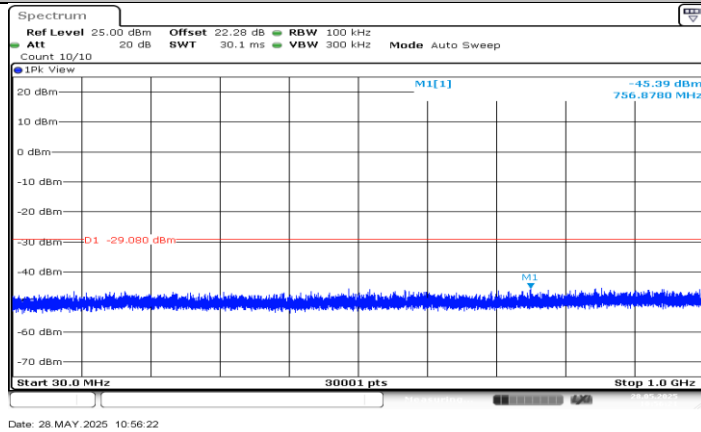
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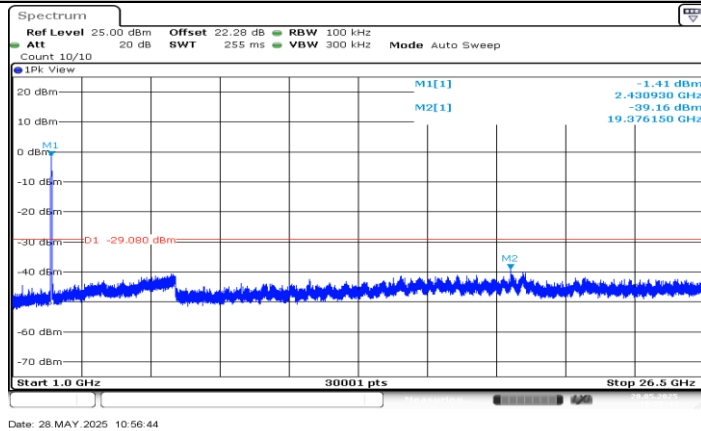
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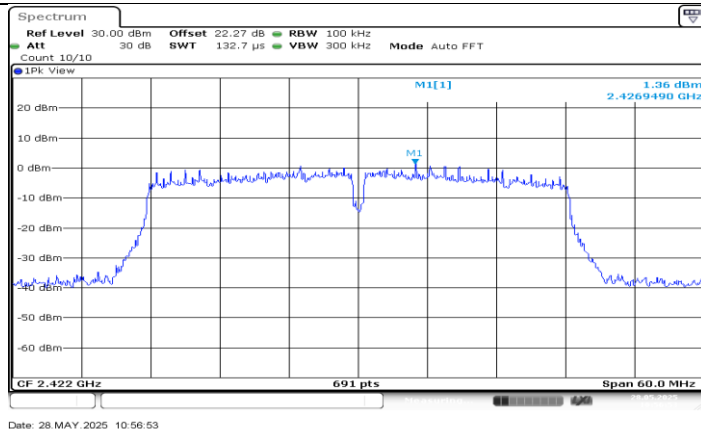
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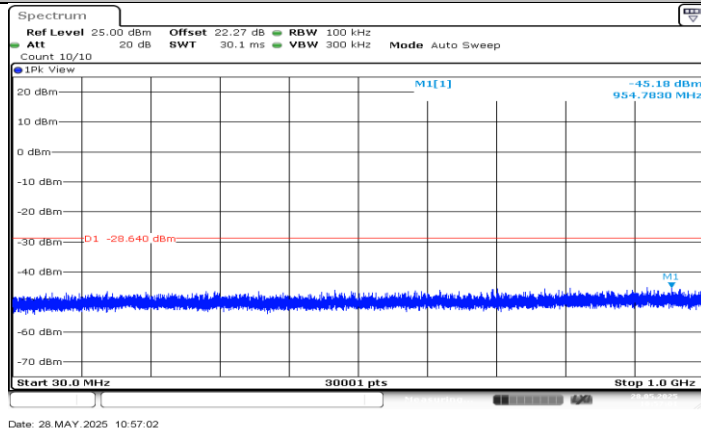
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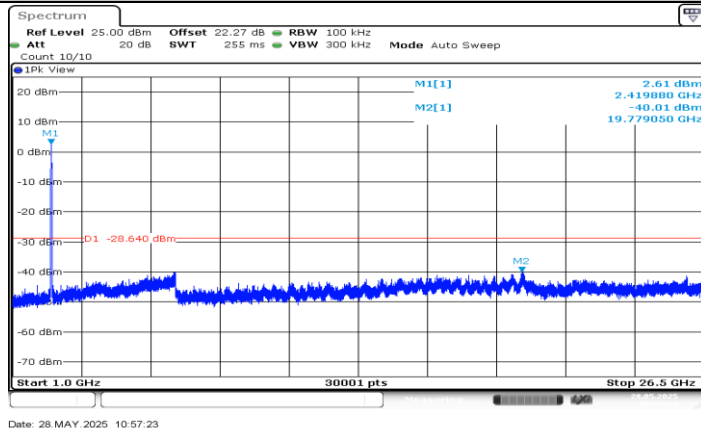
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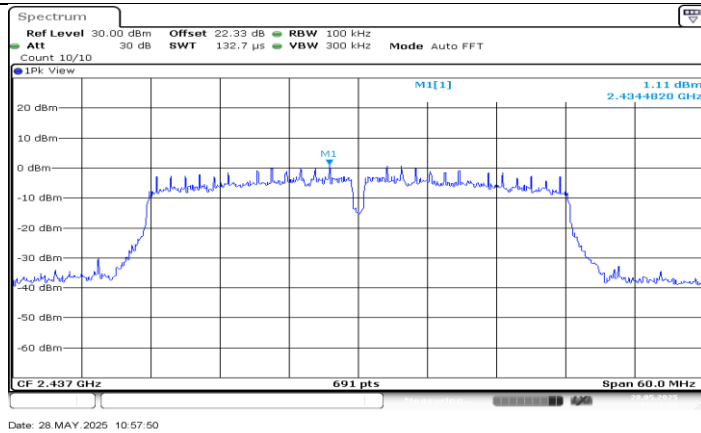
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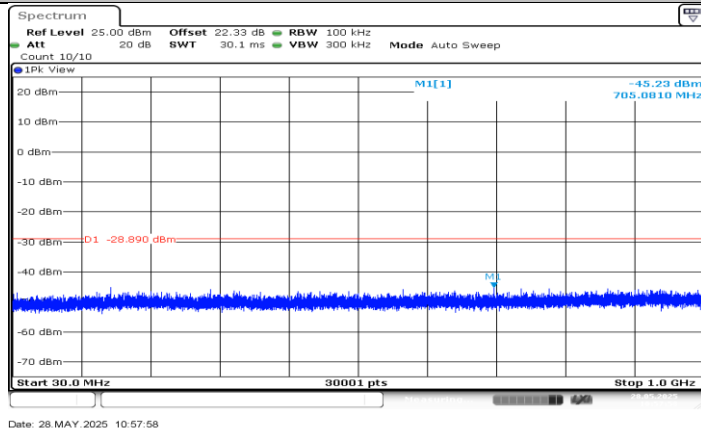
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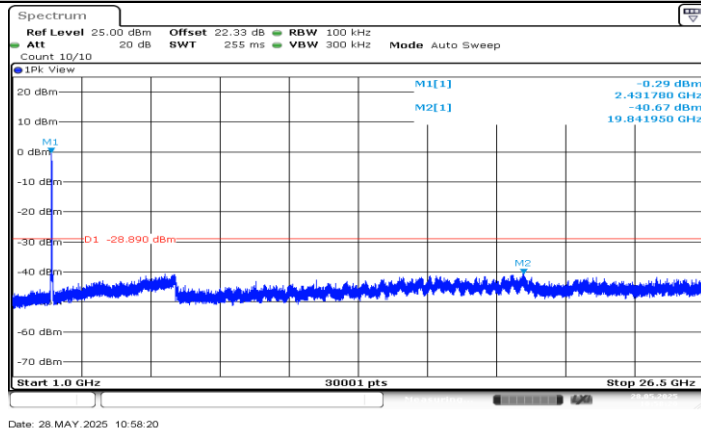
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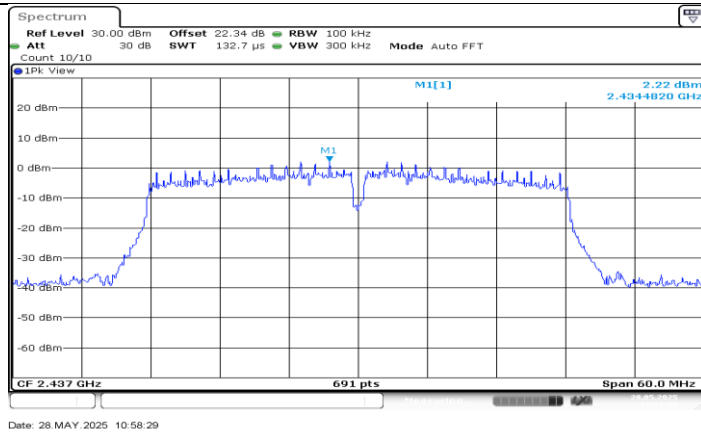
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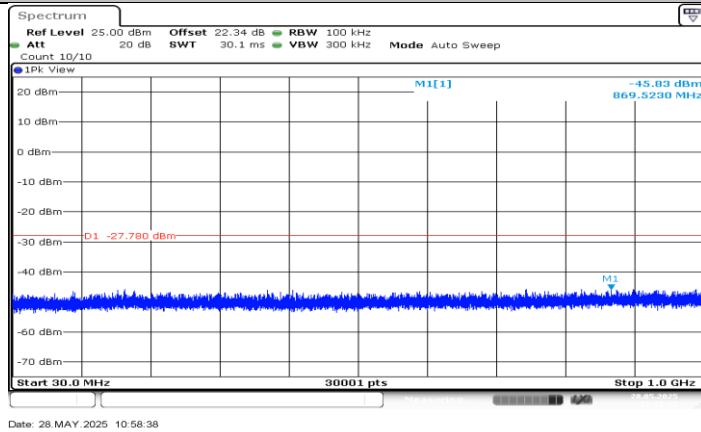
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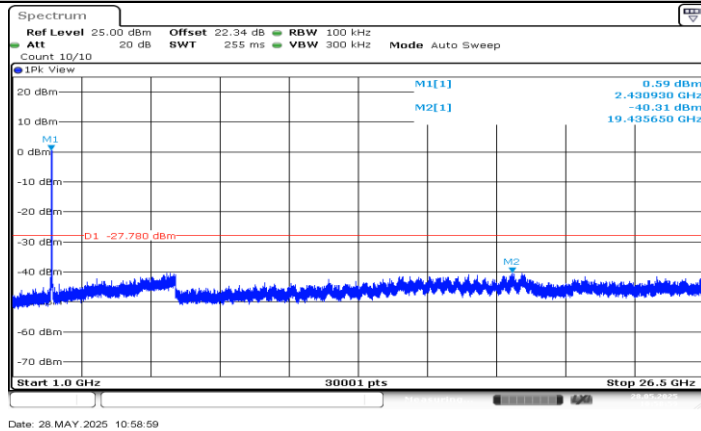
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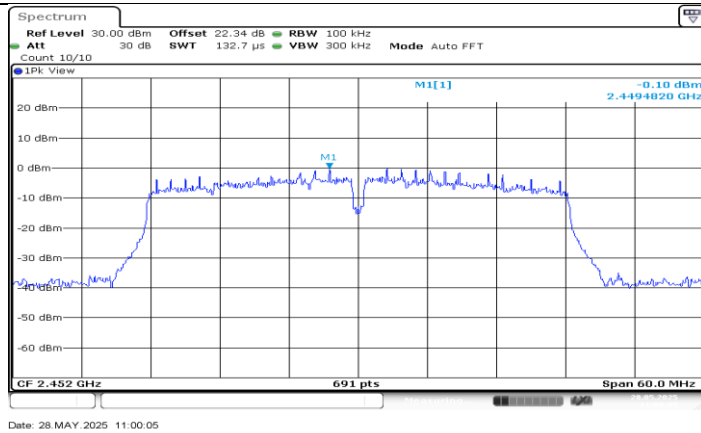
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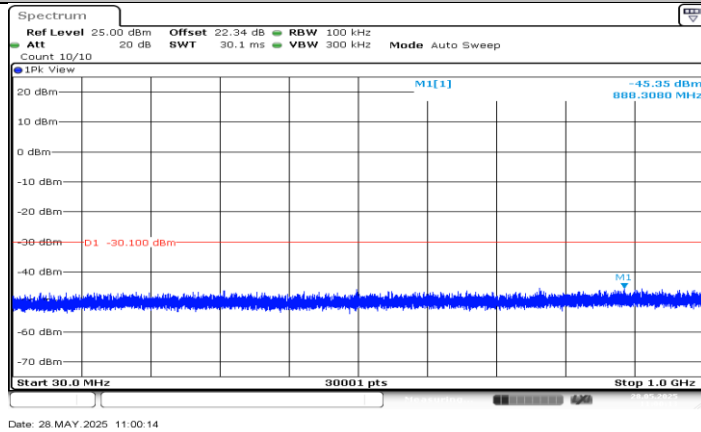
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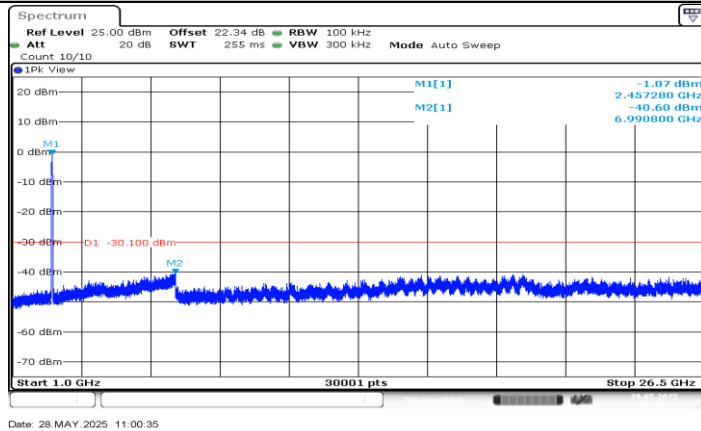
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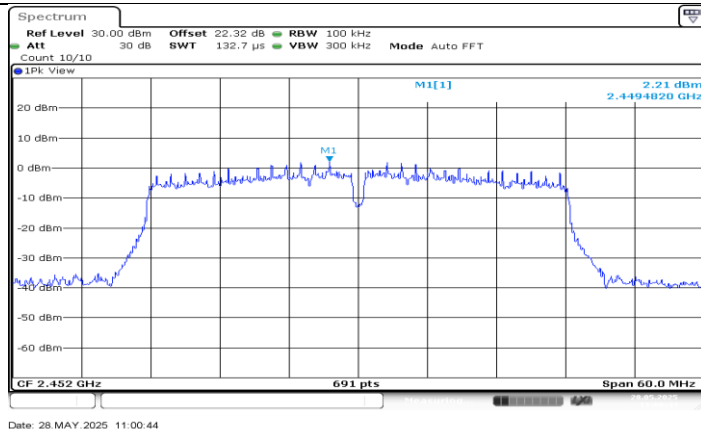
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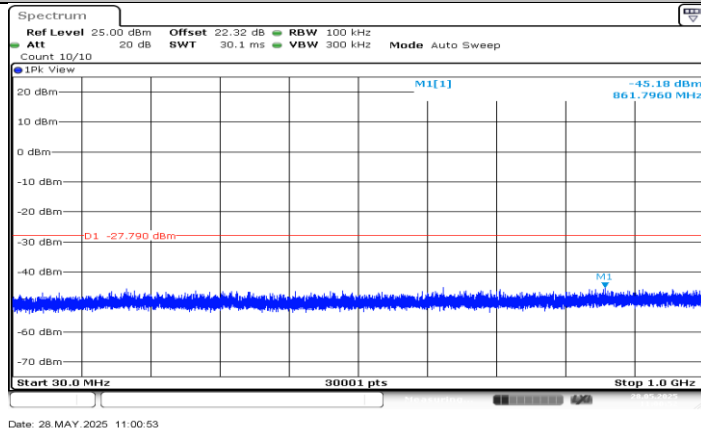
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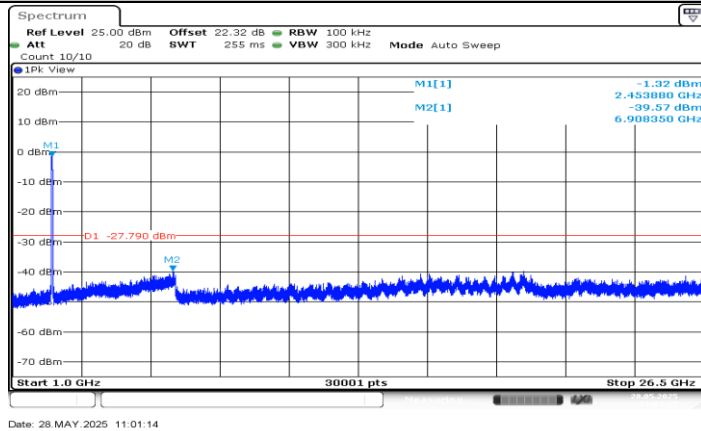
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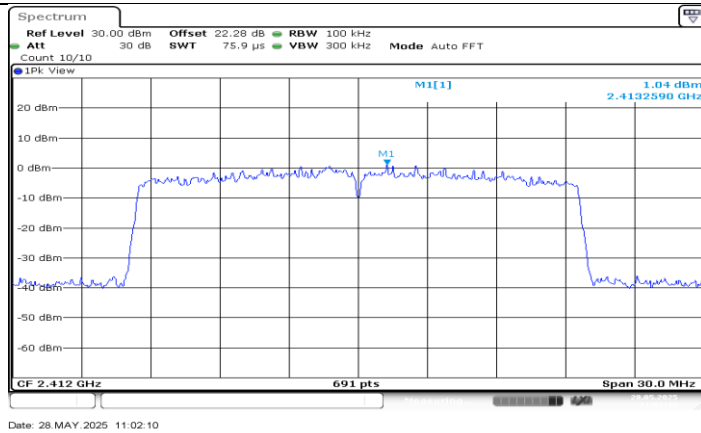
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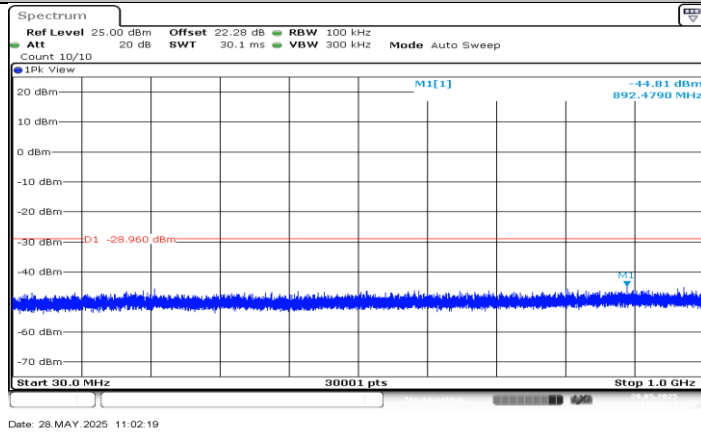
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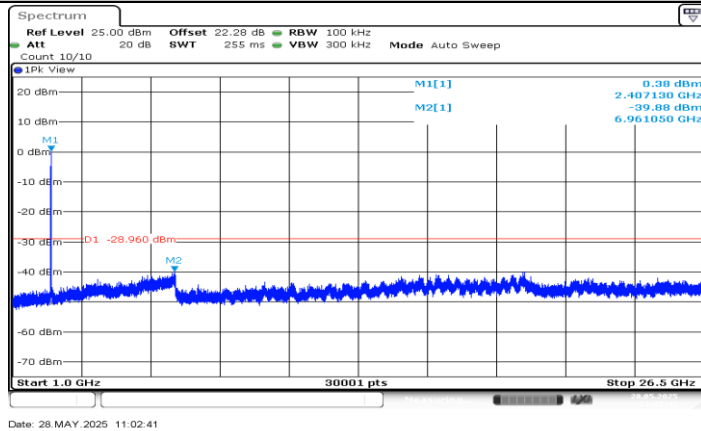
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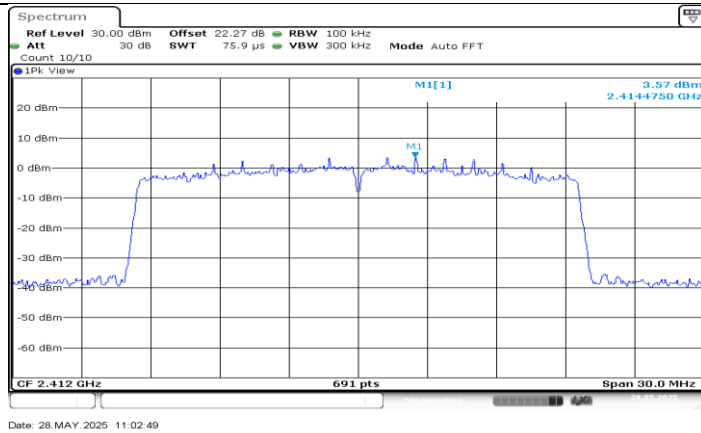
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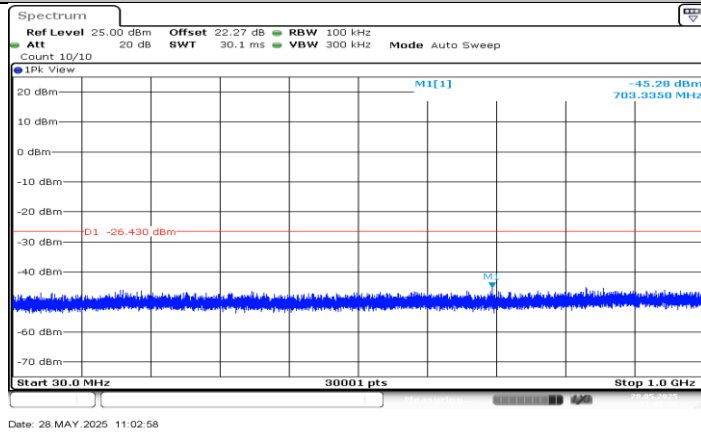
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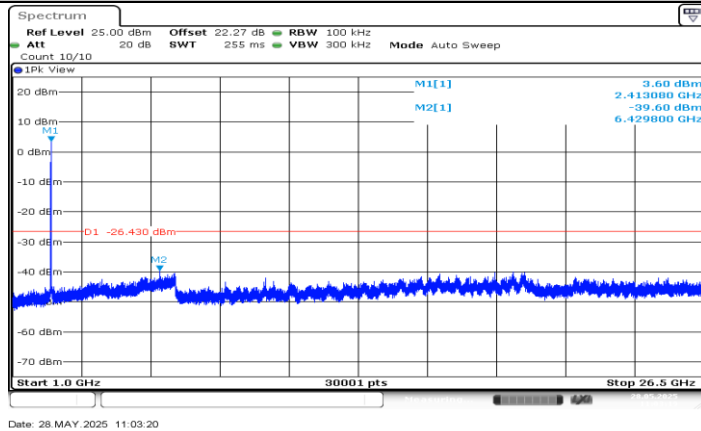
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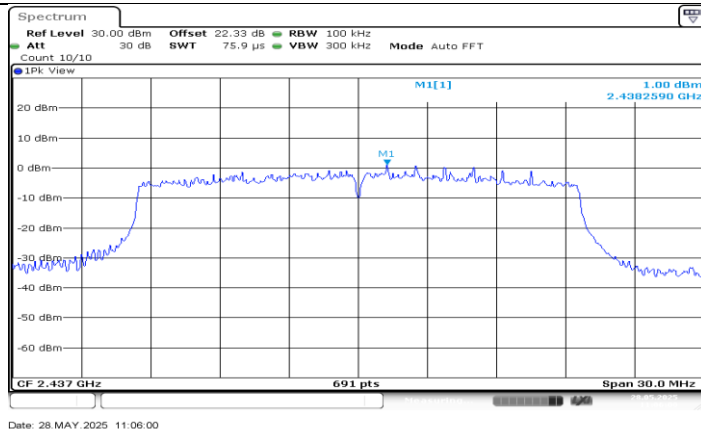
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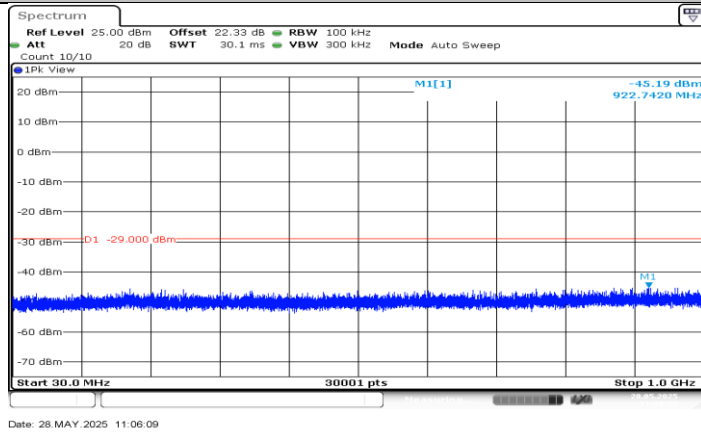
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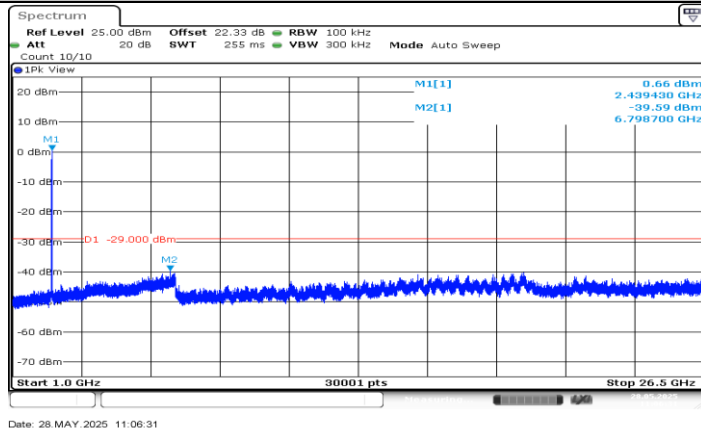
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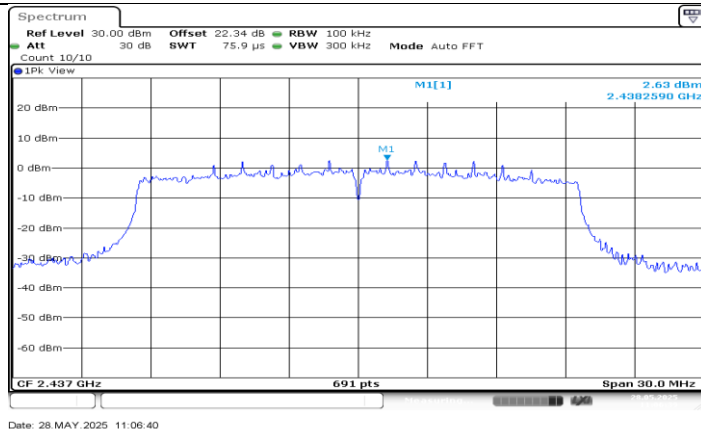
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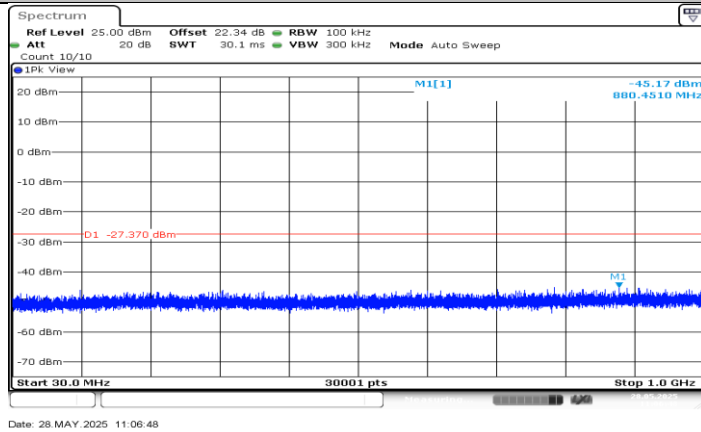
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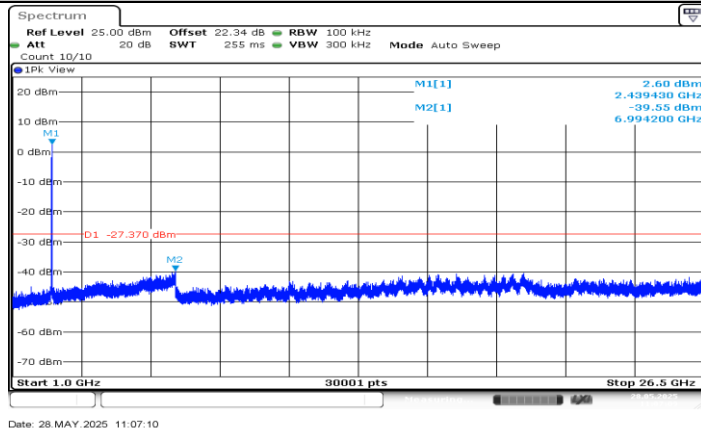
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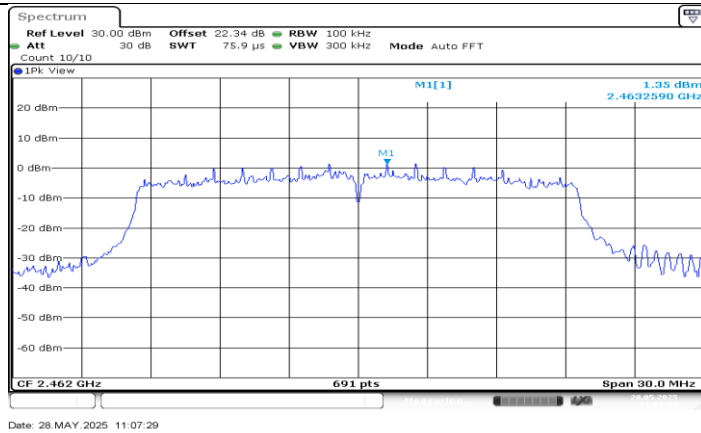
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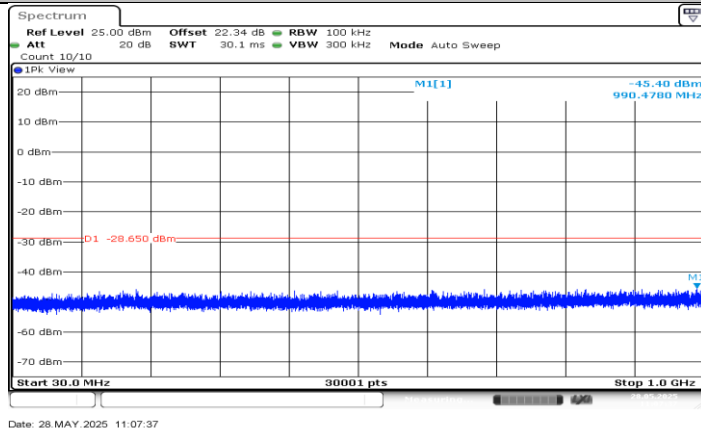
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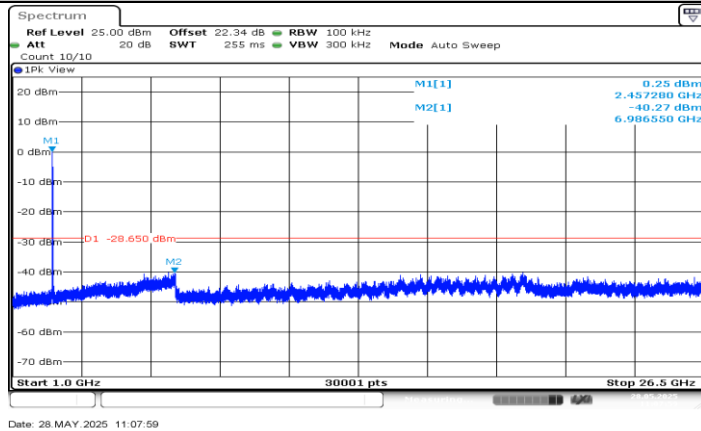
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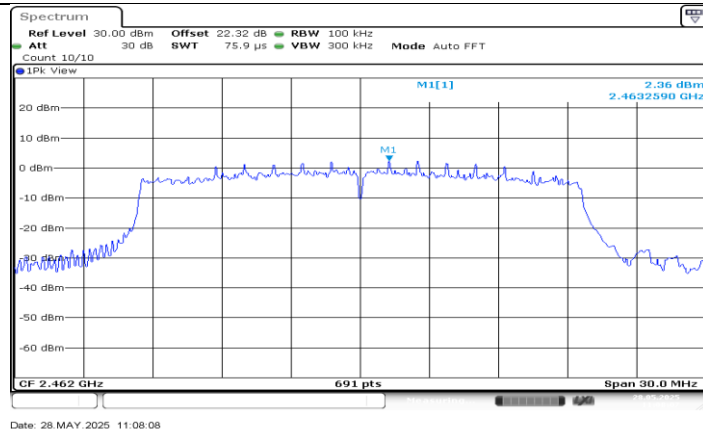
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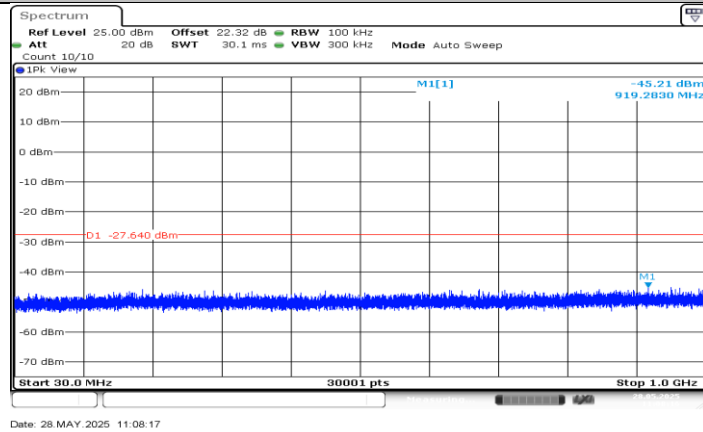
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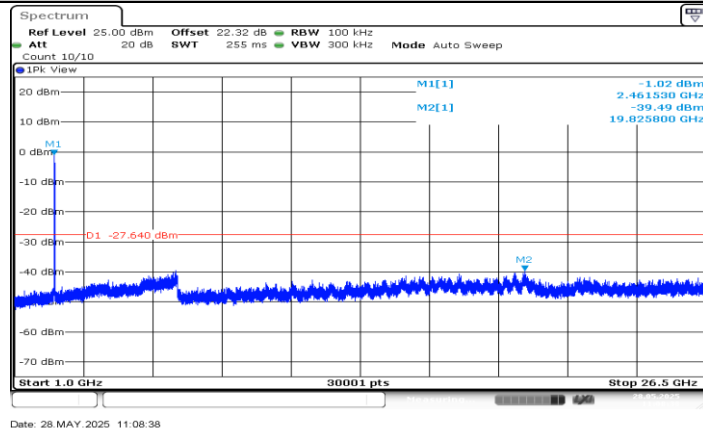
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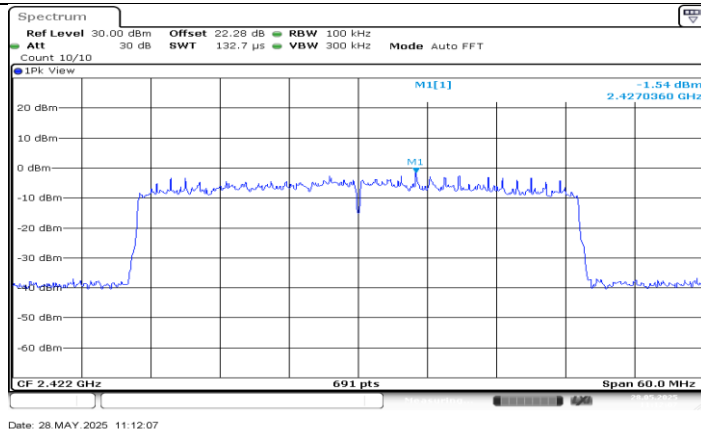
11AX20MIMO_Ant2_2462_0~Reference



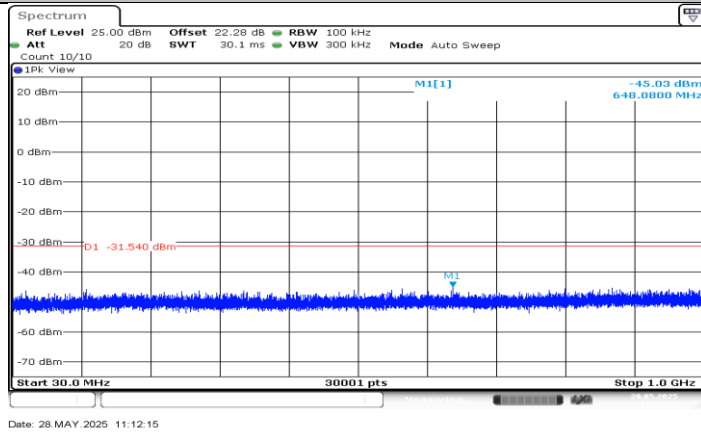
11AX20MIMO_Ant2_2462_30~1000



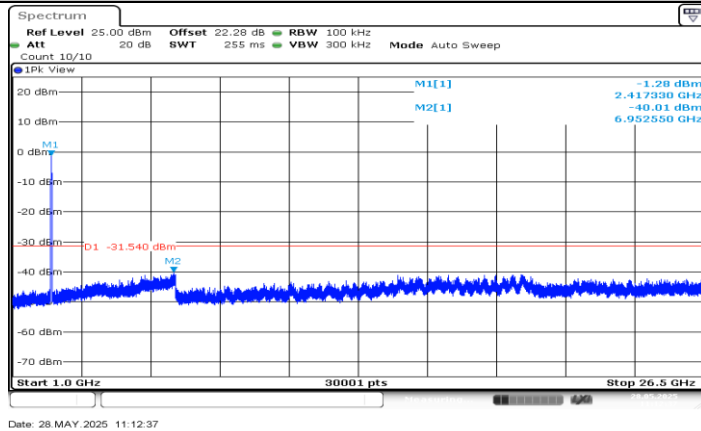
11AX20MIMO_Ant2_2462_1000~26500



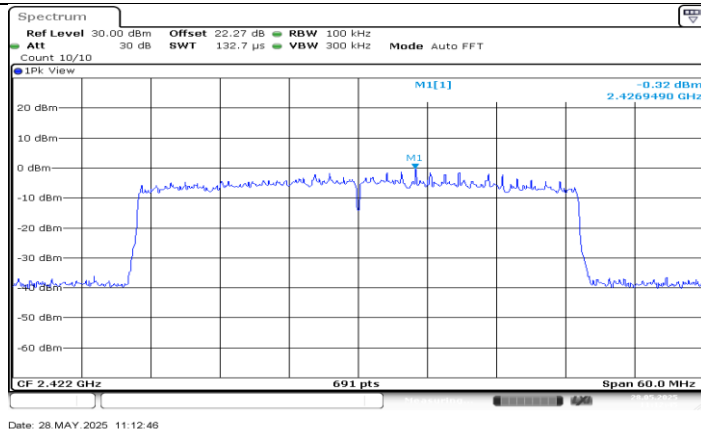
11AX40MIMO_Ant1_2422_0~Reference



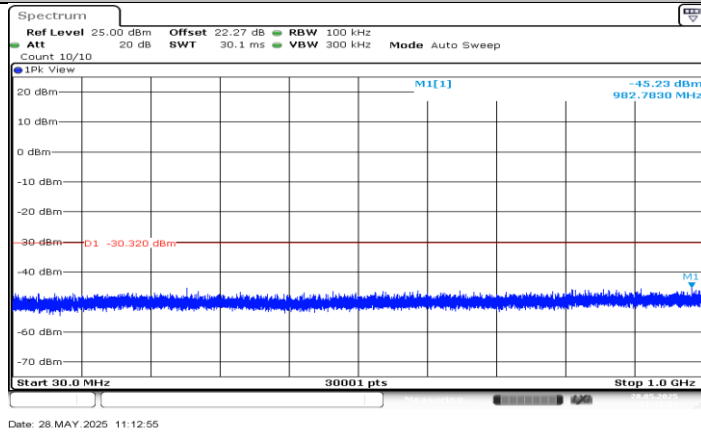
11AX40MIMO_Ant1_2422_30~1000



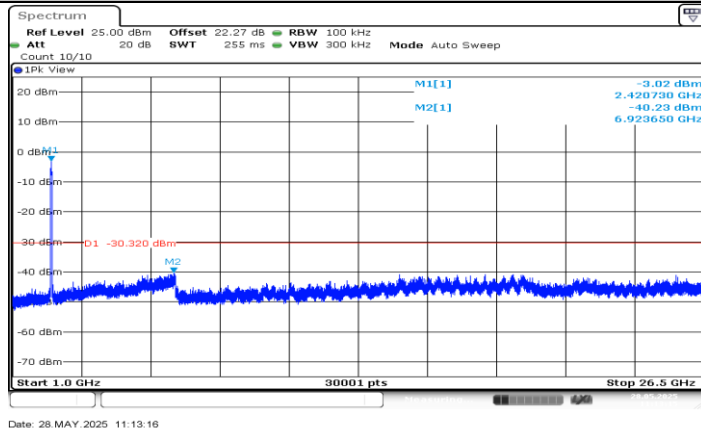
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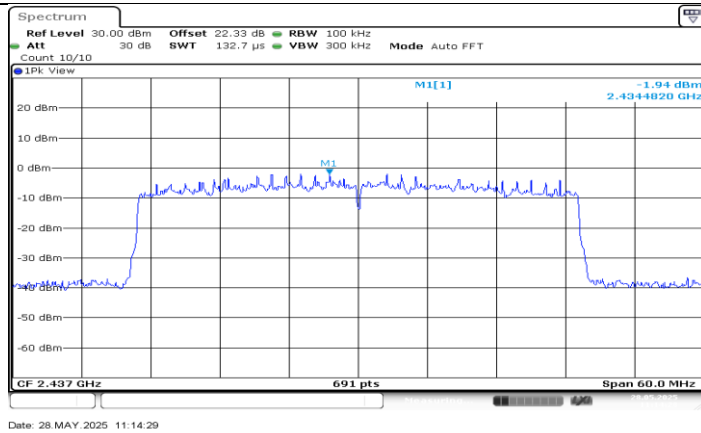
11AX40MIMO_Ant2_2422_0~Reference



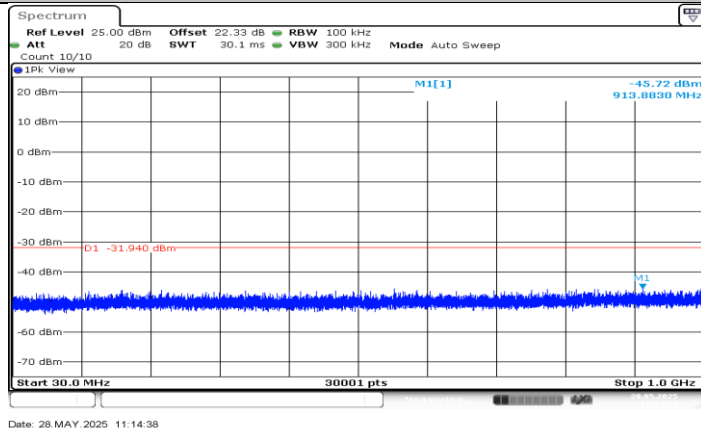
11AX40MIMO_Ant2_2422_30~1000



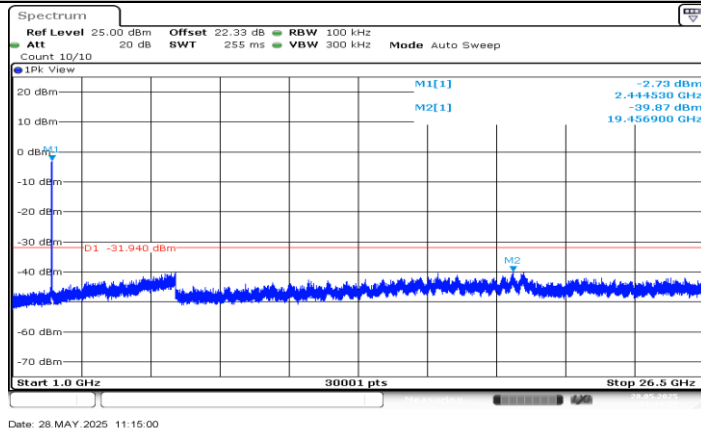
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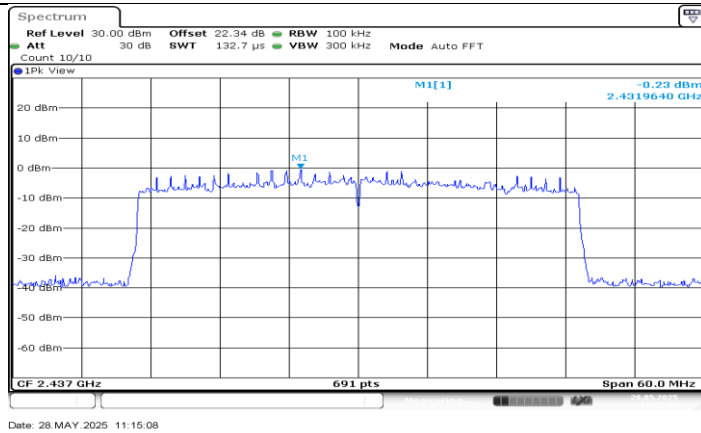
11AX40MIMO_Ant1_2437_0~Reference



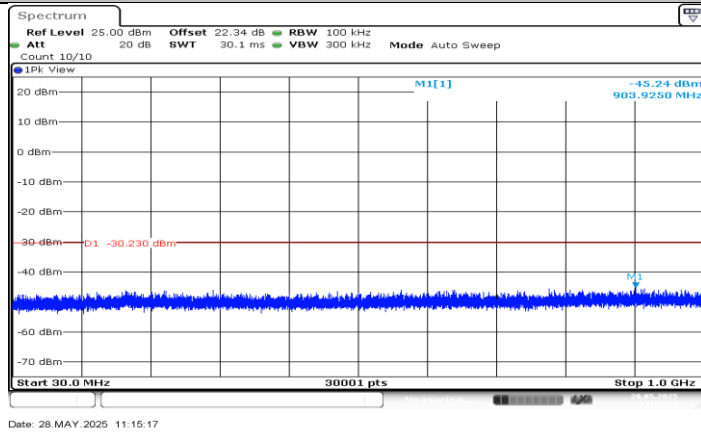
11AX40MIMO_Ant1_2437_30~1000



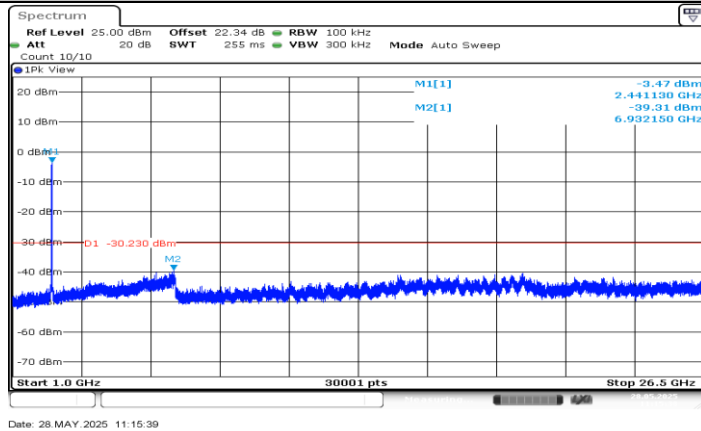
11AX40MIMO_Ant1_2437_1000~26500



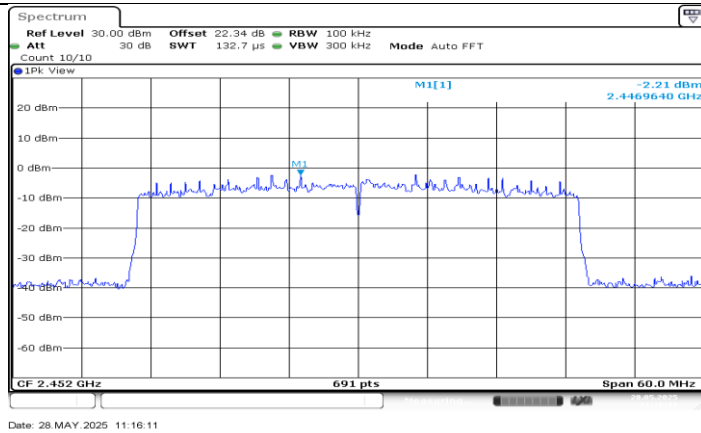
11AX40MIMO_Ant2_2437_0~Reference



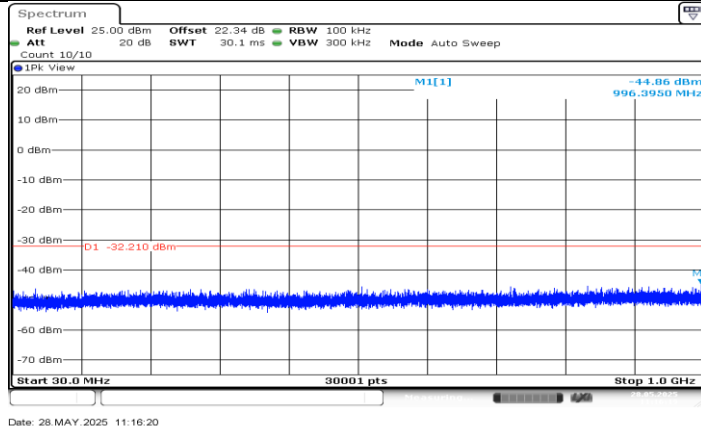
11AX40MIMO_Ant2_2437_30~1000



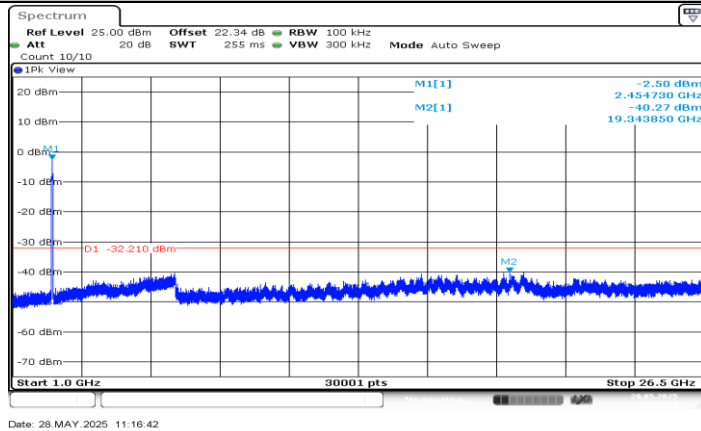
11AX40MIMO_Ant2_2437_1000~26500



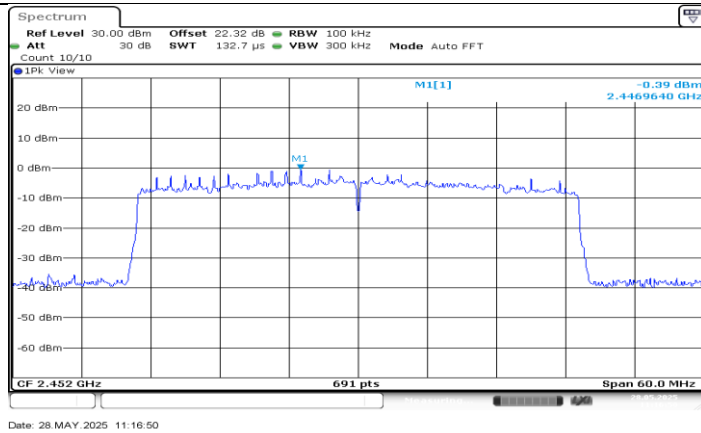
11AX40MIMO_Ant1_2452_0~Reference



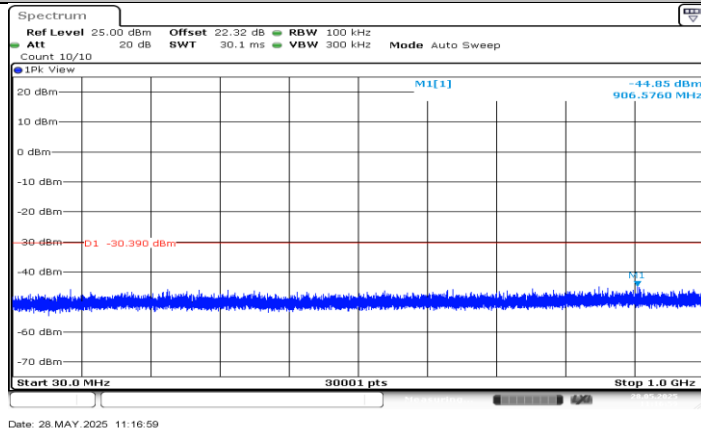
11AX40MIMO_Ant1_2452_30~1000



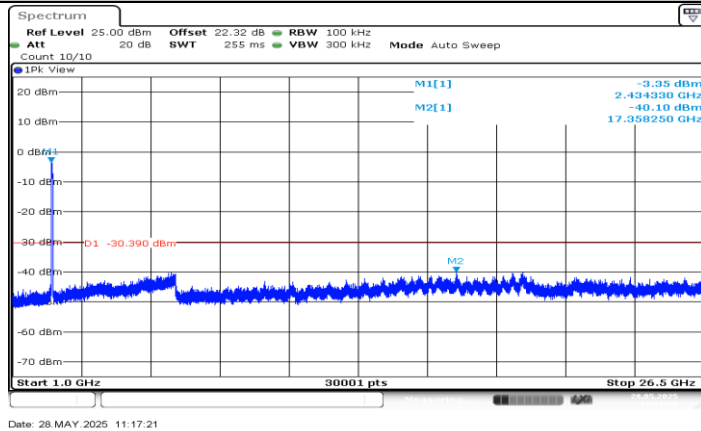
11AX40MIMO_Ant1_2452_1000~26500



11AX40MIMO_Ant2_2452_0~Reference



11AX40MIMO_Ant2_2452_30~1000



11AX40MIMO_Ant2_2452_1000~26500

11.7. APPENDIX G: DUTY CYCLE**11.7.1. Test Result**

Test Mode	On Time (msec)	Period (msec)	Duty Cycle x (Linear)	Duty Cycle (%)	Duty Cycle Correction Factor (dB)	1/T Minimum VBW (kHz)	Final setting For VBW (kHz)
11B	8.37	8.98	0.9321	93.21	0.31	0.12	1
11G	1.39	2.03	0.6847	68.47	1.64	0.72	1
11N20MIMO	1.29	1.92	0.6719	67.19	1.73	0.78	1
11N40MIMO	0.65	1.27	0.5118	51.18	2.91	1.54	2
11AX20MIMO	3.85	4.58	0.8406	84.06	0.75	0.26	1
11AX40MIMO	1.96	2.67	0.7341	73.41	1.34	0.51	1

Note:

Duty Cycle Correction Factor= $10\log(1/x)$.

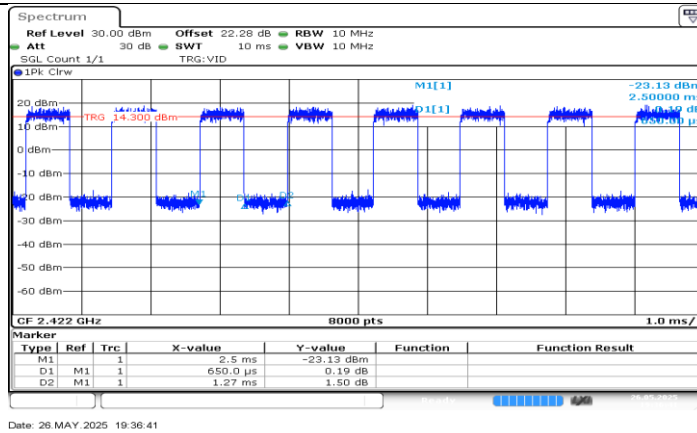
Where: x is Duty Cycle (Linear)

Where: T is On Time

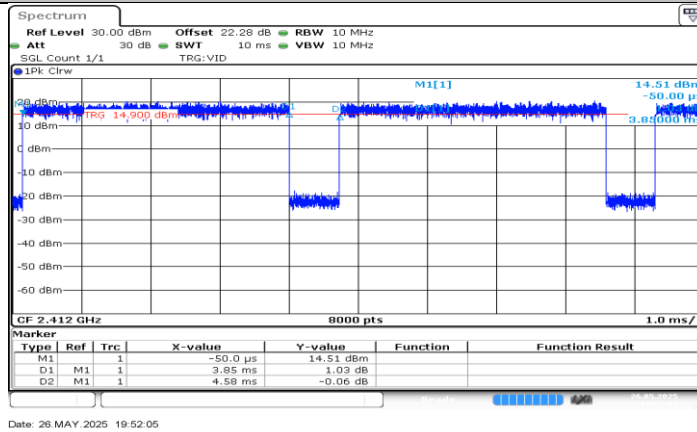
If that calculated VBW is not available on the analyzer then the next higher value should be used.

11.7.2. Test Graphs

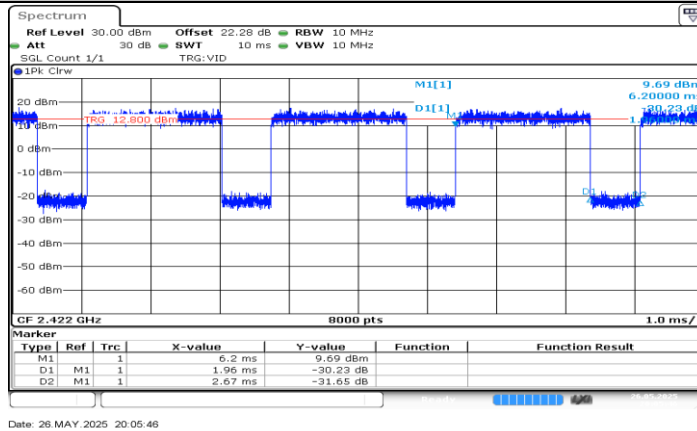




11N40MIMO_Ant1_2422



11AX20MIMO_Ant1_2412



11AX40MIMO_Ant1_2422

END OF REPORT