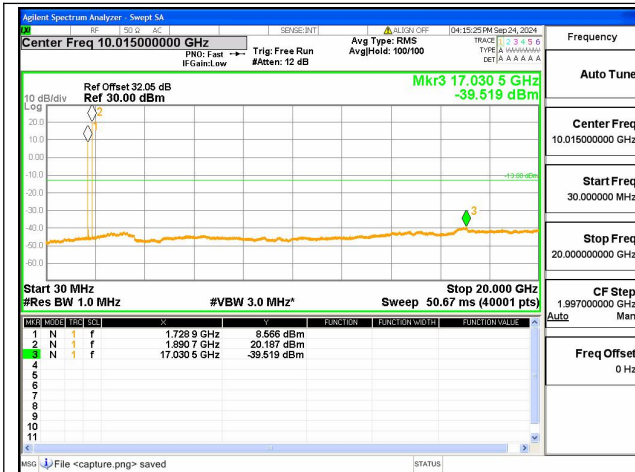
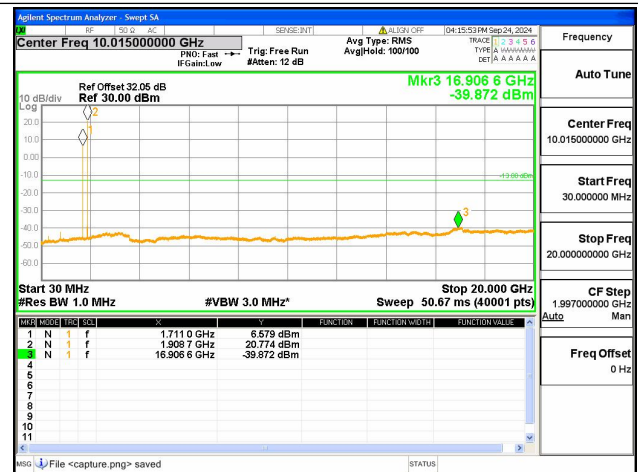
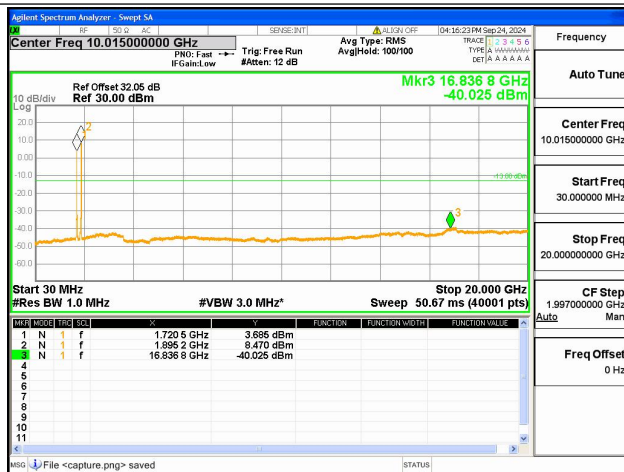




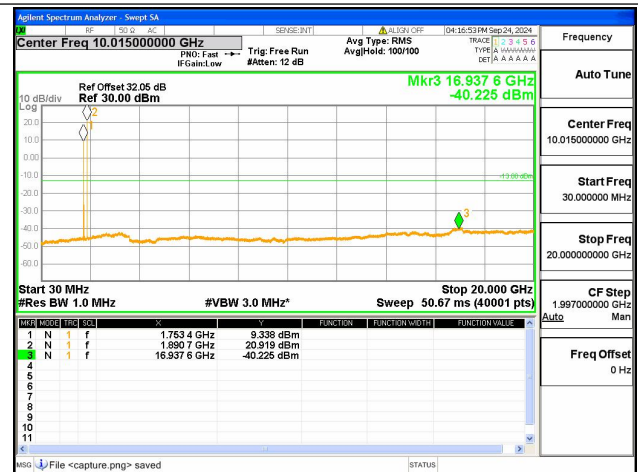
2A-66A / 20+20MHz / QPSK / High+Low CH /
1@0-1@99



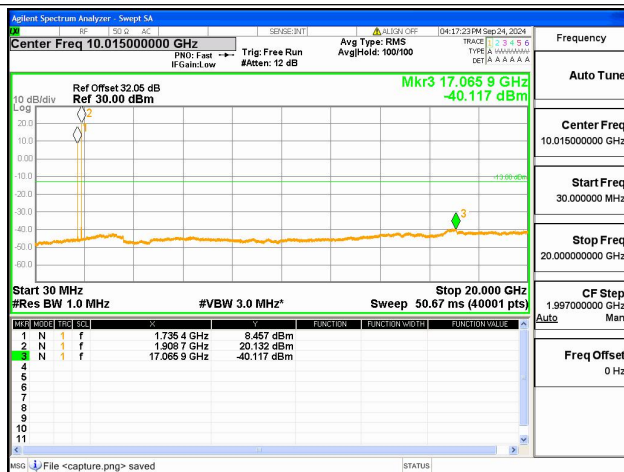
2A-66A / 20+20MHz / QPSK / High+Low CH /
1@99-1@0



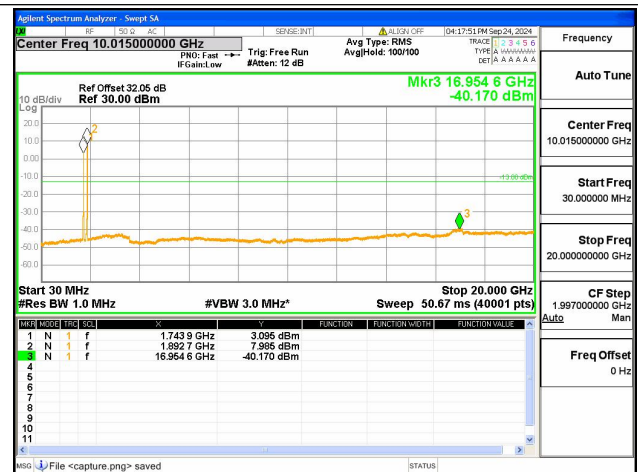
2A-66A / 20+20MHz / QPSK / High+Low CH /
100@0-100@0



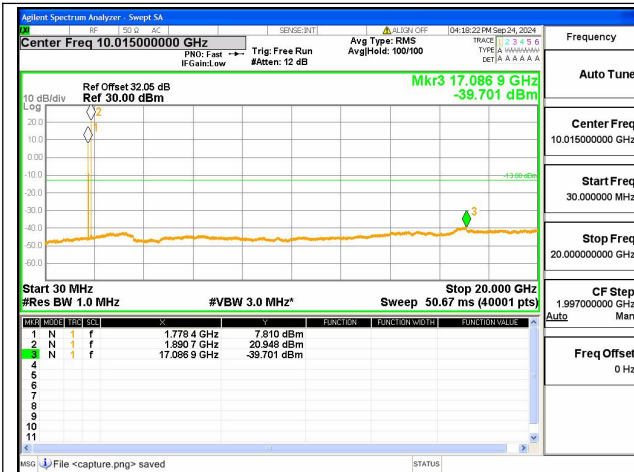
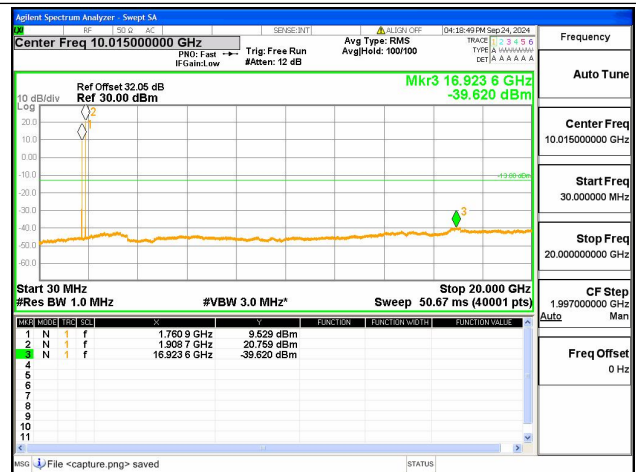
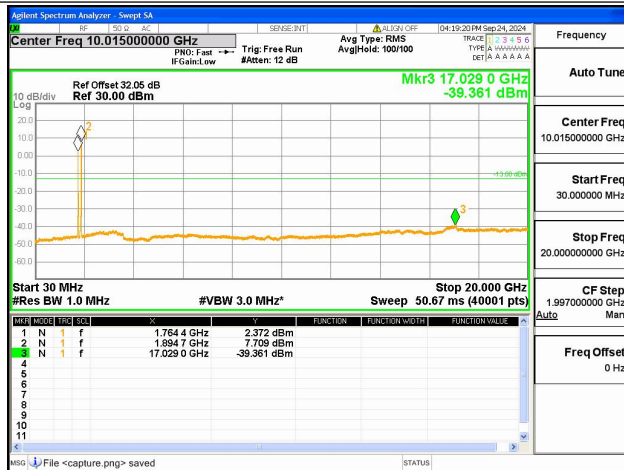
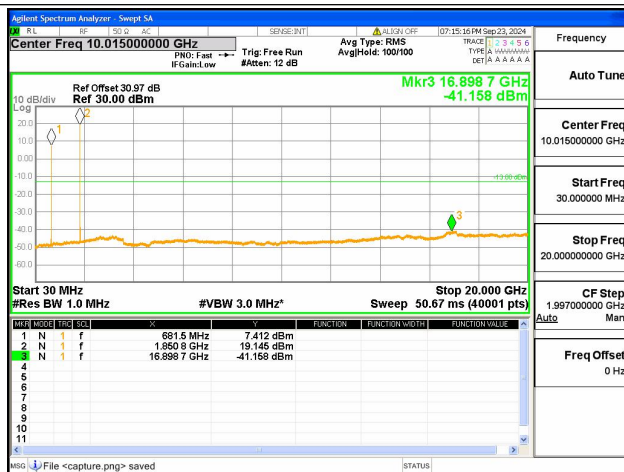
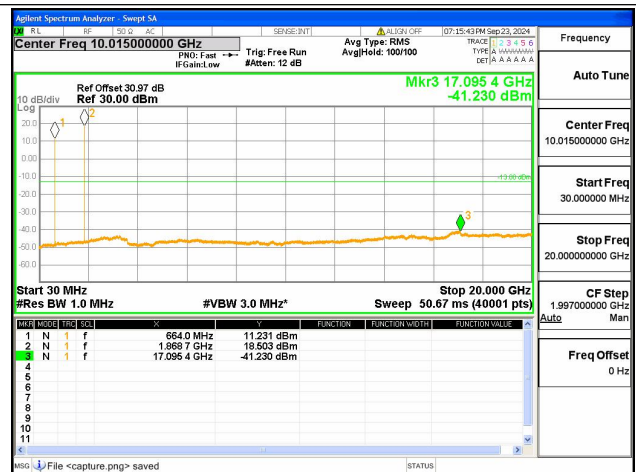
2A-66A / 20+20MHz / QPSK / High+Mid CH /
1@0-1@99



2A-66A / 20+20MHz / QPSK / High+Mid CH /
1@99-1@0

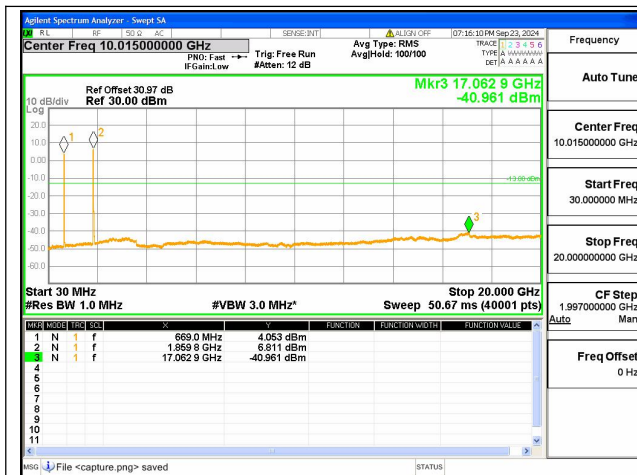


2A-66A / 20+20MHz / QPSK / High+Mid CH /
100@0-100@0

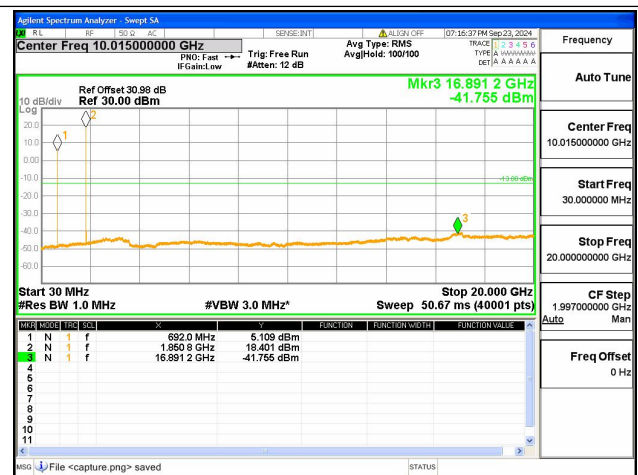
2A-66A / 20+20MHz / QPSK / High+High CH /
1@0-1@992A-66A / 20+20MHz / QPSK / High+High CH /
1@99-1@02A-66A / 20+20MHz / QPSK / High+High CH /
100@0-100@02A-71A / 20+20MHz / QPSK / Low+Low CH /
1@0-1@992A-71A / 20+20MHz / QPSK / Low+Low CH /
1@99-1@0



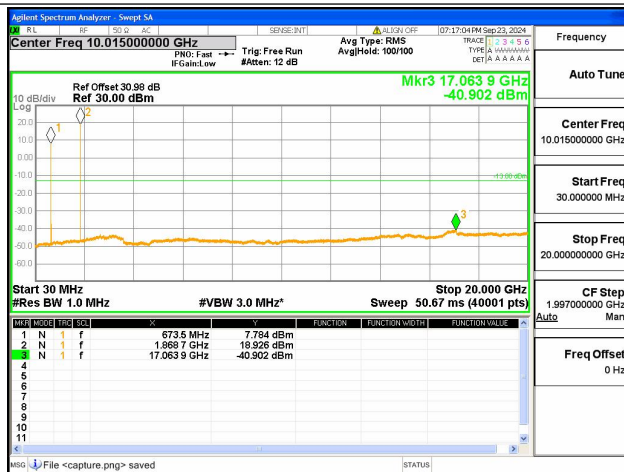
REPORT No.: SZ24070315W11



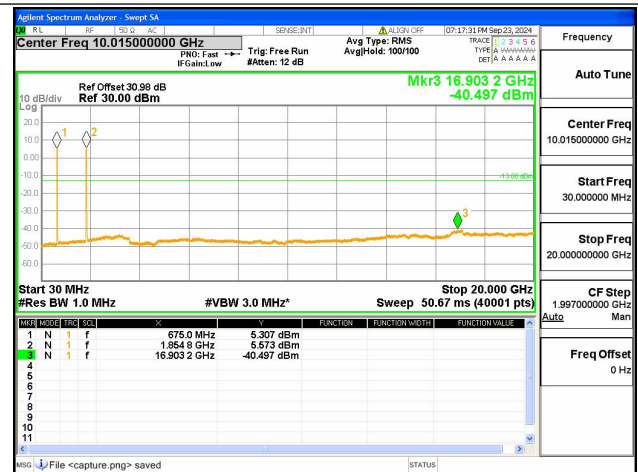
2A-71A / 20+20MHz / QPSK / Low+Low CH /
100@0-100@0



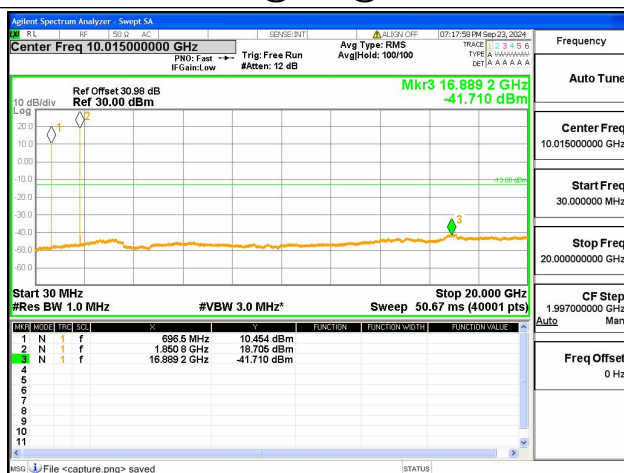
2A-71A / 20+20MHz / QPSK / Low+Mid CH /
1@0-1@99



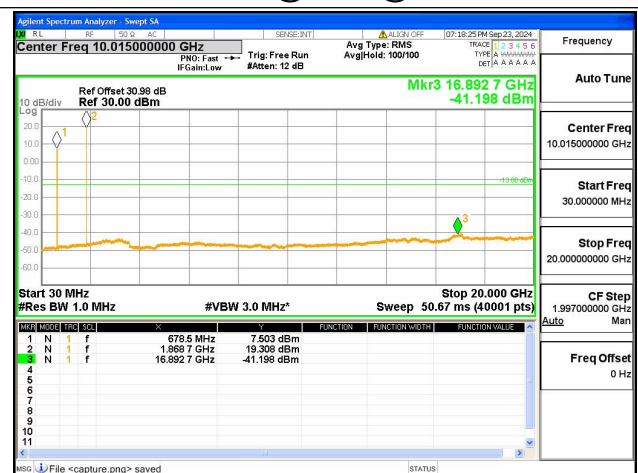
2A-71A / 20+20MHz / QPSK / Low+Mid CH /
1@99-1@0



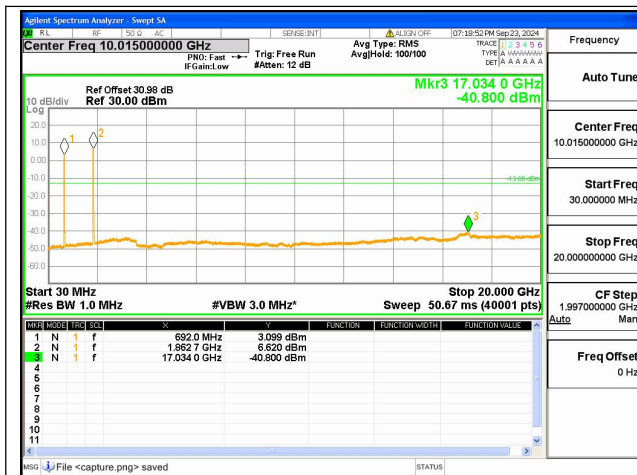
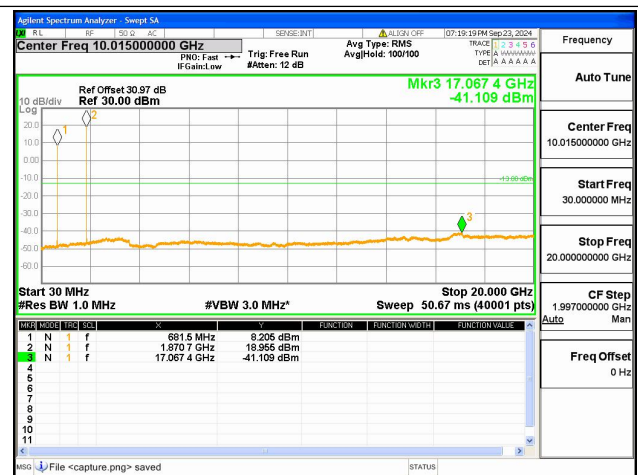
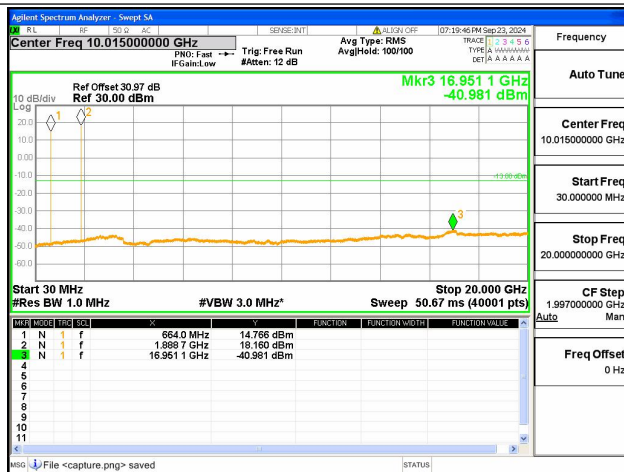
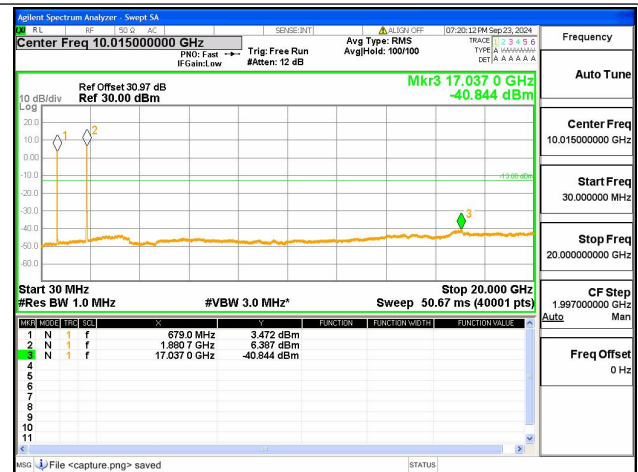
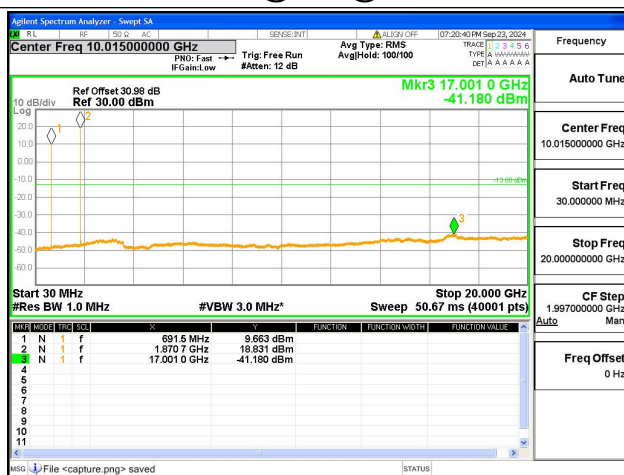
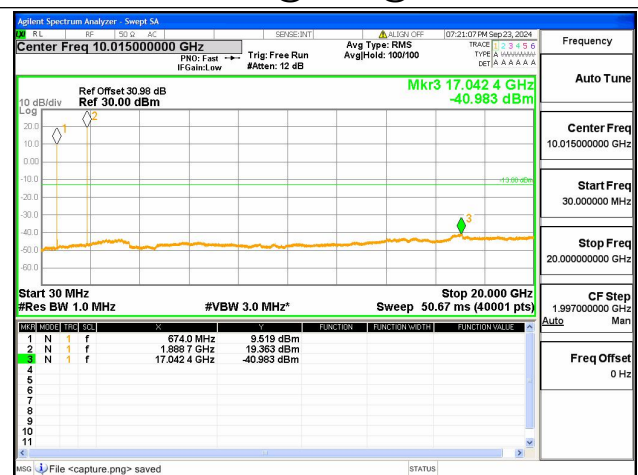
2A-71A / 20+20MHz / QPSK / Low+Mid CH /
100@0-100@0

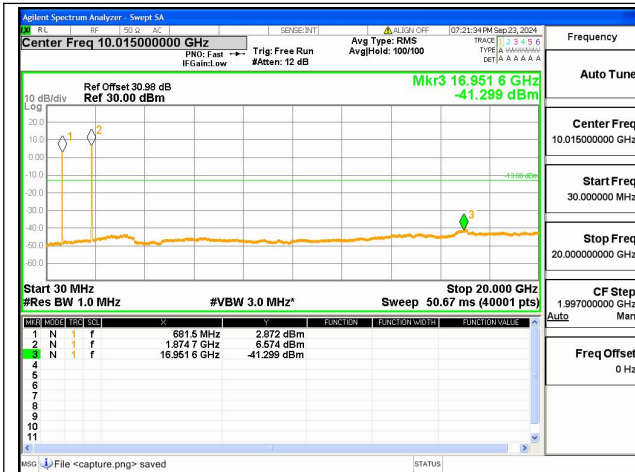
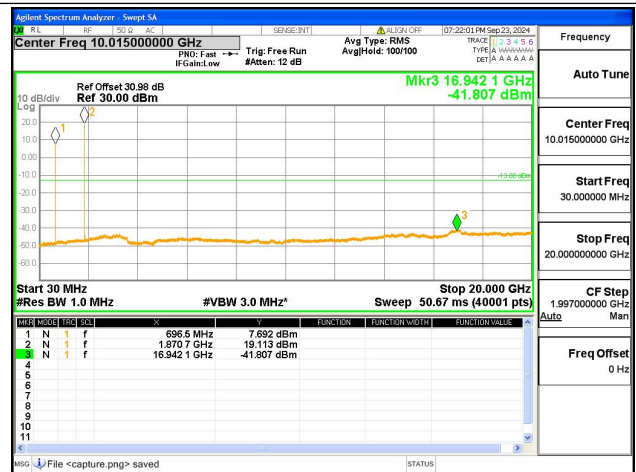
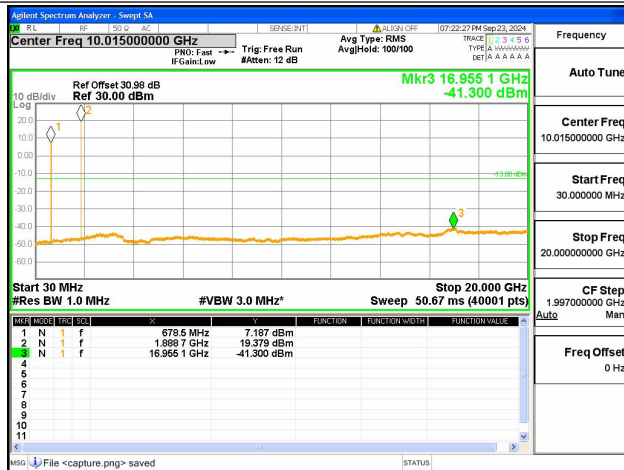
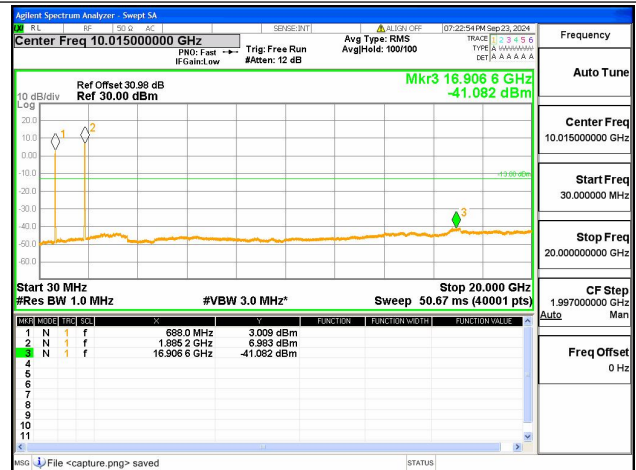
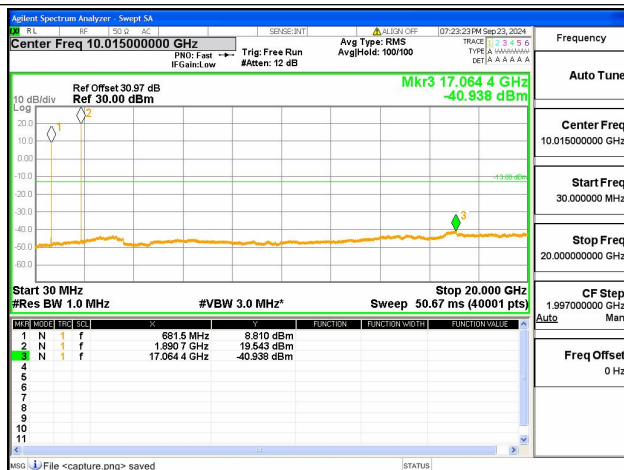
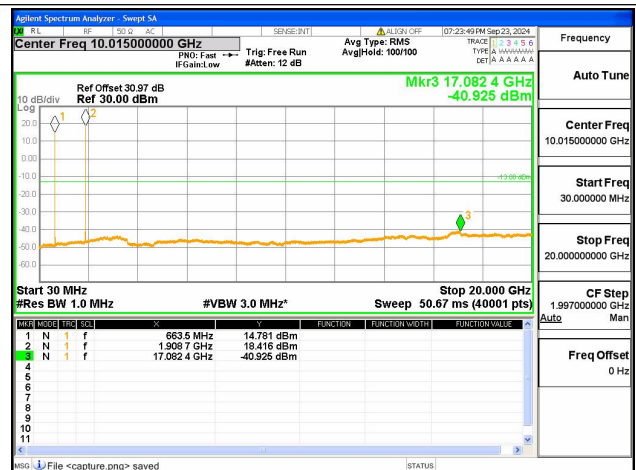


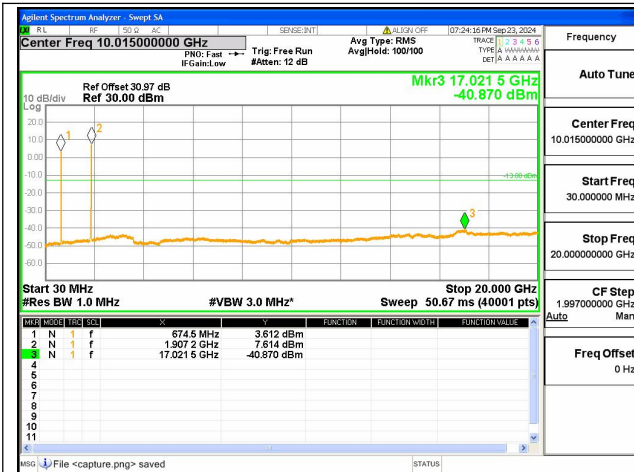
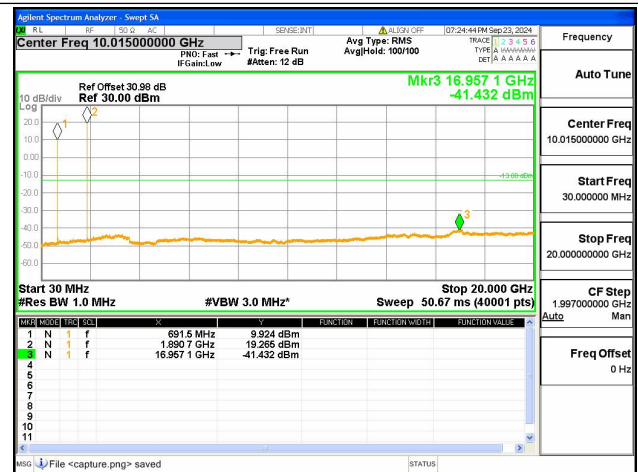
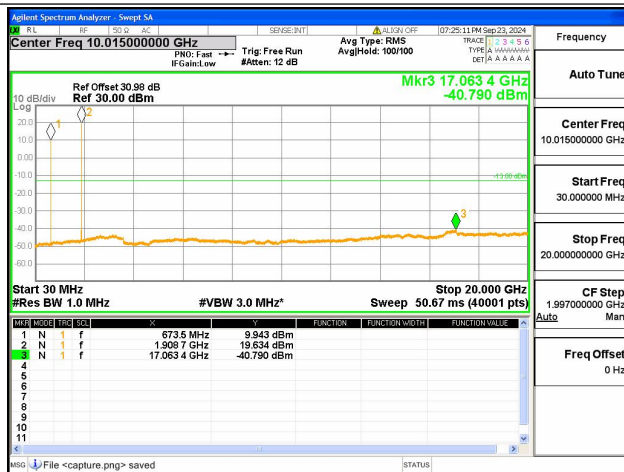
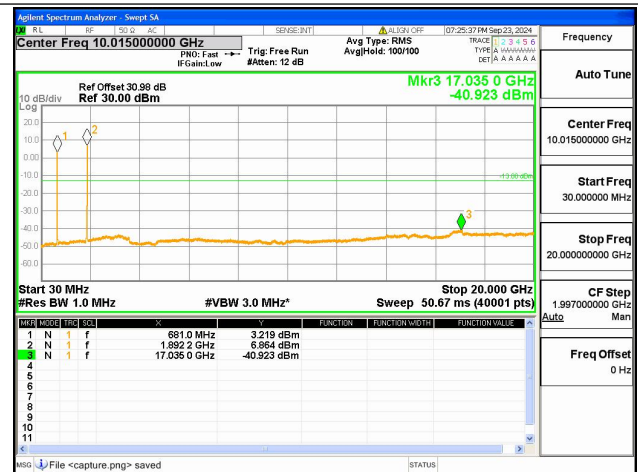
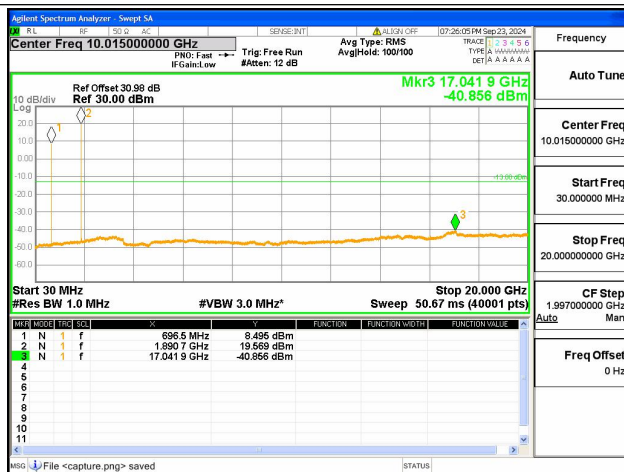
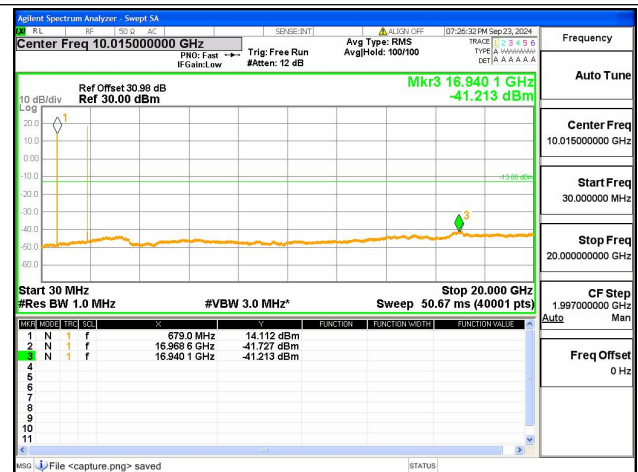
2A-71A / 20+20MHz / QPSK / Low+High CH /
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2A-71A / 20+20MHz / QPSK / Low+High CH /
100@0-100@02A-71A / 20+20MHz / QPSK / Mid+Low CH /
1@0-1@992A-71A / 20+20MHz / QPSK / Mid+Low CH /
1@99-1@02A-71A / 20+20MHz / QPSK / Mid+Low CH /
100@0-100@02A-71A / 20+20MHz / QPSK / Mid+Mid CH /
1@0-1@992A-71A / 20+20MHz / QPSK / Mid+Mid CH /
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