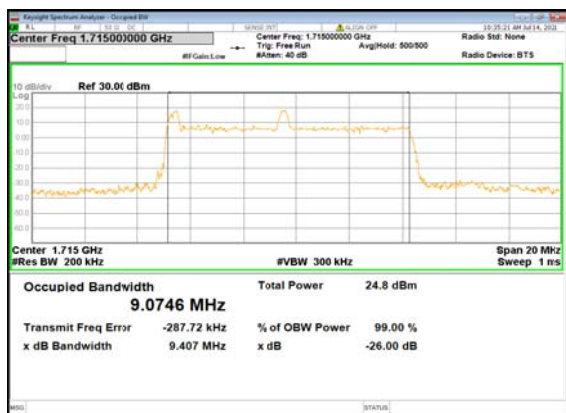


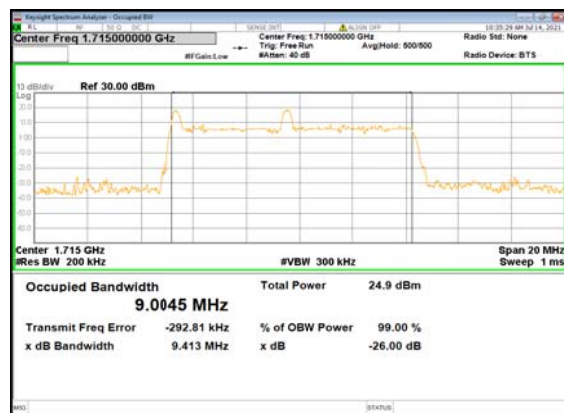


REPORT No.: SZ21030417W10

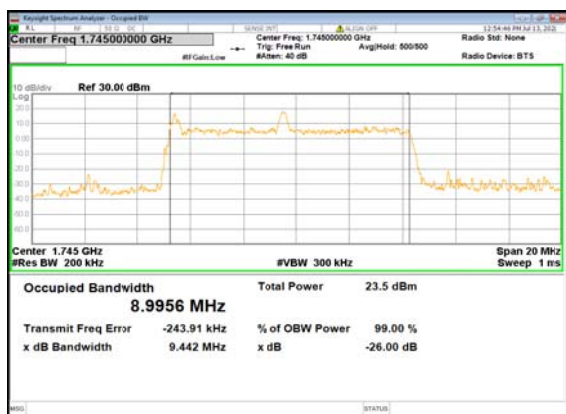
N66(10M)_DFT-s-OFDM_256
QAM_Outer_Full_Low_CH



N66(10M)_CP-OFDM_QPSK_Outer_Full
_Low_CH



N66(10M)_DFT-s-OFDM_PI_2-BPSK_Outer_
Full_Mid_CH



N66(10M)_DFT-s-OFDM_QPSK_Outer_F
ull_Mid_CH



N66(10M)_DFT-s-OFDM_16
QAM_Outer_Full_Mid_CH



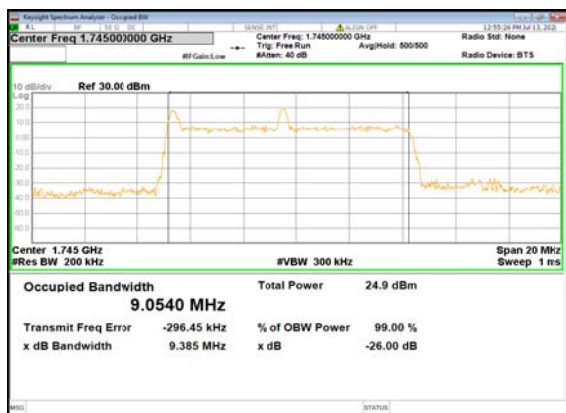
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REPORT No.: SZ21030417W10

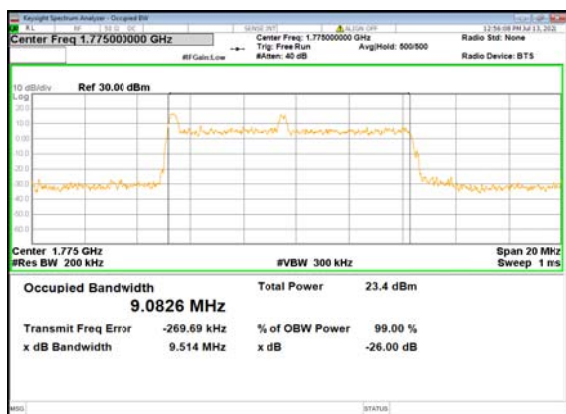
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QAM_Outer_Full_Mid_CH



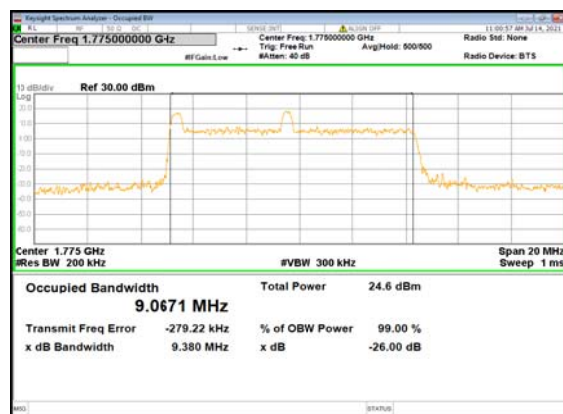
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_Mid_CH



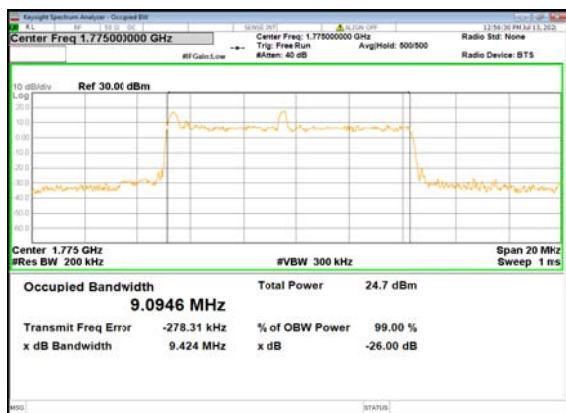
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Full_High_CH



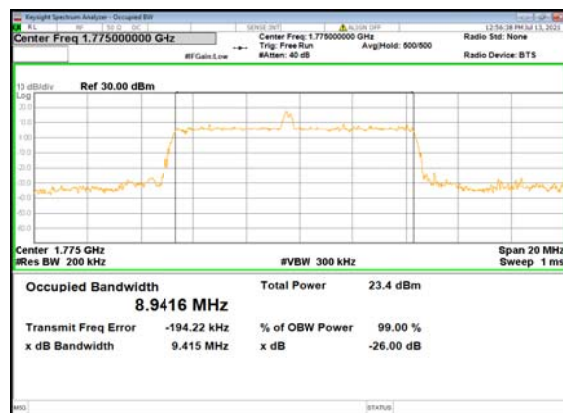
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ull_High_CH



N66(10M)_DFT-s-OFDM_16
QAM_Outer_Full_High_CH



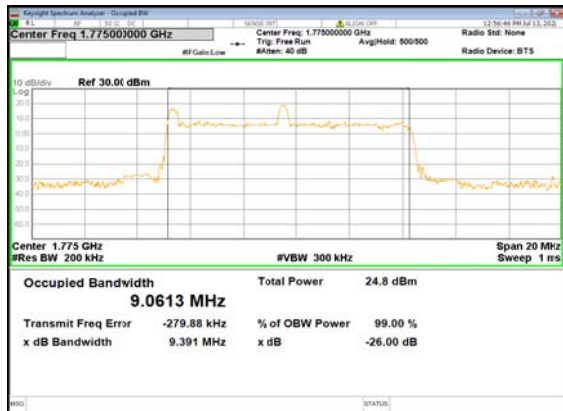
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QAM_Outer_Full_High_CH



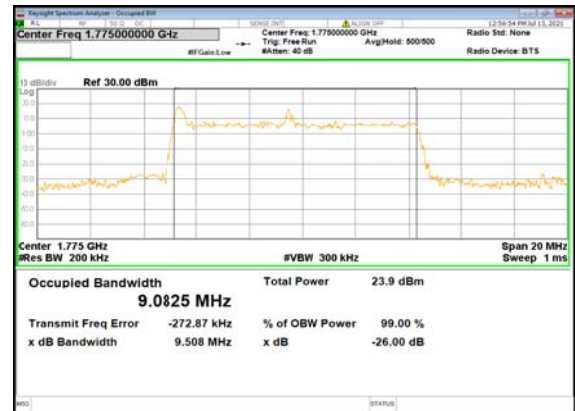


REPORT No.: SZ21030417W10

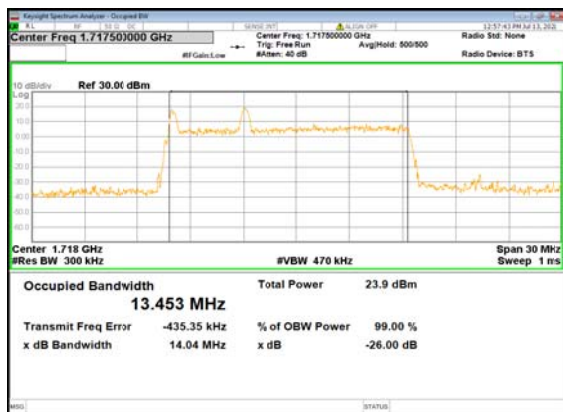
N66(10M)_DFT-s-OFDM_256 QAM_Outer_Full_High_CH



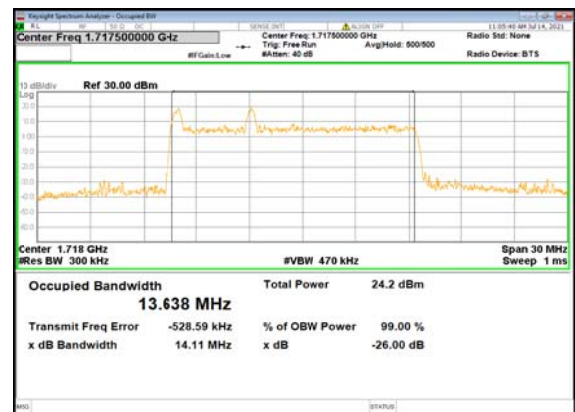
N66(10M)_CP-OFDM_QPSK_Outer_Full _High_CH



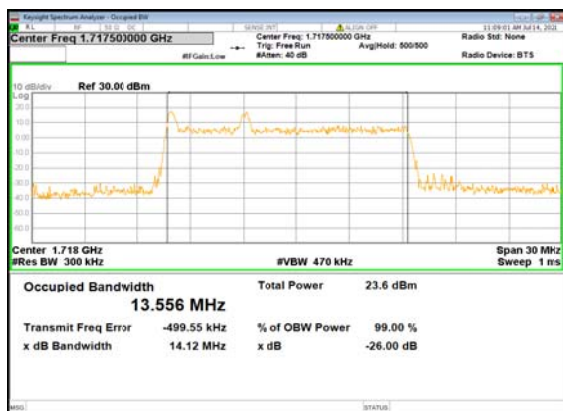
N66(15M)_DFT-s-OFDM_PI_2-BPSK_Outer_ Full_Low_CH



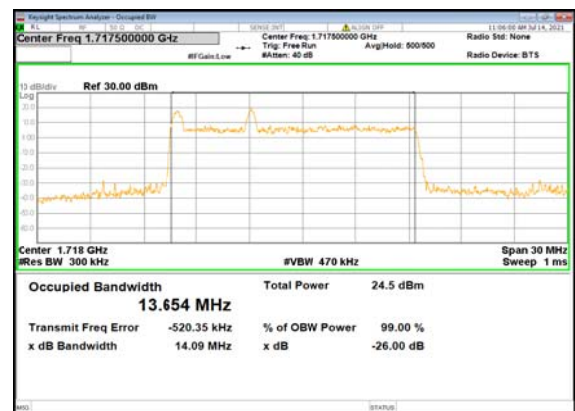
N66(15M)_DFT-s-OFDM_QPSK_Outer_F ull_Low_CH



N66(15M)_DFT-s-OFDM_16 QAM_Outer_Full_Low_CH



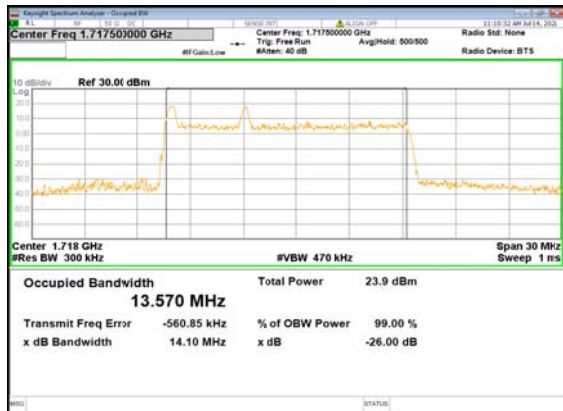
N66(15M)_DFT-s-OFDM_64 QAM_Outer_Full_Low_CH



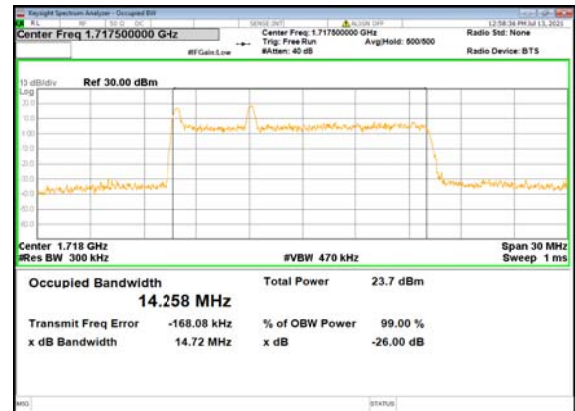


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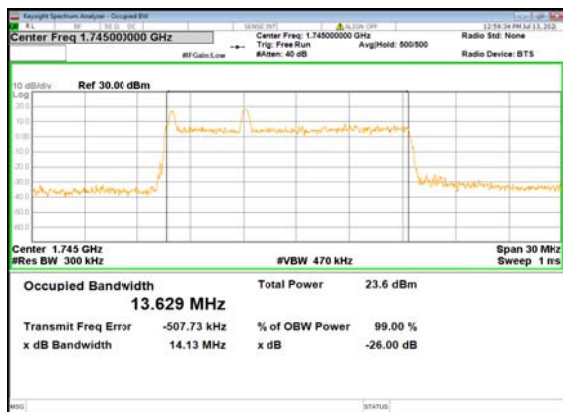
N66(15M)_DFT-s-OFDM_256 QAM_Outer_Full_Low_CH



N66(15M)_CP-OFDM_QPSK_Outer_Full _Low_CH



N66(15M)_DFT-s-OFDM_PI_2-BPSK_Outer_ Full_Mid_CH



N66(15M)_DFT-s-OFDM_QPSK_Outer_F ull_Mid_CH



N66(15M)_DFT-s-OFDM_16 QAM_Outer_Full_Mid_CH



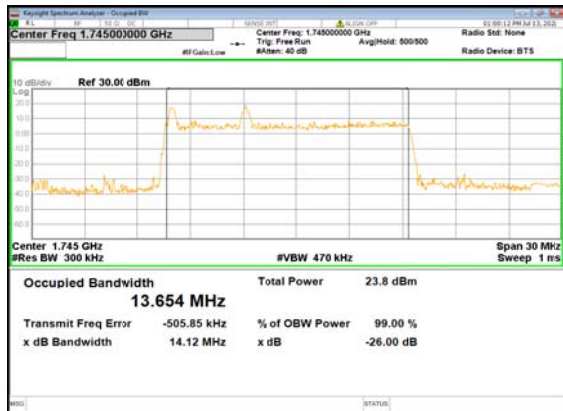
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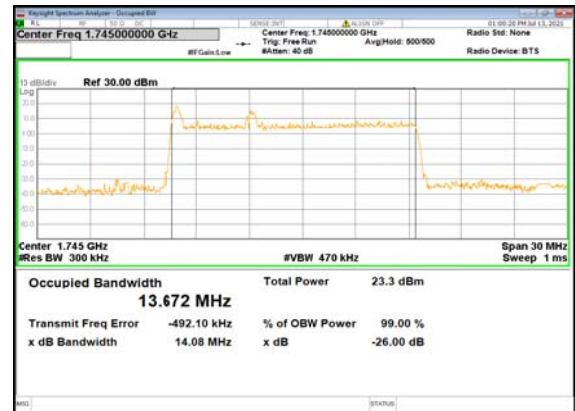


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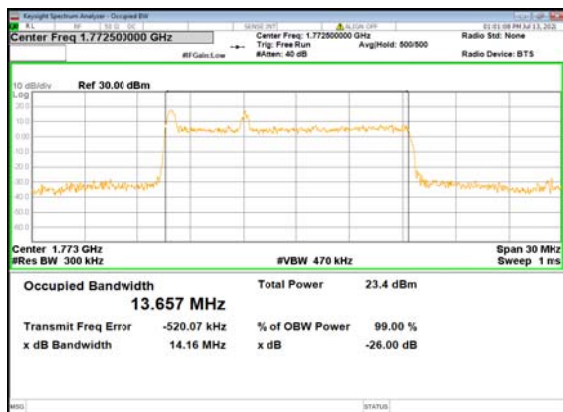
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QAM_Outer_Full_Mid_CH



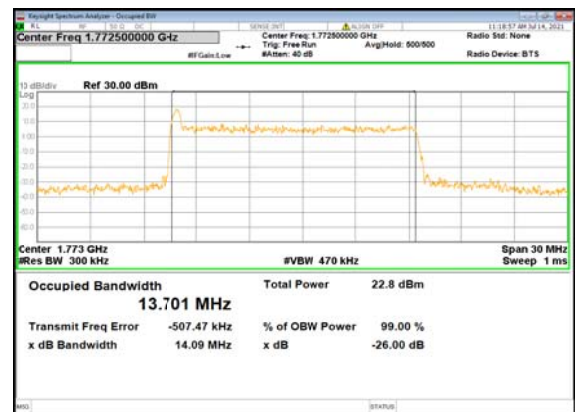
N66(15M)_CP-OFDM_QPSK_Outer_Full
_Mid_CH



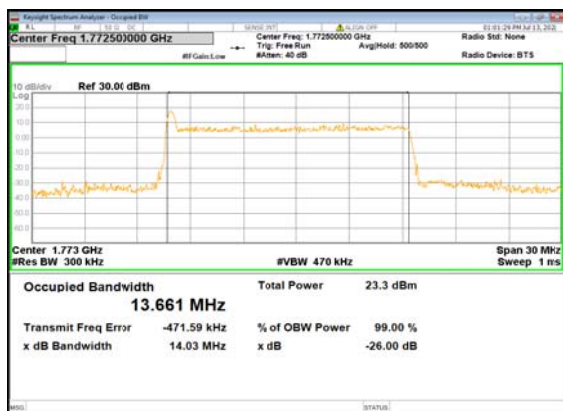
N66(15M)_DFT-s-OFDM_PI_2-BPSK_Outer_
Full_High_CH



N66(15M)_DFT-s-OFDM_QPSK_Outer_F
ull_High_CH



N66(15M)_DFT-s-OFDM_16
QAM_Outer_Full_High_CH



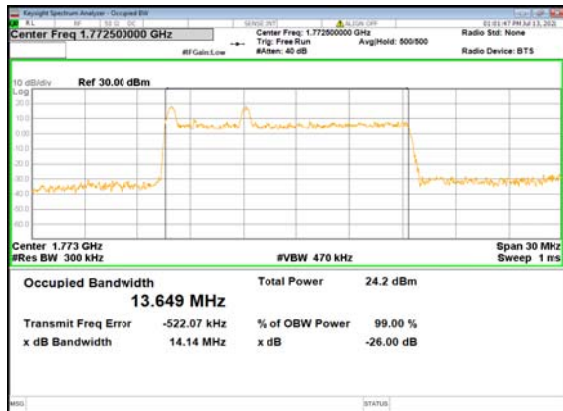
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QAM_Outer_Full_High_CH



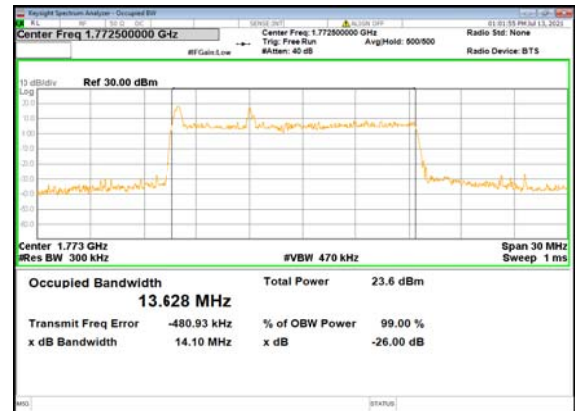


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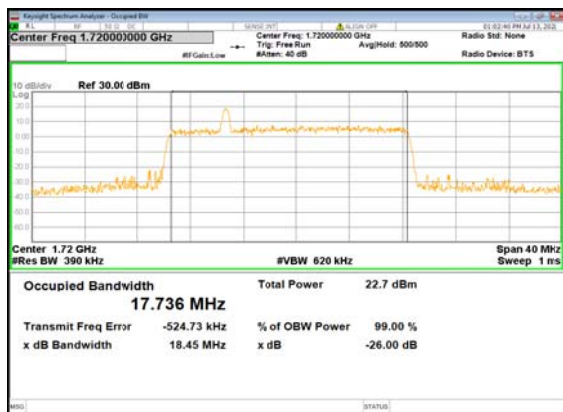
N66(15M)_DFT-s-OFDM_256 QAM_Outer_Full_High_CH



N66(15M)_CP-OFDM_QPSK_Outer_Full _High_CH



N66(20M)_DFT-s-OFDM_PI_2-BPSK_Outer_ Full_Low_CH



N66(20M)_DFT-s-OFDM_QPSK_Outer_F ull_Low_CH



N66(20M)_DFT-s-OFDM_16 QAM_Outer_Full_Low_CH



N66(20M)_DFT-s-OFDM_64 QAM_Outer_Full_Low_CH



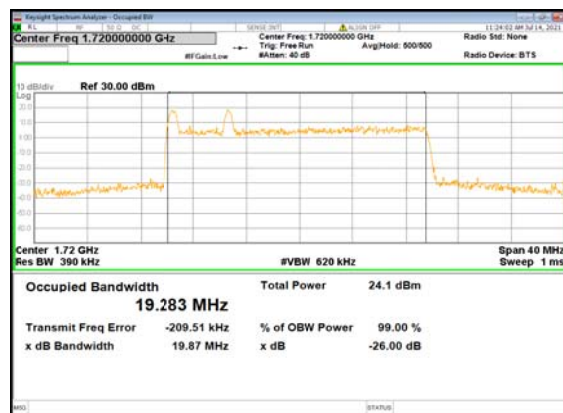


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N66(20M)_DFT-s-OFDM_256
QAM_Outer_Full_Low_CH



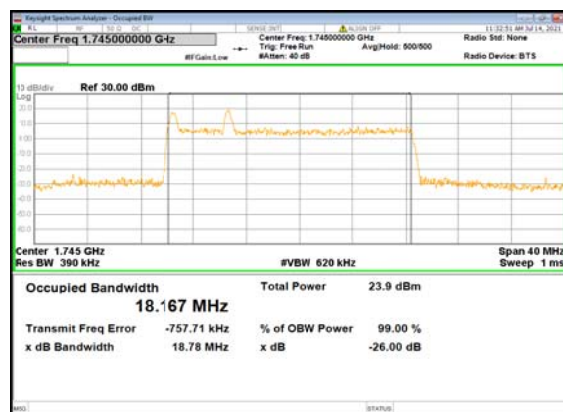
N66(20M)_CP-OFDM_QPSK_Outer_Full
_Low_CH



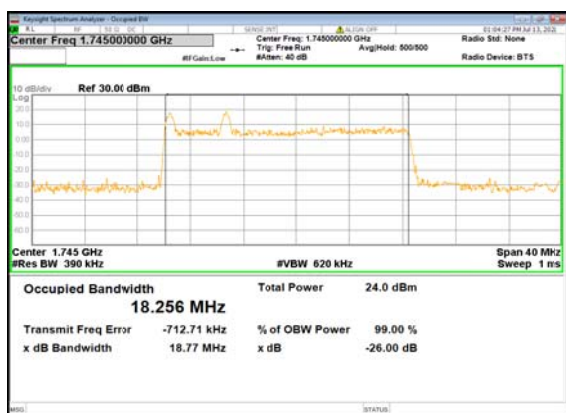
N66(20M)_DFT-s-OFDM_PI_2-BPSK_Outer_
Full_Mid_CH



N66(20M)_DFT-s-OFDM_QPSK_Outer_F
ull_Mid_CH



N66(20M)_DFT-s-OFDM_16
QAM_Outer_Full_Mid_CH



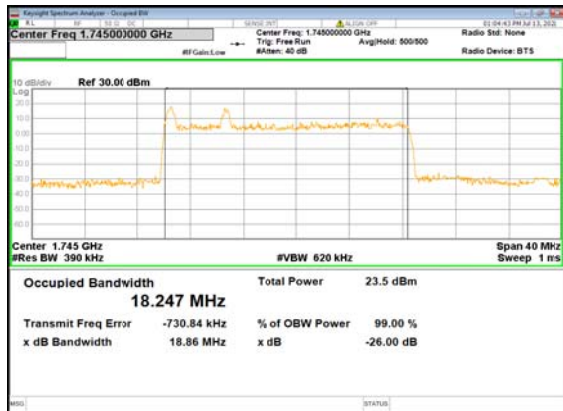
N66(20M)_DFT-s-OFDM_64
QAM_Outer_Full_Mid_CH



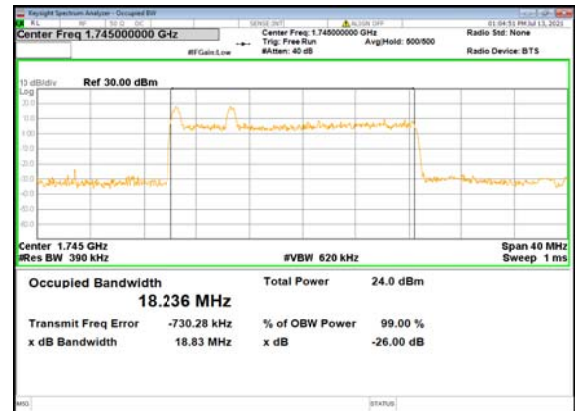


REPORT No.: SZ21030417W10

N66(20M)_DFT-s-OFDM_256
QAM_Outer_Full_Mid_CH



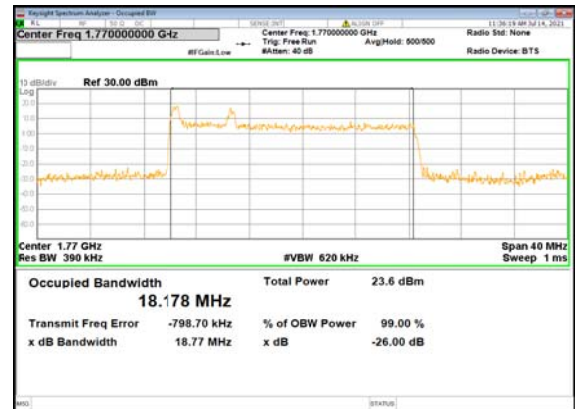
N66(20M)_CP-OFDM_QPSK_Outer_Full
_Mid_CH



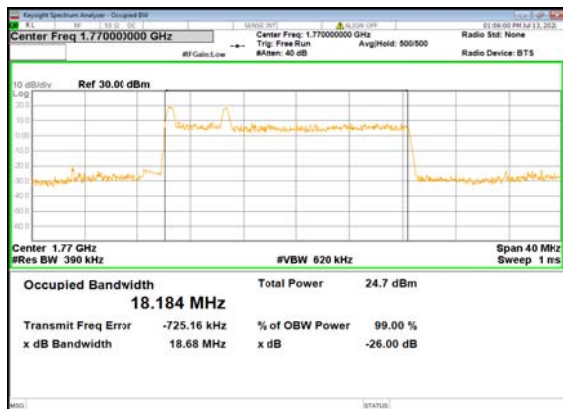
N66(20M)_DFT-s-OFDM_PI_2-BPSK_Outer_
Full_High_CH



N66(20M)_DFT-s-OFDM_QPSK_Outer_F
ull_High_CH



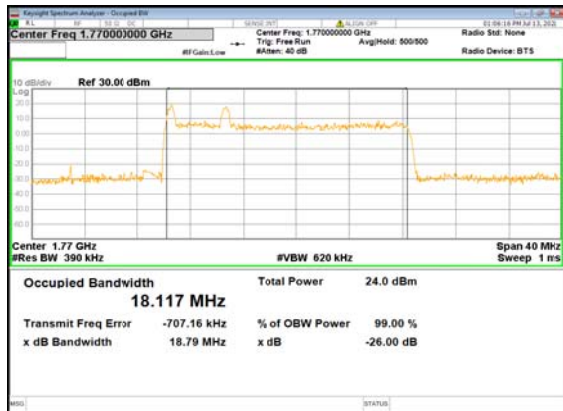
N66(20M)_DFT-s-OFDM_16
QAM_Outer_Full_High_CH



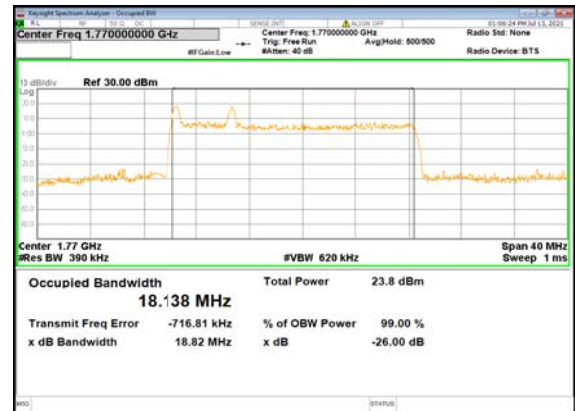
N66(20M)_DFT-s-OFDM_64
QAM_Outer_Full_High_CH



N66(20M)_DFT-s-OFDM_256 QAM_Outer_Full_High_CH



N66(20M)_CP-OFDM_QPSK_Outer_Full _High_CH



2.3. Frequency Stability

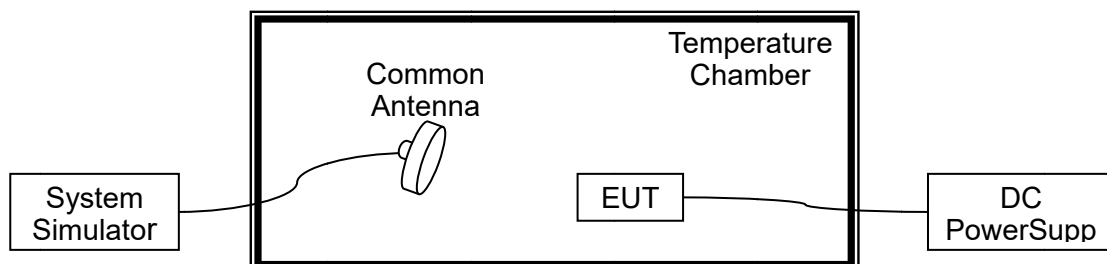
2.3.1. Requirement

According to FCC section 2.1055 & 22.355 & 27.54 & 24.235, the frequency stability shall be sufficient to ensure that the fundamental emission stays within the authorized frequency block. According to FCC section 2.1055, the test conditions are:

- The temperature is varied from -30°C to +50°C at intervals of not more than 10°C.
- For hand carried battery powered equipment, the primary supply voltage is reduced to the battery operating end point which shall be specified by the manufacture. The supply voltage shall be measured at the input to the cable normally provided with the equipment, or at the power supply terminals if cables are not normally provided.

Note: The operating temperature of EUT is from 0°C to 35°C, which are specified by the applicant.

2.3.2. Test Description





The EUT which is powered by the DC Power Supply directly, is located in the Temperature Chamber. The EUT is commanded by the System Simulator (SS) to operate at the maximum output power. A call is established between the EUT and the SS via a Common Antenna.

2.3.3. Test procedure

KDB 971168 D01v03 Section 9.0 and ANSI/TIA-603-E-2016.

2.3.4. Test Result

The nominal, highest and lowest extreme voltages are separately 3.85VDC, 4.4VDC and 3.55VDC, which are specified by the applicant; the normal temperature here used is 20°C.

NR n2, QPSK, Channel 376000, SCS 15kHz, Frequency 1880MHz Limit =±2.5ppm					
Voltage (%)	Power (VDC)	Temp (°C)	Fre. Dev. (Hz)	Deviation (ppm)	Result
100	3.85	+20 (Ref)	18	0.010	PASS
100		-10	-20	-0.011	
100		0	23	0.012	
100		+10	-14	-0.007	
100		+20	-18	-0.010	
100		+30	25	0.013	
100		+40	40	0.021	
100		+50	-15	-0.008	
100		+55	28	0.015	
115	4.4	+20	20	0.011	
85	3.55	+20	32	0.017	

NR n5, QPSK, Channel 167300, SCS 15kHz, Frequency 836.5MHz Limit =±2.5ppm					
Voltage (%)	Power (VDC)	Temp (°C)	Fre. Dev. (Hz)	Deviation (ppm)	Result
100	3.85	+20 (Ref)	19	0.023	PASS
100		-10	37	0.044	
100		0	-21	-0.025	
100		+10	-26	-0.031	
100		+20	14	0.017	



100		+30	44	0.053	
100		+40	31	0.037	
100		+50	24	0.029	
100		+55	-22	-0.026	
115	4.4	+20	-20	-0.024	
85	3.55	+20	46	0.055	

NR n66, QPSK, Channel 349000, SCS 15kHz, Frequency 1745MHz Limit =±2.5ppm					
Voltage (%)	Power (VDC)	Temp (°C)	Fre. Dev. (Hz)	Deviation (ppm)	Result
100	3.85	+20 (Ref)	-14	-0.008	PASS
100		-10	26	0.015	
100		0	-18	-0.010	
100		+10	48	0.028	
100		+20	-21	-0.012	
100		+30	34	0.019	
100		+40	24	0.014	
100		+50	29	0.017	
100		+55	35	0.020	
115	4.4	+20	40	0.023	
85	3.55	+20	25	0.014	

2.4. Peak to Average Ratio

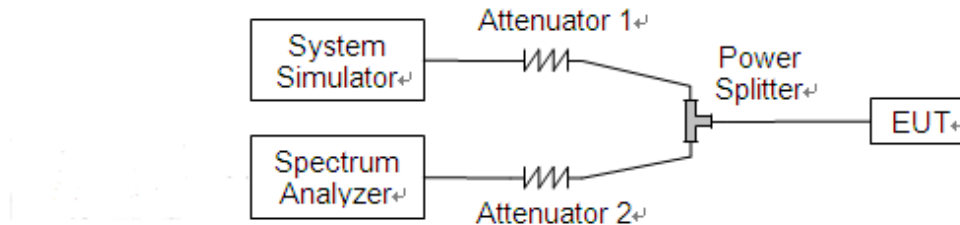
2.4.1. Requirement

According to FCC section 24.232(d), the peak to average ratio (PAR) of the transmission may not exceed 13dB.

According to FCC section 27.50(d)(5), the peak to average ratio (PAR) of the transmission may not exceed 13dB.

2.4.2. Test Description

Test Set:



The EUT is coupled to the Spectrum Analyzer (SA) and the System Simulator (SS) with Attenuators through the Power Splitter; the RF load attached to the EUT antenna terminal is 50Ohm; the path loss as the factor is calibrated to correct the reading. The EUT is commanded by the SS to operate at the maximum output power. A call is established between the EUT and the SS.

2.4.3. Test procedure

KDB 971168 D01v03 Section 5.7 and ANSI/TIA-603-E-2016.

2.4.4. Test Result

Record the maximum PAPR level associated with a probability of 0.1%.

N2					
BW(MHz)	Channel Level	Modulation	Peak to Average Radio(dB)	Limit (dB)	Verdict
5	Low	BPSK	4.73	<=13	PASS
5	Low	QPSK	5.61	<=13	PASS
5	Mid	BPSK	4.59	<=13	PASS

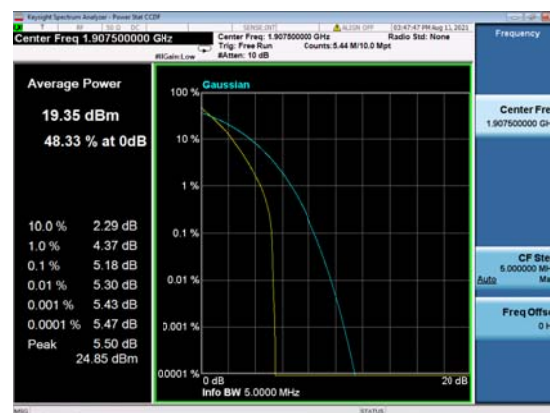
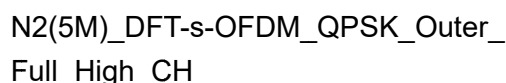
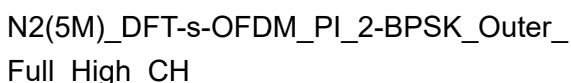
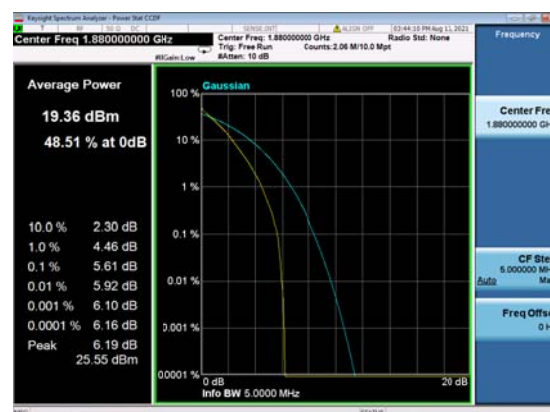
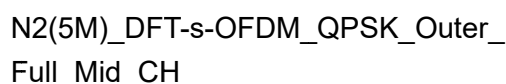
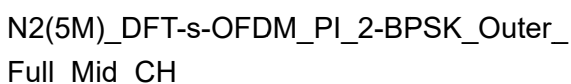
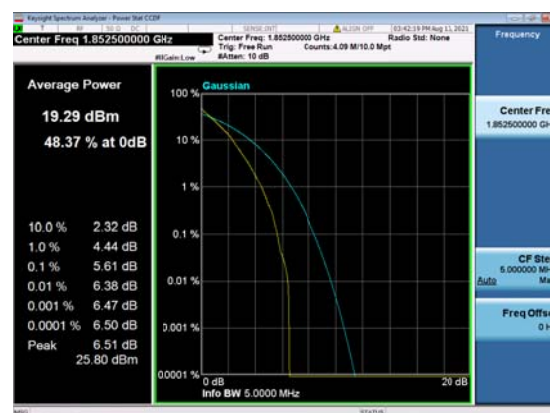
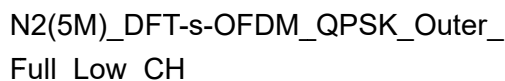
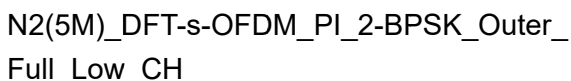


5	Mid	QPSK	5.61	≤ 13	PASS
5	High	BPSK	4.68	≤ 13	PASS
5	High	QPSK	5.18	≤ 13	PASS
10	Low	BPSK	4.61	≤ 13	PASS
10	Low	QPSK	5.82	≤ 13	PASS
10	Mid	BPSK	4.63	≤ 13	PASS
10	Mid	QPSK	5.37	≤ 13	PASS
10	High	BPSK	4.57	≤ 13	PASS
10	High	QPSK	4.68	≤ 13	PASS
15	Low	BPSK	4.16	≤ 13	PASS
15	Low	QPSK	4.64	≤ 13	PASS
15	Mid	BPSK	4.82	≤ 13	PASS
15	Mid	QPSK	5.38	≤ 13	PASS
15	High	BPSK	4.35	≤ 13	PASS
15	High	QPSK	4.68	≤ 13	PASS
20	Low	BPSK	4.78	≤ 13	PASS
20	Low	QPSK	5.25	≤ 13	PASS
20	Mid	BPSK	4.71	≤ 13	PASS
20	Mid	QPSK	5.24	≤ 13	PASS
20	High	BPSK	5.21	≤ 13	PASS
20	High	QPSK	5.36	≤ 13	PASS

N66					
BW(MHz)	Channel Level	Modulation	Peak to Average Radio(dB)	Limit (dB)	Verdict
5	Low	BPSK	4.56	≤ 13	PASS
5	Low	QPSK	5.48	≤ 13	PASS
5	Mid	BPSK	4.64	≤ 13	PASS
5	Mid	QPSK	5.62	≤ 13	PASS
5	High	BPSK	4.62	≤ 13	PASS



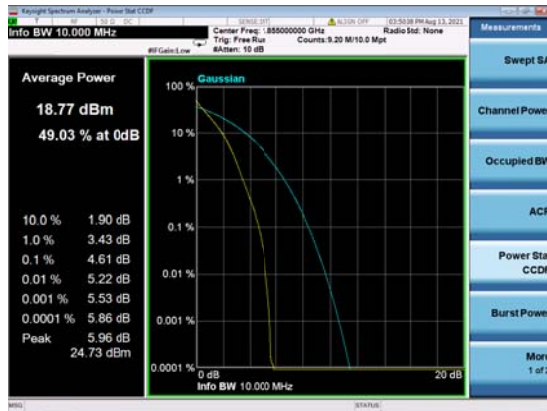
5	High	QPSK	5.56	≤ 13	PASS
10	Low	BPSK	4.66	≤ 13	PASS
10	Low	QPSK	5.56	≤ 13	PASS
10	Mid	BPSK	4.72	≤ 13	PASS
10	Mid	QPSK	5.81	≤ 13	PASS
10	High	BPSK	4.59	≤ 13	PASS
10	High	QPSK	5.59	≤ 13	PASS
15	Low	BPSK	4.76	≤ 13	PASS
15	Low	QPSK	5.61	≤ 13	PASS
15	Mid	BPSK	4.79	≤ 13	PASS
15	Mid	QPSK	5.64	≤ 13	PASS
15	High	BPSK	4.79	≤ 13	PASS
15	High	QPSK	5.63	≤ 13	PASS
20	Low	BPSK	4.67	≤ 13	PASS
20	Low	QPSK	5.46	≤ 13	PASS
20	Mid	BPSK	4.83	≤ 13	PASS
20	Mid	QPSK	5.67	≤ 13	PASS
20	High	BPSK	4.82	≤ 13	PASS
20	High	QPSK	5.51	≤ 13	PASS



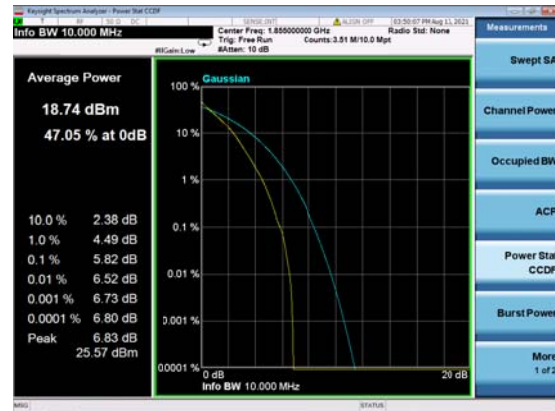


REPORT No.: SZ21030417W10

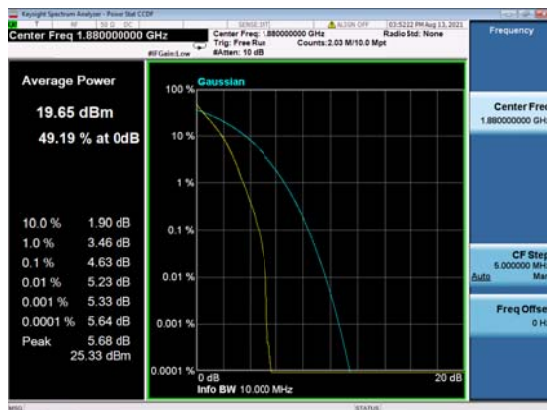
N2(10M)_DFT-s-OFDM_PI_2-BPSK_Outer_
Full_Low_CH



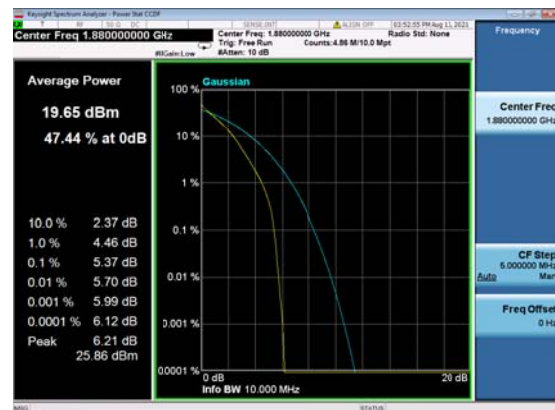
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Full_Low_CH



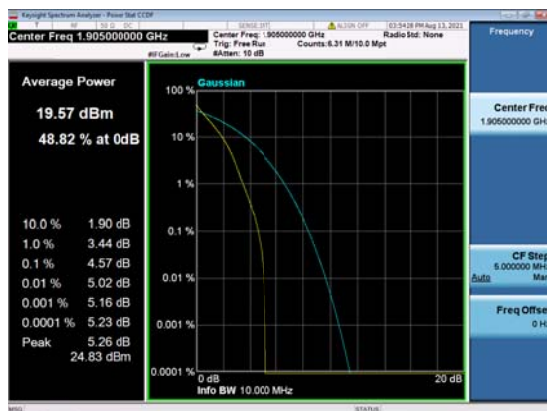
N2(10M)_DFT-s-OFDM_PI_2-BPSK_Outer_
Full_Mid_CH



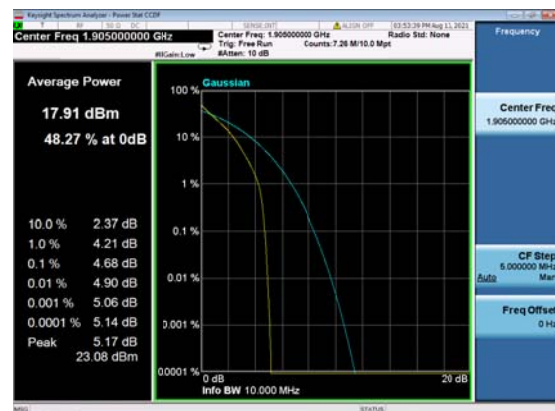
N2(10M)_DFT-s-OFDM_QPSK_Outer_
Full_Mid_CH



N2(10M)_DFT-s-OFDM_PI_2-BPSK_Outer_
Full_High_CH



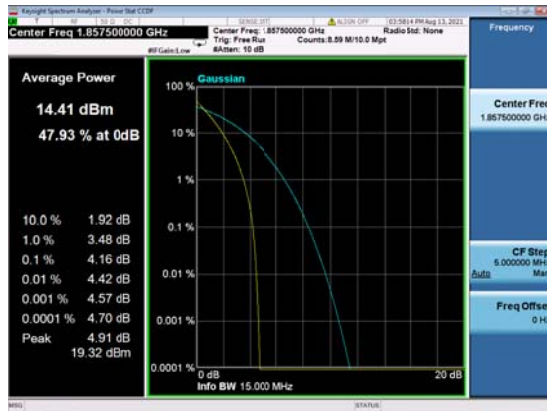
N2(10M)_DFT-s-OFDM_QPSK_Outer_
Full_High_CH



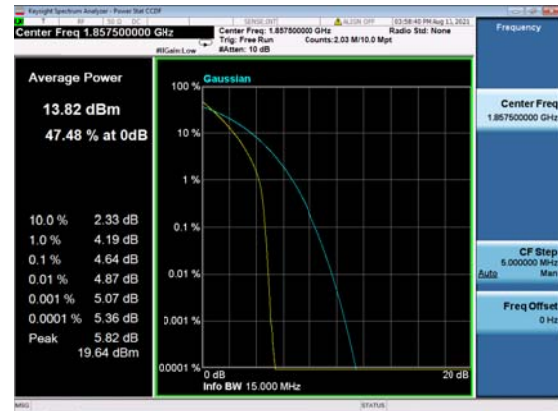


REPORT No.: SZ21030417W10

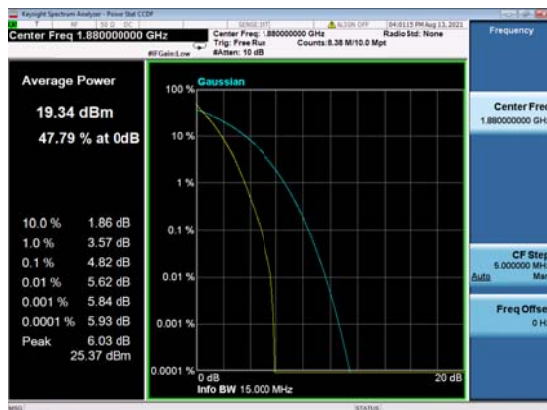
N2(15M)_DFT-s-OFDM_PI_2-BPSK_Outer_
Full_Low_CH



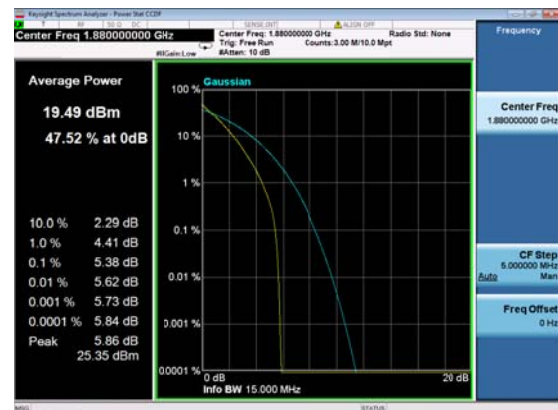
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Full_Low_CH



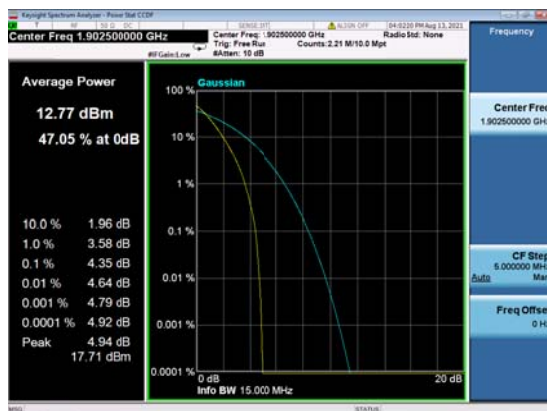
N2(15M)_DFT-s-OFDM_PI_2-BPSK_Outer_
Full_Mid_CH



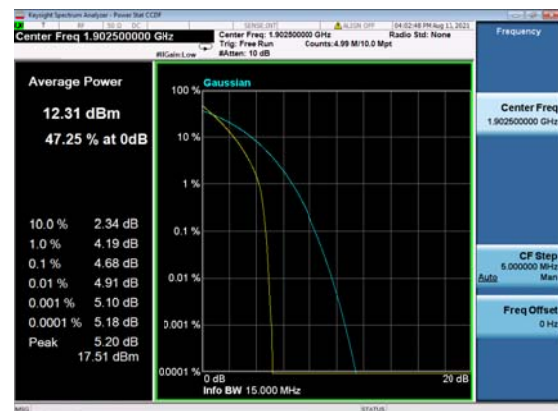
N2(15M)_DFT-s-OFDM_QPSK_Outer_
Full_Mid_CH



N2(15M)_DFT-s-OFDM_PI_2-BPSK_Outer_
Full_High_CH



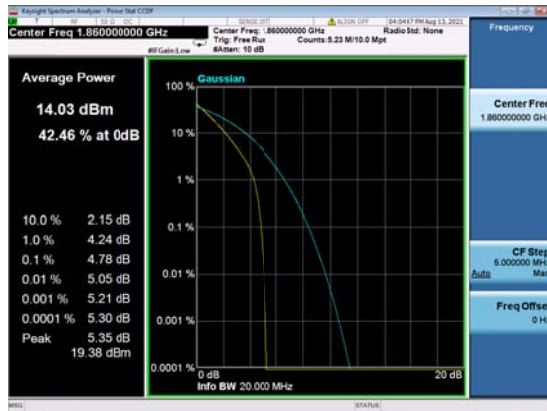
N2(15M)_DFT-s-OFDM_QPSK_Outer_
Full_High_CH



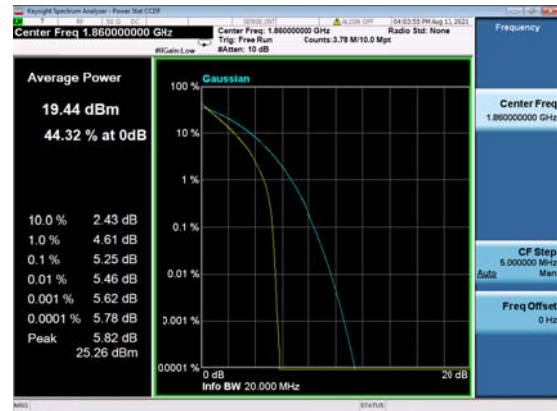


REPORT No.: SZ21030417W10

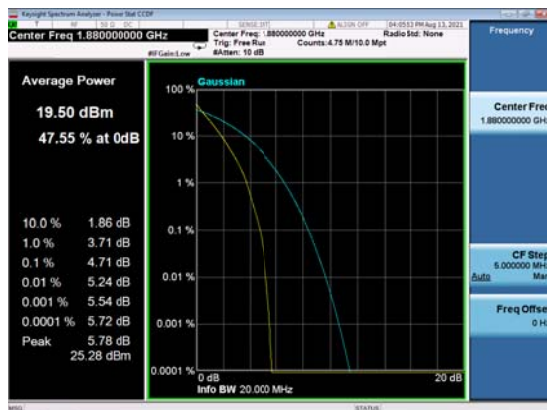
N2(20M)_DFT-s-OFDM_PI_2-BPSK_Outer_
Full_Low_CH



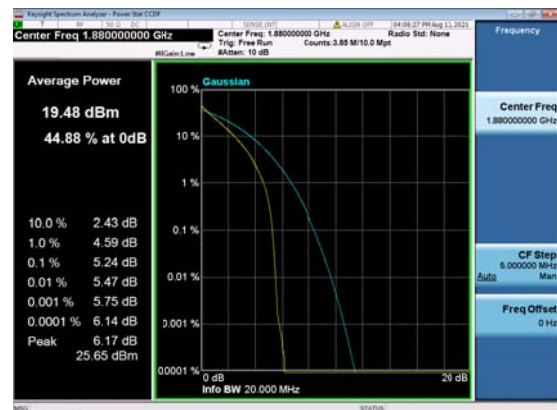
N2(20M)_DFT-s-OFDM_QPSK_Outer_
Full_Low_CH



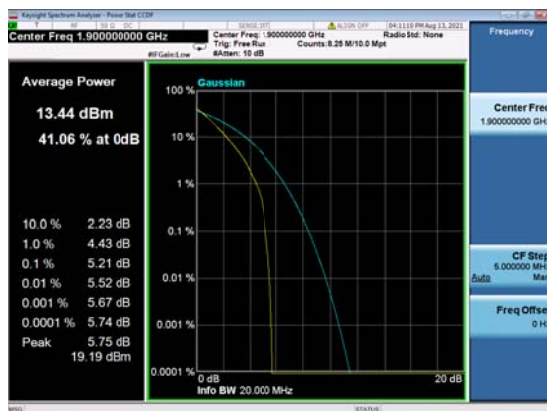
N2(20M)_DFT-s-OFDM_PI_2-BPSK_Outer_
Full_Mid_CH



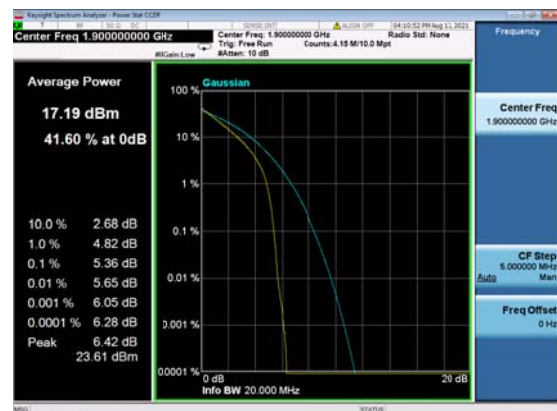
N2(20M)_DFT-s-OFDM_QPSK_Outer_
Full_Mid_CH



N2(20M)_DFT-s-OFDM_PI_2-BPSK_Outer_
Full_High_CH



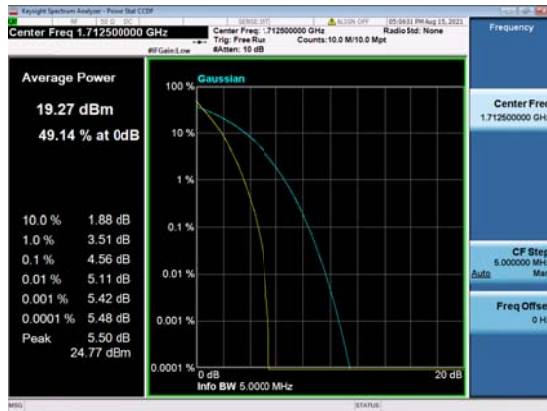
N2(20M)_DFT-s-OFDM_QPSK_Outer_
Full_High_CH



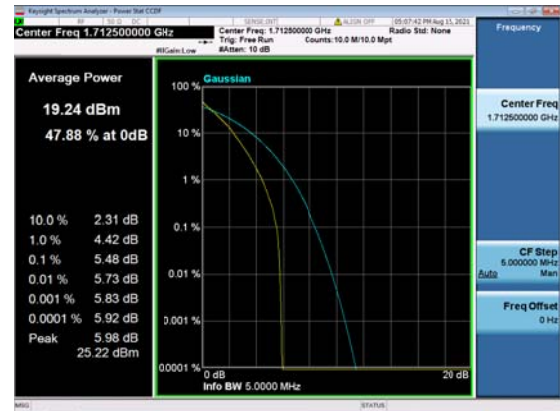


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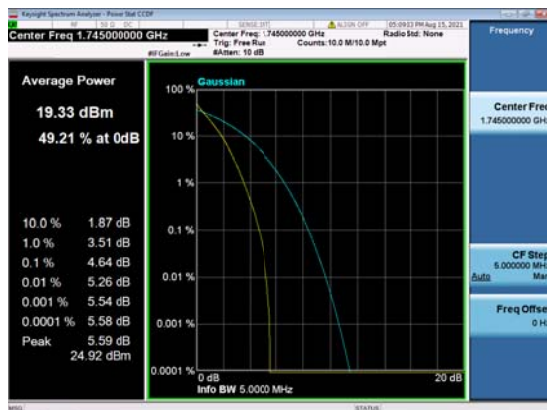
N66(5M)_DFT-s-OFDM_PI_2-BPSK_Outer_
Full_Low_CH



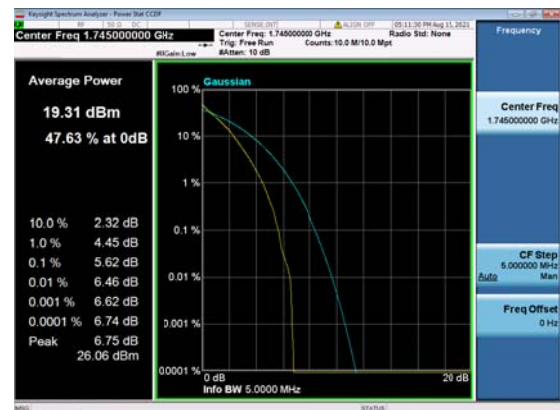
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Full_Low_CH



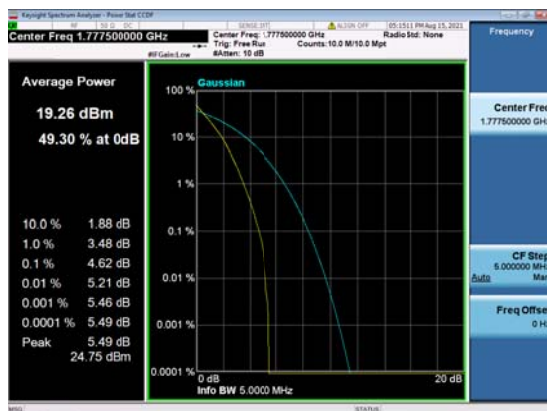
N66(5M)_DFT-s-OFDM_PI_2-BPSK_Outer_
Full_Mid_CH



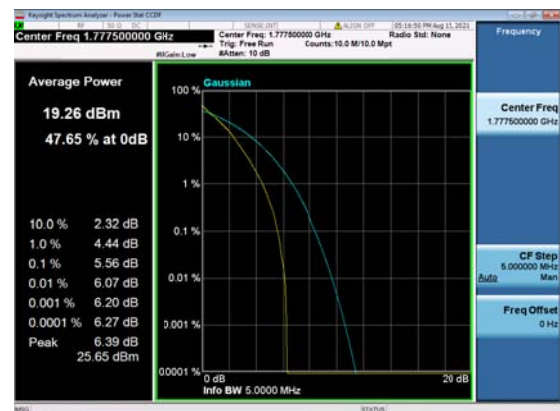
N66(5M)_DFT-s-OFDM_QPSK_Outer_
Full_Mid_CH

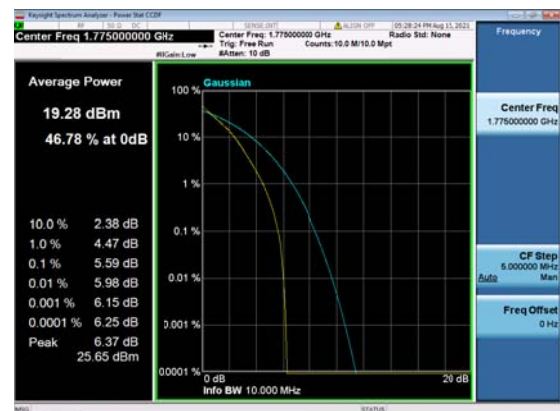
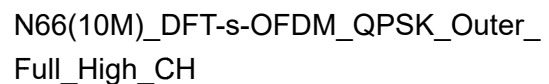
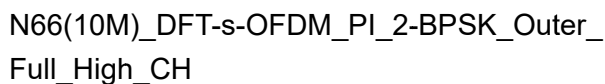
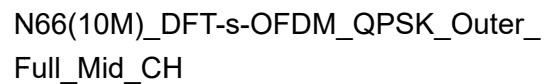
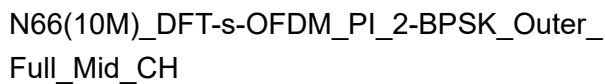
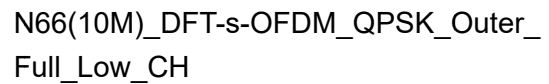
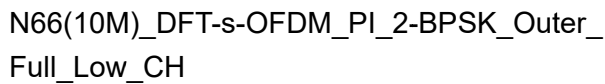


N66(5M)_DFT-s-OFDM_PI_2-BPSK_Outer_
Full_High_CH



N66(5M)_DFT-s-OFDM_QPSK_Outer_
Full_High_CH

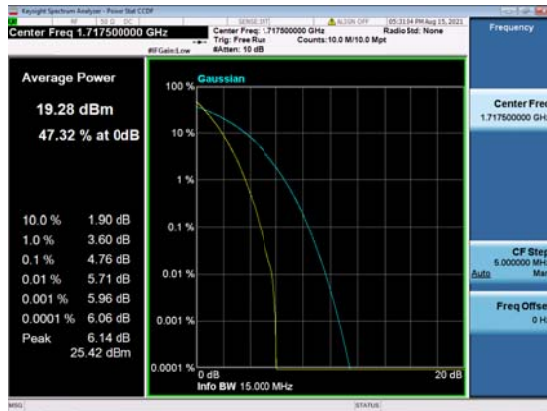




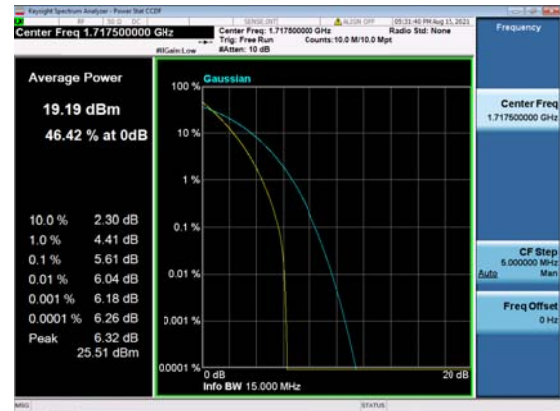


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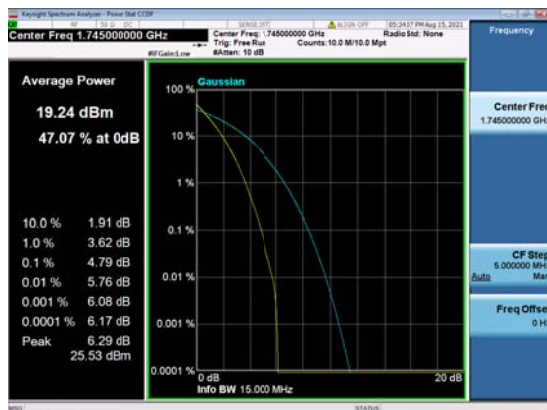
N66(15M)_DFT-s-OFDM_PI_2-BPSK_Outer_
Full_Low_CH



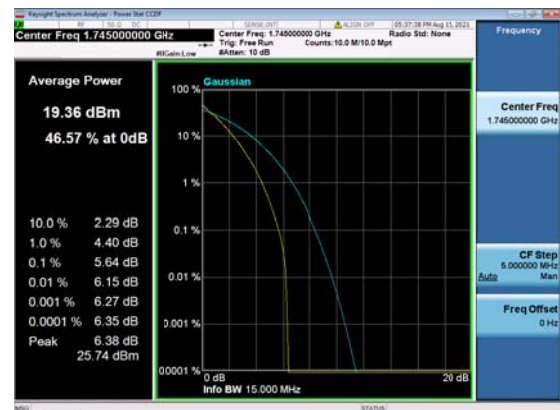
N66(15M)_DFT-s-OFDM_QPSK_Outer_
Full_Low_CH



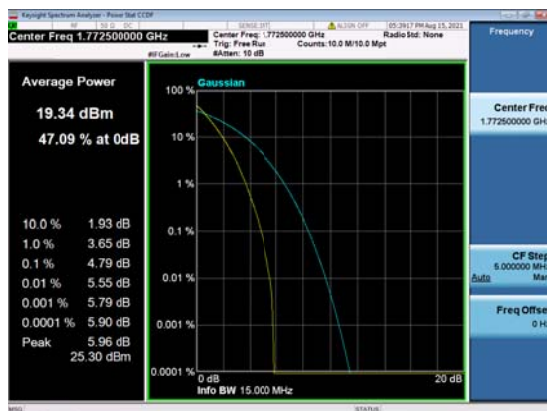
N66(15M)_DFT-s-OFDM_PI_2-BPSK_Outer_
Full_Mid_CH



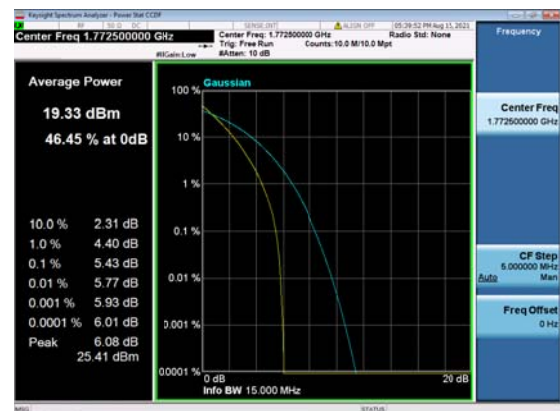
N66(15M)_DFT-s-OFDM_QPSK_Outer_
Full_Mid_CH



N66(15M)_DFT-s-OFDM_PI_2-BPSK_Outer_
Full_High_CH



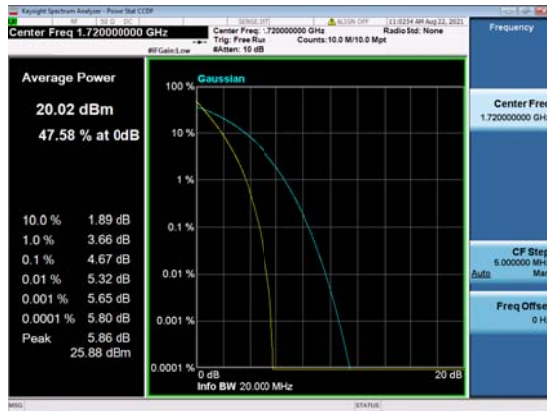
N66(15M)_DFT-s-OFDM_QPSK_Outer_
Full_High_CH



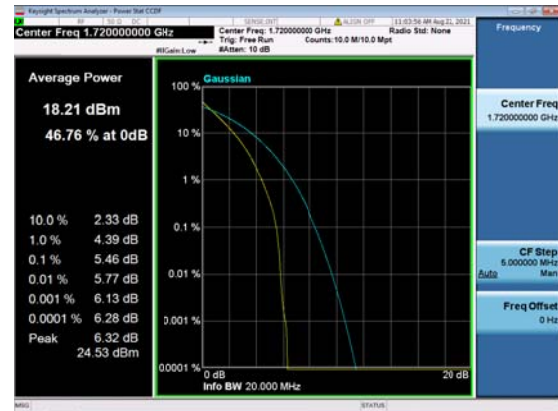


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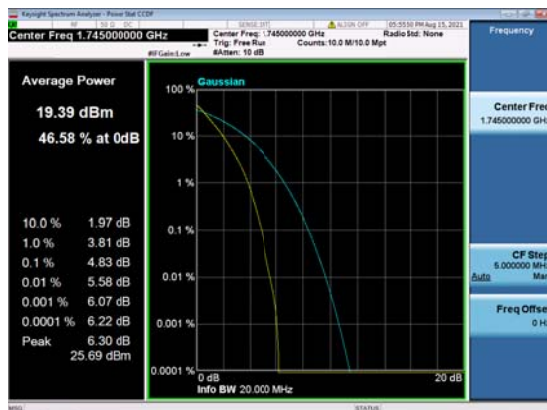
N66(20M)_DFT-s-OFDM_PI_2-BPSK_Outer_
Full_Low_CH



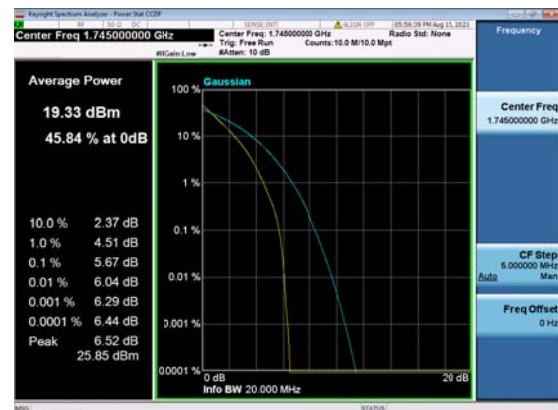
N66(20M)_DFT-s-OFDM_QPSK_Outer_
Full_Low_CH



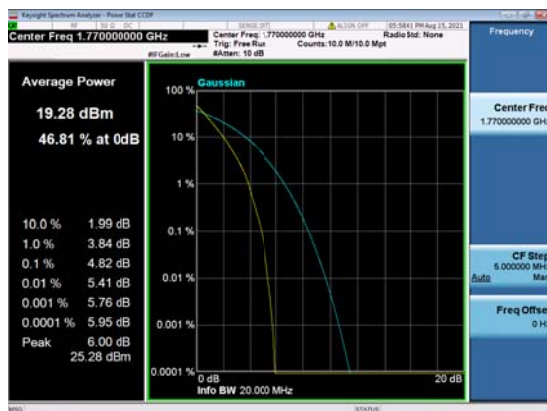
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Full_Mid_CH



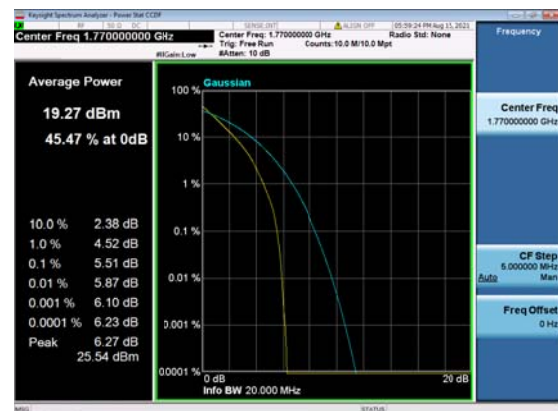
N66(20M)_DFT-s-OFDM_QPSK_Outer_
Full_Mid_CH



N66(20M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full_
_High_CH



N66(20M)_DFT-s-OFDM_QPSK_Outer_Full_
High_CH

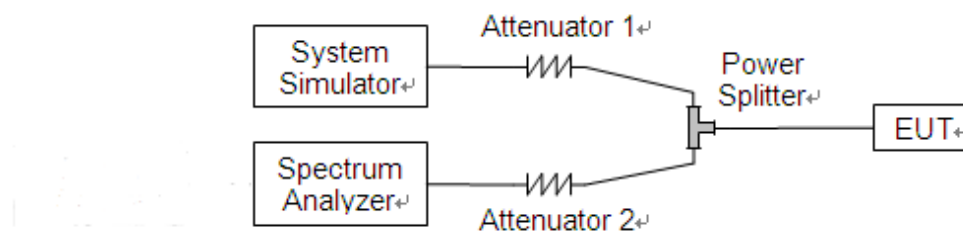


2.5. Conducted Spurious Emissions

2.5.1. Requirement

According to FCC section 2.1051, the power of any emission outside of the authorized operating frequency ranges must be attenuated below the transmitting power (P) by a factor of at least $43+10*\log(P)$ dB. This calculated to be -13dBm.

2.5.2. Test Description



The EUT is coupled to the Spectrum Analyzer (SA) and the System Simulator (SS) with Attenuators through the Power Splitter; the RF load attached to the EUT antenna terminal is 50Ohm; the path loss as the factor is calibrated to correct the reading. The EUT is commanded by the SS to operate at the maximum output power. A call is established between the EUT and the SS.



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2.5.3. Test procedure

KDB 971168 D01v03 Section 6.0 and ANSI/TIA-603-E-2016.

2.5.4. Test Result

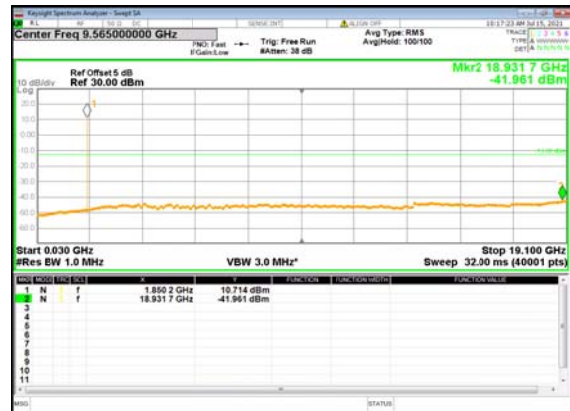


REPORT No.: SZ21030417W10

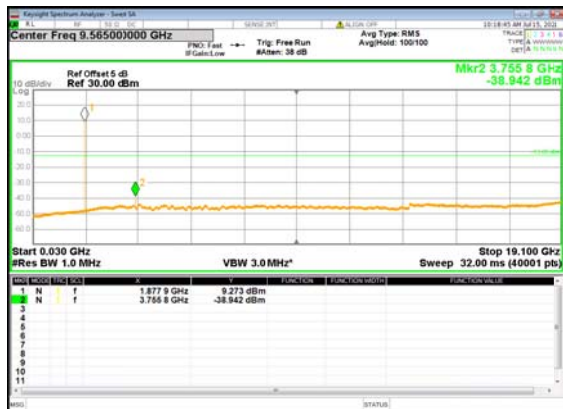
N2(5M)_DFT-s-OFDM_BPSK_Edge_1RB_
Left_Low_CH



N2(5M)_DFT-s-OFDM_QPSK_Edge_1RB_
Left_Low_CH



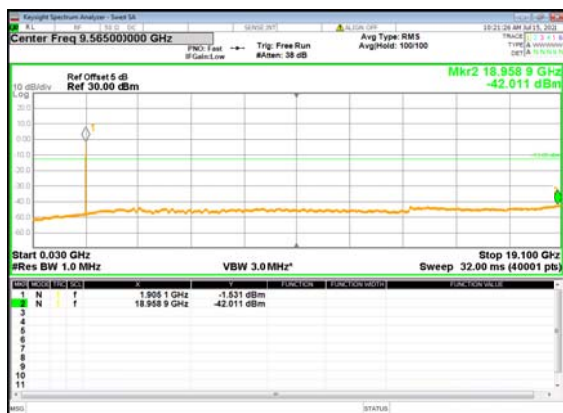
N2(5M)_DFT-s-OFDM_BPSK_Edge_1RB_
Left_Mid_CH



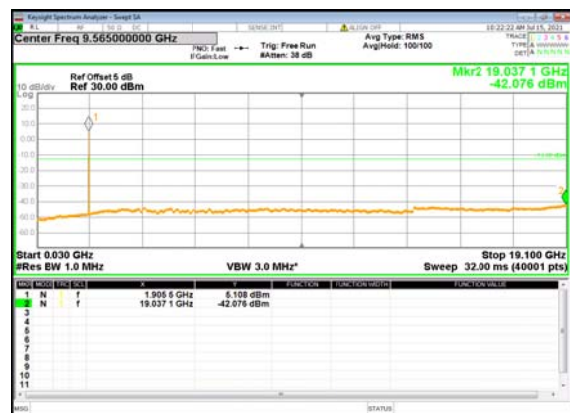
N2(5M)_DFT-s-OFDM_QPSK_Edge_1RB_
Left_Mid_CH



N2(5M)_DFT-s-OFDM_BPSK_Edge_1RB_
Left_High_CH



N2(5M)_DFT-s-OFDM_QPSK_Edge_1RB_
Left_High_CH



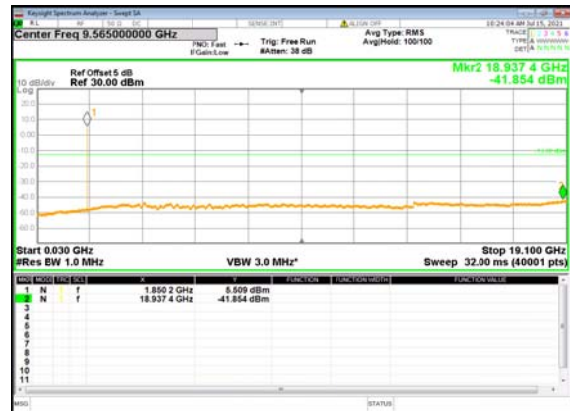


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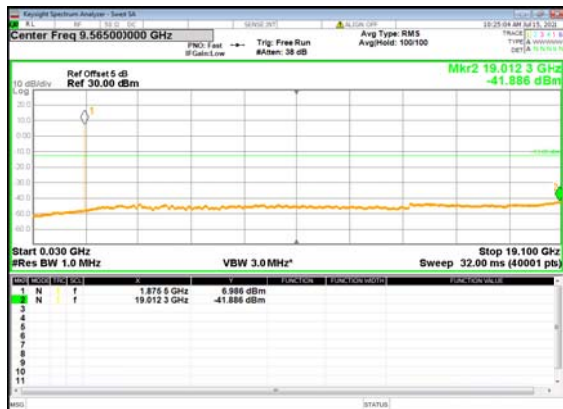
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Left_Low_CH



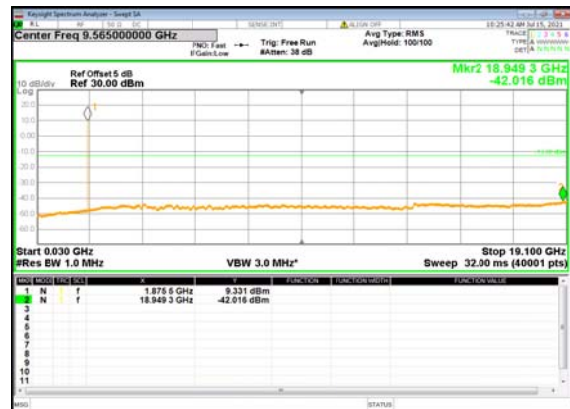
N2(10M)_DFT-s-OFDM_QPSK_Edge_1RB_
Left_Low_CH



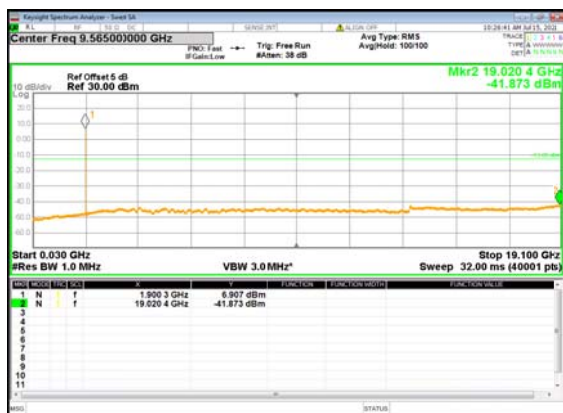
N2(10M)_DFT-s-OFDM_BPSK_Edge_1RB_
Left_Mid_CH



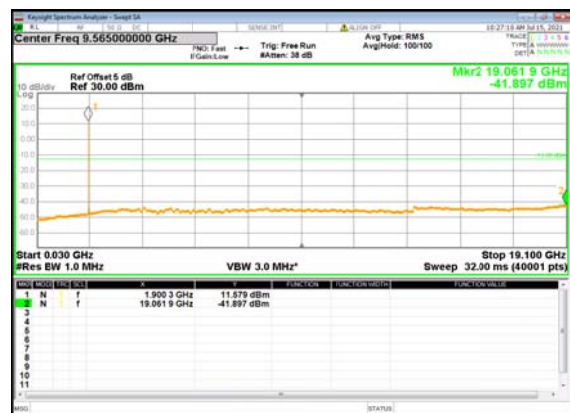
N2(10M)_DFT-s-OFDM_QPSK_Edge_1RB_
Left_Mid_CH



N2(10M)_DFT-s-OFDM_BPSK_Edge_1RB_
Left_High_CH



N2(10M)_DFT-s-OFDM_QPSK_Edge_1RB_
Left_High_CH



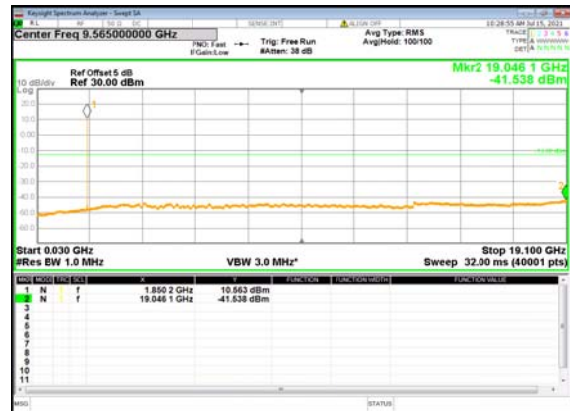


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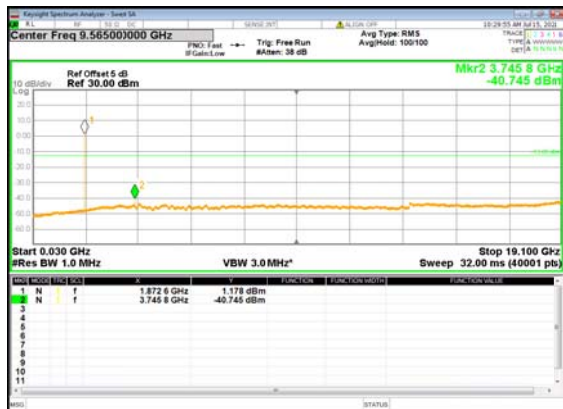
N2(15M)_DFT-s-OFDM_BPSK_Edge_1RB_
Left_Low_CH



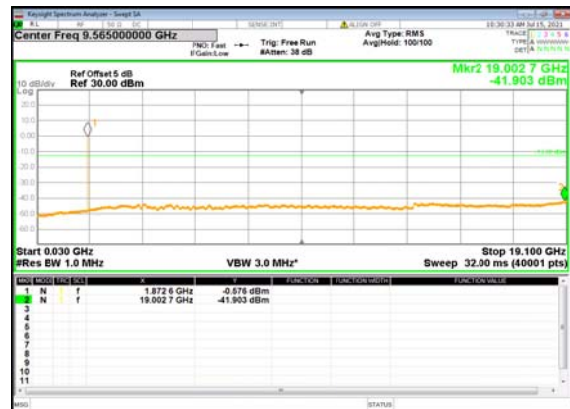
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Left_Low_CH



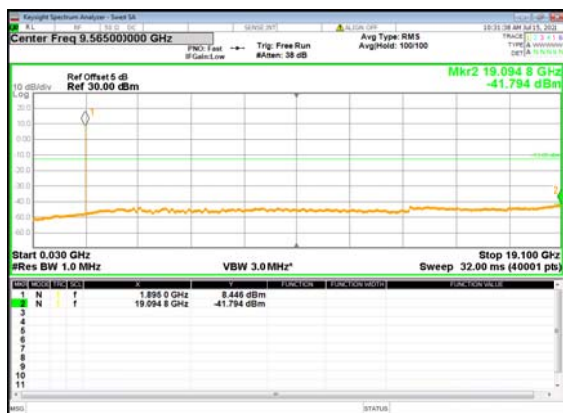
N2(15M)_DFT-s-OFDM_BPSK_Edge_1RB_
Left_Mid_CH



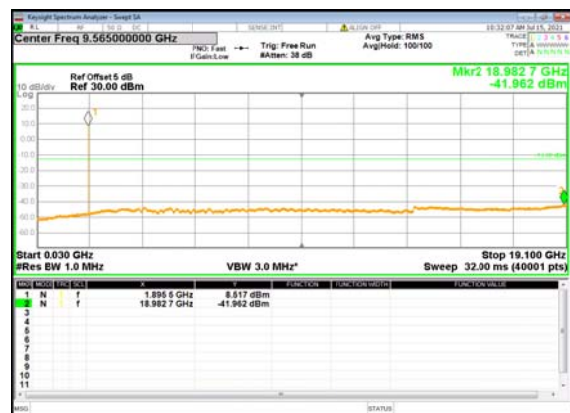
N2(15M)_DFT-s-OFDM_QPSK_Edge_1RB_
Left_Mid_CH



N2(15M)_DFT-s-OFDM_BPSK_Edge_1RB_
Left_High_CH



N2(15M)_DFT-s-OFDM_QPSK_Edge_1RB_
Left_High_CH



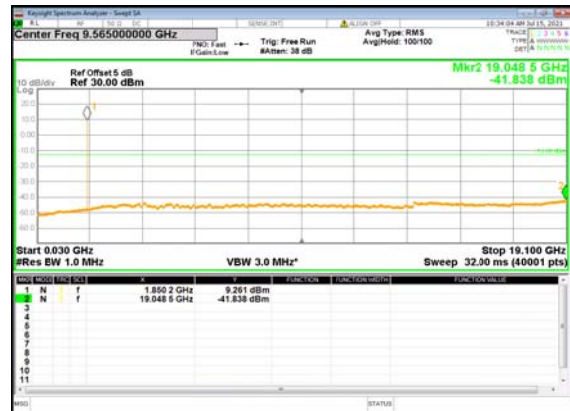


REPORT No.: SZ21030417W10

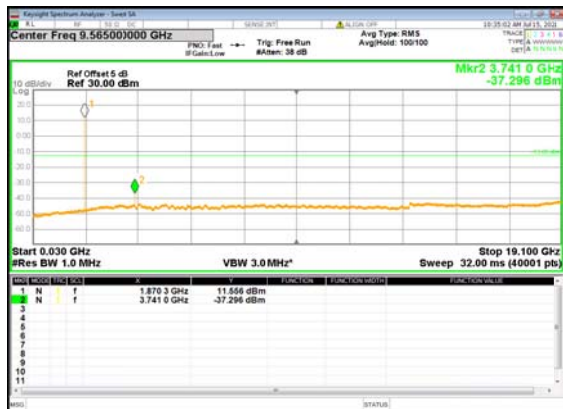
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Left_Low_CH



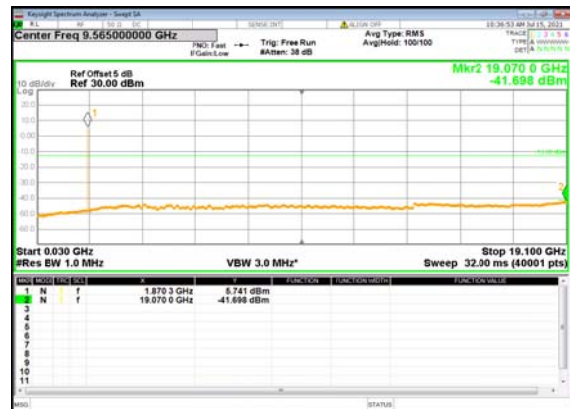
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Left_Low_CH



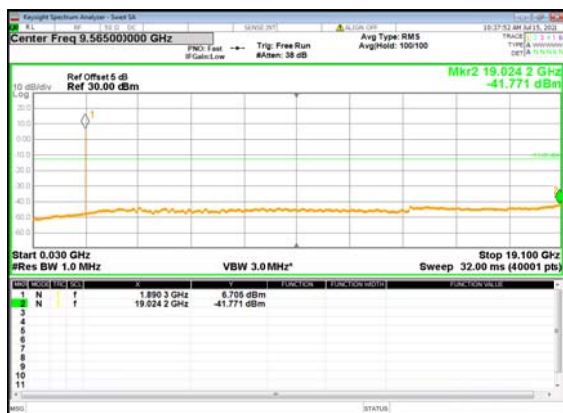
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Left_Mid_CH



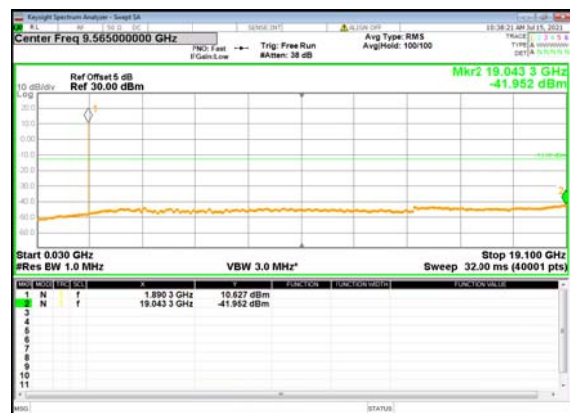
N2(20M)_DFT-s-OFDM_QPSK_Edge_1RB_
Left_Mid_CH



N2(20M)_DFT-s-OFDM_BPSK_Edge_1RB_L
eft_High_CH



N2(20M)_DFT-s-OFDM_QPSK_Edge_1RB_L
eft_High_CH





N5(5M)_DFT-s-OFDM_BPSK_Edge_1RB_
Left_Low_CH



N5(5M)_DFT-s-OFDM_QPSK_Edge_1RB_
Left_Low_CH



N5(5M)_DFT-s-OFDM_BPSK_Edge_1RB_
Left_Mid_CH



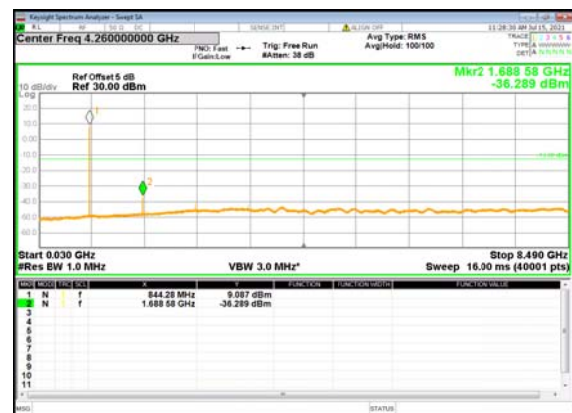
N5(5M)_DFT-s-OFDM_QPSK_Edge_1RB_
Left_Mid_CH

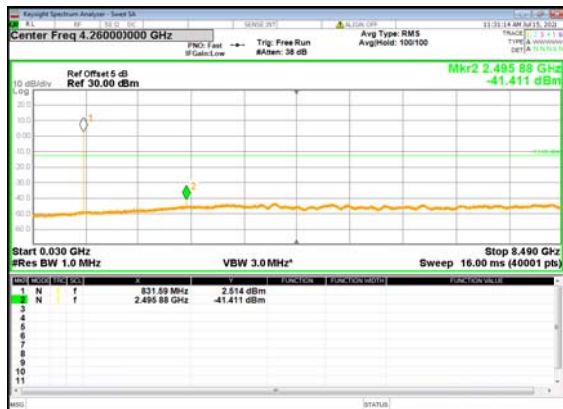
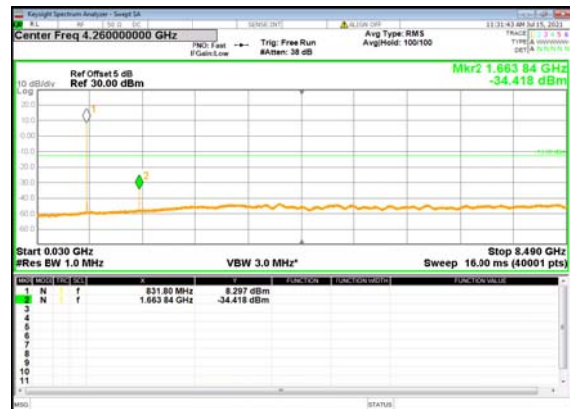


N5(5M)_DFT-s-OFDM_BPSK_Edge_1RB_
Left_High_CH



N5(5M)_DFT-s-OFDM_QPSK_Edge_1RB_
Left_High_CH



N5(10M)_DFT-s-OFDM_BPSK_Edge_1RB_
Left_Low_CHN5(10M)_DFT-s-OFDM_QPSK_Edge_1RB_
Left_Low_CHN5(10M)_DFT-s-OFDM_BPSK_Edge_1RB_
Left_Mid_CHN5(10M)_DFT-s-OFDM_QPSK_Edge_1RB_
Left_Mid_CHN5(10M)_DFT-s-OFDM_BPSK_Edge_1RB_
Left_High_CHN5(10M)_DFT-s-OFDM_QPSK_Edge_1RB_
Left_High_CH



N5(15M)_DFT-s-OFDM_BPSK_Edge_1RB_
Left_Low_CH



N5(15M)_DFT-s-OFDM_QPSK_Edge_1RB_
Left_Low_CH



N5(15M)_DFT-s-OFDM_BPSK_Edge_1RB_
Left_Mid_CH



N5(15M)_DFT-s-OFDM_QPSK_Edge_1RB_
Left_Mid_CH



N5(15M)_DFT-s-OFDM_BPSK_Edge_1RB_
Left_High_CH



N5(15M)_DFT-s-OFDM_QPSK_Edge_1RB_
Left_High_CH





REPORT No.: SZ21030417W10

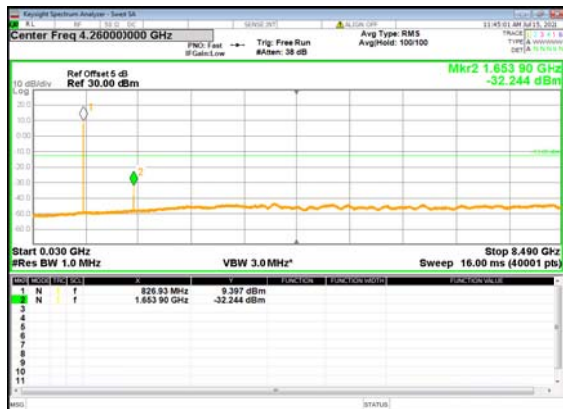
N5(20M)_DFT-s-OFDM_BPSK_Edge_1RB_
Left_Low_CH



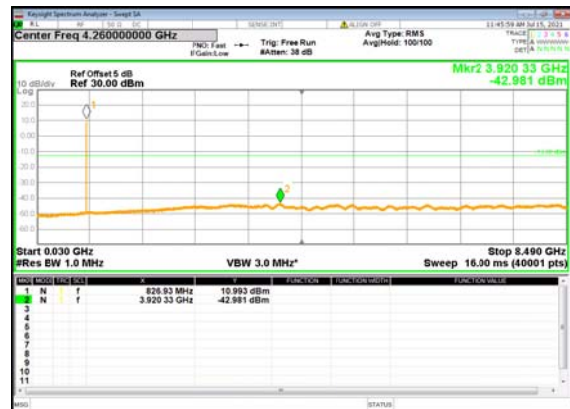
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Left_Low_CH



N5(20M)_DFT-s-OFDM_BPSK_Edge_1RB_
Left_Mid_CH



N5(20M)_DFT-s-OFDM_QPSK_Edge_1RB_
Left_Mid_CH



N5(20M)_DFT-s-OFDM_BPSK_Edge_1RB_
Left_High_CH



N5(20M)_DFT-s-OFDM_QPSK_Edge_1RB_
Left_High_CH





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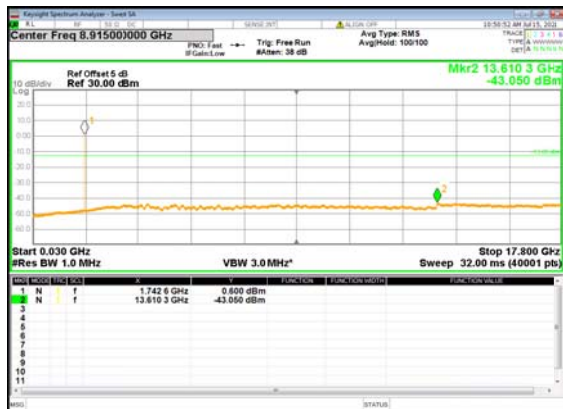
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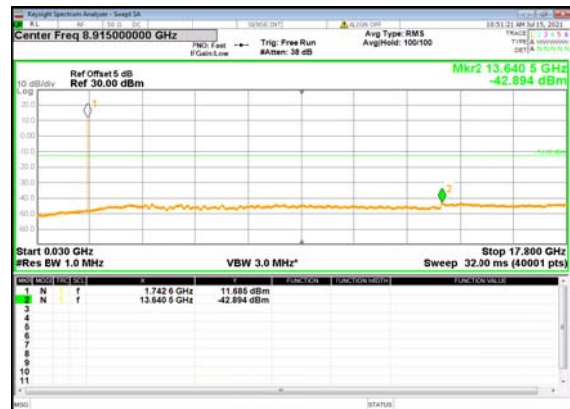
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Left_Low_CH



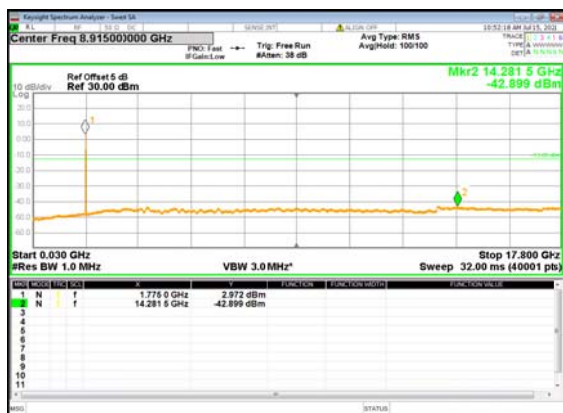
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Left_Mid_CH



N66(5M)_DFT-s-OFDM_QPSK_Edge_1RB_
Left_Mid_CH



N66(5M)_DFT-s-OFDM_BPSK_Edge_1RB_
Left_High_CH



N66(5M)_DFT-s-OFDM_QPSK_Edge_1RB_
Left_High_CH



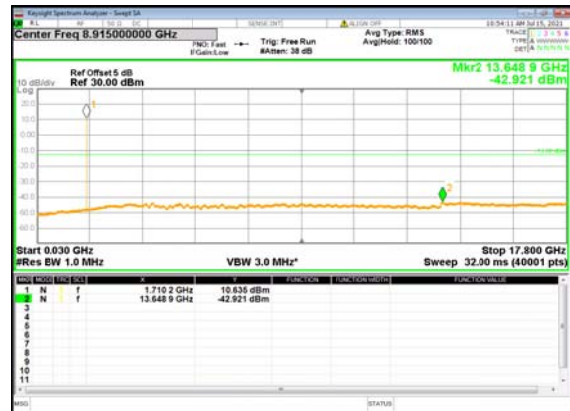


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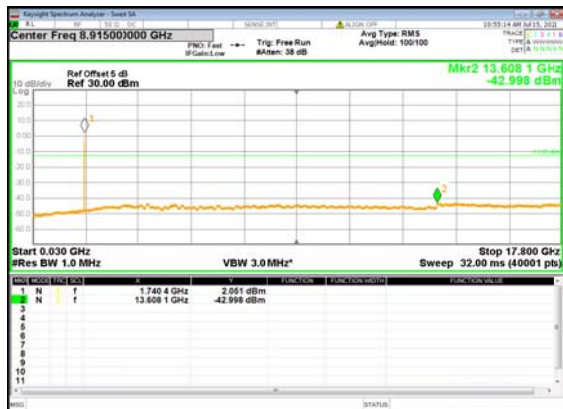
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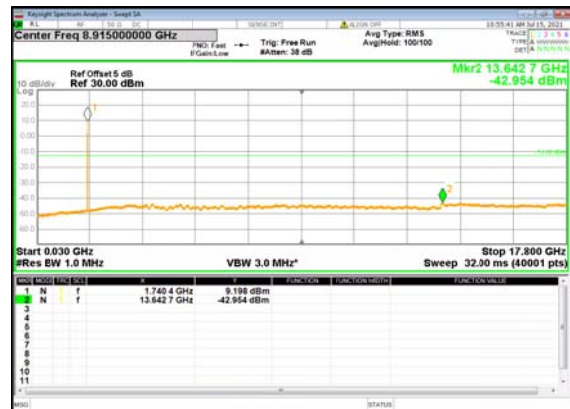
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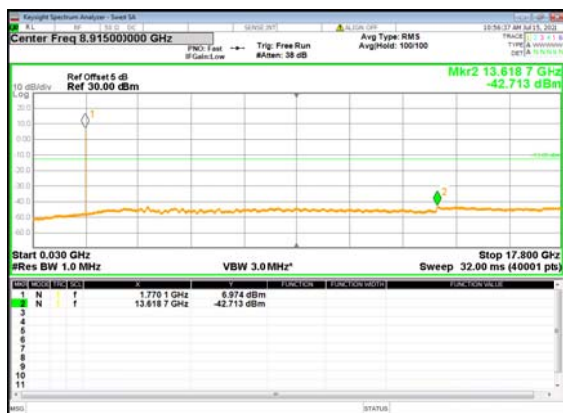
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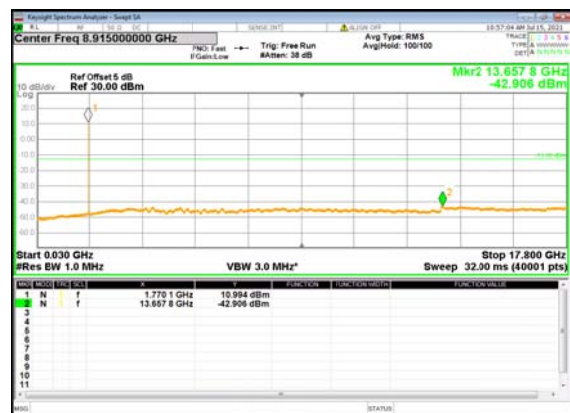
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Left_Mid_CH



N66(10M)_DFT-s-OFDM_BPSK_Edge_1RB_
Left_High_CH



N66(10M)_DFT-s-OFDM_QPSK_Edge_1RB_
Left_High_CH





REPORT No.: SZ21030417W10

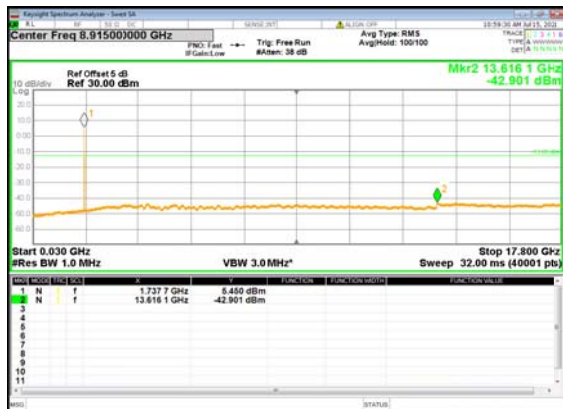
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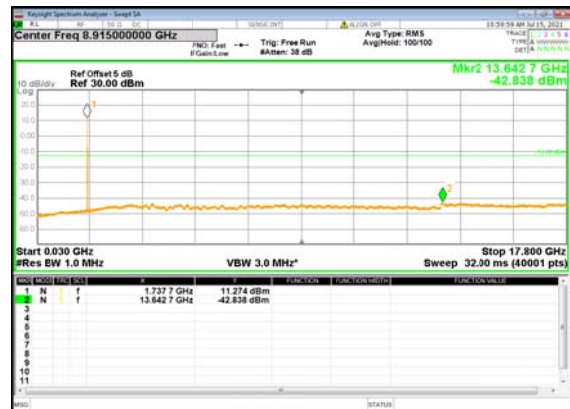
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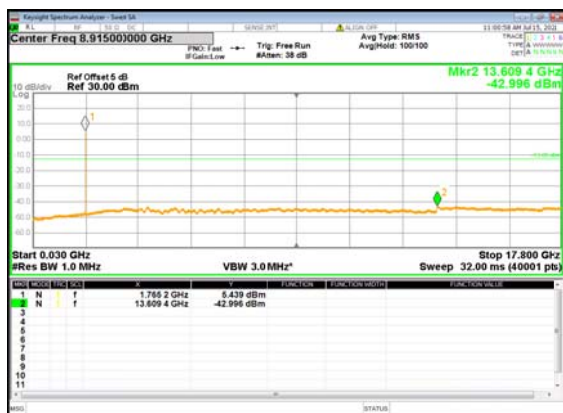
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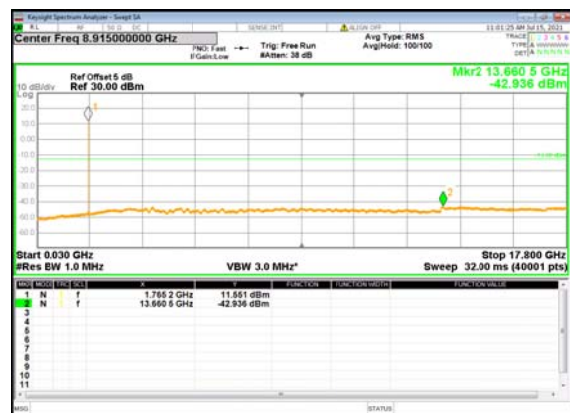
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N66(15M)_DFT-s-OFDM_BPSK_Edge_1RB_
Left_High_CH



N66(15M)_DFT-s-OFDM_QPSK_Edge_1RB_
Left_High_CH





REPORT No.: SZ21030417W10

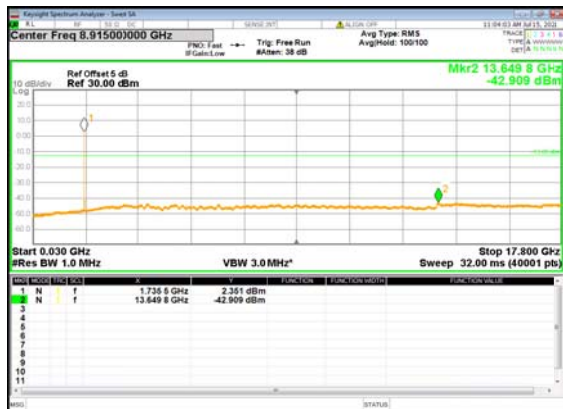
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Left_Low_CH



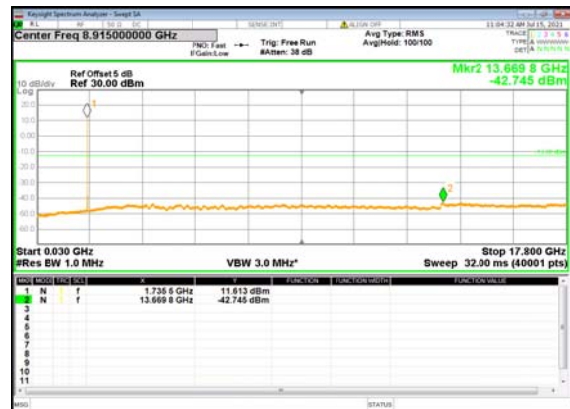
N66(20M)_DFT-s-OFDM_QPSK_Edge_1RB_
Left_Low_CH



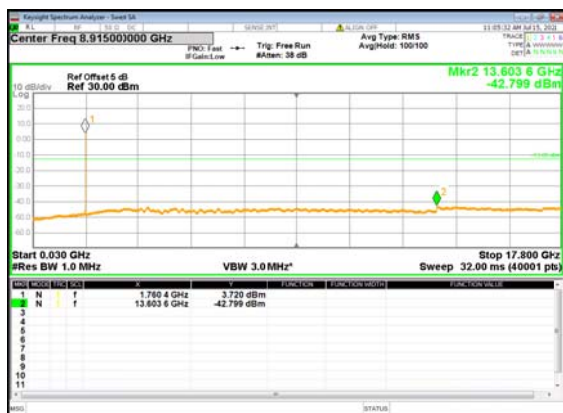
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Left_Mid_CH



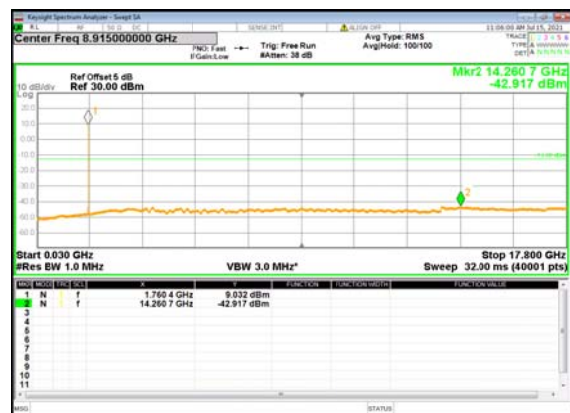
N66(20M)_DFT-s-OFDM_QPSK_Edge_1RB_
Left_Mid_CH



N66(20M)_DFT-s-OFDM_BPSK_Edge_1RB_
Left_High_CH



N66(20M)_DFT-s-OFDM_QPSK_Edge_1RB_
Left_High_CH





2.6. Band Edge

2.6.1. Requirement

N2

According to FCC section 24.238(a), The power of any emission outside of the authorized operating frequency ranges must be attenuated below the transmitting power (P) by a factor of at least $43 + 10 \log(P)$ dB.

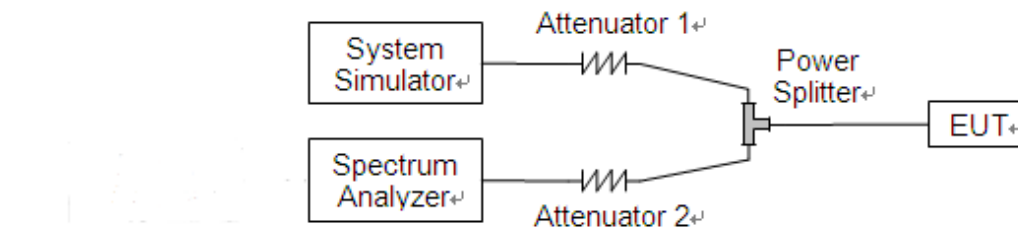
N5

According to FCC section 22.917(a), the power of any emission outside of the authorized operating frequency ranges must be attenuated below the transmitting power (P) by a factor of at least $43 + 10 \log(P)$ dB.

N66

According to FCC section 27.53(h), the power of any emission outside a licensee's frequency block shall be attenuated below the transmitter power (P) by at least $43 + 10 \log_{10}(P)$ dB a 1MHz bandwidth.

2.6.2. Test Description



The EUT is coupled to the Spectrum Analyzer (SA) and the System Simulator (SS) with Attenuators through the Power Splitter; the RF load attached to the EUT antenna terminal is 50Ohm; the path loss as the factor is calibrated to correct the reading. The EUT is commanded by the SS to operate at the maximum output power. A call is established between the EUT and the SS.

2.6.3. Test procedure

KDB 971168 D01v03 Section 6.0 and ANSI/TIA-603-E-2016.



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2.6.4. Test Result

The center frequency of spectrum is the band edge frequency and span is 2MHz, Record the max trace into the test report.