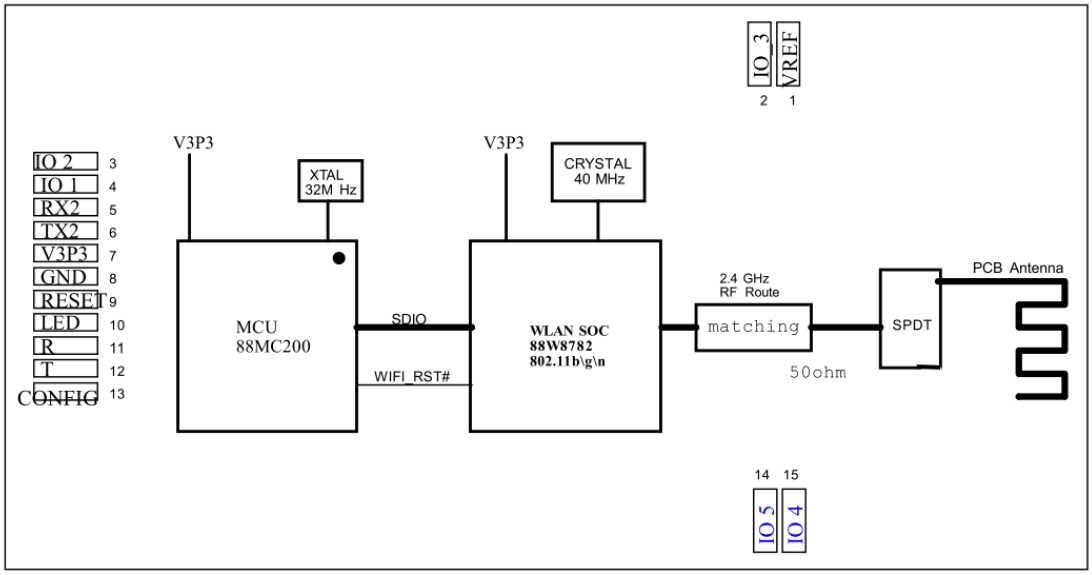
The device is designed to support IEEE802.11b/g/n payload data rates. The device provides the combined functions of Direct Sequence Spread Spectrum (DSSS) and Orthogonal Frequency Division Multiplexing (OFDM) baseband modulation, Medium Access Controller (MAC), CPU, memory, host interfaces, and direct conversion WLAN RF radio on a single module.

On the transmit path, data from the host system is queued in memory by the local CPU. The MAC retrieves data for transmission at the appropriate time.

System Block



IEEE 802.11/Standards

802.11b data rates of 1, 2, 5.5 and 11 Mbps

802.11g data rates 6, 9, 12, 18, 24, 36, 48, and 54 Mbps

802.11n compliant, with maximum data rates up to 72 Mbps (20 MHz channel)

Wlan

The device is a fully self-contained small form-factor, single stream, 802.11 b/g/n wifi module, which provide a wireless interface to any equipment with a Serial/GPIO/I2C/ADC interface for data transfer. It integrate MAC, baseband processor, RF transceiver with power amplifier in hardware and all wifi protocol and configuration functionality and networking stack, in embedded firmware to make a fully self-contained 802.11 b/g/n wifi solution for a variety of applications. It support AP and STA wireless networking and support WIFI Direct mode. It can set output power, wifi channel automatically.