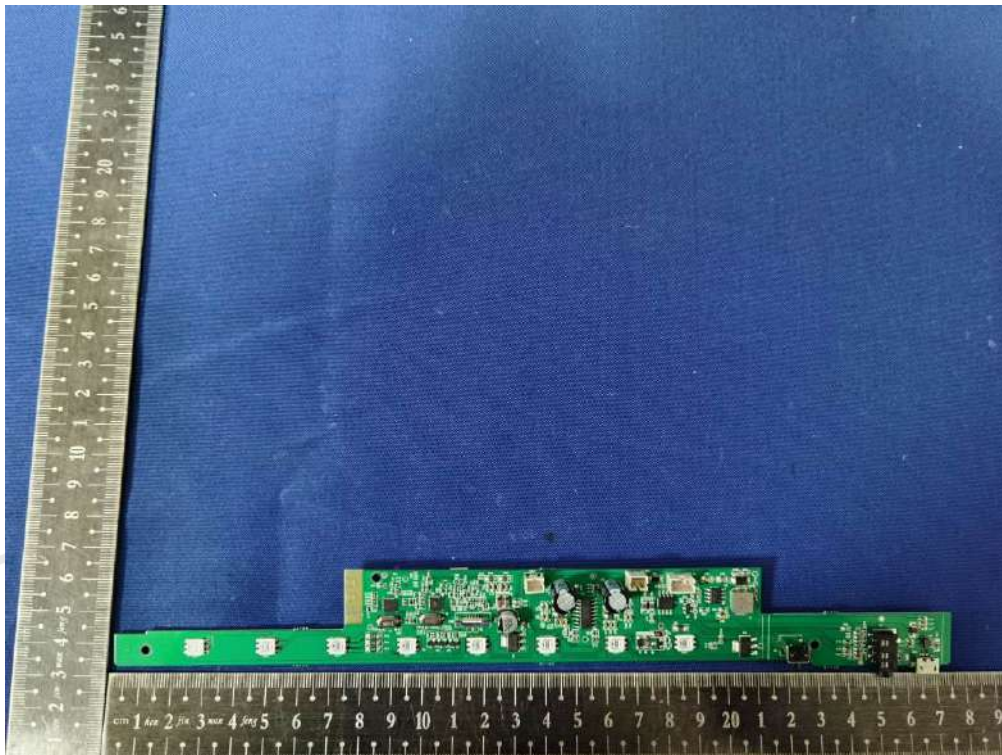
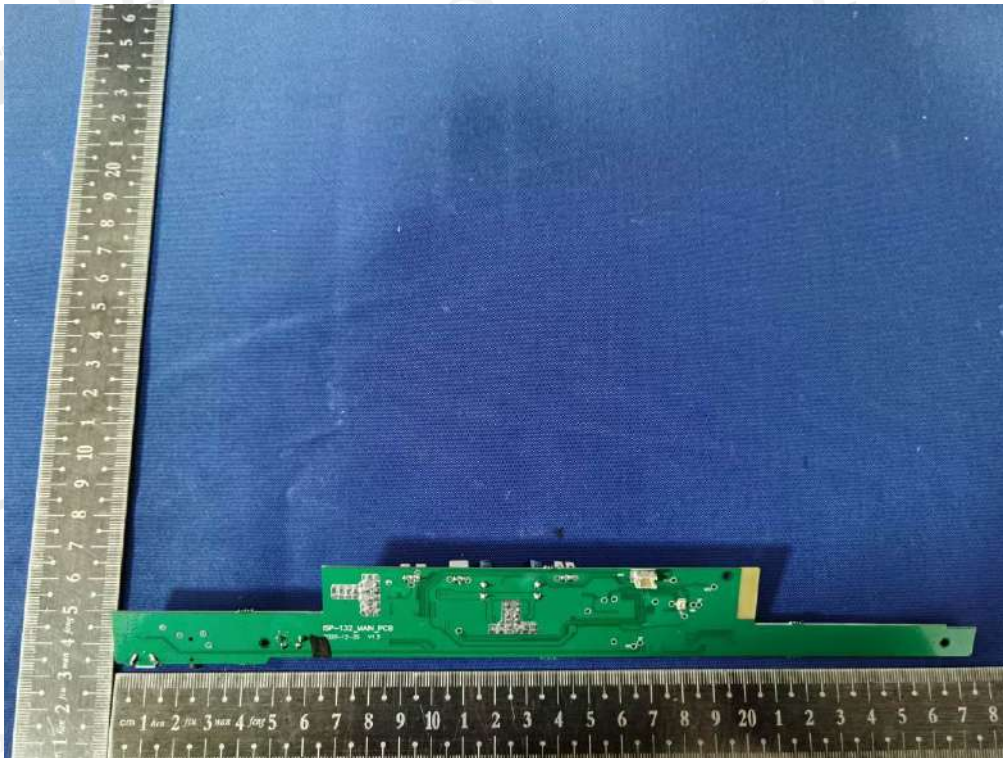




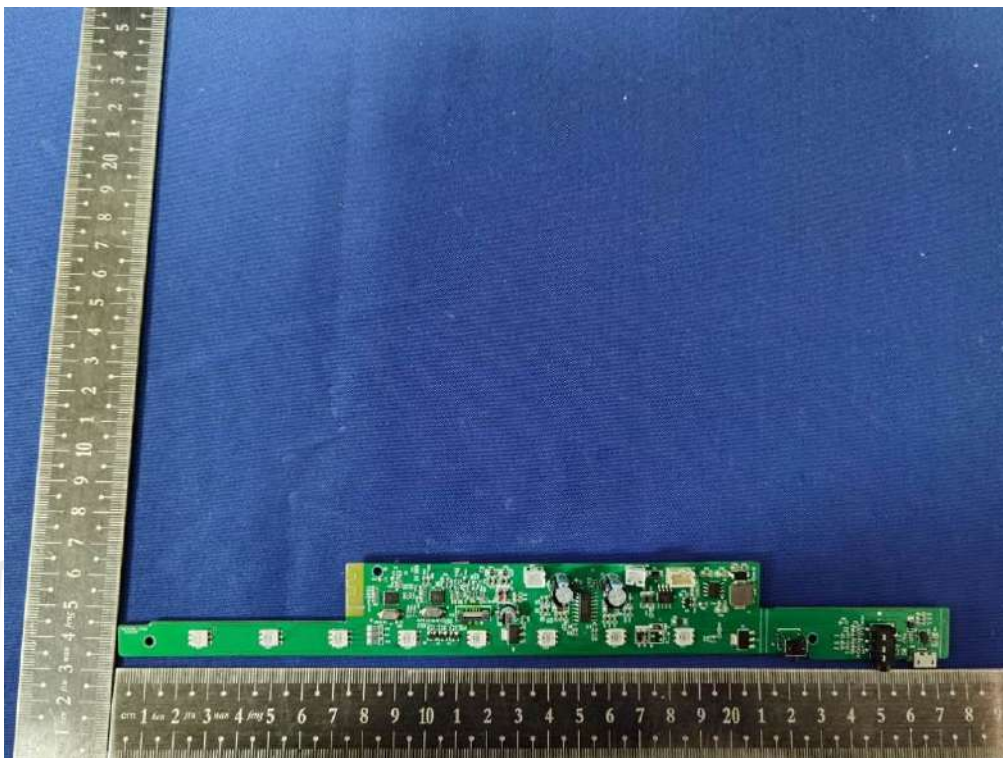


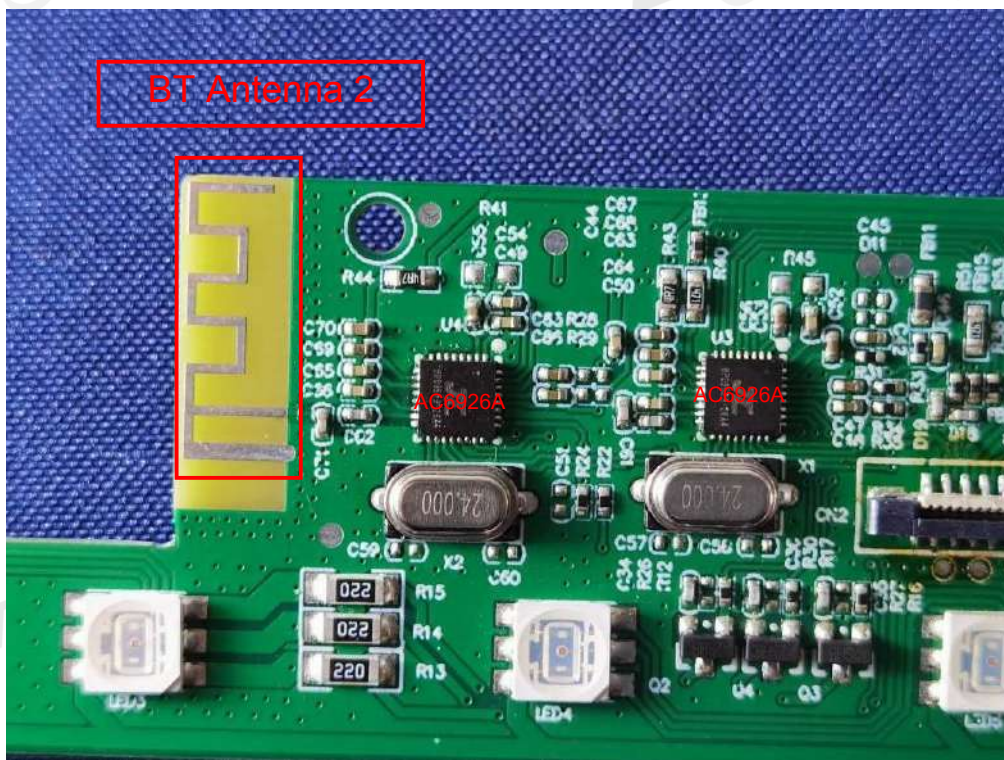
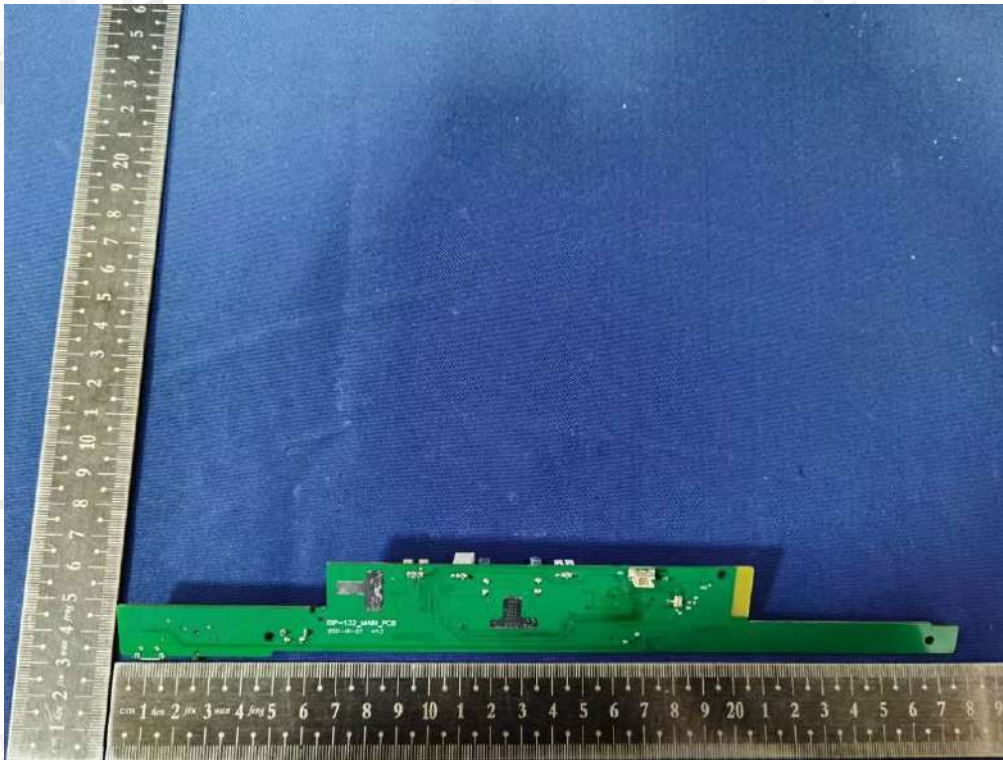
PCB Mainboard 1:

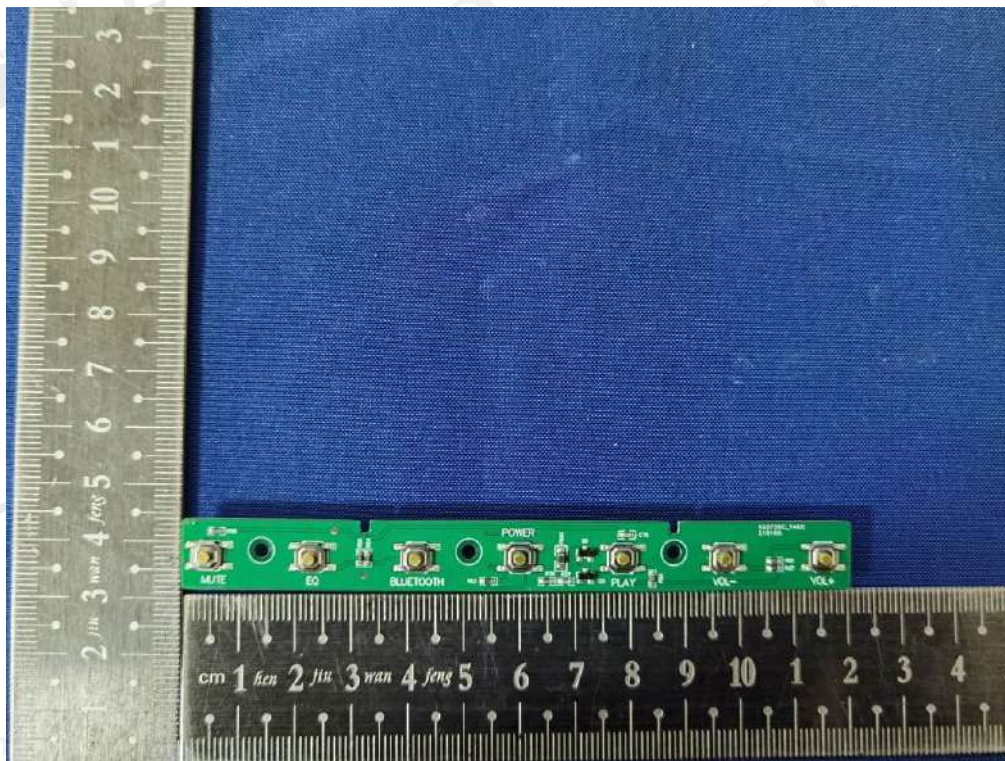


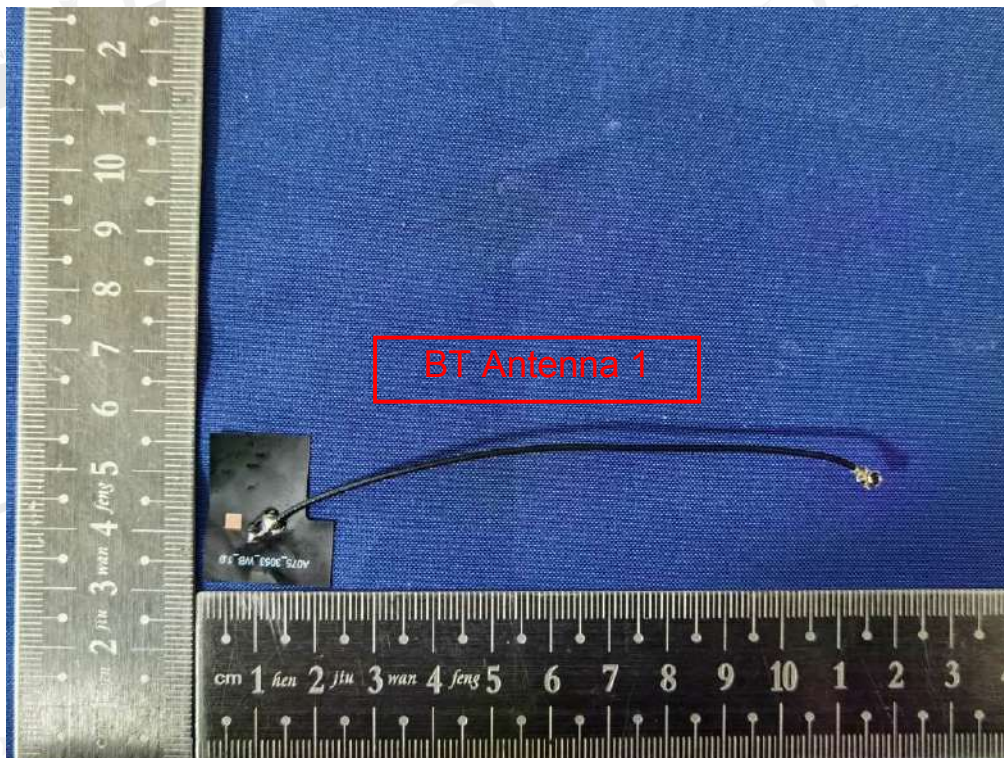


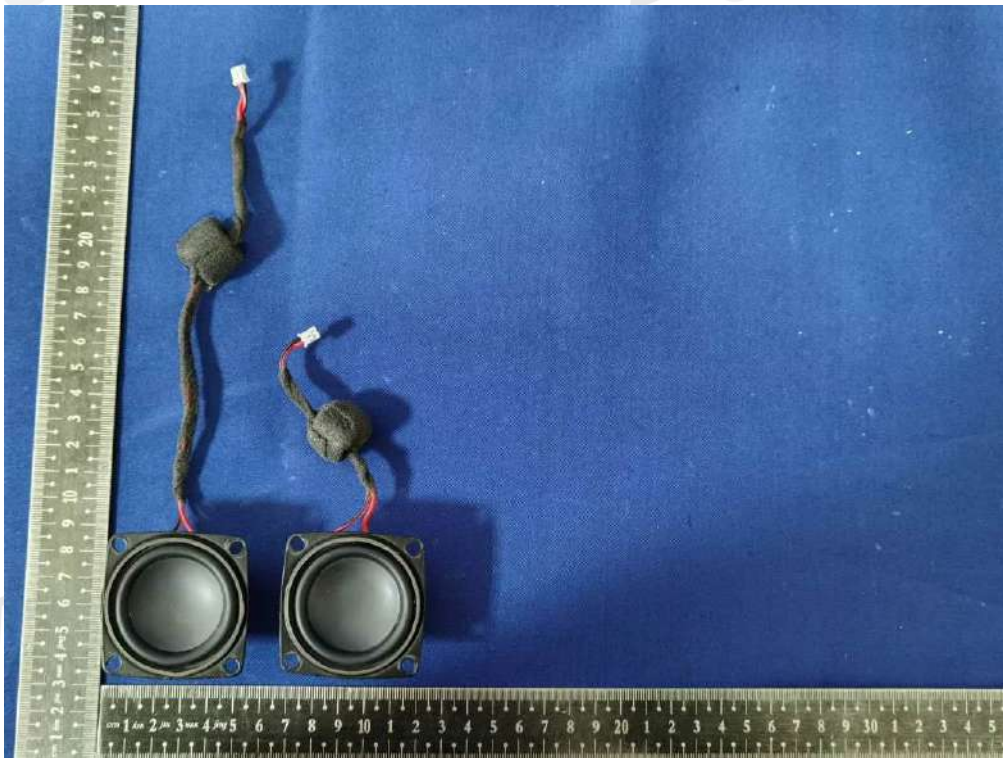
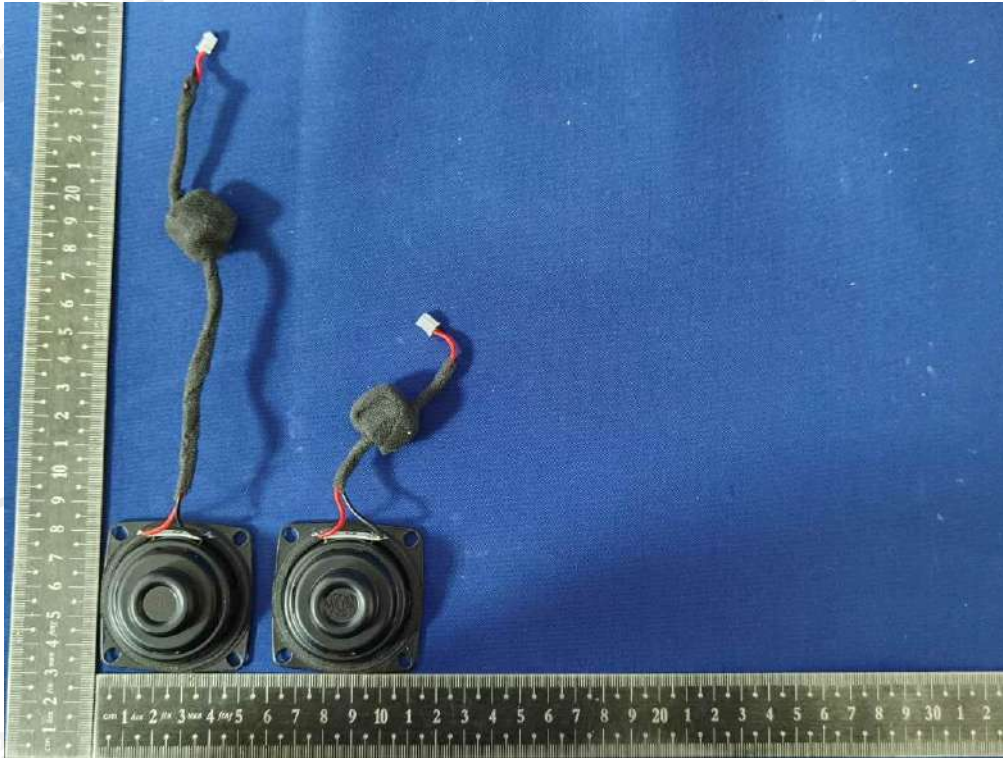
PCB Mainboard 2:

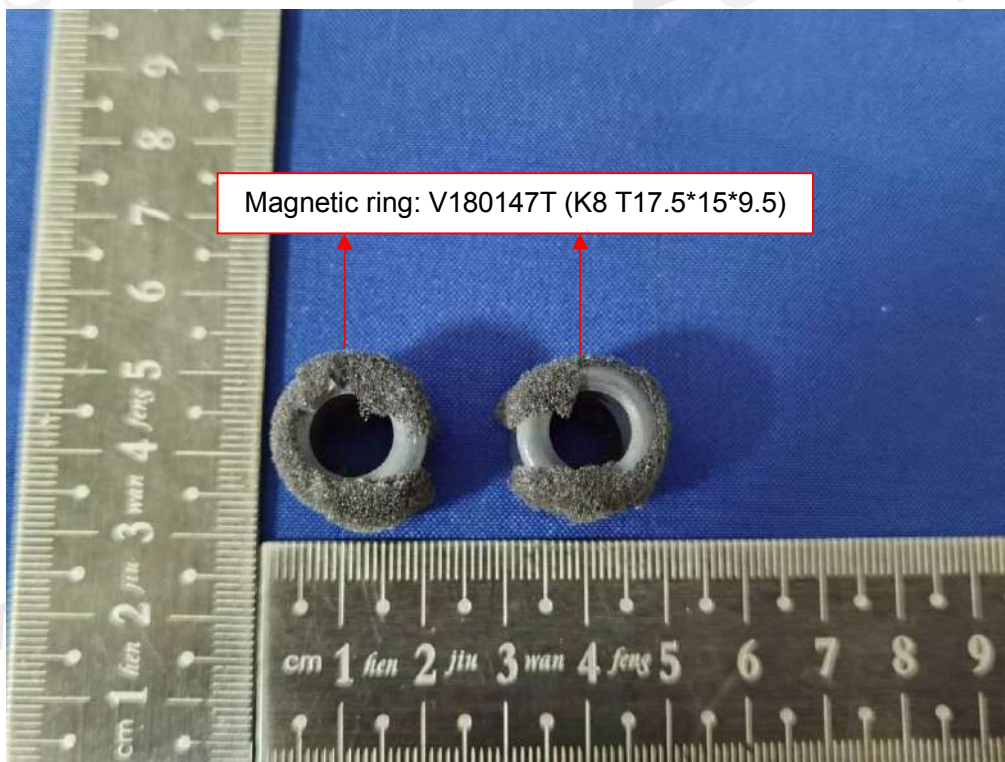


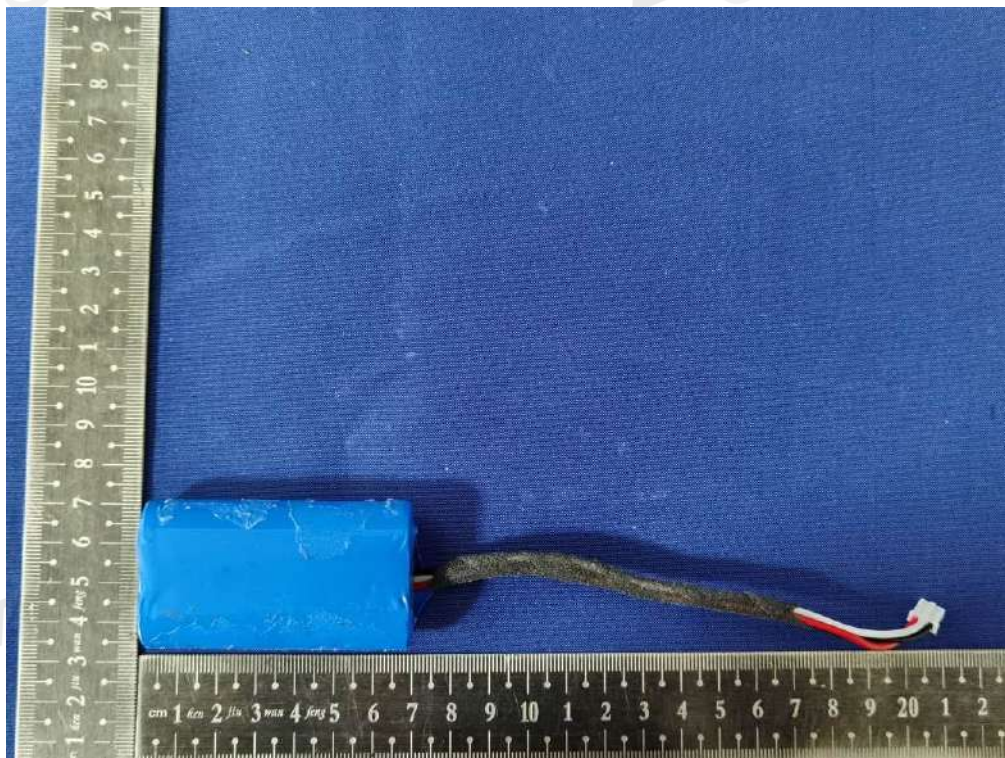












END OF REPORT