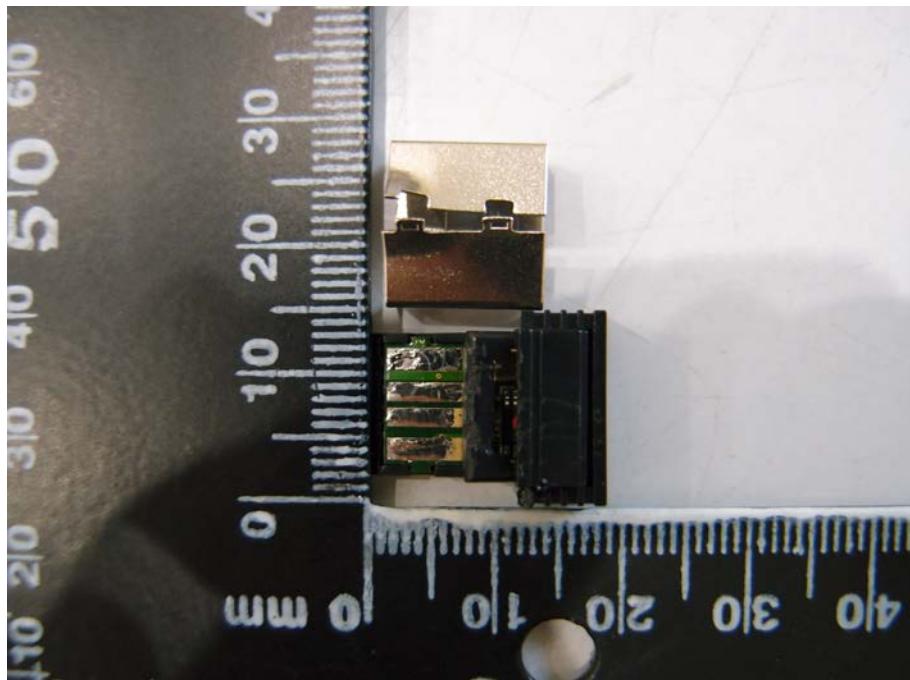
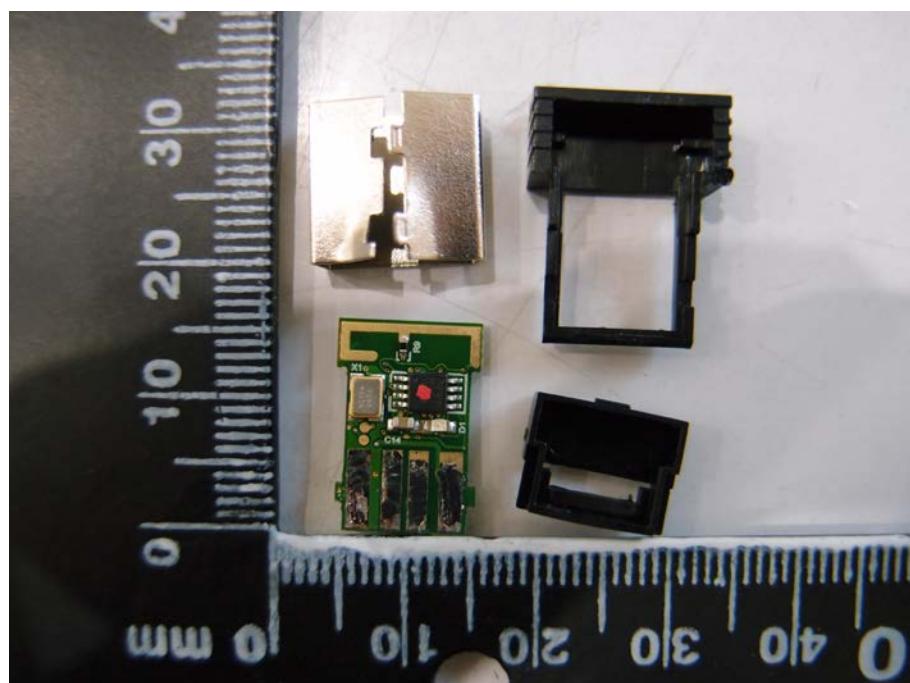


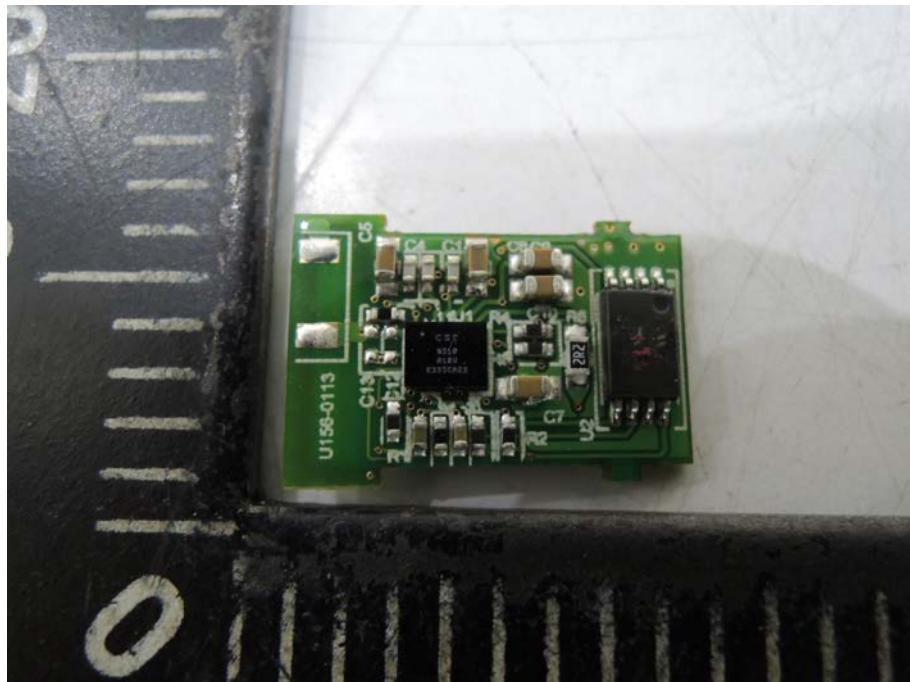
## 1 Internal Photographs of the EUT



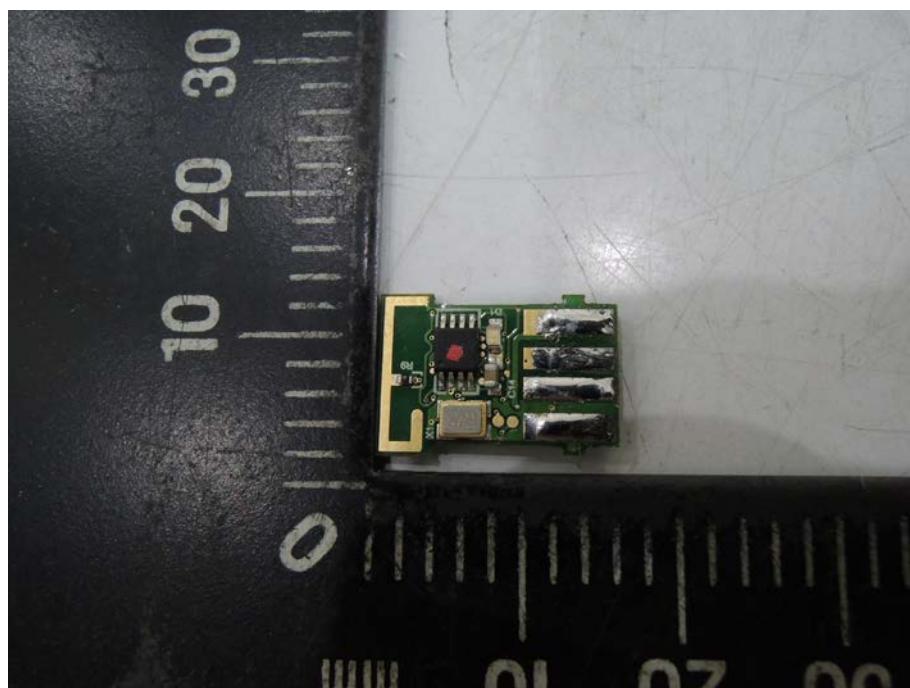
View of the EUT's I/O port



View of the EUT's I/O port



Front View of the PCB



Rear View of the PCB