

1. Duty Cycle

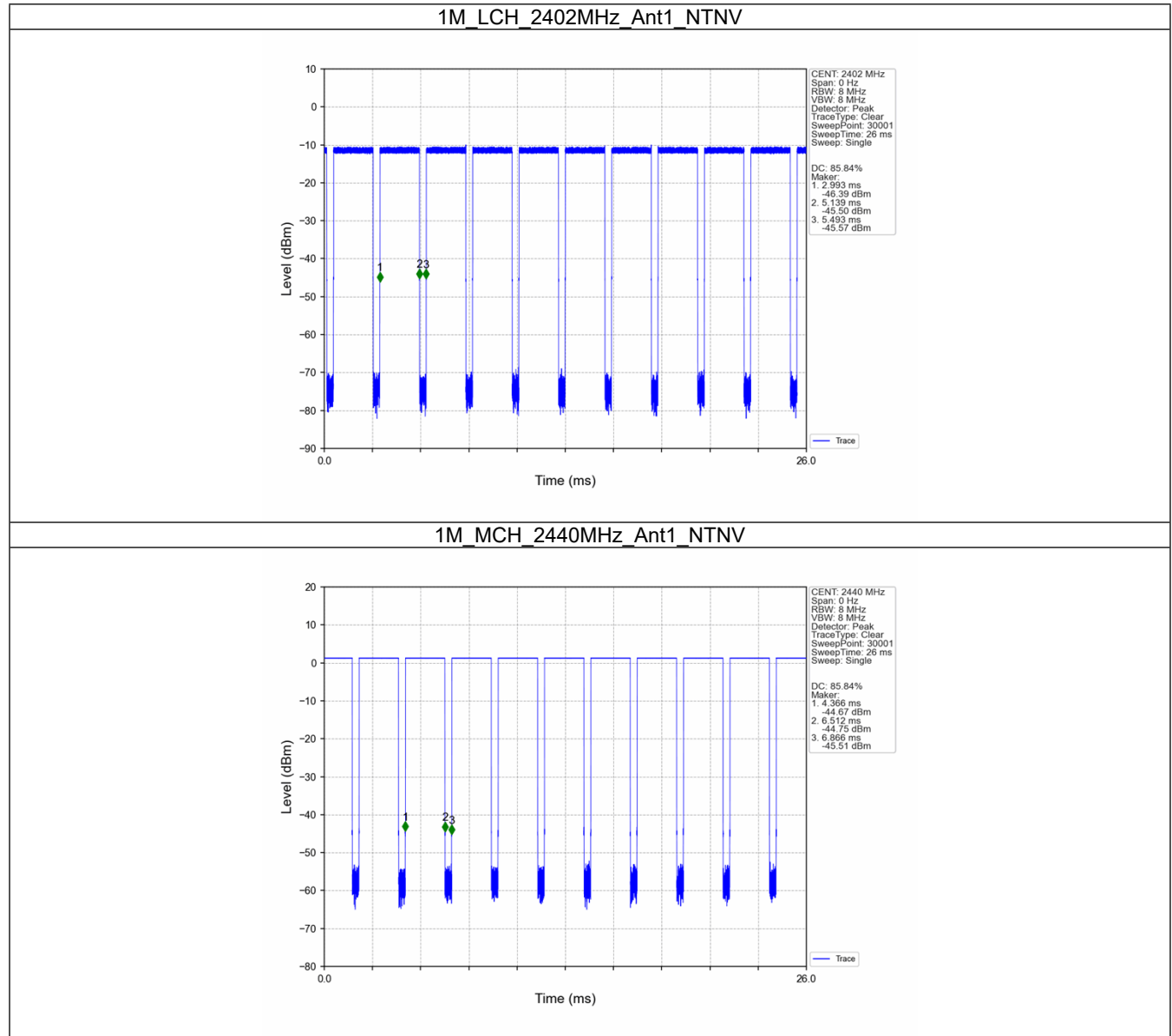
1.1 Test Result

1.1.1 Ant1

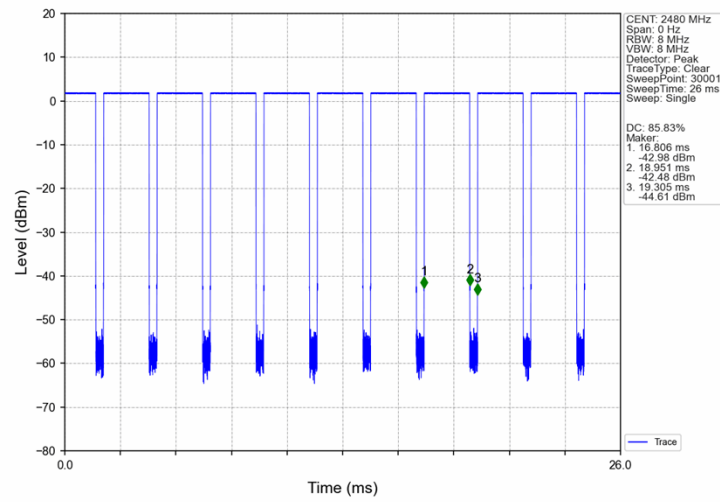
Ant1							
Mode	TX Type	Frequency (MHz)	T_on (ms)	Period (ms)	Duty Cycle (%)	Duty Cycle Correction Factor (dB)	Max. DC Variation (%)
1M	SISO	2402	2.146	2.500	85.84	0.66	0.03
		2440	2.146	2.500	85.84	0.66	0.03
		2480	2.145	2.499	85.83	0.66	0.03

1.2 Test Graph

1.2.1 Ant1



1M_HCH_2480MHz_Ant1_NTNV



2. Bandwidth

2.1 Test Result

2.1.1 OBW

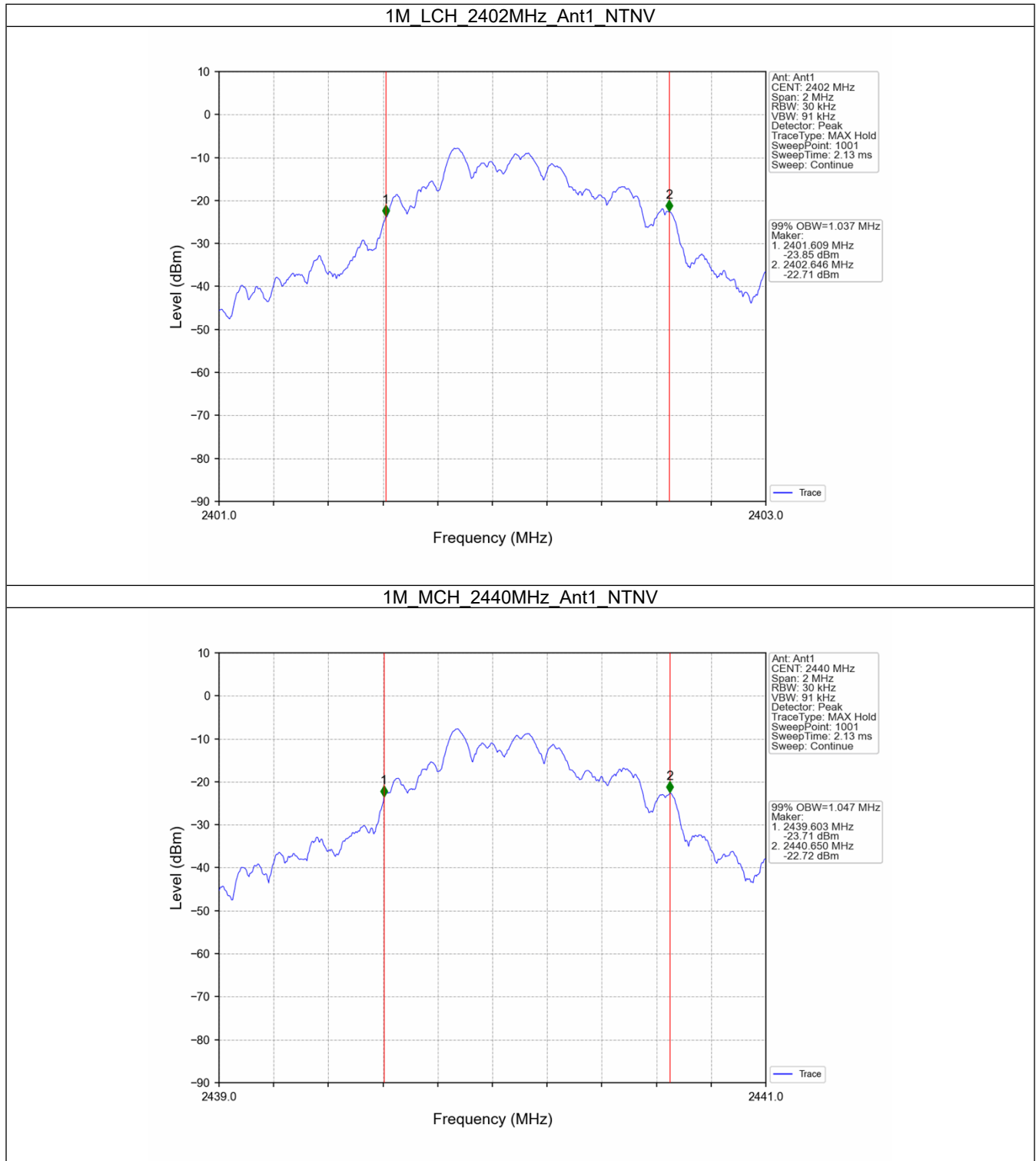
Mode	TX Type	Frequency (MHz)	ANT	99% Occupied Bandwidth (MHz)		Verdict
				Result	Limit	
1M	SISO	2402	1	1.037	/	Pass
		2440	1	1.047	/	Pass
		2480	1	1.057	/	Pass

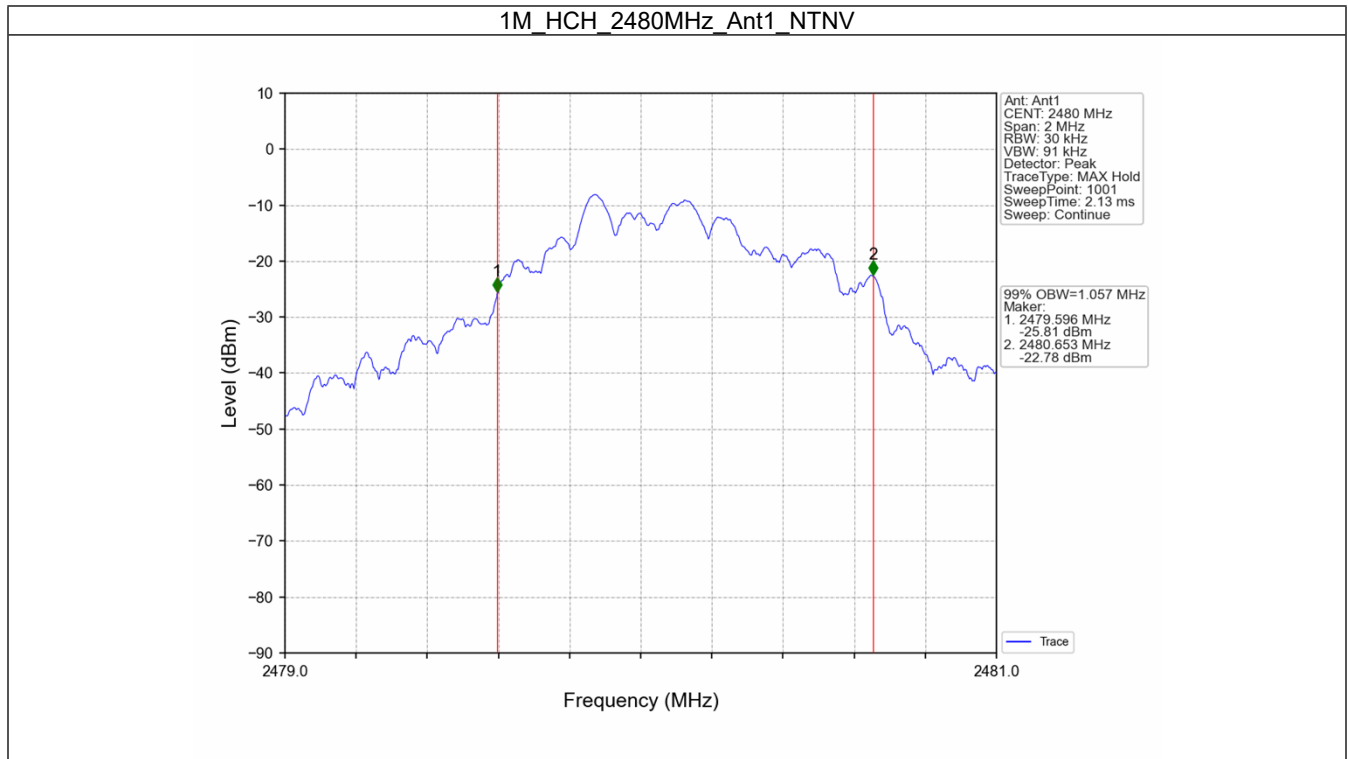
2.1.2 6dB BW

Mode	TX Type	Frequency (MHz)	ANT	6dB Bandwidth (MHz)		Verdict
				Result	Limit	
1M	SISO	2402	1	0.551	≥ 0.5	Pass
		2440	1	0.555	≥ 0.5	Pass
		2480	1	0.551	≥ 0.5	Pass

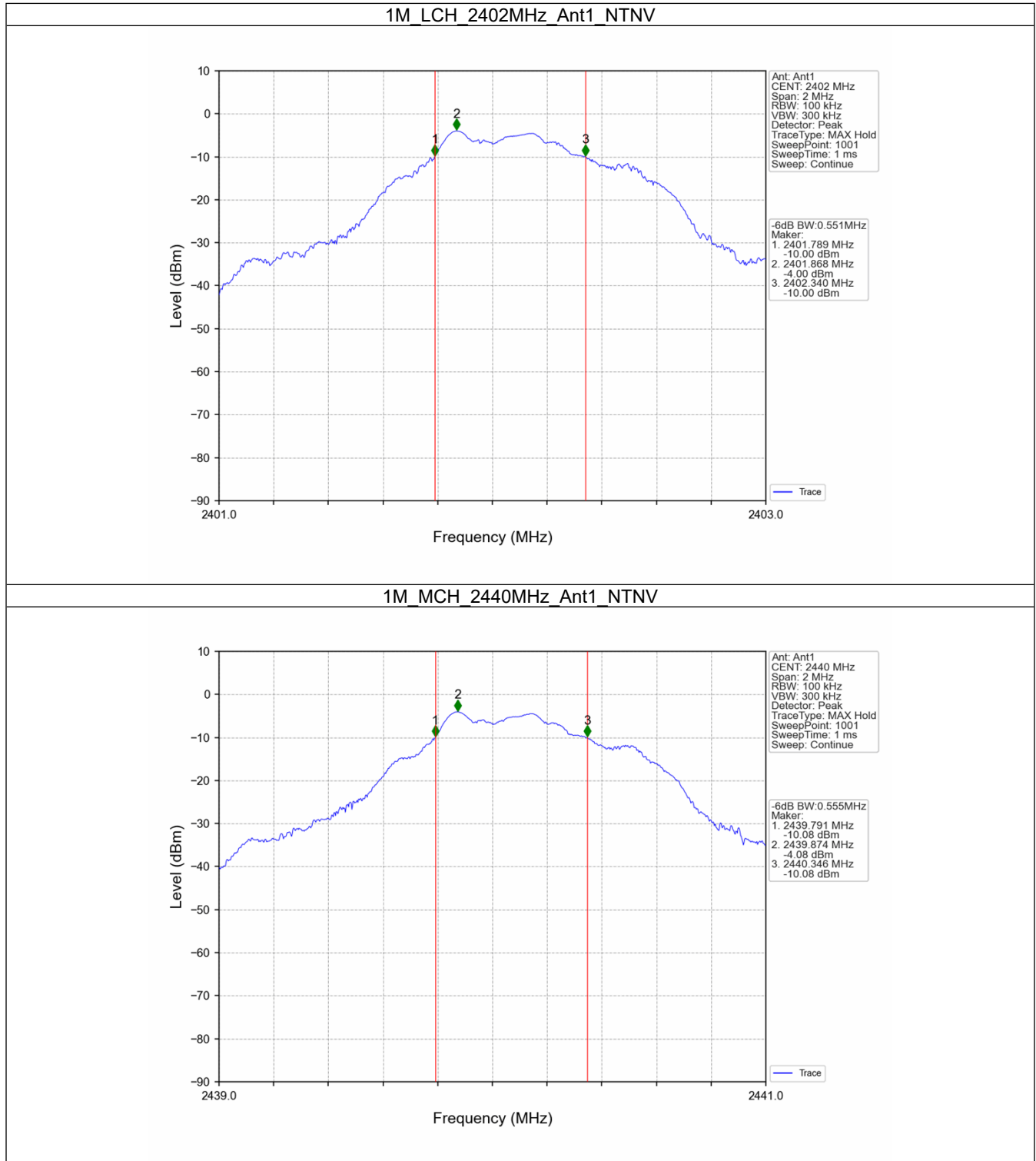
2.2 Test Graph

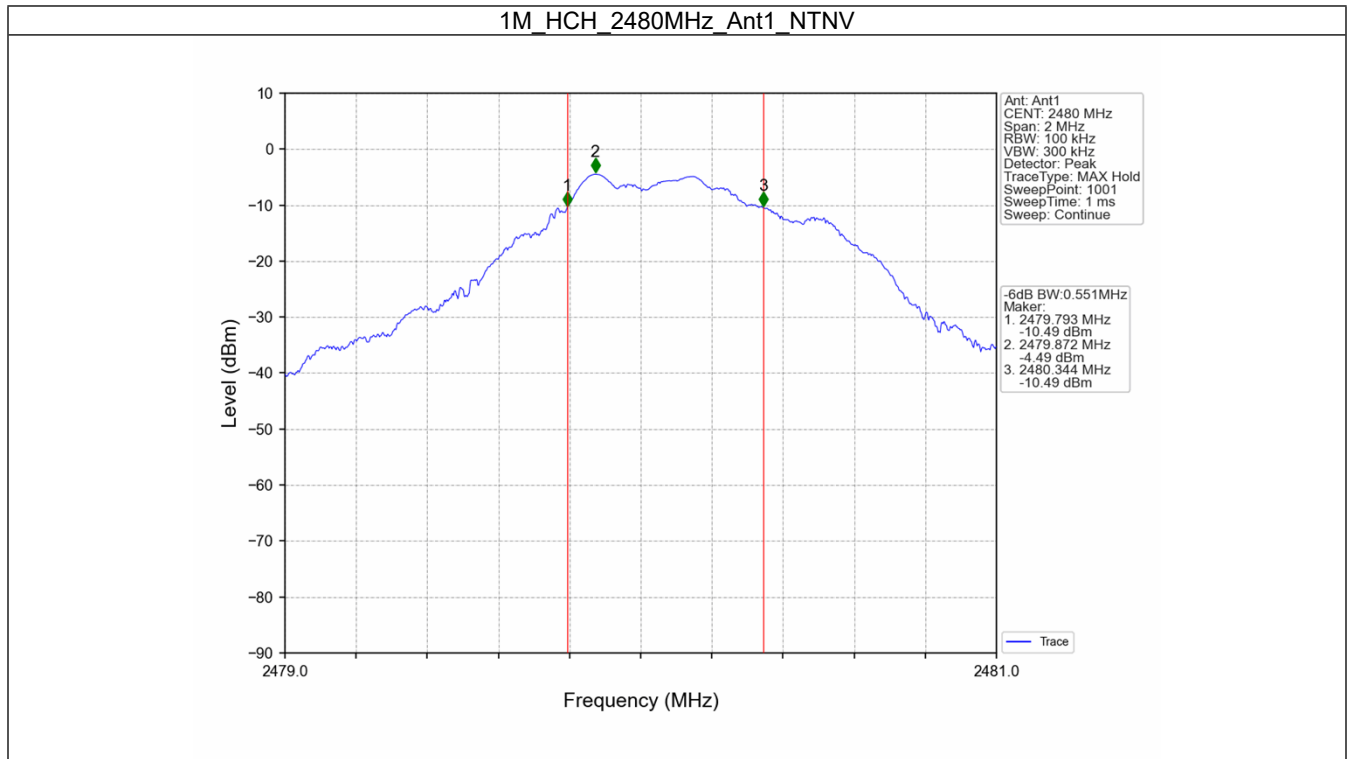
2.2.1 OBW





2.2.2 6dB BW





3. Maximum Conducted Output Power

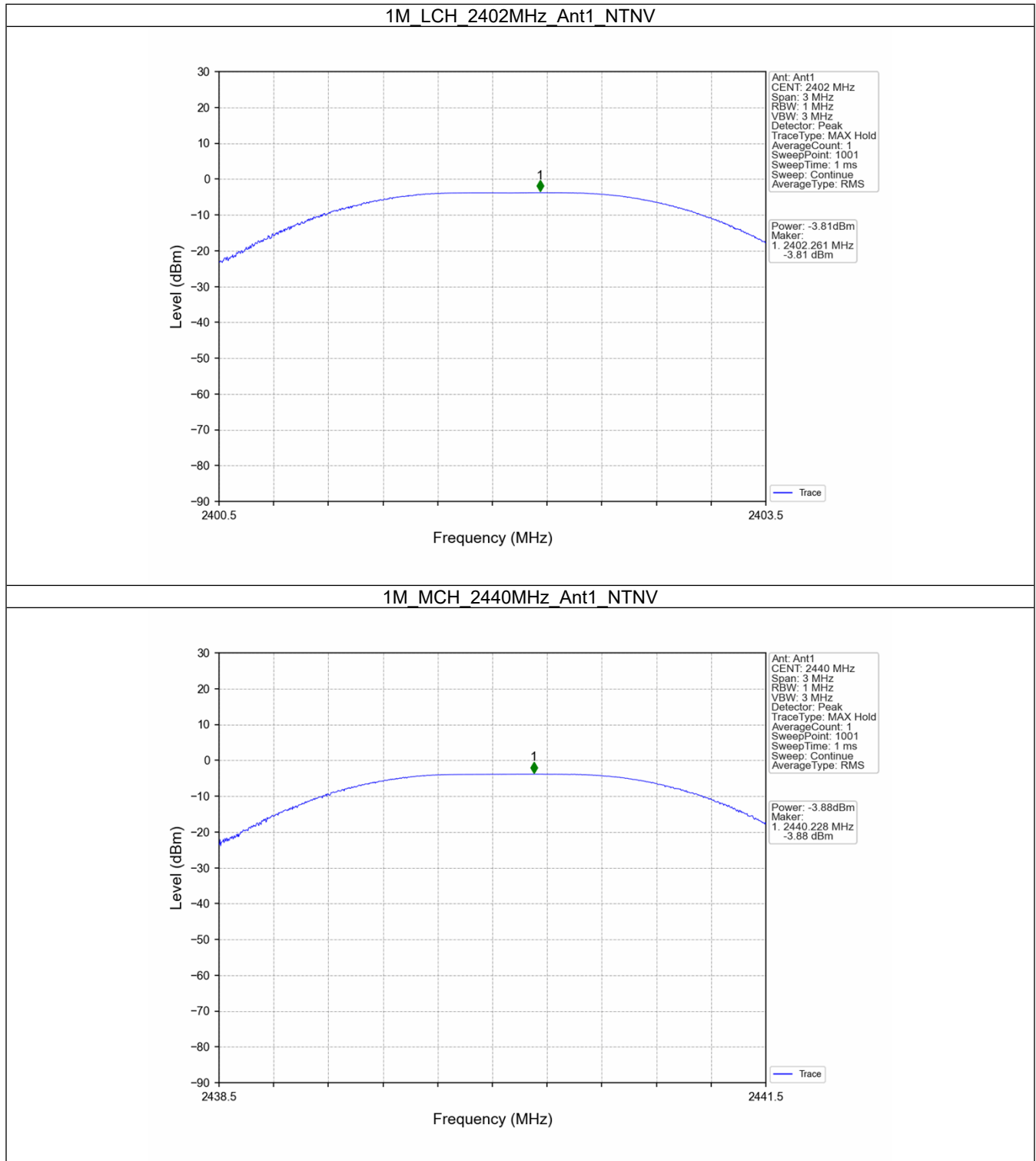
3.1 Test Result

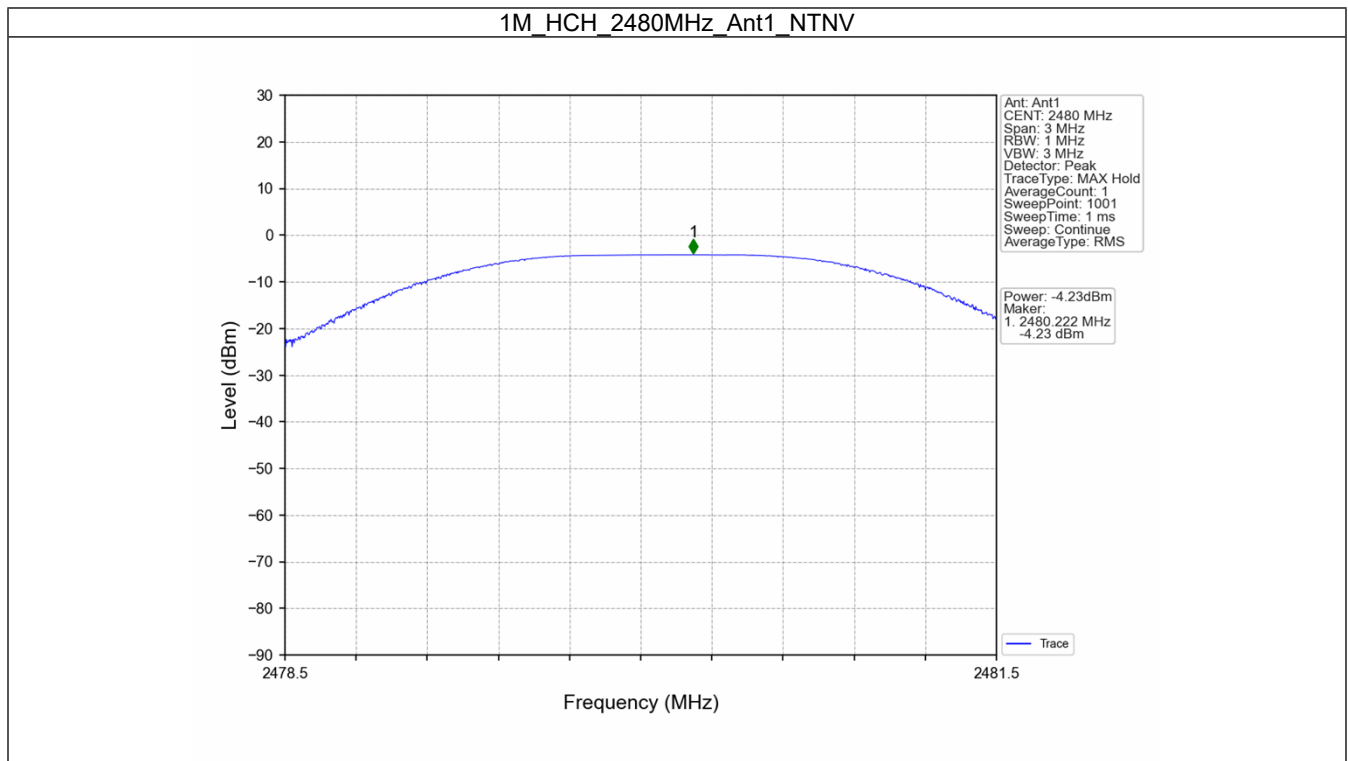
3.1.1 Power

Mode	TX Type	Frequency (MHz)	Maximum Peak Conducted Output Power (dBm)		Verdict
			ANT1	Limit	
1M	SISO	2402	-3.81	<=30	Pass
		2440	-3.88	<=30	Pass
		2480	-4.23	<=30	Pass

3.2 Test Graph

3.2.1 Power





4. Maximum Power Spectral Density

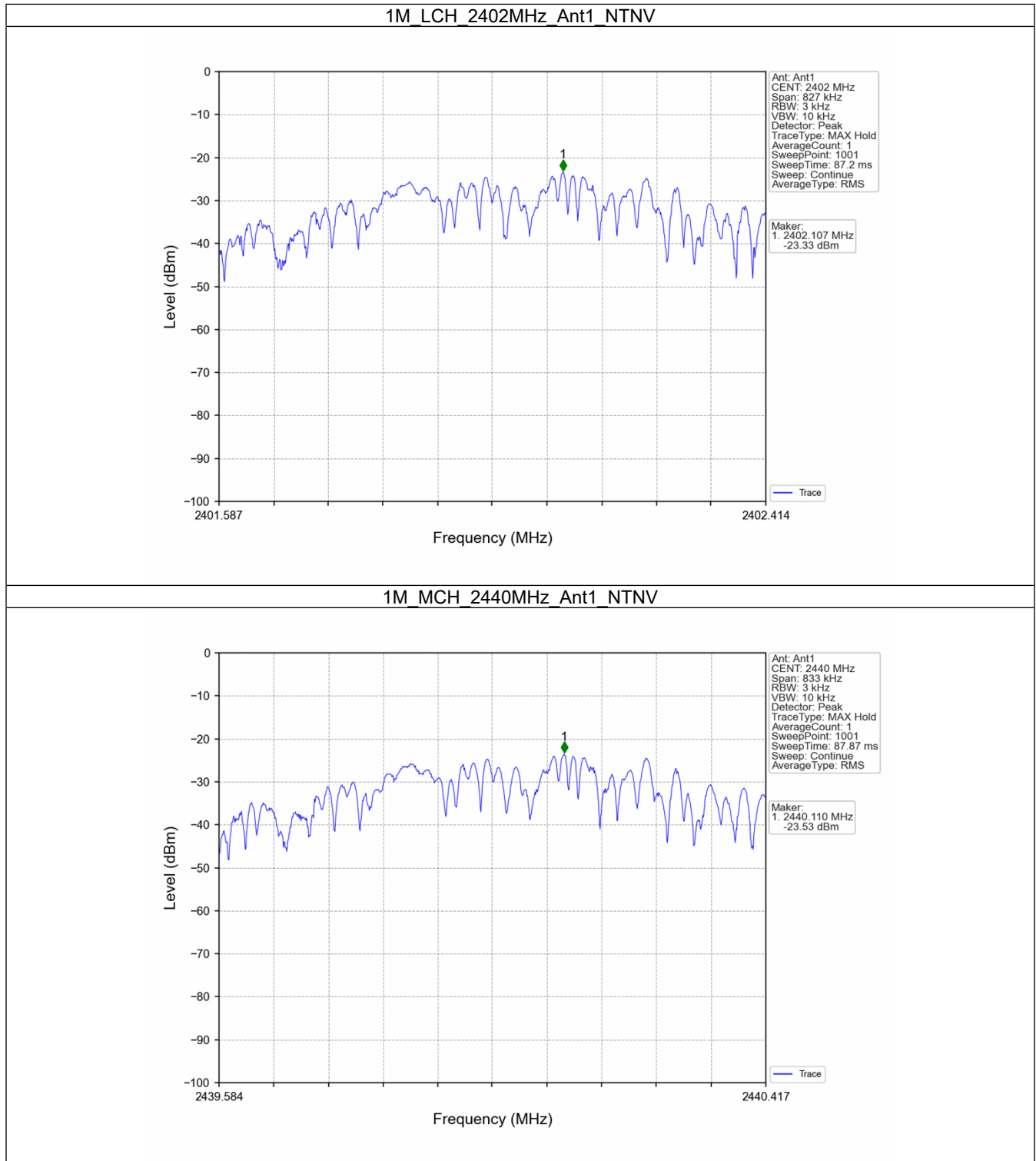
4.1 Test Result

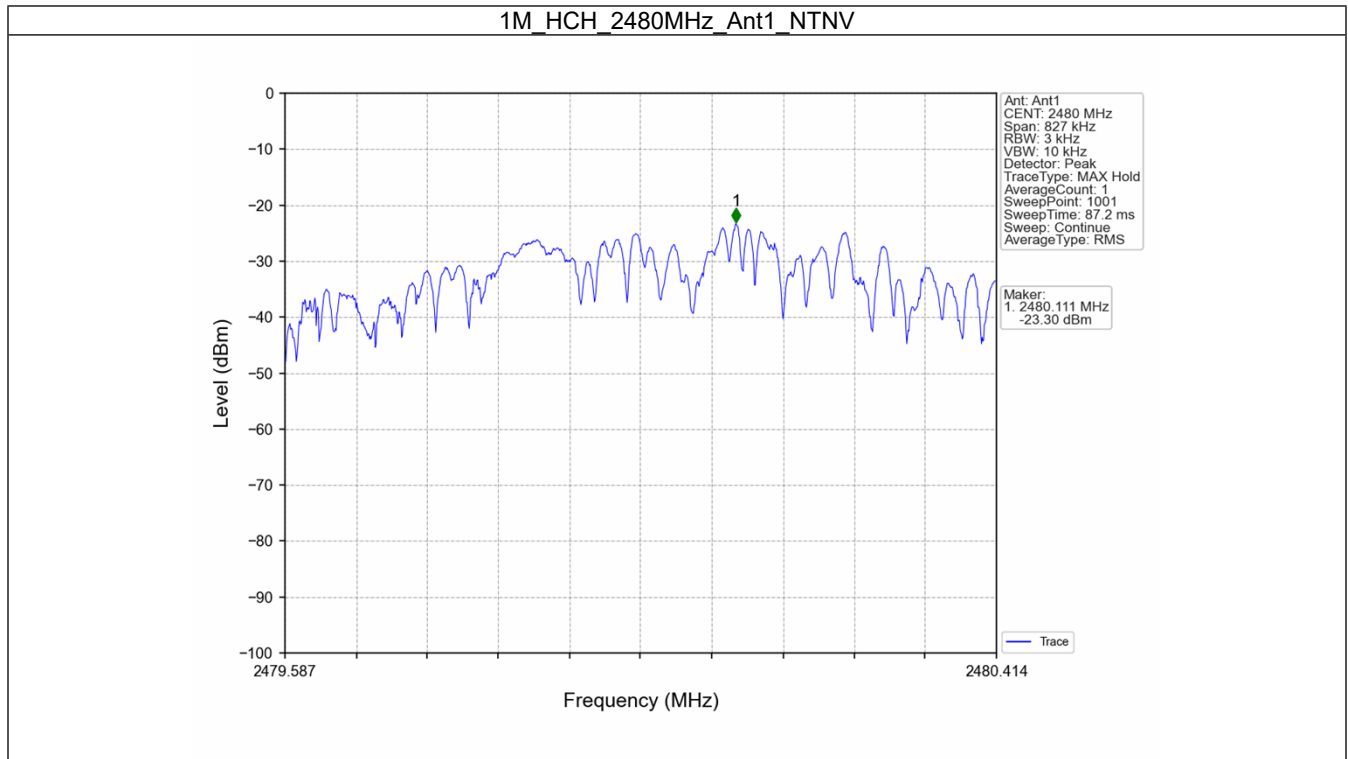
4.1.1 PSD

Mode	TX Type	Frequency (MHz)	Maximum PSD (dBm/3kHz)		Verdict
			ANT1	Limit	
1M	SISO	2402	-23.33	<=8	Pass
		2440	-23.53	<=8	Pass
		2480	-23.30	<=8	Pass

4.2 Test Graph

4.2.1 PSD

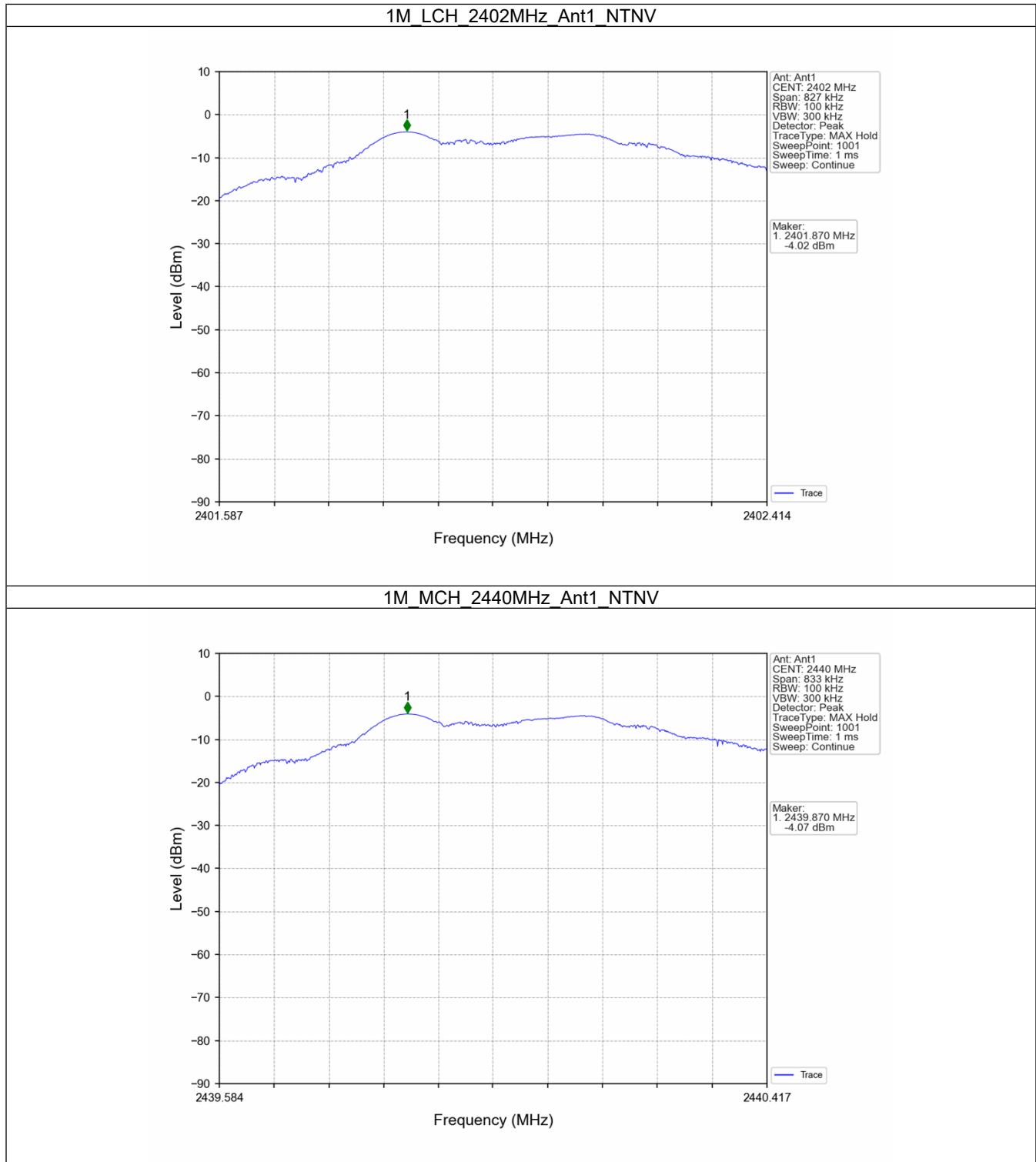


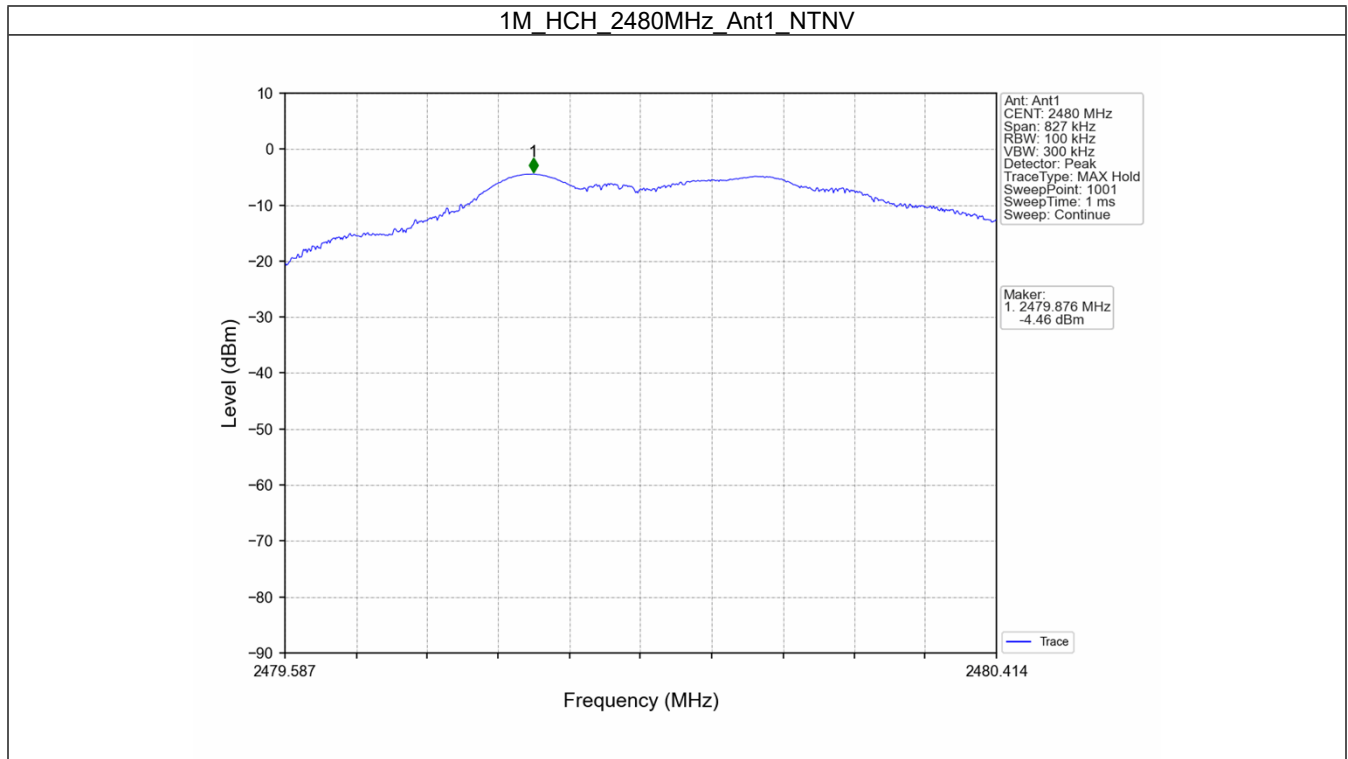


5.1.1 Ref

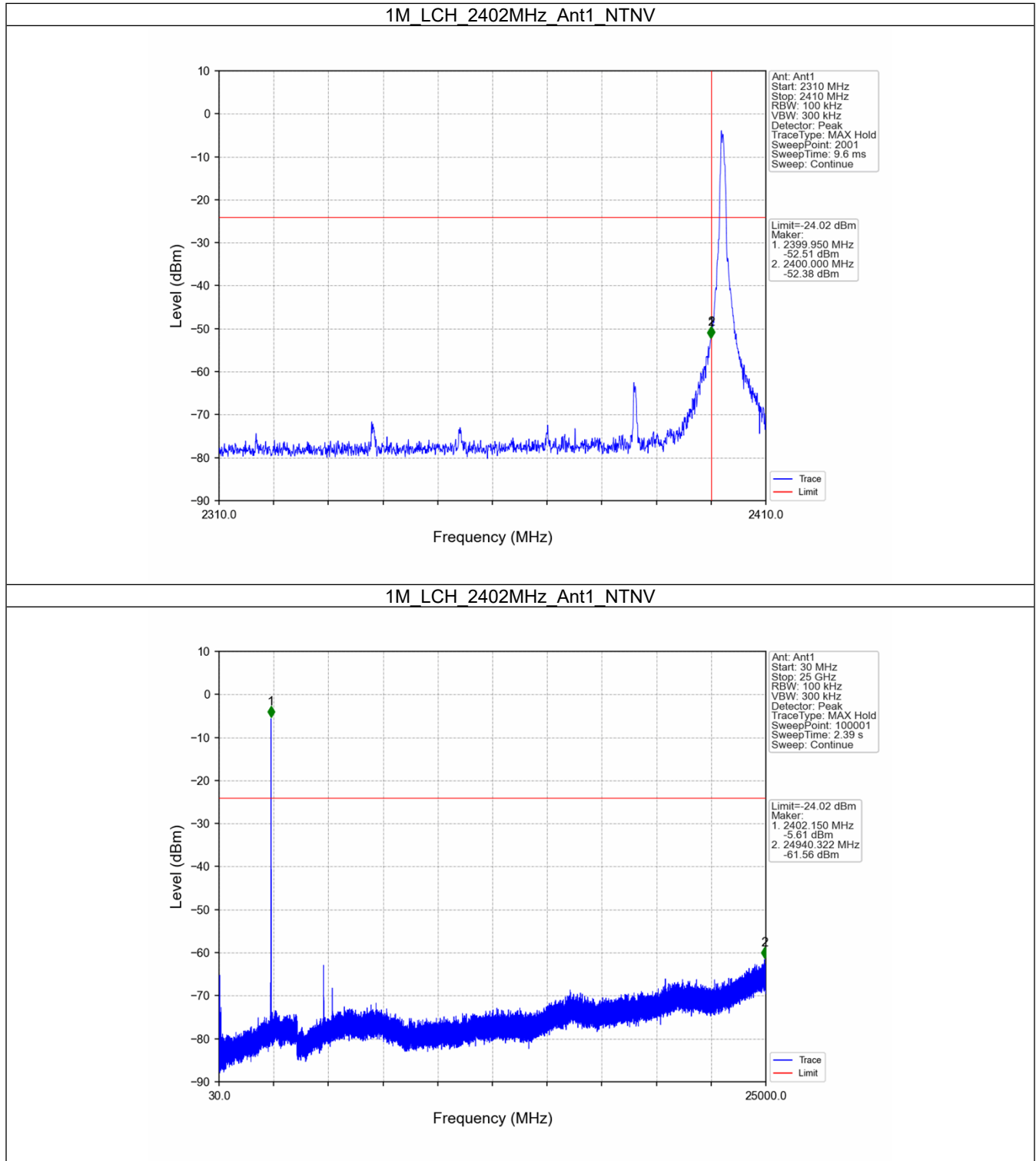
5.2 Test Graph

5.2.1 Ref

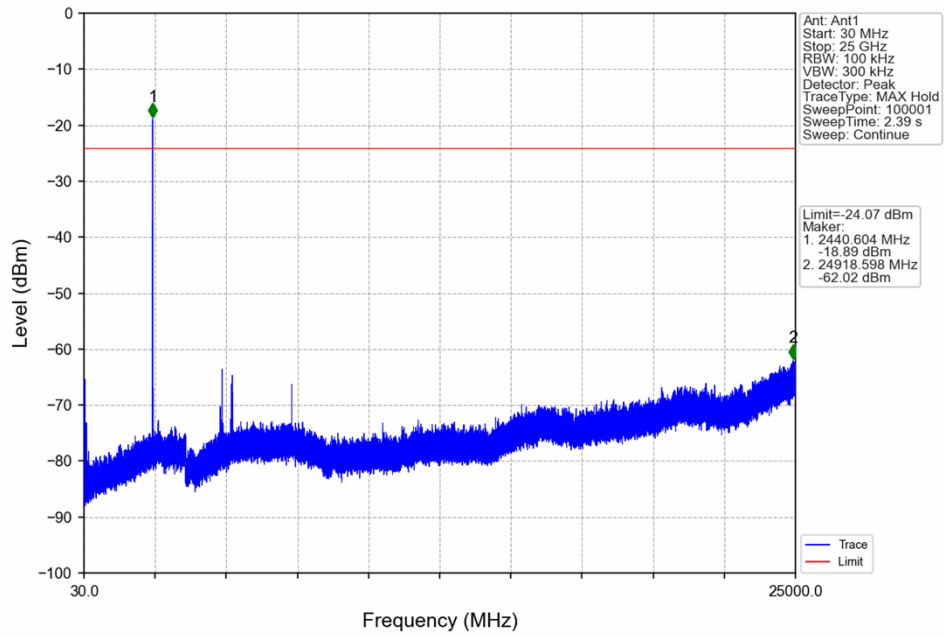




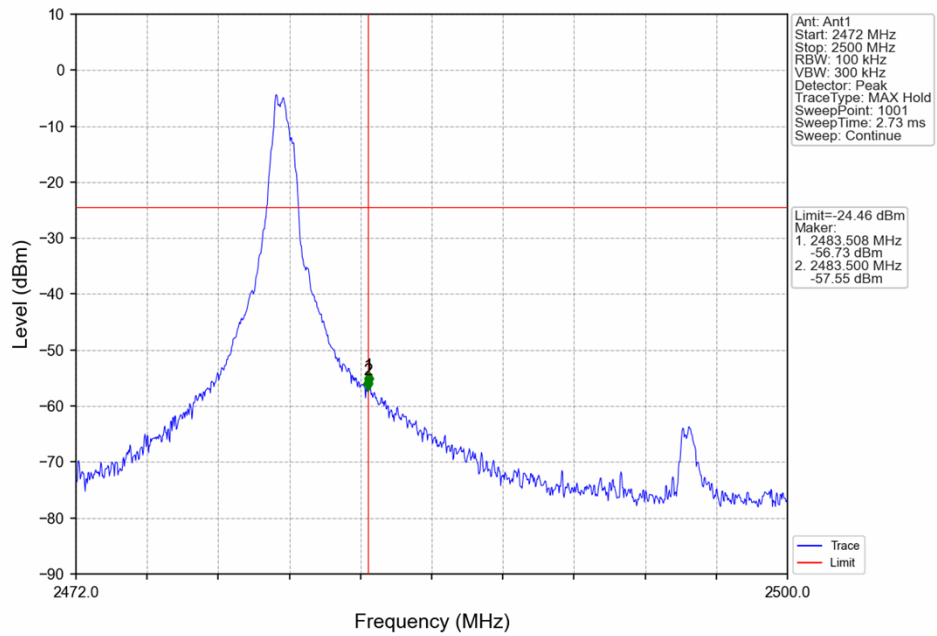
5.2.2 CSE

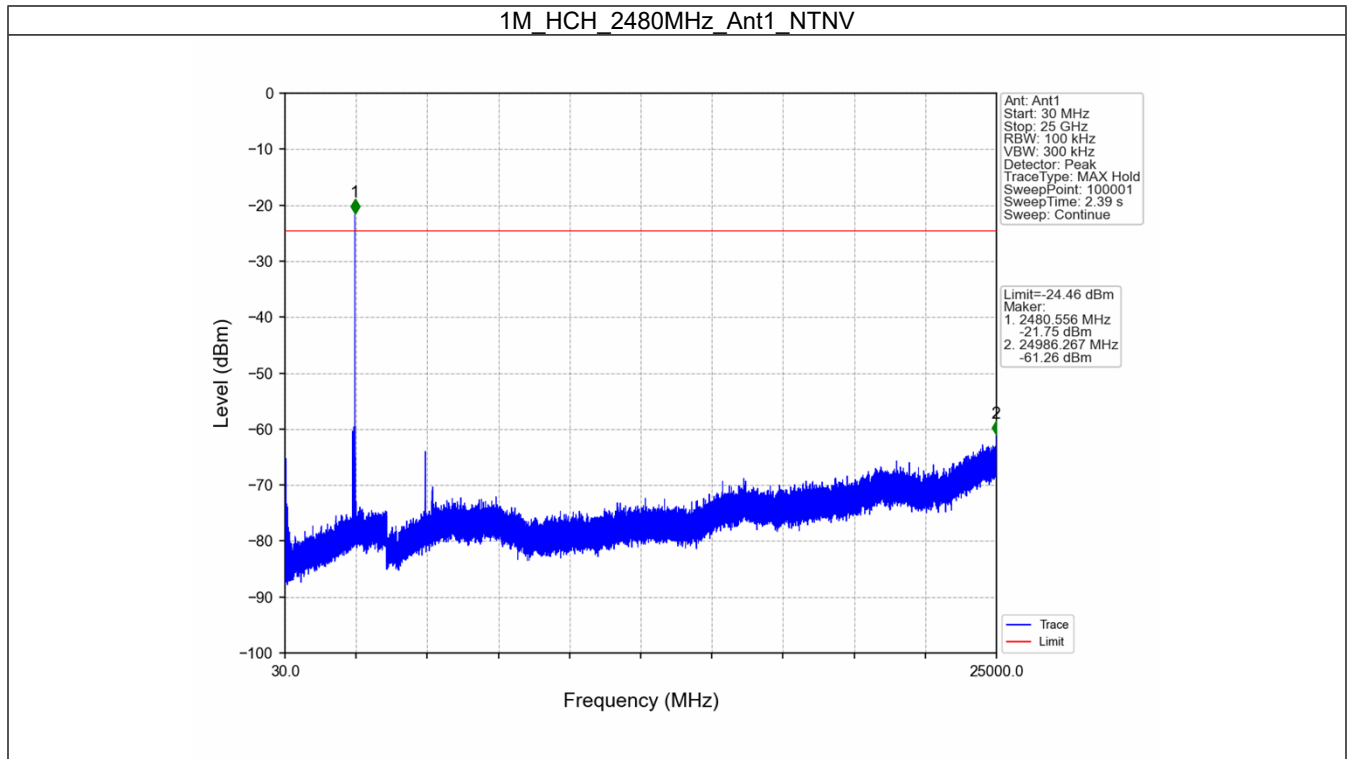


1M_MCH_2440MHz_Ant1_NTNV



1M_HCH_2480MHz_Ant1_NTNV





6. Form731

6.1 Test Result

6.1.1 Form731

Lower Freq (MHz)	High Freq (MHz)	MAX Power (W)	MAX Power (dBm)
2402	2480	0.0004	-3.81