

Appendix D.4: Maximum conducted output power

Test Result Channel Power

Test Mode	Antenna	Frequency[MHz]	Channel Power [dBm]	Duty Cycle [%]	DC Factor [dBm]	Result [dBm]	Limit [dBm]	Verdict
11A	Ant2	5180	8.49	95.87	0.18	8.67	≤23.98	PASS
		5200	8.71	95.85	0.18	8.89	≤23.98	PASS
		5240	8.96	96.31	0.16	9.12	≤23.98	PASS
11N20SISO	Ant2	5180	8.63	99.00	0.04	8.67	≤23.98	PASS
		5200	8.77	98.88	0.05	8.82	≤23.98	PASS
		5240	8.94	98.88	0.05	8.99	≤23.98	PASS
11N40SISO	Ant2	5190	8.79	98.88	0.05	8.84	≤23.98	PASS
		5230	9.08	98.88	0.05	9.13	≤23.98	PASS
11AC20SISO	Ant2	5180	8.51	99.00	0.04	8.55	≤23.98	PASS
		5200	8.70	98.88	0.05	8.75	≤23.98	PASS
		5240	9.04	99.00	0.04	9.08	≤23.98	PASS
11AC40SISO	Ant2	5190	8.77	98.88	0.05	8.82	≤23.98	PASS
		5230	8.96	98.88	0.05	9.01	≤23.98	PASS
11AC80SISO	Ant2	5210	8.89	98.88	0.05	8.94	≤23.98	PASS
11AX20SISO	Ant2	5180	8.32	99.00	0.04	8.36	≤23.98	PASS
		5200	12.80	98.88	0.05	12.85	≤23.98	PASS
		5240	13.02	98.88	0.05	13.07	≤23.98	PASS
11AX40SISO	Ant2	5190	12.63	98.88	0.05	12.68	≤23.98	PASS
		5230	13.05	99.00	0.04	13.09	≤23.98	PASS
11AX80SISO	Ant2	5210	12.78	98.88	0.05	12.83	≤23.98	PASS

Note: The Duty Cycle Factor is compensated in the graph.

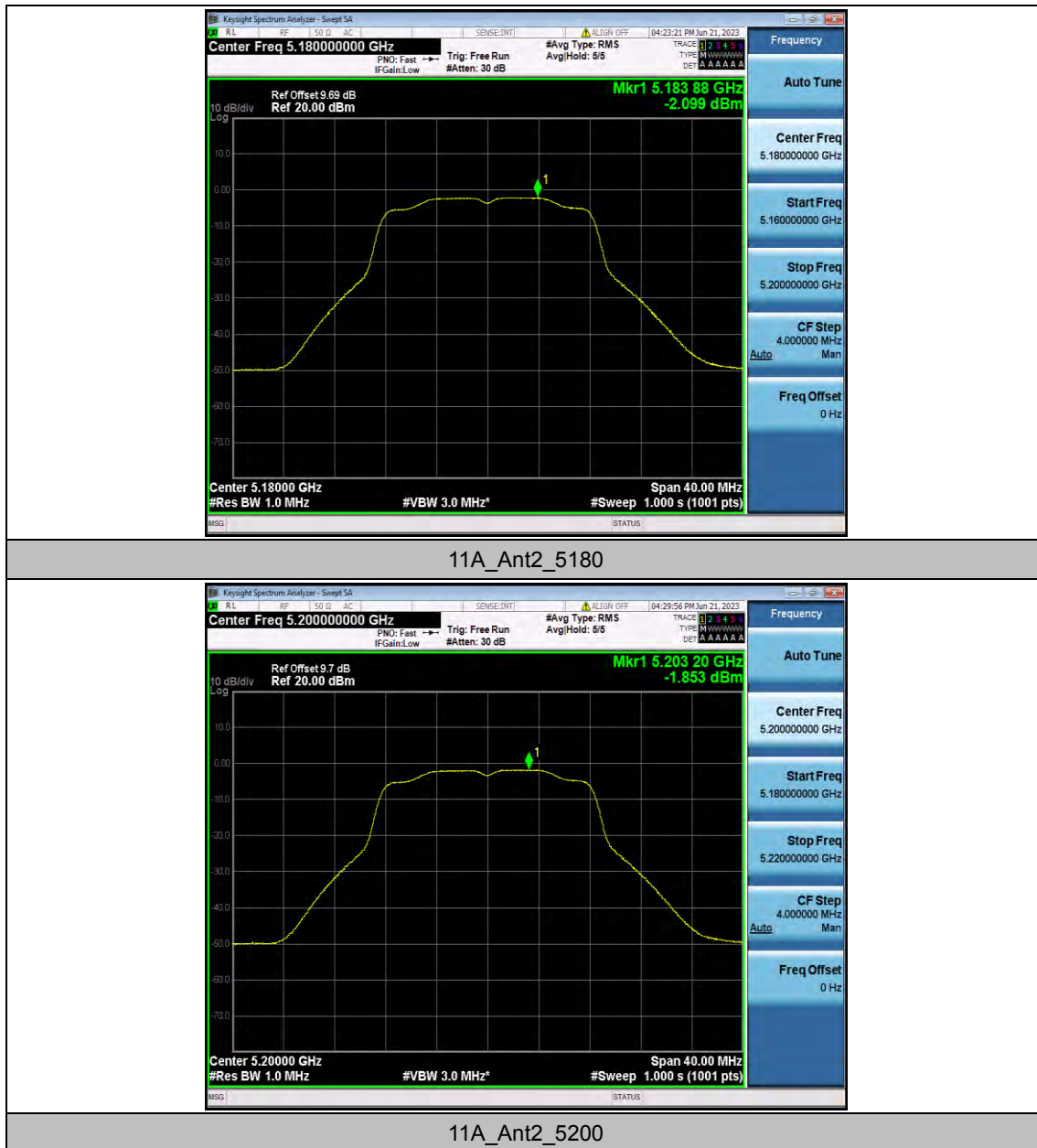
Appendix D.5: Maximum power spectral density

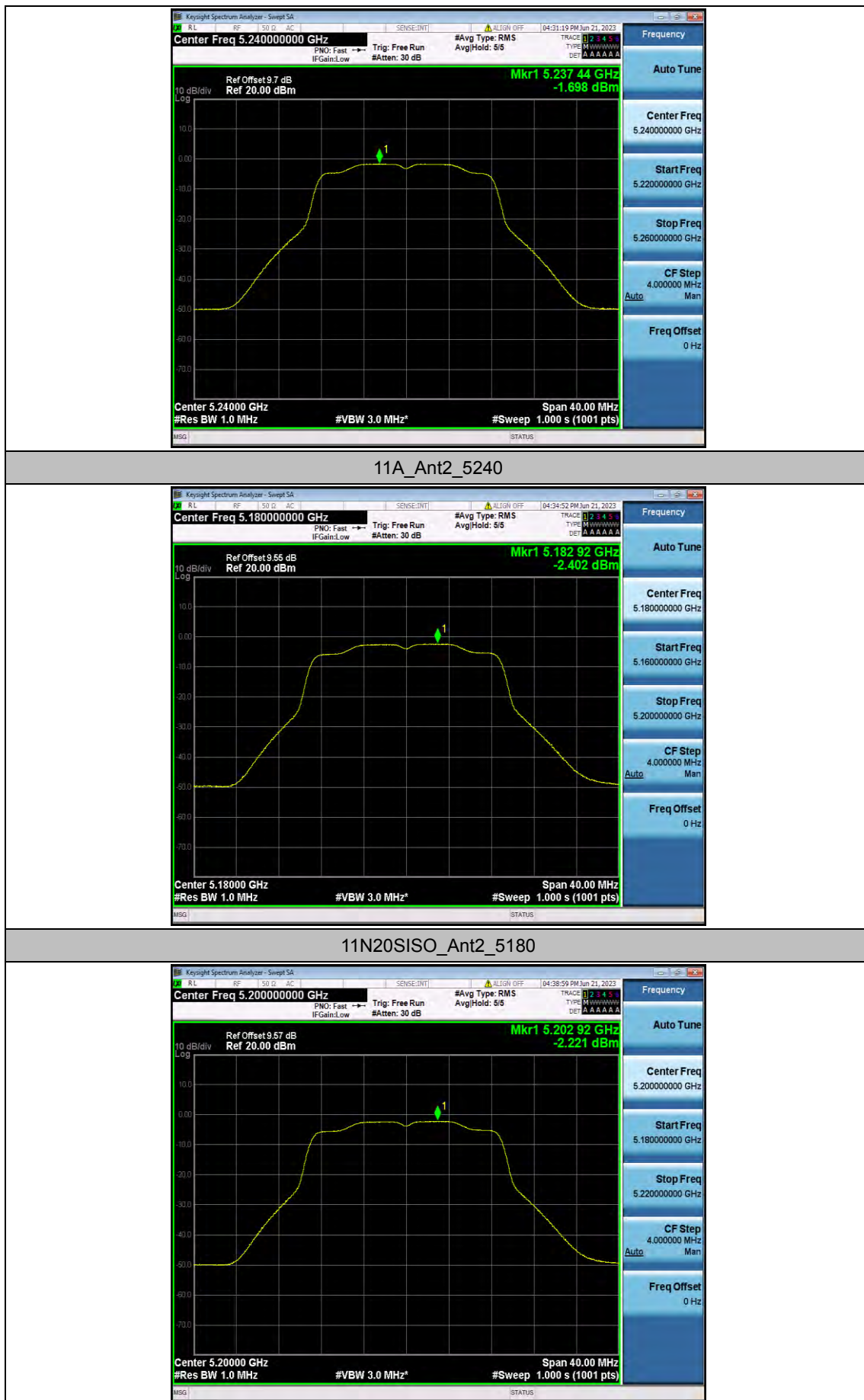
Test Result

Test Mode	Antenna	Frequency[MHz]	Result [dBm/MHz]	Limit[dBm/MHz]	Verdict
11A	Ant2	5180	-2.1	≤11.00	PASS
		5200	-1.85	≤11.00	PASS
		5240	-1.7	≤11.00	PASS
11N20SISO	Ant2	5180	-2.4	≤11.00	PASS
		5200	-2.22	≤11.00	PASS
		5240	-2.11	≤11.00	PASS
11N40SISO	Ant2	5190	-5.45	≤11.00	PASS
		5230	-5.15	≤11.00	PASS
11AC20SISO	Ant2	5180	-2.48	≤11.00	PASS
		5200	-2.24	≤11.00	PASS
		5240	-1.98	≤11.00	PASS
11AC40SISO	Ant2	5190	-5.5	≤11.00	PASS
		5230	-5.22	≤11.00	PASS
11AC80SISO	Ant2	5210	-8.23	≤11.00	PASS
11AX20SISO	Ant2	5180	-2.95	≤11.00	PASS
		5200	1.53	≤11.00	PASS
		5240	1.83	≤11.00	PASS
11AX40SISO	Ant2	5190	-1.81	≤11.00	PASS
		5230	-1.38	≤11.00	PASS
11AX80SISO	Ant2	5210	-4.42	≤11.00	PASS

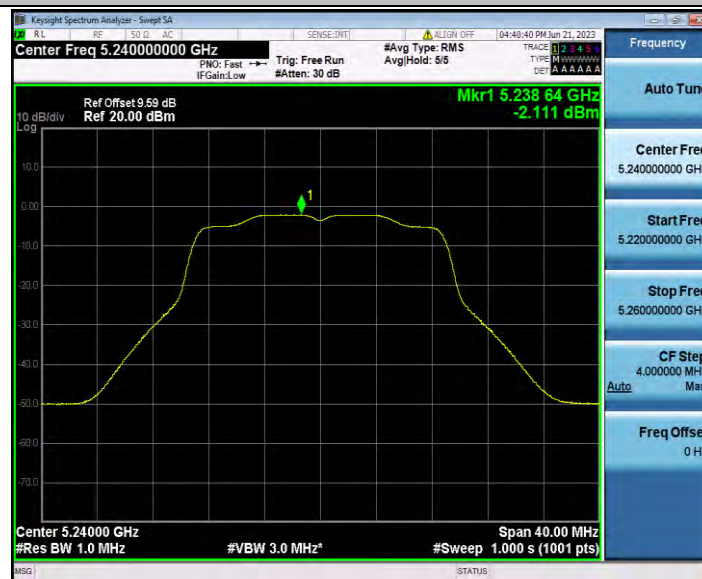
Note: 1.The Result and Limit Unit is dBm/500 kHz in the band 5.725–5.85 GHz.
 2.The Duty Cycle Factor and RBW Factor is compensated in the graph.

Test Graphs





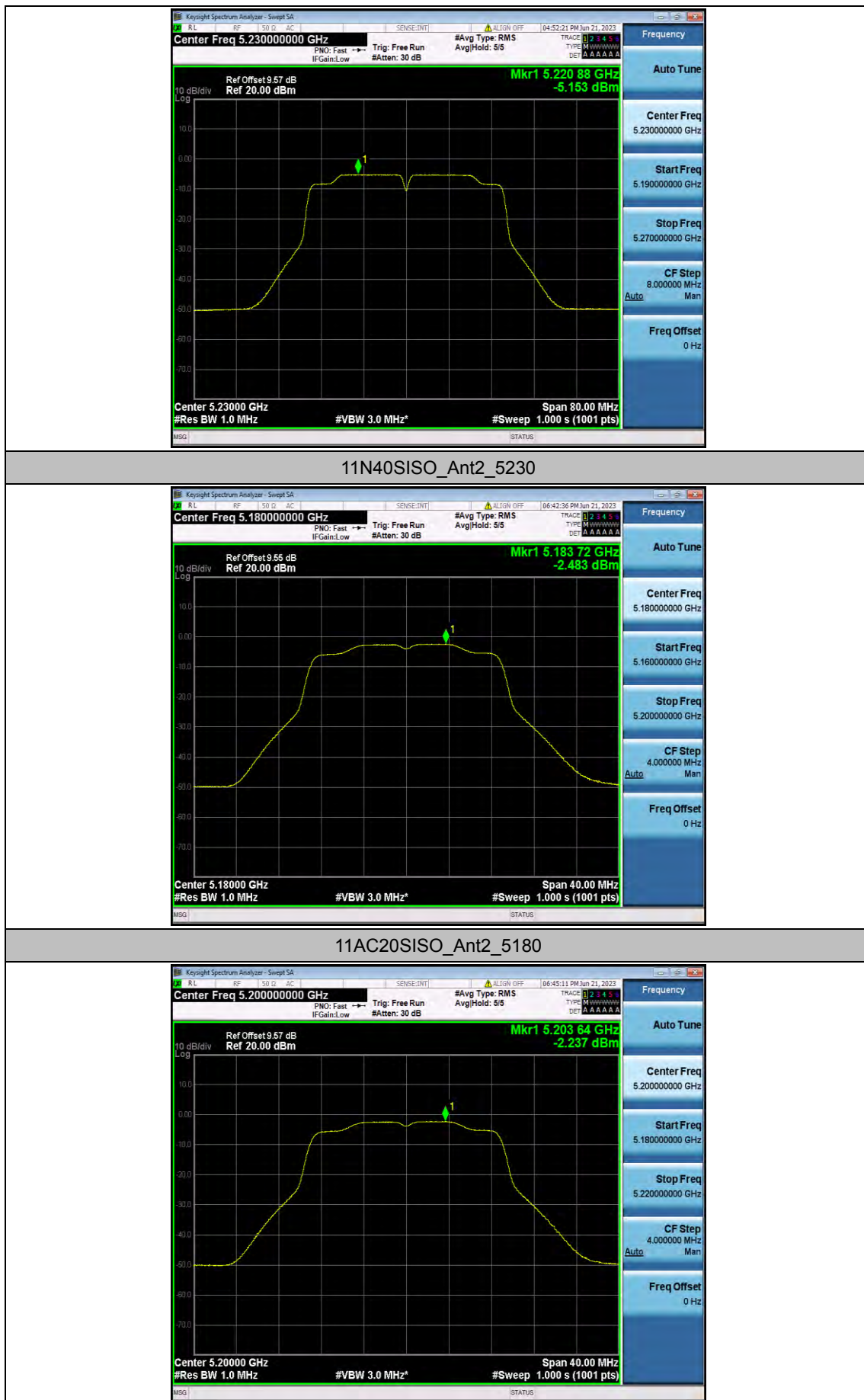
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11N20SISO_Ant2_5240



11N40SISO_Ant2_5190



11AC20SISO_Ant2_5200



11AC20SISO_Ant2_5240



11AC40SISO_Ant2_5190



11AC40SISO_Ant2_5230



11AC80SISO_Ant2_5210



11AX20SISO_Ant2_5180



11AX20SISO_Ant2_5200



11AX20SISO_Ant2_5240



11AX40SISO_Ant2_5190



11AX40SISO_Ant2_5230



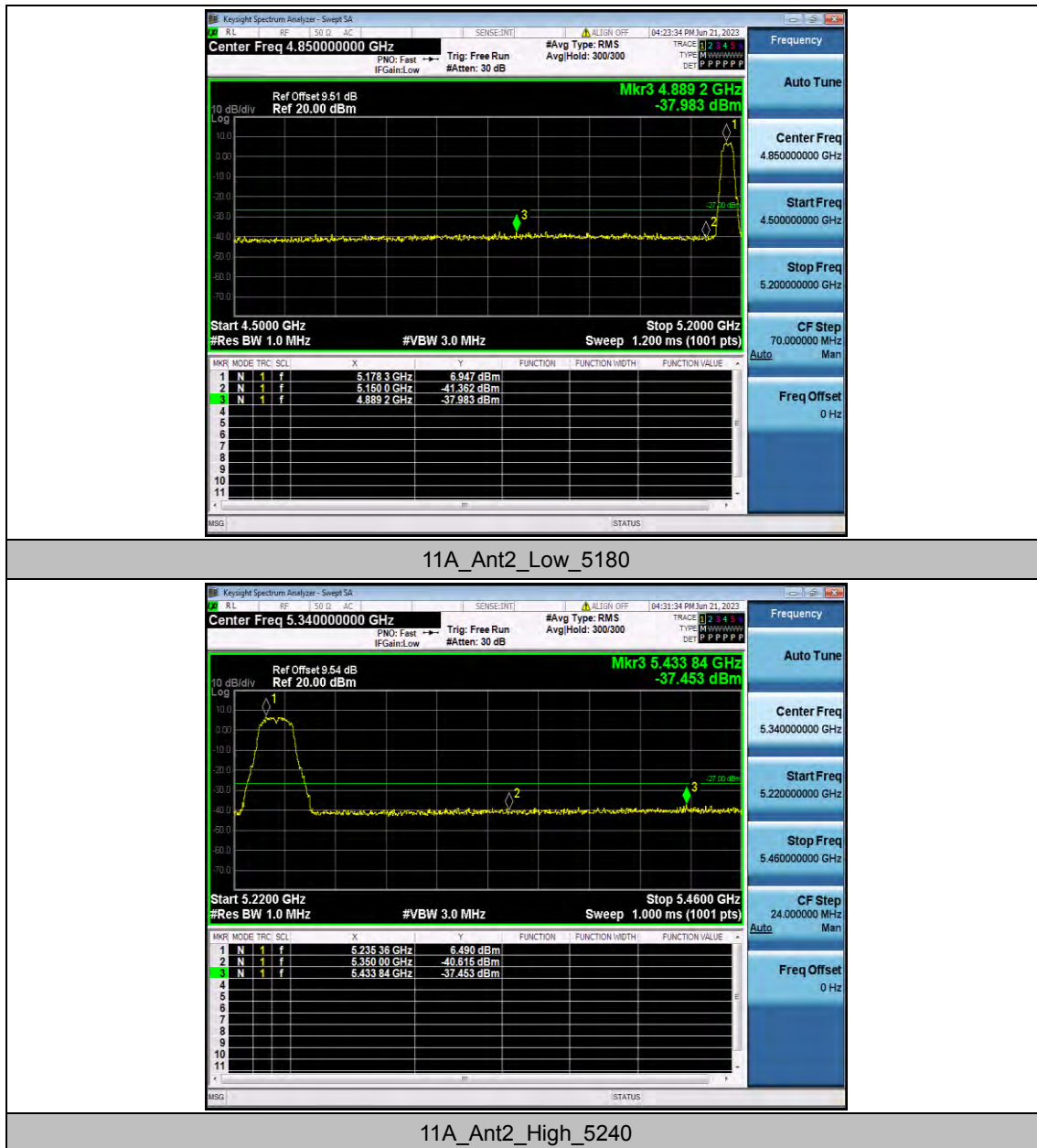
11AX80SISO_Ant2_5210

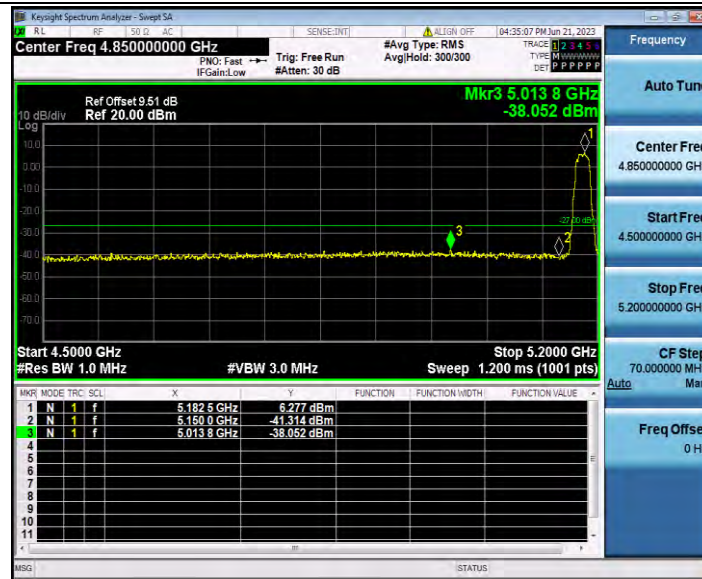
Appendix D.6: Band edge measurements

Test Result B1

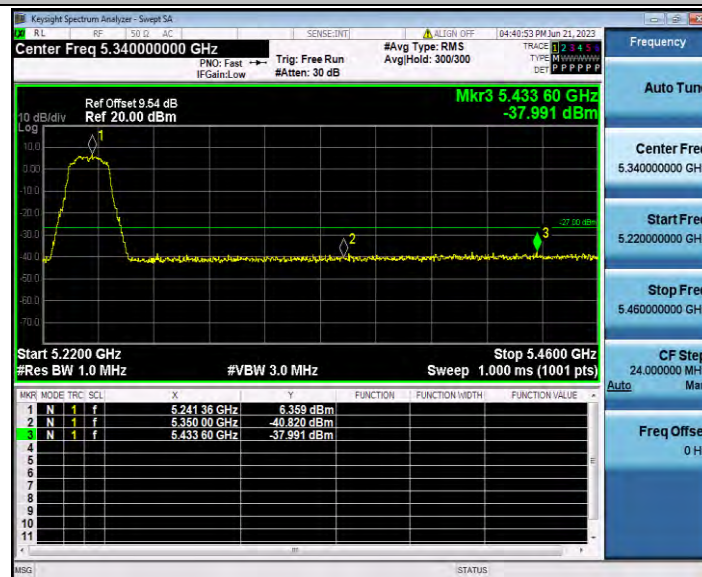
Test Mode	Antenna	ChName	Frequency[MHz]	Result[dBm]	Limit[dBm]	Verdict
11A	Ant2	Low	5180	-37.98	≤ -27	PASS
		High	5240	-37.45	≤ -27	PASS
11N20SISO	Ant2	Low	5180	-38.05	≤ -27	PASS
		High	5240	-37.99	≤ -27	PASS
11N40SISO	Ant2	Low	5190	-37.79	≤ -27	PASS
		High	5230	-38.49	≤ -27	PASS
11AC20SISO	Ant2	Low	5180	-37.71	≤ -27	PASS
		High	5240	-37.87	≤ -27	PASS
11AC40SISO	Ant2	Low	5190	-37.79	≤ -27	PASS
		High	5230	-37.94	≤ -27	PASS
11AC80SISO	Ant2	Low	5210	-37.75	≤ -27	PASS
		High	5210	-37.02	≤ -27	PASS
11AX20SISO	Ant2	Low	5180	-37.91	≤ -27	PASS
		High	5240	-37.95	≤ -27	PASS
11AX40SISO	Ant2	Low	5190	-37.54	≤ -27	PASS
		High	5230	-37.65	≤ -27	PASS
11AX80SISO	Ant2	Low	5210	-33.21	≤ -27	PASS
		High	5210	-37.88	≤ -27	PASS

Test Graphs B1

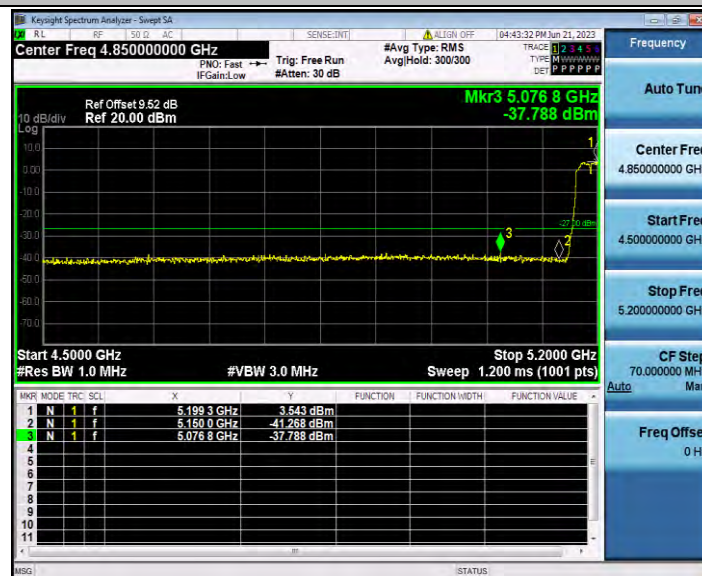




11N20SISO_Ant2_Low_5180



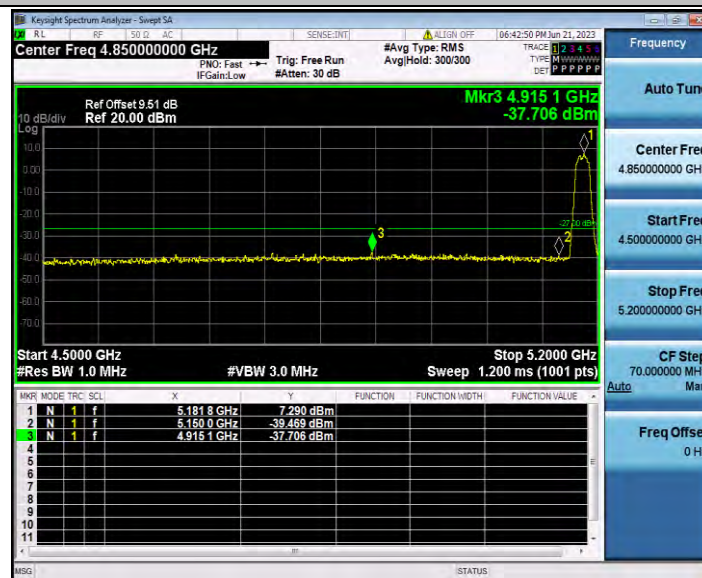
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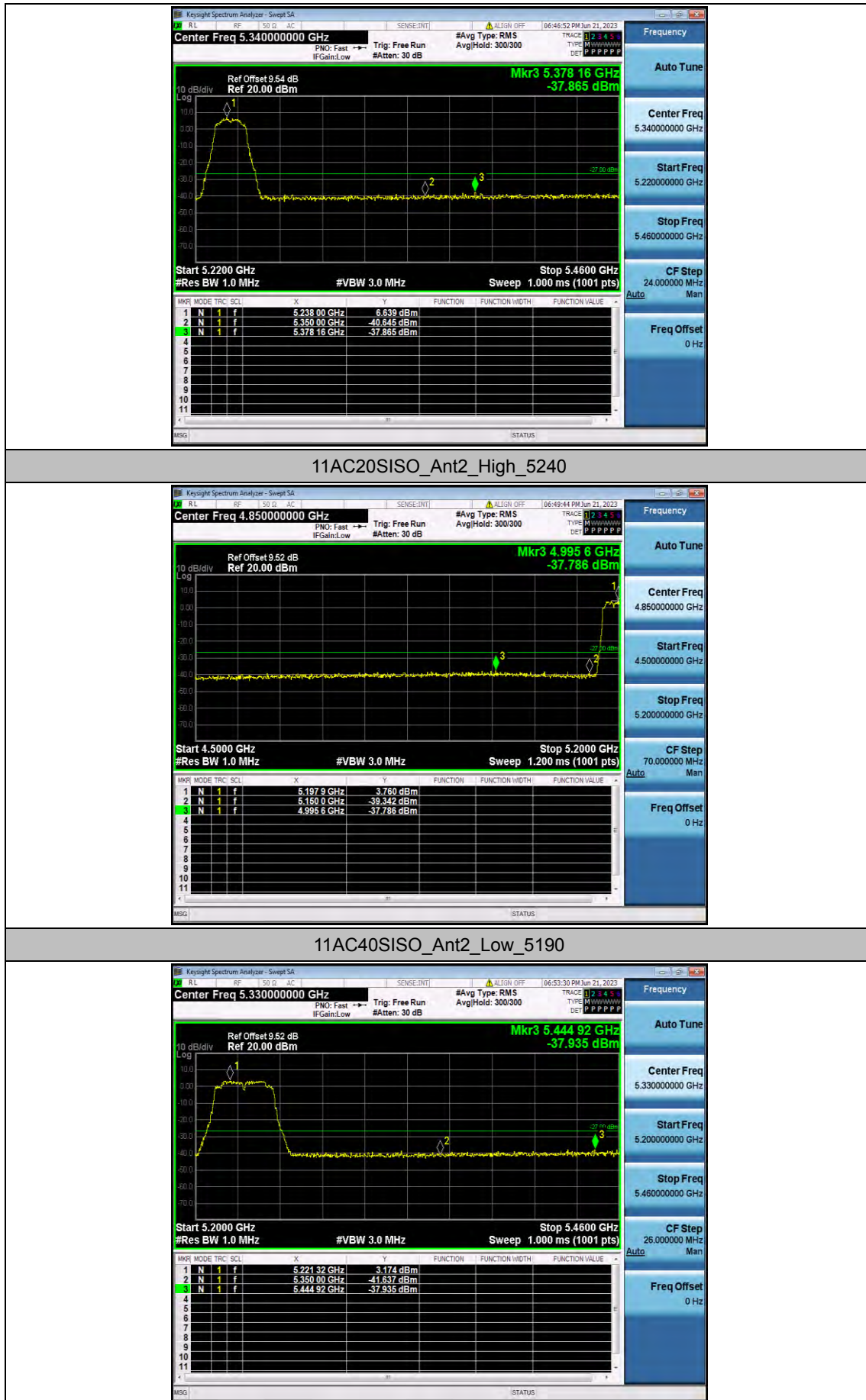
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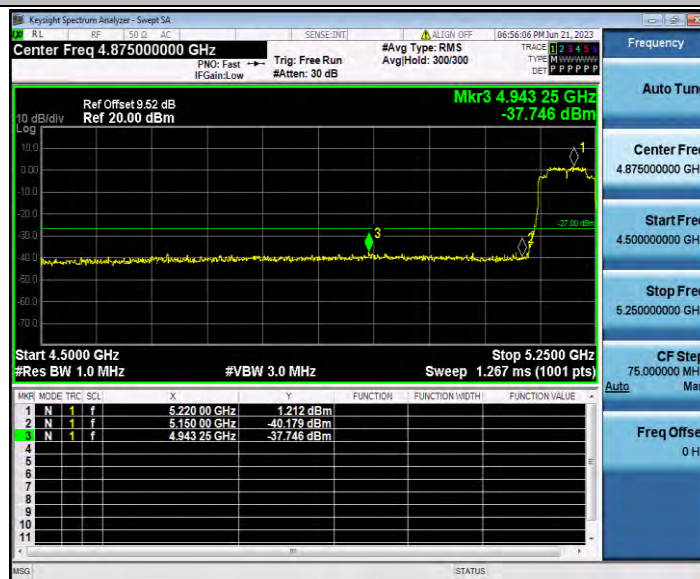
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11AC20SISO_Ant2_Low_5180



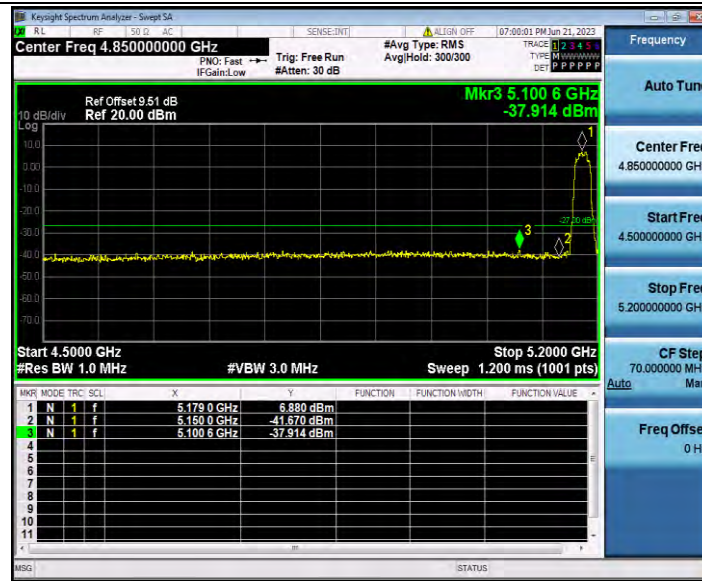
11AC40SISO_Ant2_High_5230



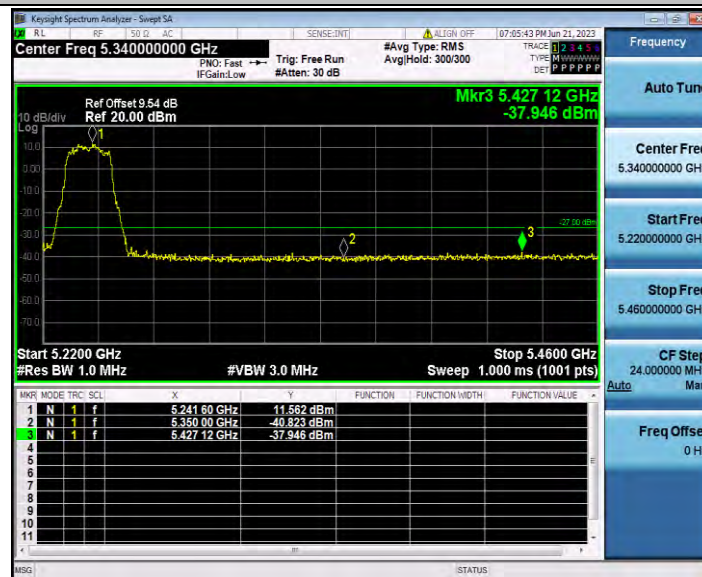
11AC80SISO_Ant2_Low_5210



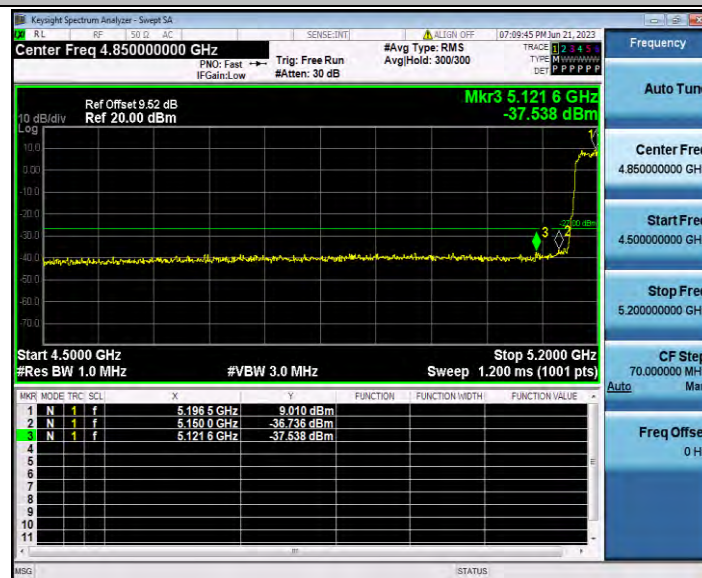
11AC80SISO_Ant2_High_5210



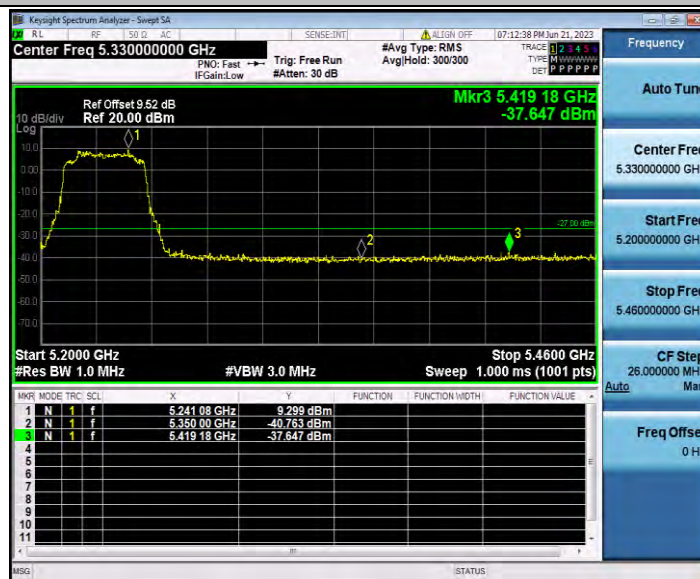
11AX20SISO_Ant2_Low_5180



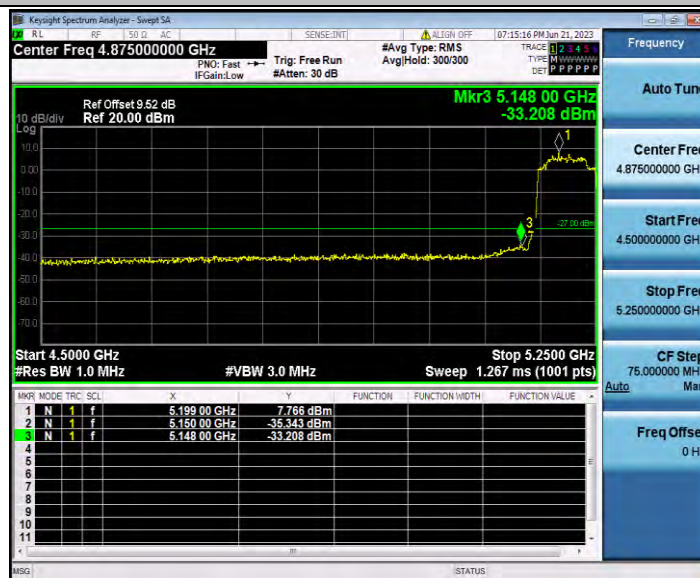
11AX20SISO_Ant2_High_5240



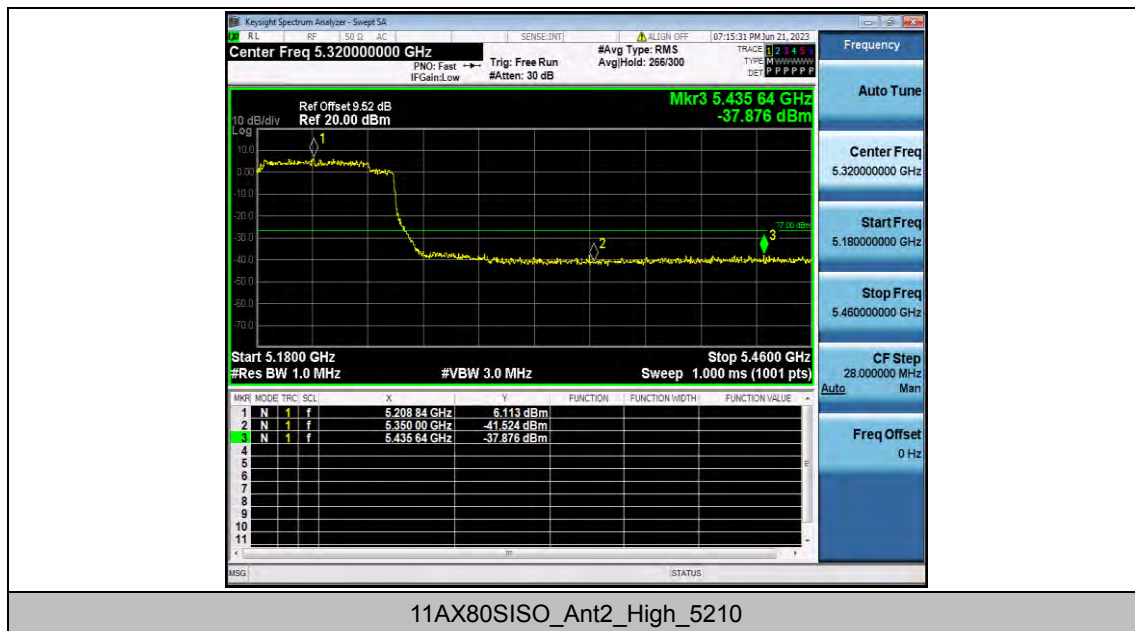
11AX40SISO_Ant2_Low_5190



11AX40SISO_Ant2_High_5230



11AX80SISO_Ant2_Low_5210



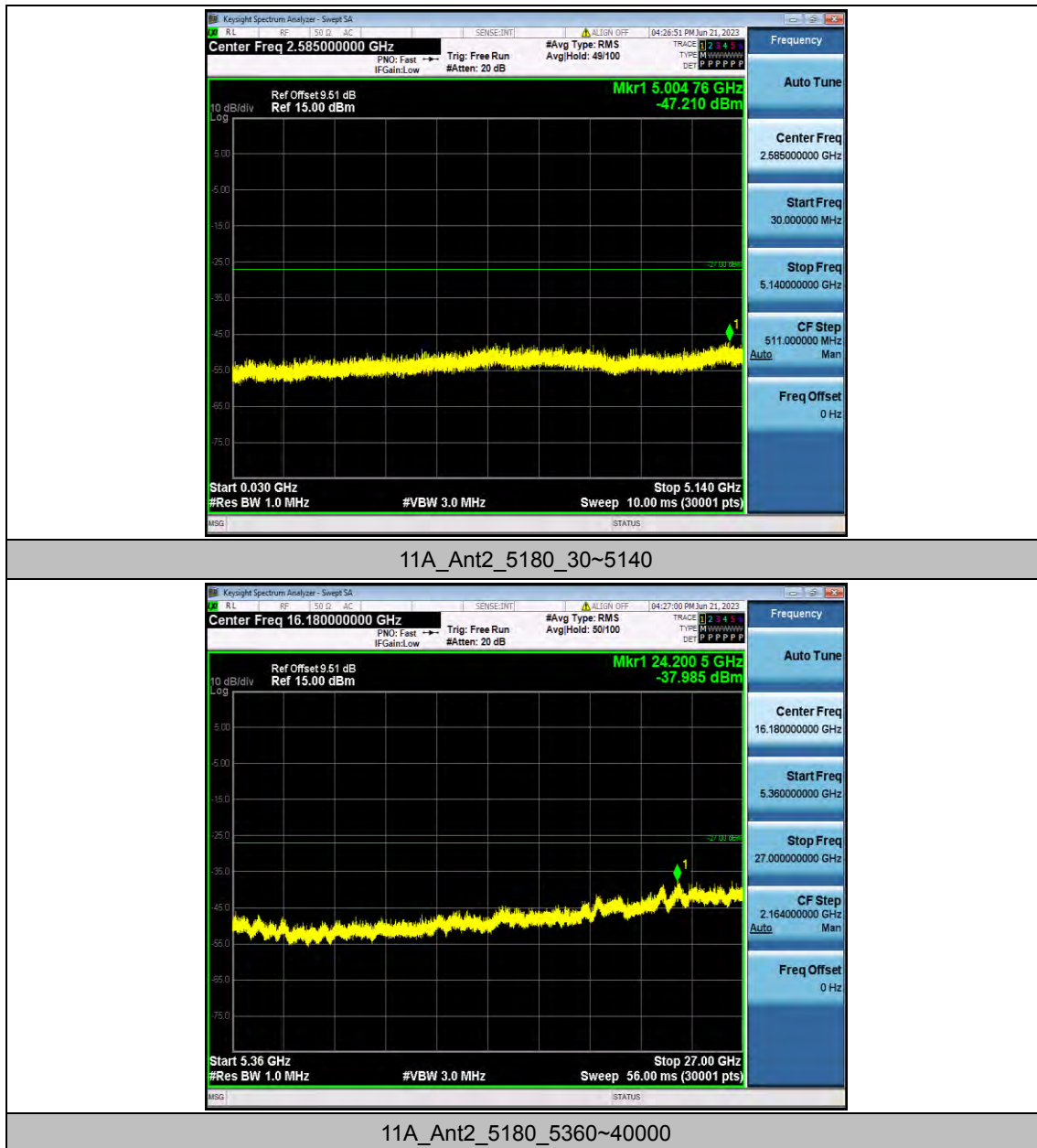
Appendix D.7: Conducted Spurious Emission

Test Result

Test Mode	Antenna	Frequency[MHz]	FreqRange [MHz]	Max. Fre [MHz]	Max. Level [dBm]	Limit [dBm]	Verdict
11A	Ant2	5180	30~5140	5004.76	-47.21	≤-27	PASS
			5360~40000	24200.51	-37.99	≤-27	PASS
		5200	30~5140	4830.5	-46.26	≤-27	PASS
			5360~40000	25217.59	-38.09	≤-27	PASS
		5240	30~5140	4984.66	-46.87	≤-27	PASS
			5360~40000	24278.41	-37.79	≤-27	PASS
11N20SISO	Ant2	5180	30~5140	3187.81	-47.18	≤-27	PASS
			5360~40000	26803.8	-37.55	≤-27	PASS
		5200	30~5140	4977.33	-47.51	≤-27	PASS
			5360~40000	26478.48	-38.13	≤-27	PASS
		5240	30~5140	2656.54	-46.71	≤-27	PASS
			5360~40000	24214.93	-37.9	≤-27	PASS
11N40SISO	Ant2	5190	30~5140	4935.43	-47.04	≤-27	PASS
			5360~40000	26516.71	-37.79	≤-27	PASS
		5230	30~5140	5002.03	-47	≤-27	PASS
			5360~40000	24222.15	-37.01	≤-27	PASS
11AC20SISO	Ant2	5180	30~5140	3169.24	-46.54	≤-27	PASS
			5360~40000	26508.77	-38.15	≤-27	PASS
		5200	30~5140	1747.3	-44.94	≤-27	PASS
			5360~40000	26068.04	-37.73	≤-27	PASS
		5240	30~5140	2565.75	-43.69	≤-27	PASS
			5360~40000	24824.46	-37.72	≤-27	PASS
11AC40SISO	Ant2	5190	30~5140	4988.06	-46.09	≤-27	PASS
			5360~40000	24280.57	-37.64	≤-27	PASS
		5230	30~5140	2902.16	-46.83	≤-27	PASS
			5360~40000	26997.11	-38.46	≤-27	PASS
11AC80SISO	Ant2	5210	30~5140	5138.81	-45.79	≤-27	PASS
			5360~40000	23537.6	-37.38	≤-27	PASS
11AX20SISO	Ant2	5180	30~5140	4927.42	-47.26	≤-27	PASS
			5360~40000	26759.8	-38.51	≤-27	PASS
		5200	30~5140	5132.16	-46.24	≤-27	PASS
			5360~40000	24282.02	-38.02	≤-27	PASS
		5240	30~5140	2559.96	-43.61	≤-27	PASS
			5360~40000	23469.79	-38.17	≤-27	PASS
11AX40SISO	Ant2	5190	30~5140	5138.98	-42.66	≤-27	PASS
			5360~40000	26909.11	-37.99	≤-27	PASS

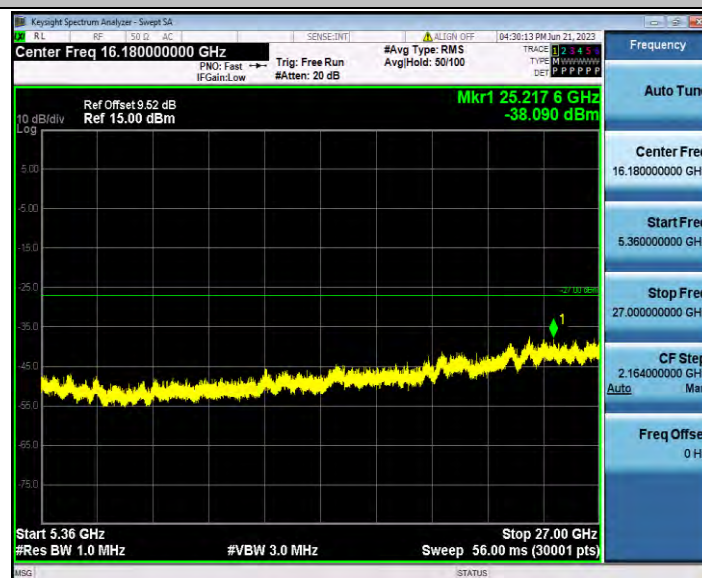
		5230	30~5140	2569.84	-44.37	≤-27	PASS
			5360~40000	24253.16	-38.07	≤-27	PASS
11AX80SISO	Ant2	5210	30~5140	5138.81	-38.81	≤-27	PASS
			5360~40000	25017.05	-37.16	≤-27	PASS

Test Graphs

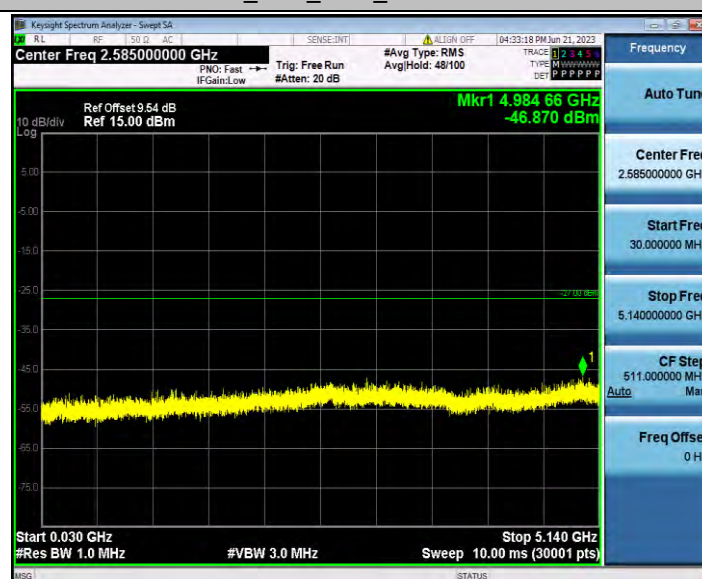




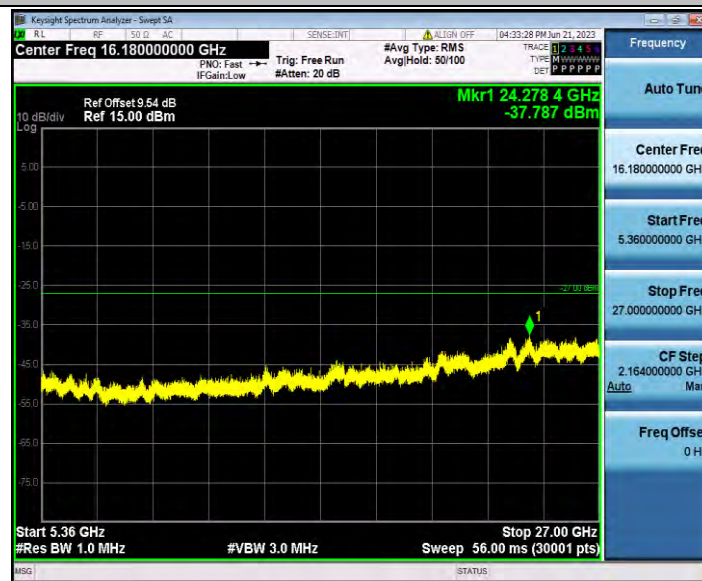
11A_Ant2_5200_30~5140



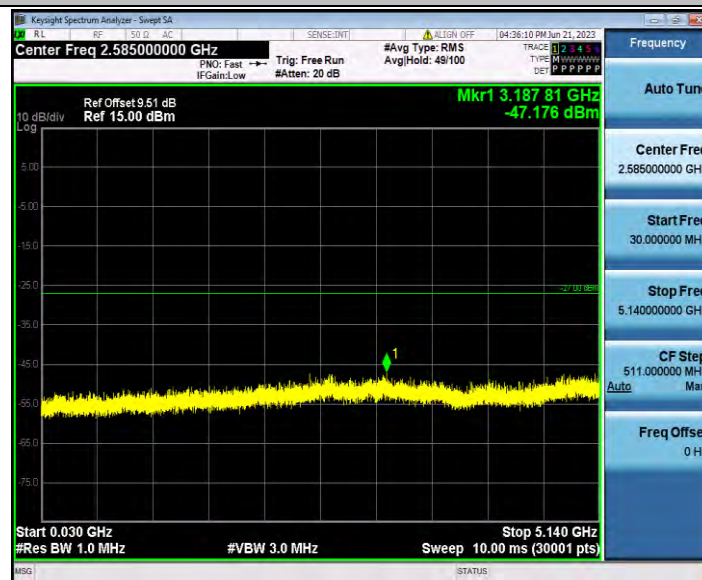
11A_Ant2_5200_5360~40000



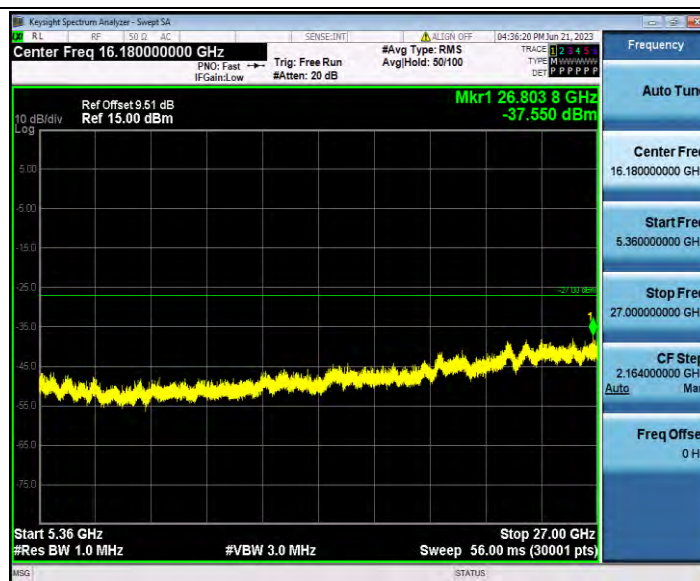
11A_Ant2_5240_30~5140



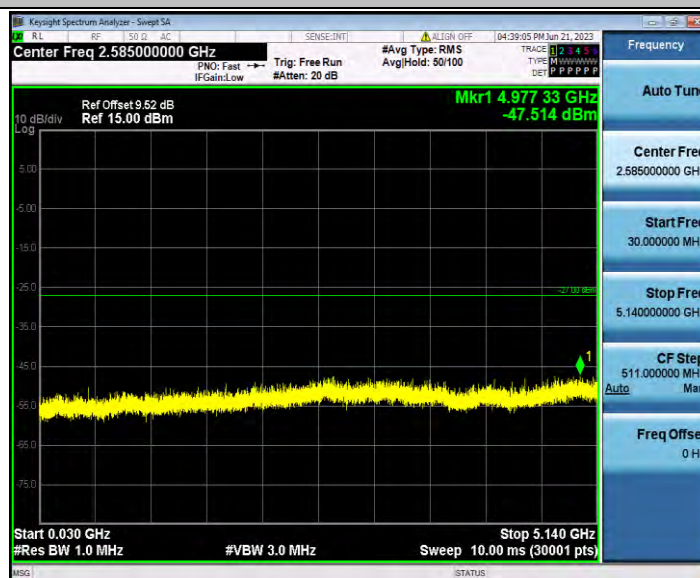
11A_Ant2_5240_5360~40000



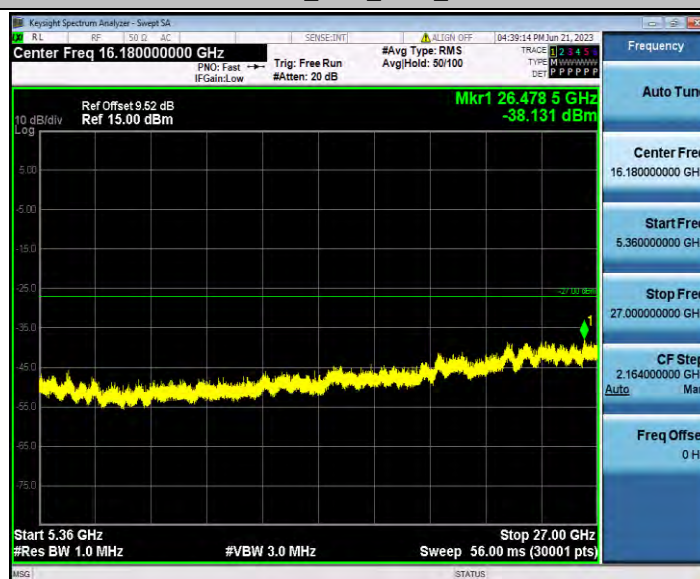
11N20SISO_Ant2_5180_30~5140



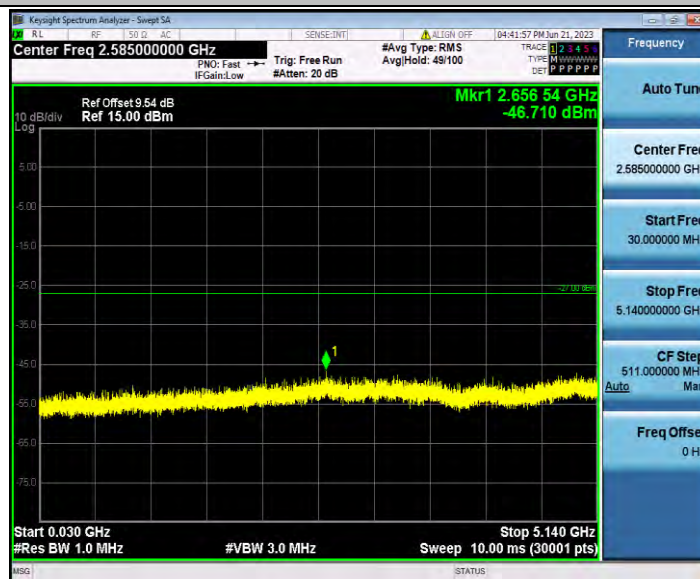
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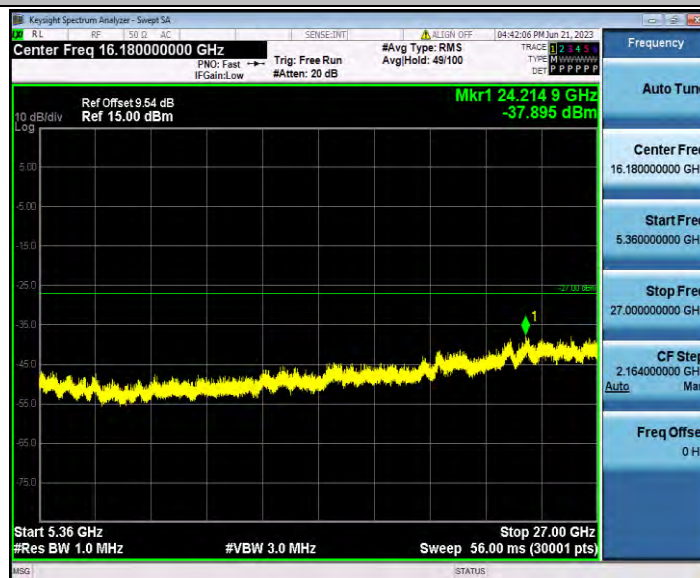
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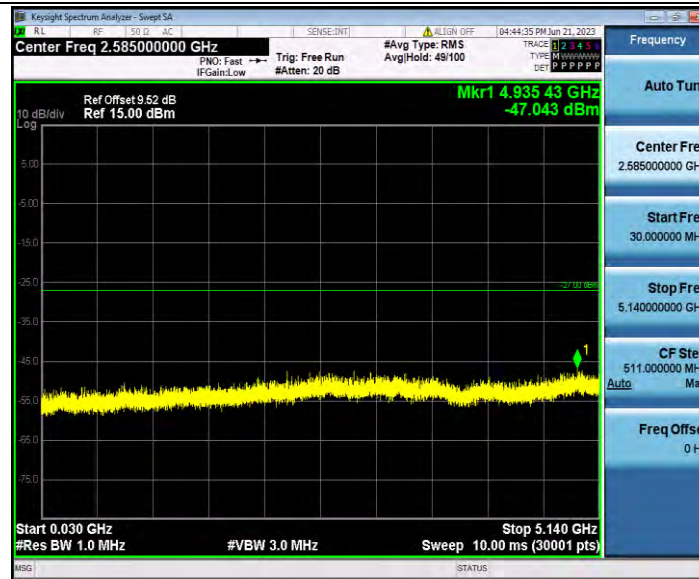
11N20SISO_Ant2_5200_5360~40000



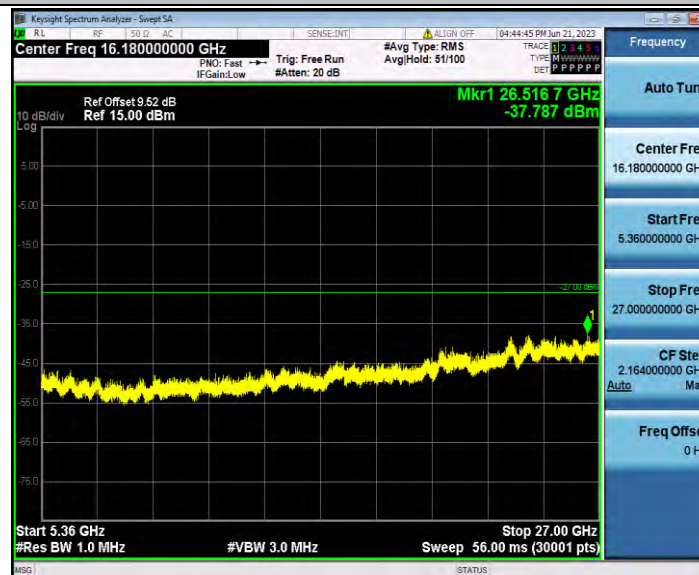
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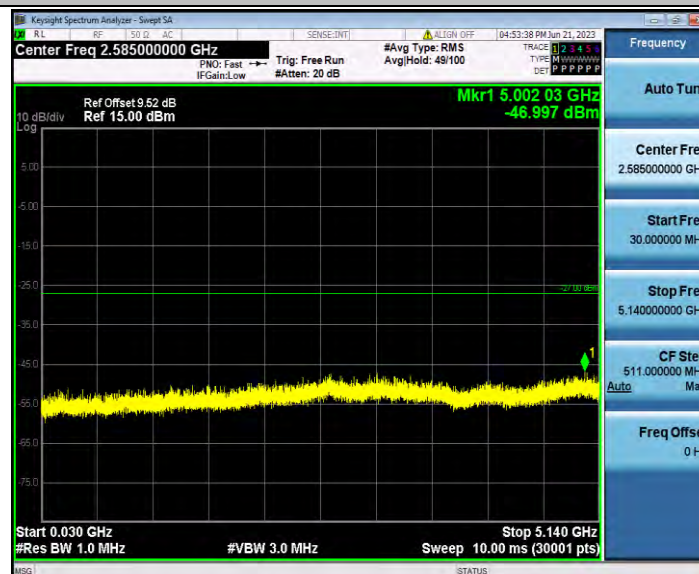
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11N40SISO_Ant2_5190_30~5140



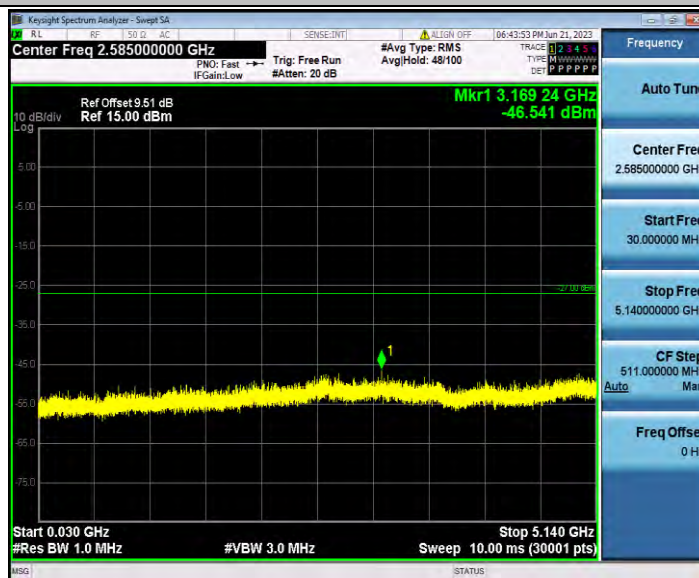
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11N40SISO_Ant2_5230_30~5140



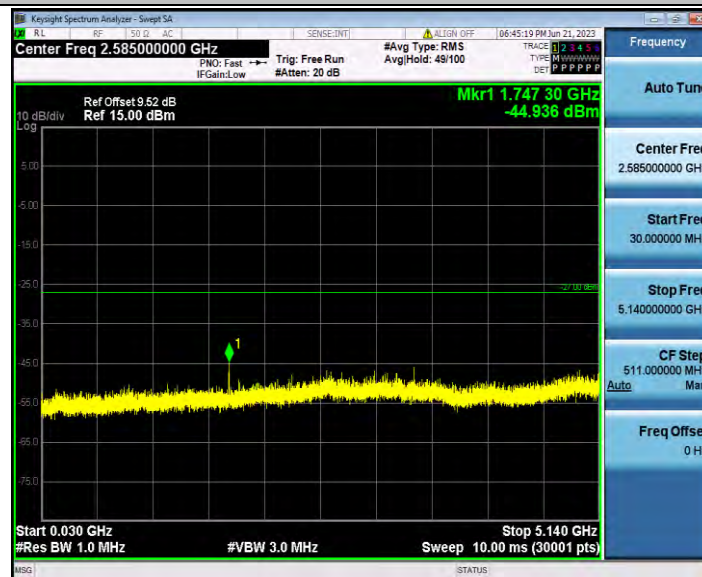
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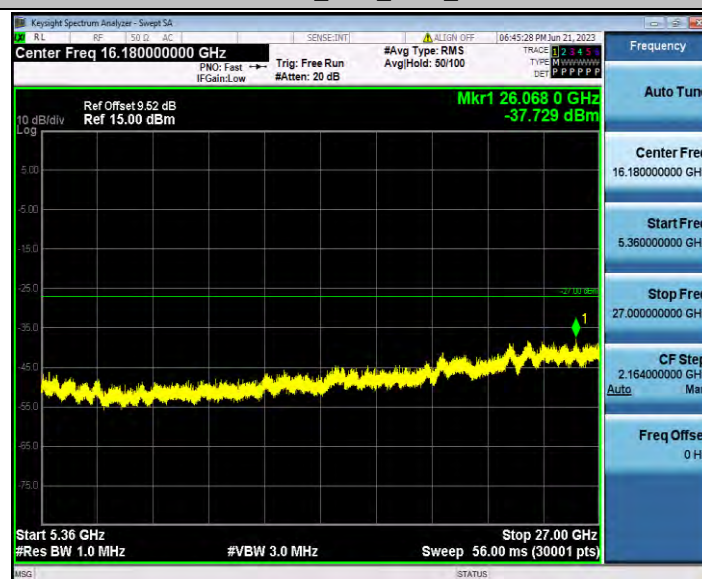
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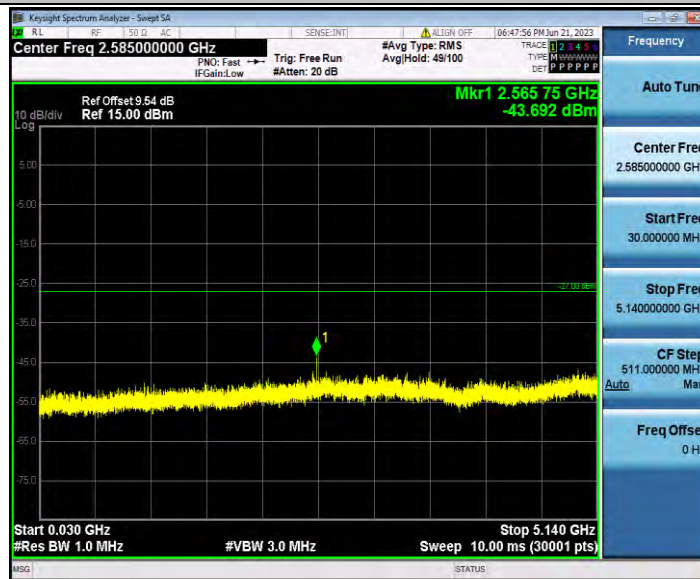
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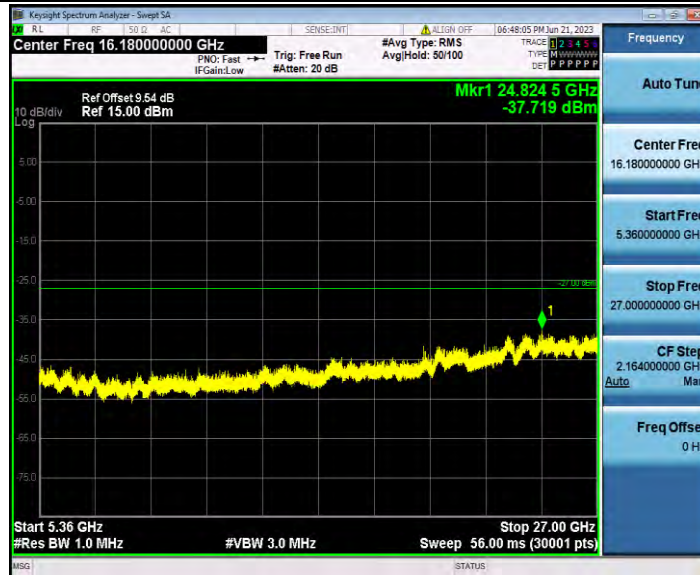
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11AC20SISO_Ant2_5200_5360~40000



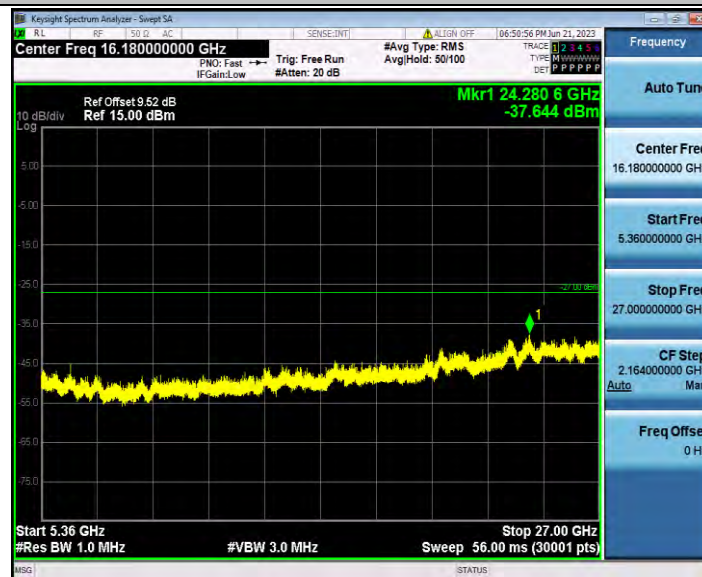
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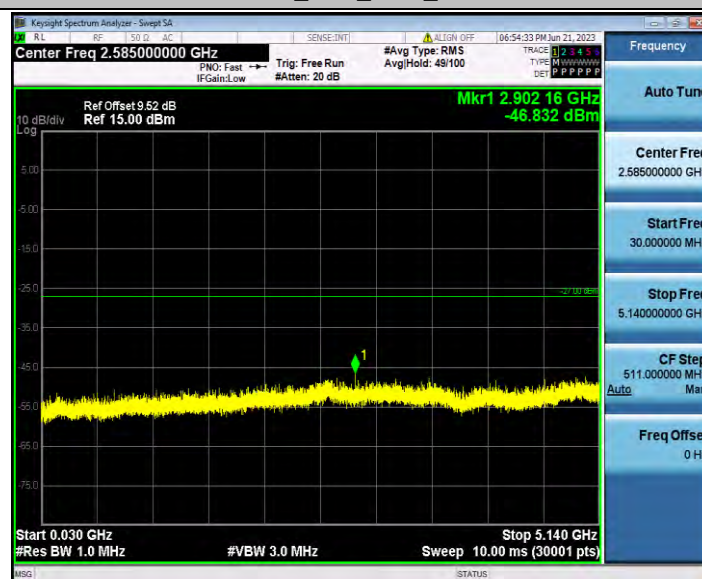
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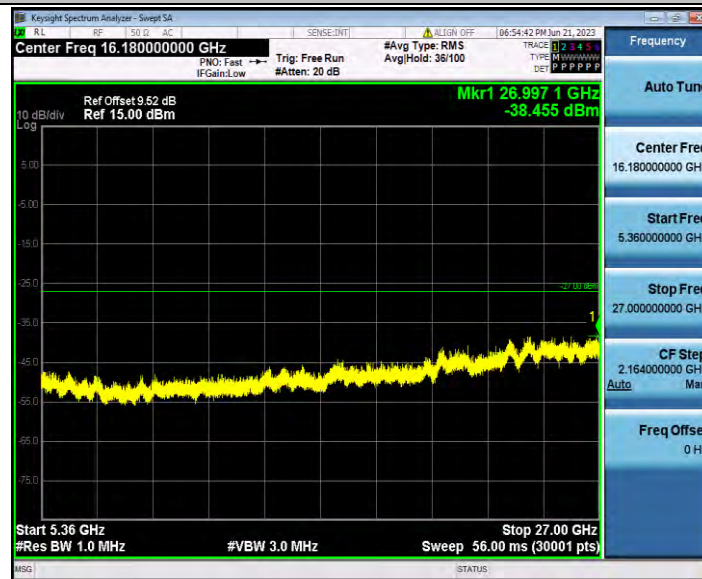
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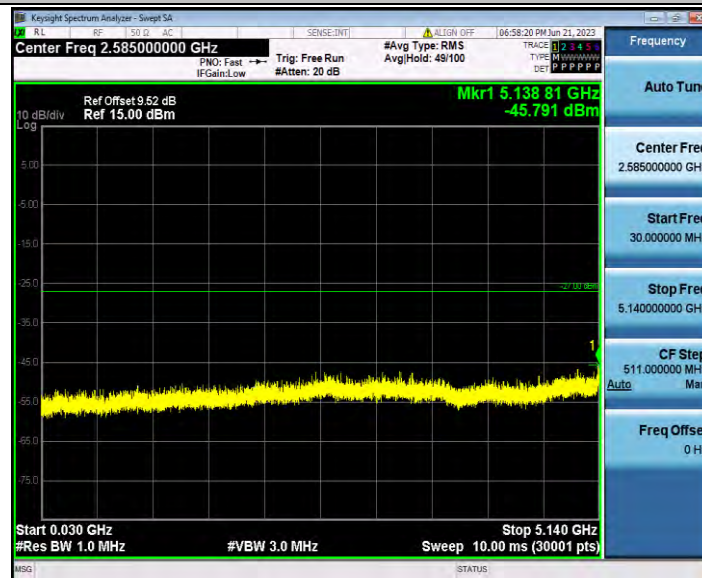
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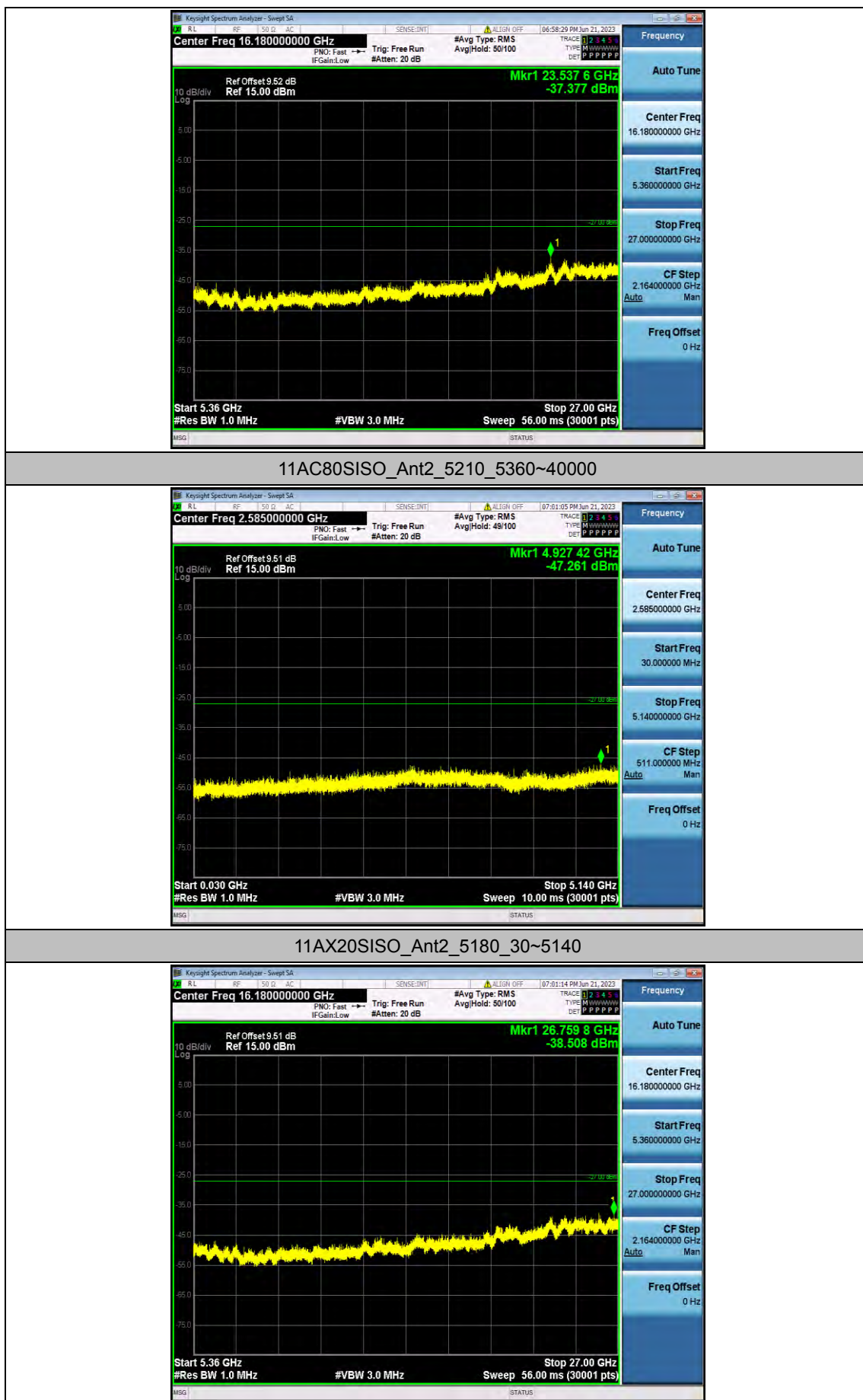
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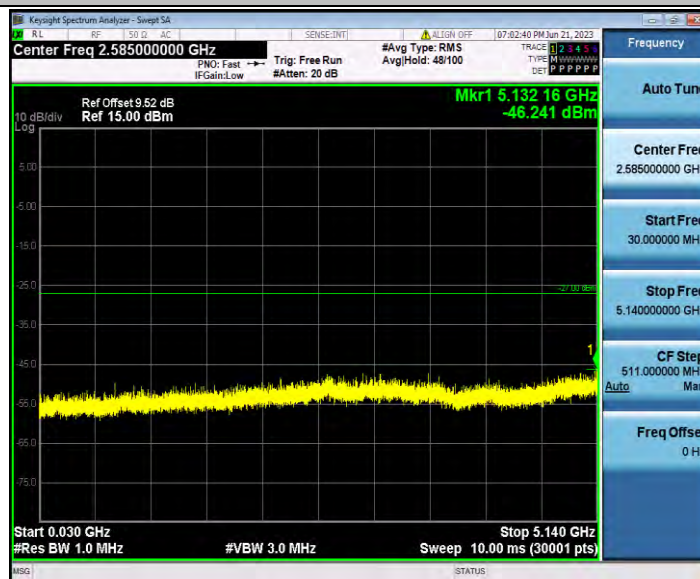
11AC40SISO_Ant2_5230_5360~40000



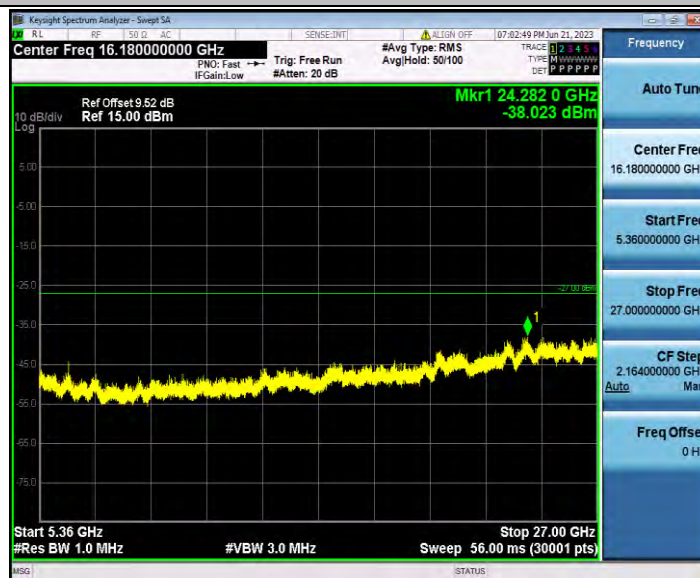
11AC80SISO_Ant2_5210_30~5140



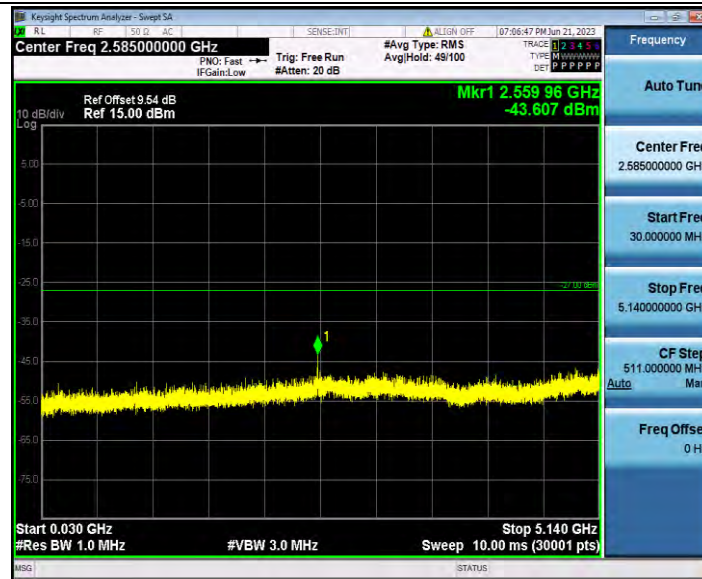
11AX20SISO_Ant2_5180_5360~40000



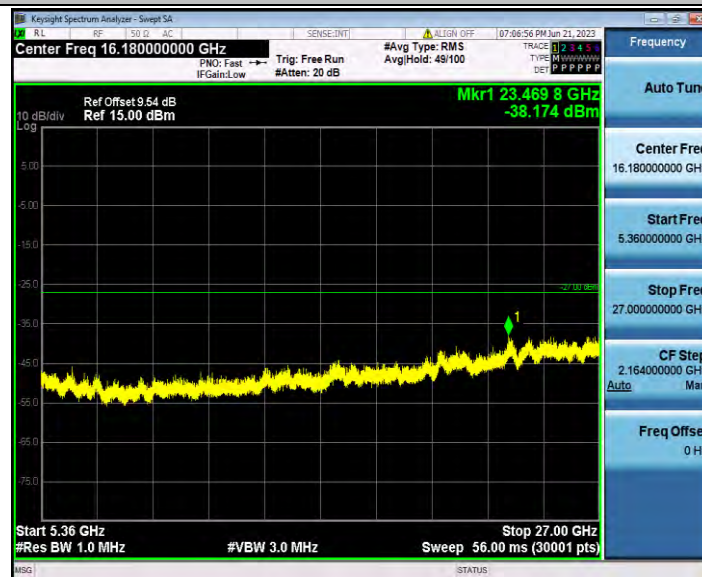
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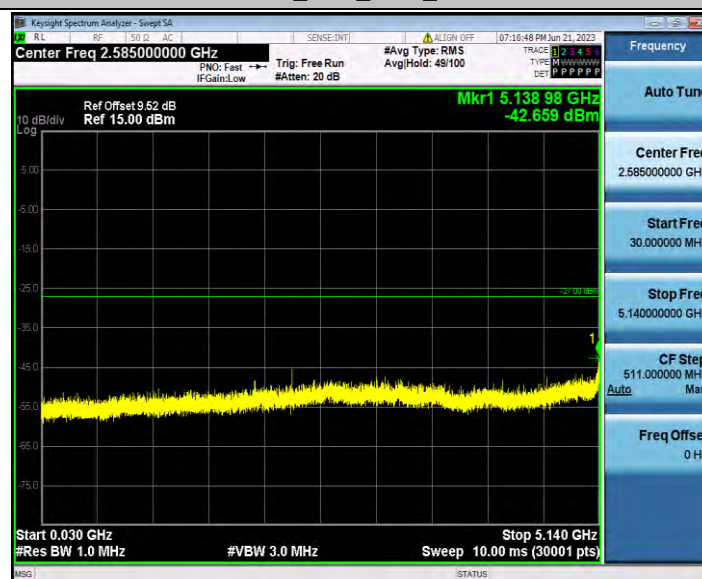
11AX20SISO_Ant2_5200_5360~40000



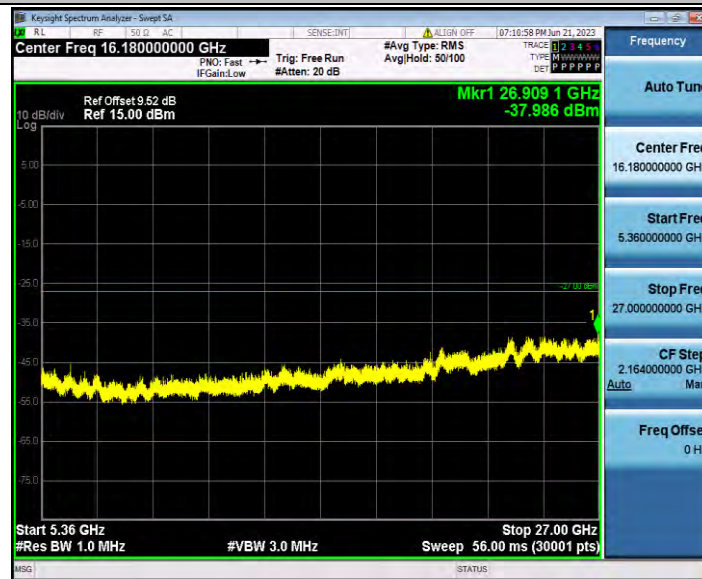
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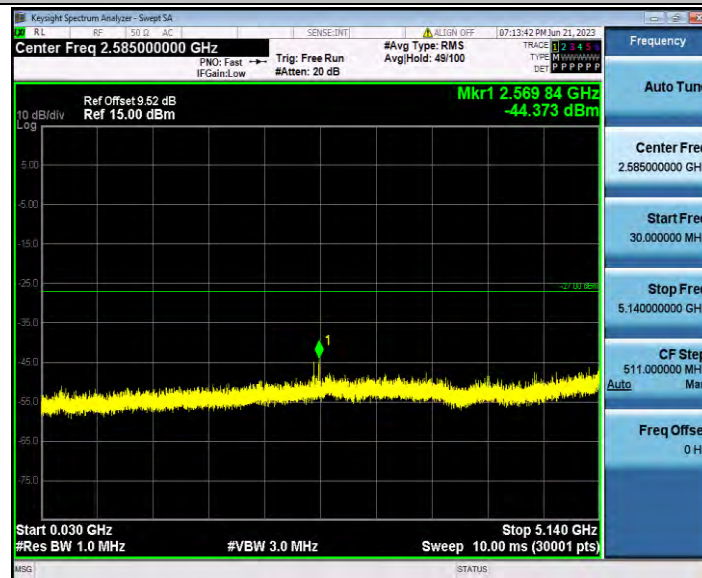
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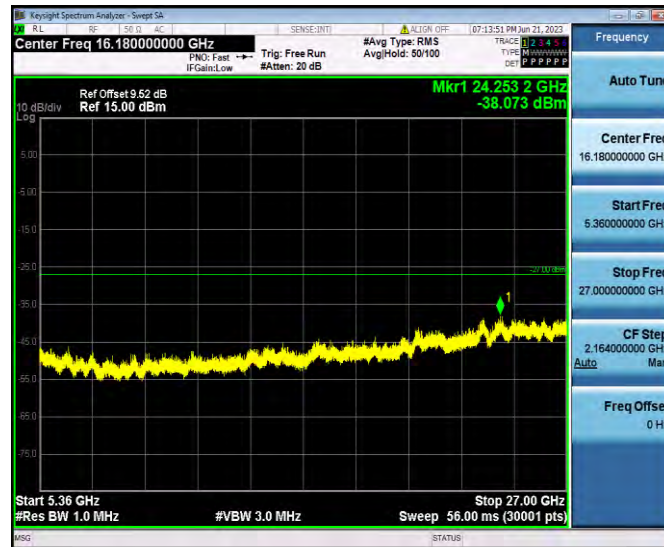
11AX40SISO_Ant2_5190_30~5140



11AX40SISO_Ant2_5190_5360~40000



11AX40SISO_Ant2_5230_30~5140



11AX40SISO_Ant2_5230_5360~40000



11AX80SISO_Ant2_5210_5360~40000



11AX80SISO_Ant2_5210_5360~40000