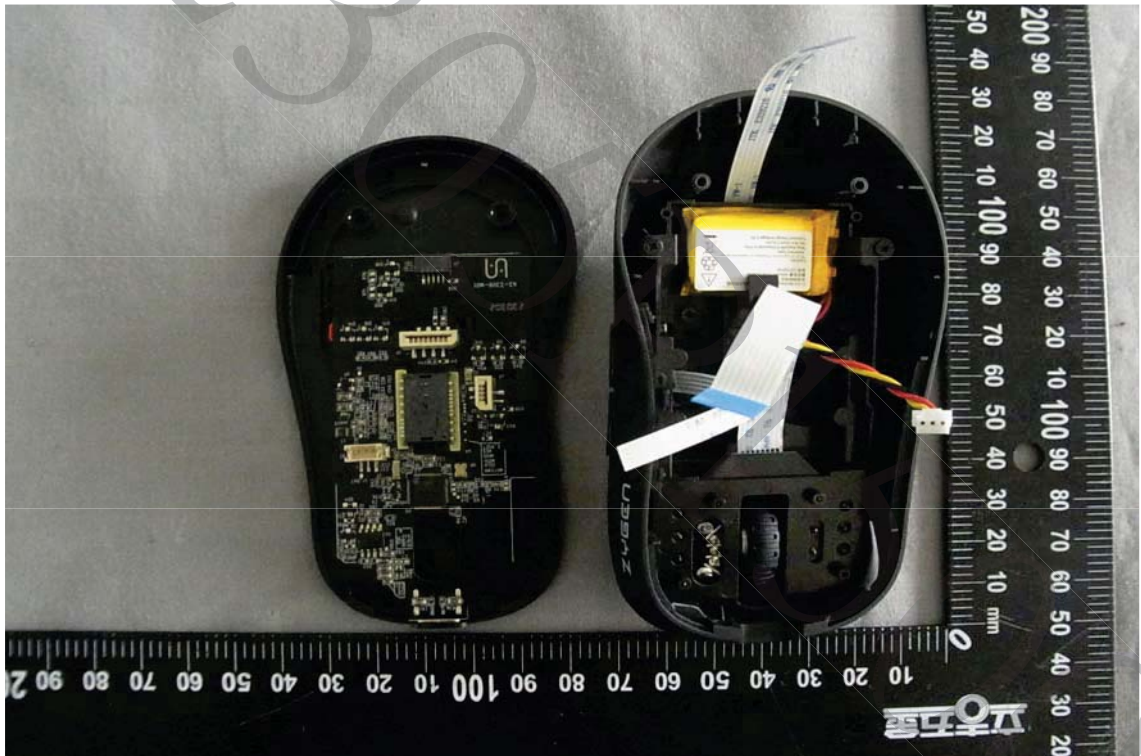


Appendix II - Photograph of EUT 2

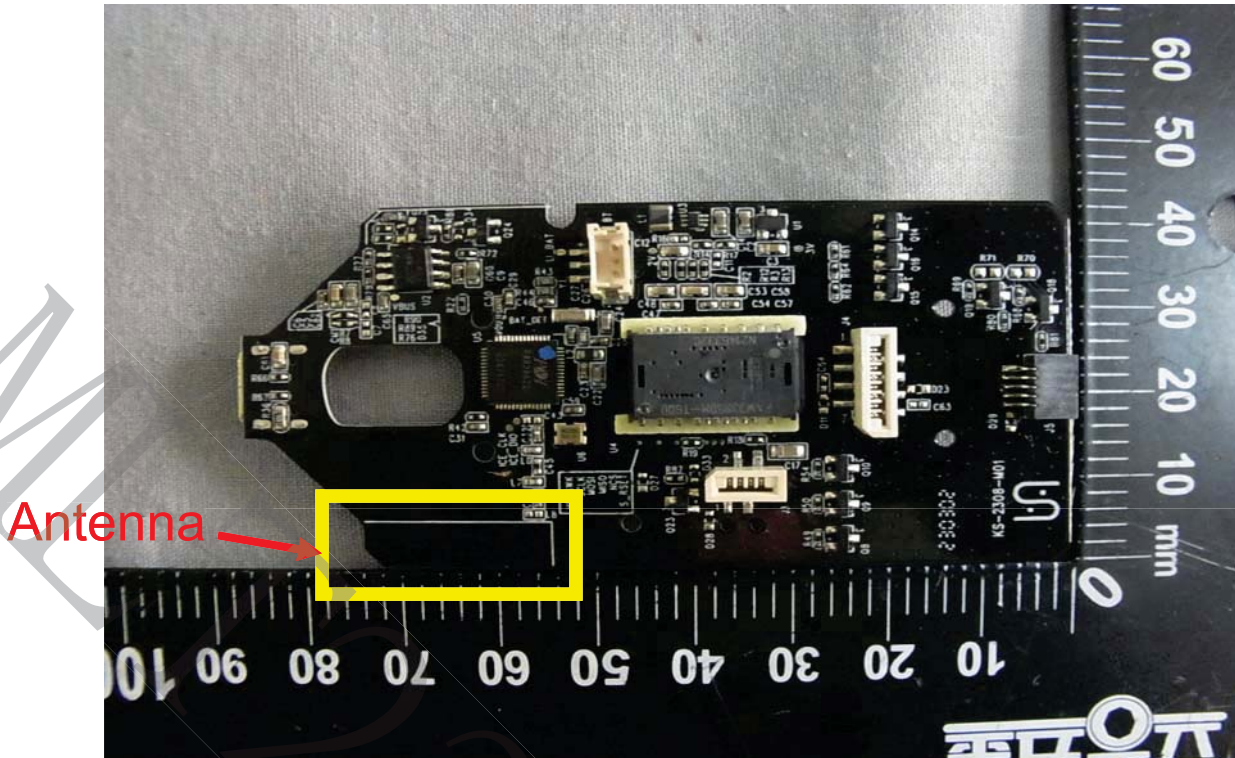
< I/O Port of EUT >



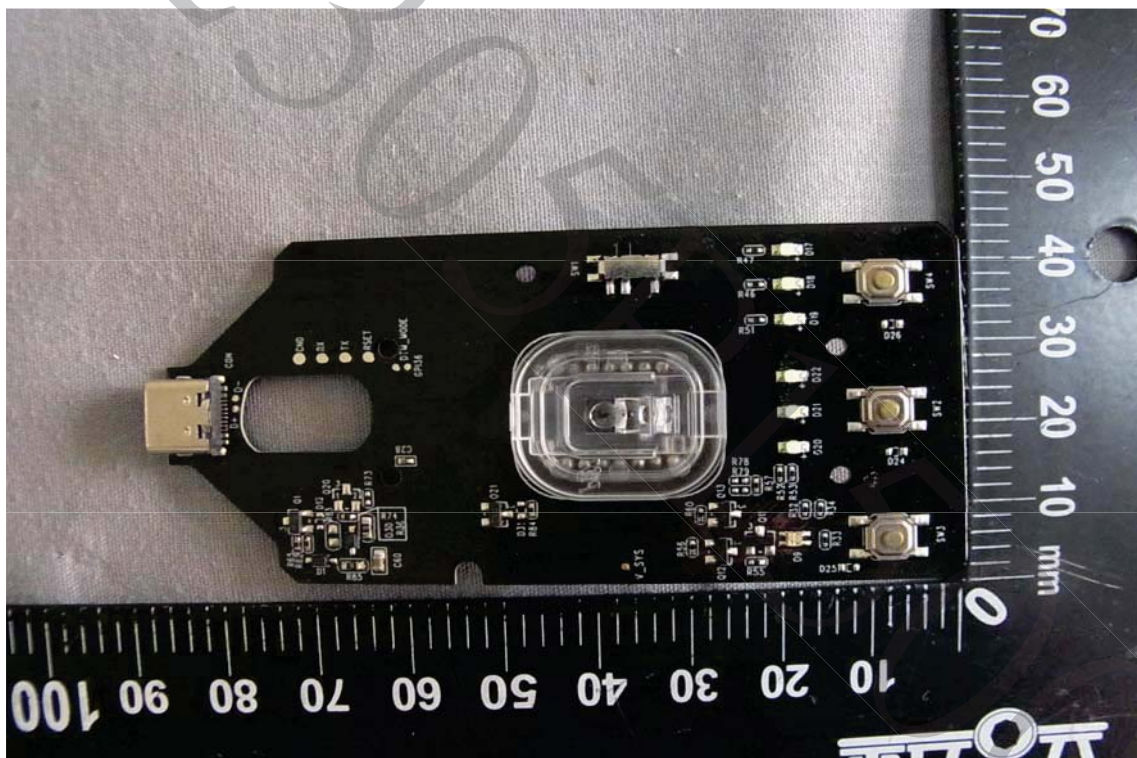
< Inside View of EUT >



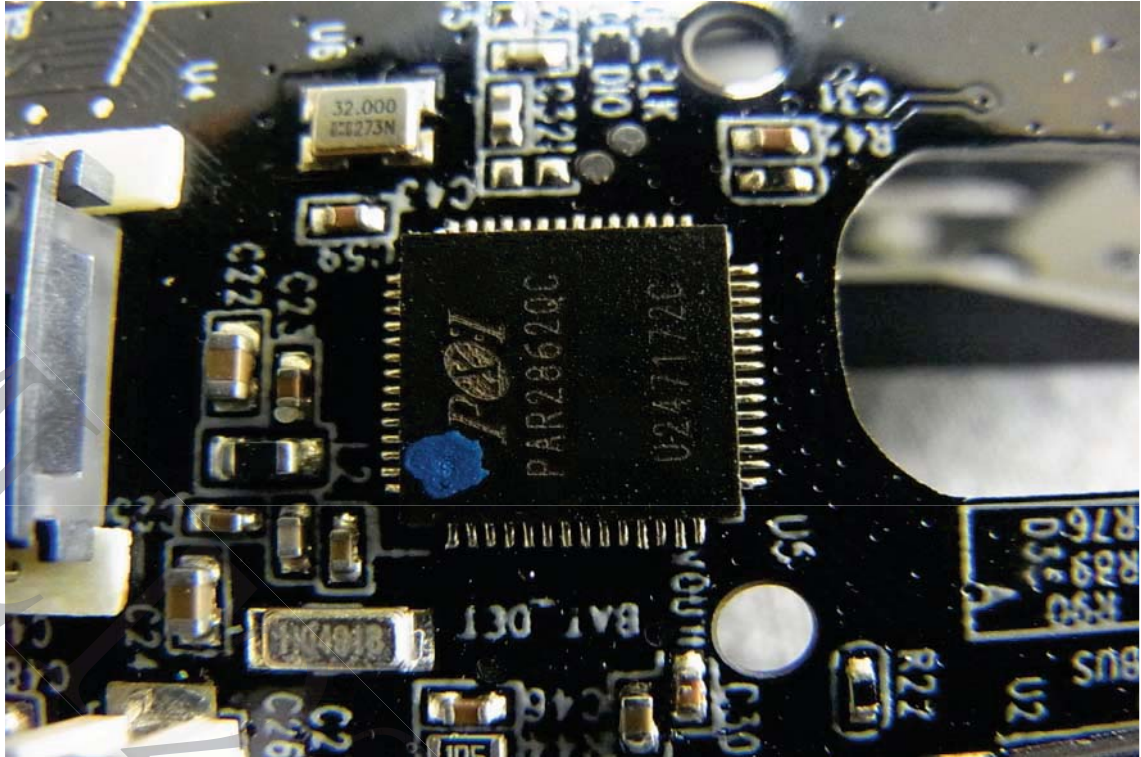
< PCB1-F >



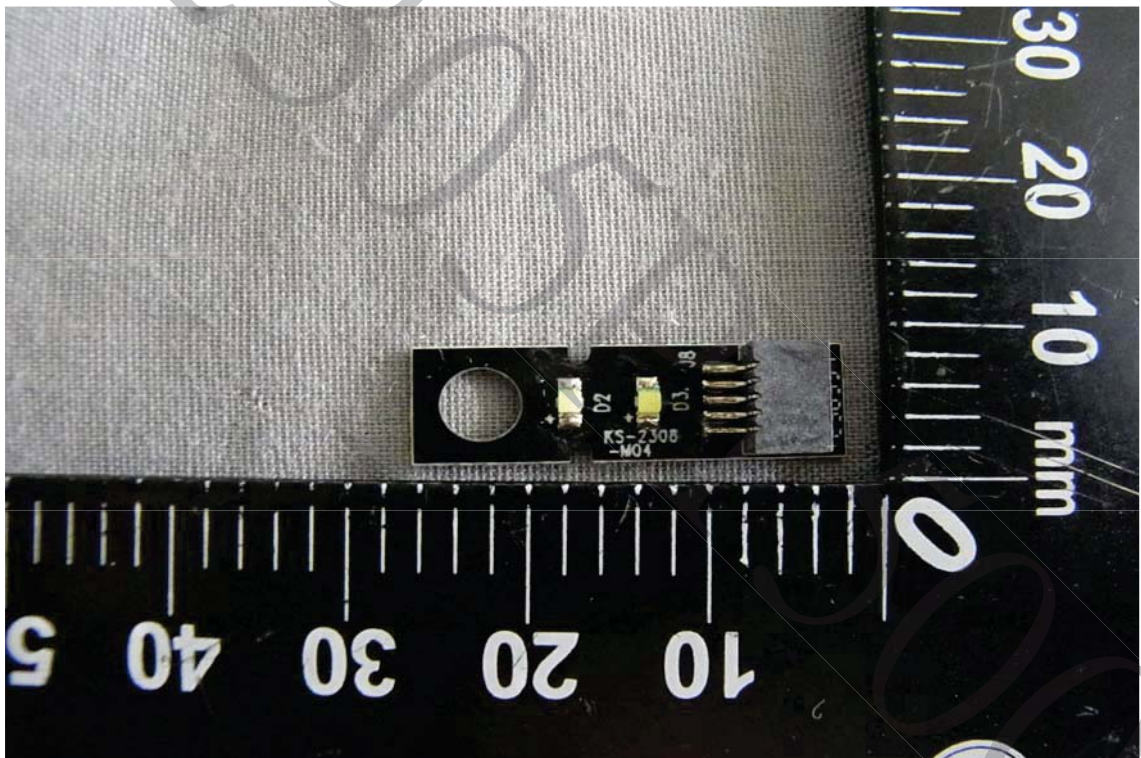
< PCB1-R >



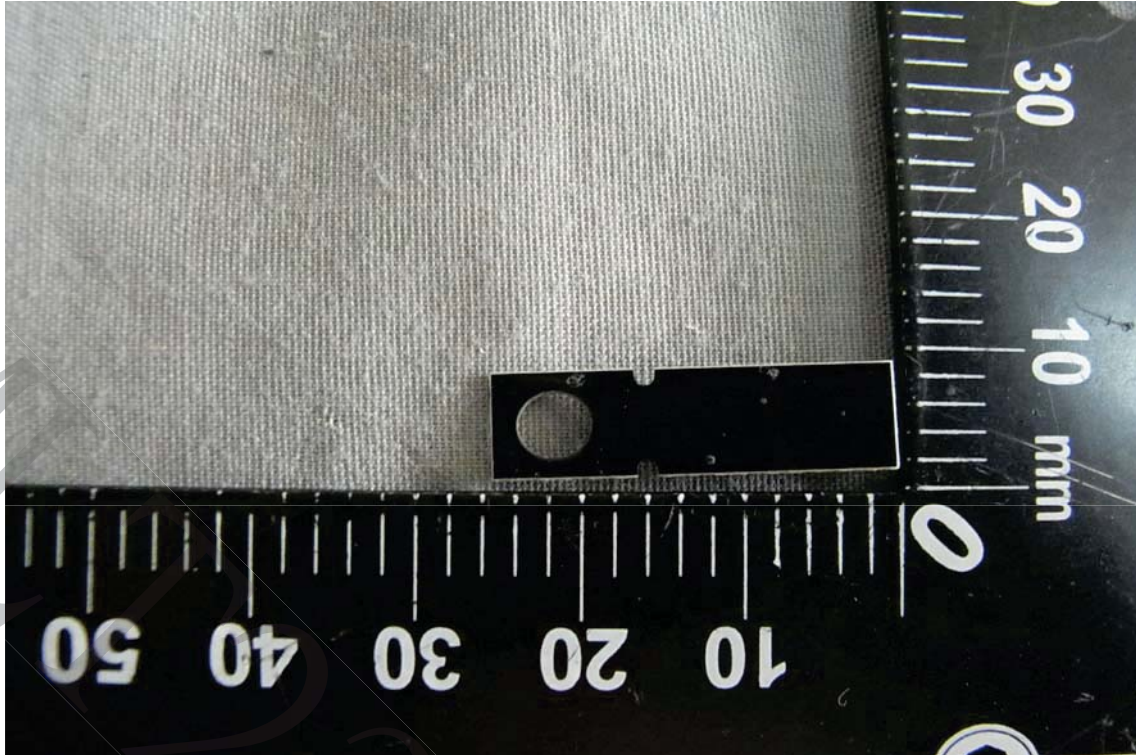
< Module Close in >



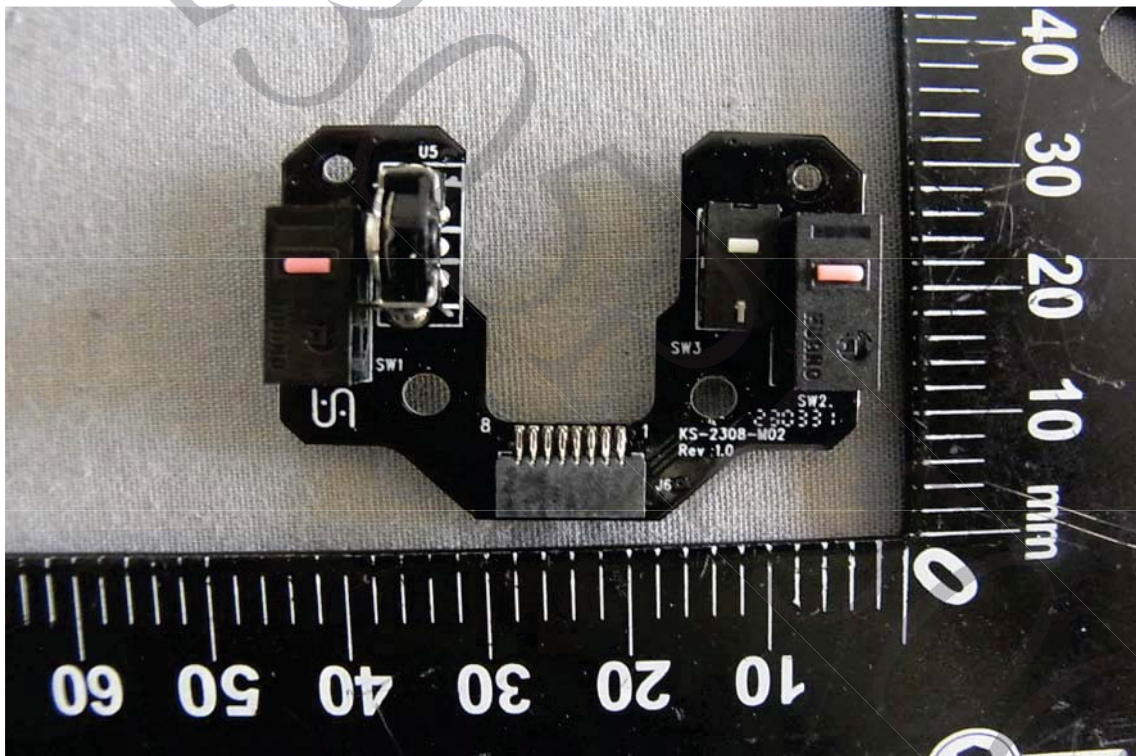
< PCB2-F >



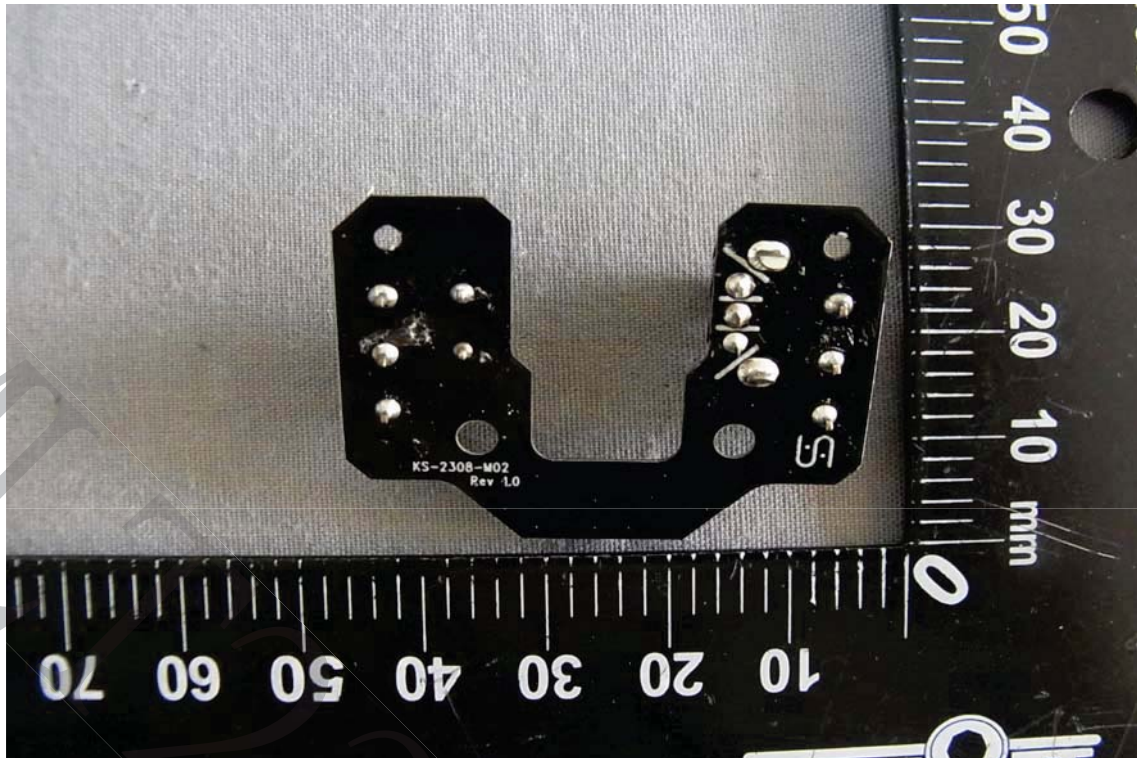
< PCB2-R >



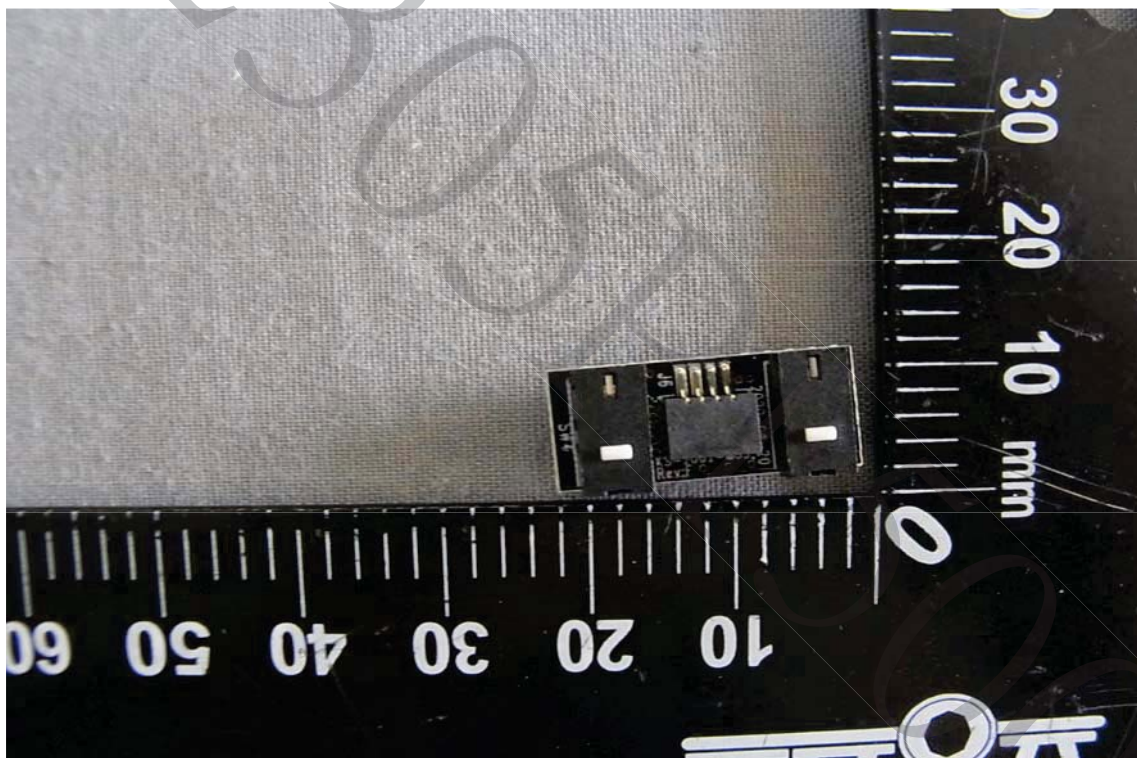
< PCB3-F >



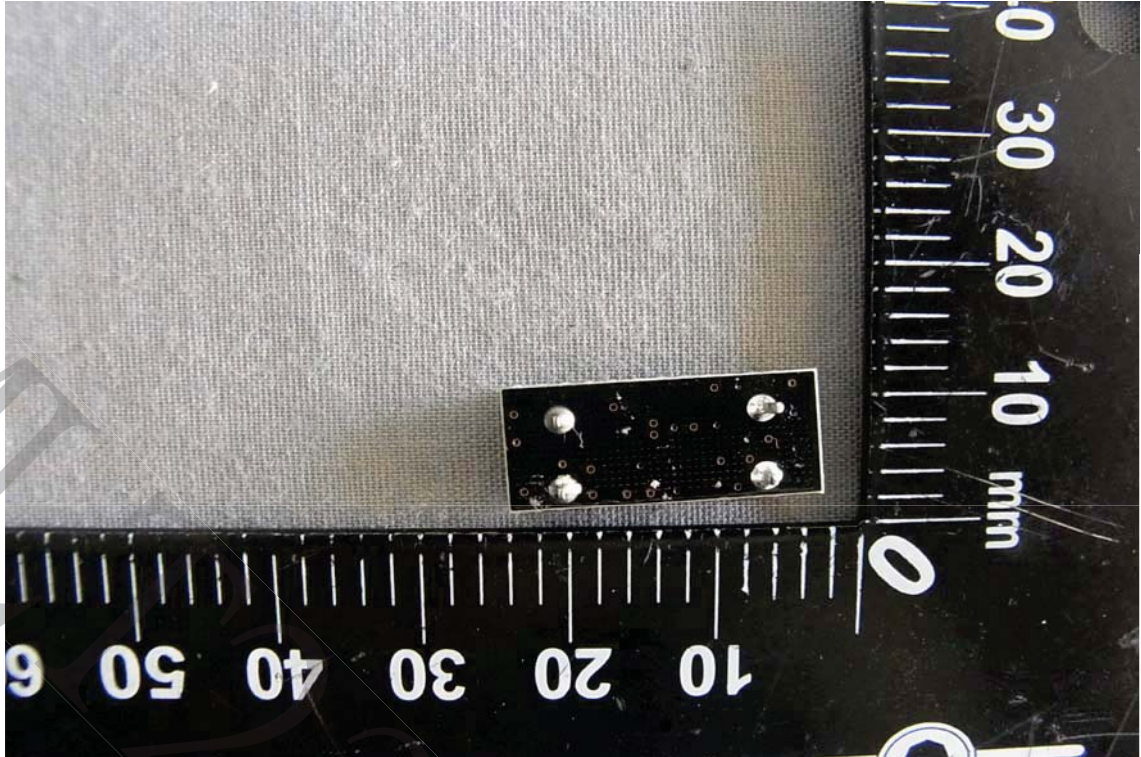
< PCB3-R >



< PCB4-F >



< PCB4-R >



< Battery >

