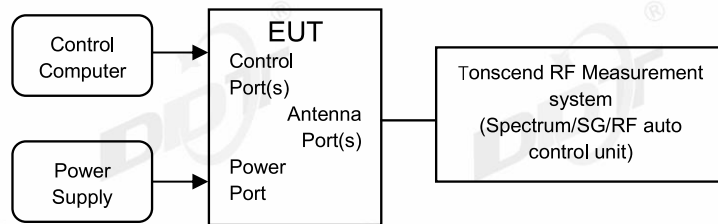


7. Duty Cycle

7.1. Block diagram of test setup



7.2. Limit

Just for Report.

7.3. Test procedure

- (1) Connected the EUT's antenna port to the Spectrum Analyzer by suitable attenuator, The cable loss and attenuator loss have been put into spectrum analyzer as amplitude offset.
set the Spectrum Analyzer as below:
 Centre Frequency: The centre frequency of the middle hopping channel.
 Resolution BW: 10 MHz.
 Video BW: 10 MHz.
 Span: Zero span.
 Detector: Peak.
 Trace Mode: Clear Write.
 Sweep: Video Trigger
- (2) When the trace is complete, measure the sending time of 1 burst and the duty cycle of 1 burst cycle.
- (3) Calculate dwell time follow below formula:
 Duty cycle= Pulse's on time / Burst cycle

7.4. Test result

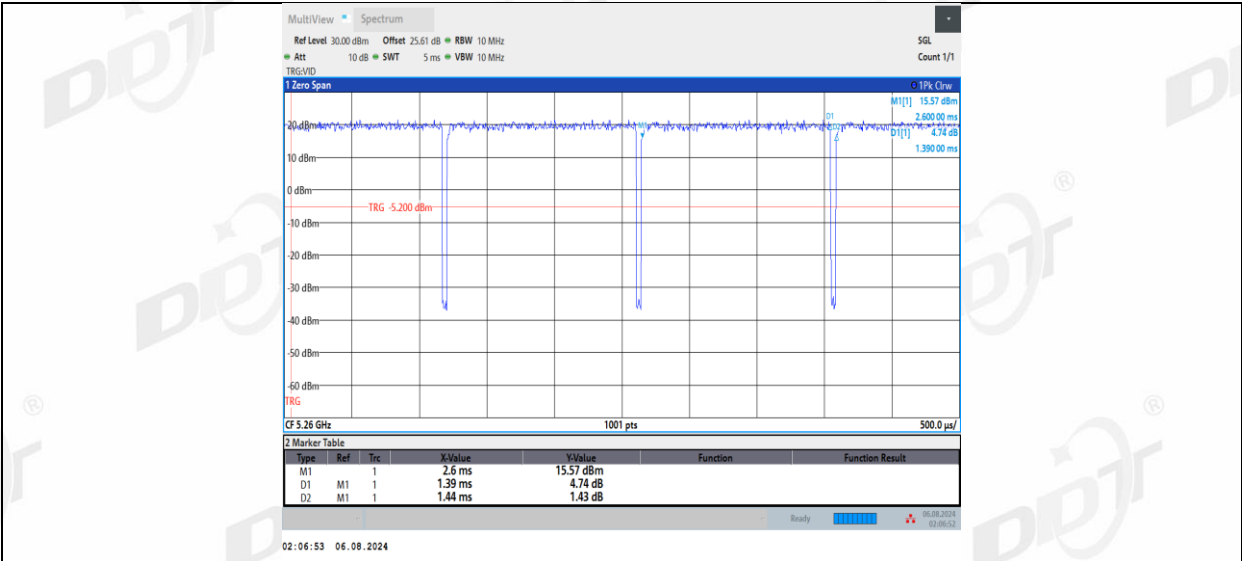
Test Engineer:	Haofeng	Test Site:	RF Measurement System 4#
Ambient Condition:	26.9℃,45.5%RH	Test Date:	2024.08.06-2024.08.08
Test Power Supply:	DC 5V	Sample Number:	S24071734-003

Test Mode	Antenna	Frequency[MHz]	Transmission Duration [ms]	Transmission Period [ms]	Duty Cycle [%]
11A	Ant1	5180	1.39	1.44	96.53
		5200	1.40	1.44	97.22
		5240	1.40	1.44	97.22
		5260	1.39	1.44	96.53
		5280	1.40	1.44	97.22
		5320	1.40	1.44	97.22
		5500	1.40	1.50	93.33
		5580	1.40	1.44	97.22
		5700	1.40	1.44	97.22
		5720	1.40	1.44	97.22
		5745	1.40	1.44	97.22
		5785	1.39	1.44	96.53
		5825	1.40	1.44	97.22
11N20SISO	Ant1	5180	1.31	1.35	97.04
		5200	1.31	1.35	97.04
		5240	1.31	1.35	97.04
		5260	1.31	1.35	97.04
		5280	1.31	1.35	97.04
		5320	1.30	1.35	96.30
		5500	1.30	1.35	96.30
		5580	1.31	1.35	97.04
		5700	1.31	1.35	97.04
		5720	1.31	1.35	97.04
		5745	1.31	1.35	97.04
		5785	1.31	1.35	97.04
		5825	1.31	1.36	96.32
11N40SISO	Ant1	5190	0.65	0.69	94.20
		5230	0.65	0.69	94.20
		5270	0.65	0.69	94.20
		5310	0.65	0.69	94.20
		5510	0.65	0.69	94.20
		5550	0.64	0.69	92.75
		5670	0.65	0.69	94.20
		5710	0.65	0.69	94.20
		5755	0.65	0.69	94.20
		5795	0.65	0.69	94.20
11AC20SISO	Ant1	5180	1.31	1.36	96.32
		5200	1.32	1.36	97.06
		5240	1.32	1.36	97.06
		5260	1.32	1.36	97.06
		5280	1.32	1.36	97.06
		5320	1.32	1.36	97.06
		5500	1.32	1.36	97.06
		5580	1.32	1.36	97.06
		5700	1.32	1.36	97.06
		5720	1.32	1.36	97.06
		5745	1.32	1.36	97.06
		5785	1.32	1.36	97.06
		5825	1.32	1.36	97.06
11AC40SISO	Ant1	5190	0.65	0.70	92.86
		5230	0.65	0.70	92.86
		5270	0.66	0.70	94.29
		5310	0.66	0.70	94.29
		5510	0.66	0.70	94.29
		5550	0.66	0.70	94.29

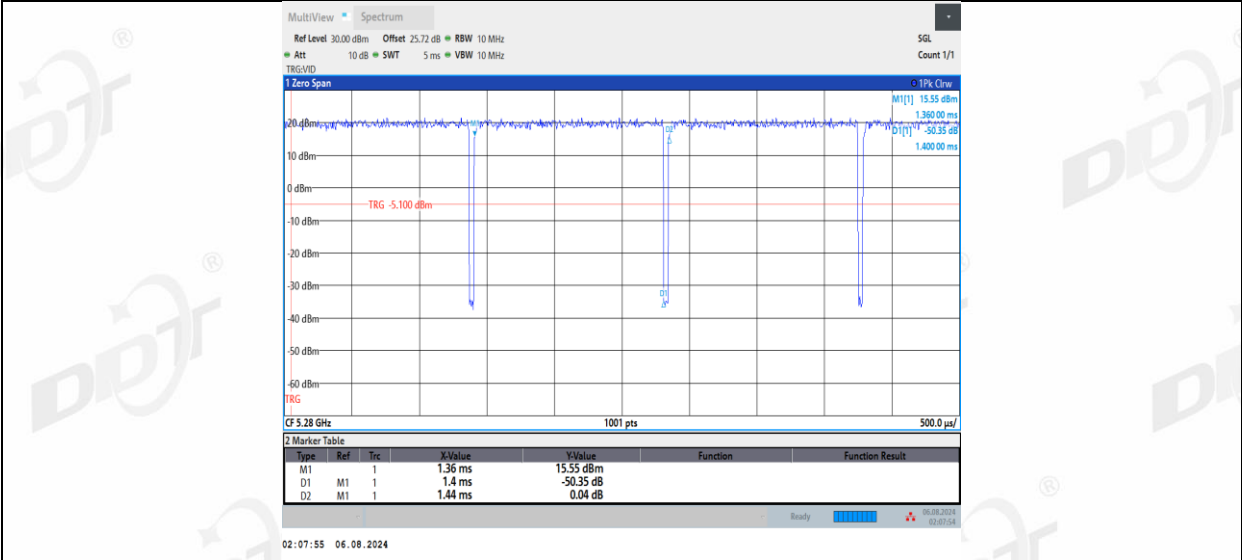
		5670	0.65	0.70	92.86
		5710	0.66	0.70	94.29
		5755	0.65	0.70	92.86
		5795	0.66	0.70	94.29
11AC80SISO	Ant1	5210	0.32	0.36	88.89
		5290	0.32	0.37	86.49
		5530	0.32	0.37	86.49
		5610	0.32	0.36	88.89
		5690	0.32	0.37	86.49
		5775	0.32	0.36	88.89

7.5. Test graphs

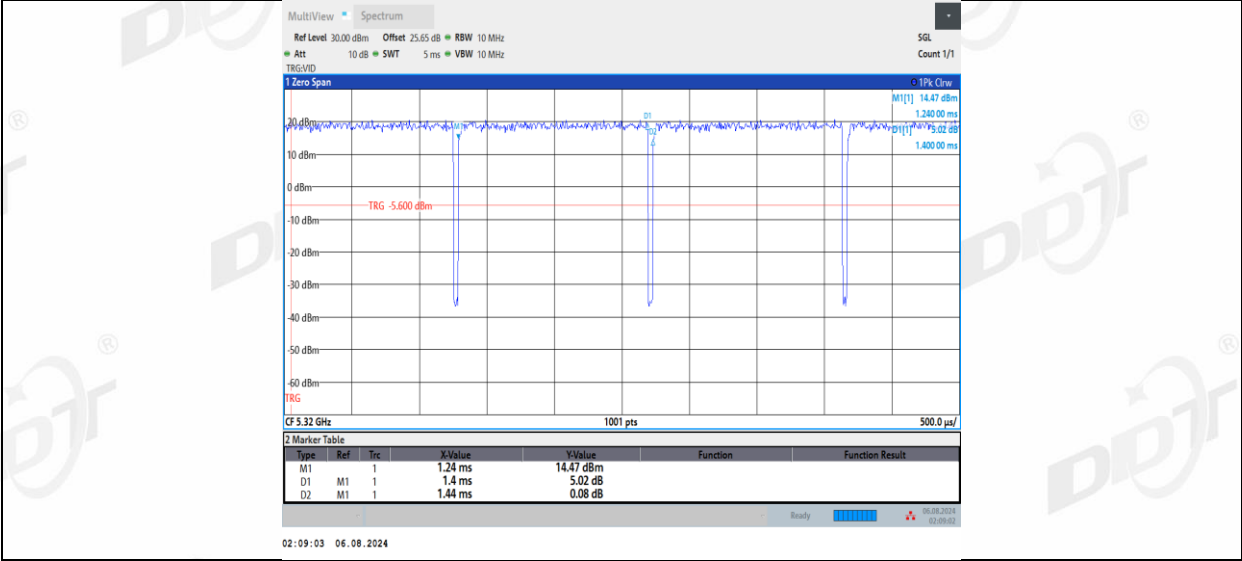




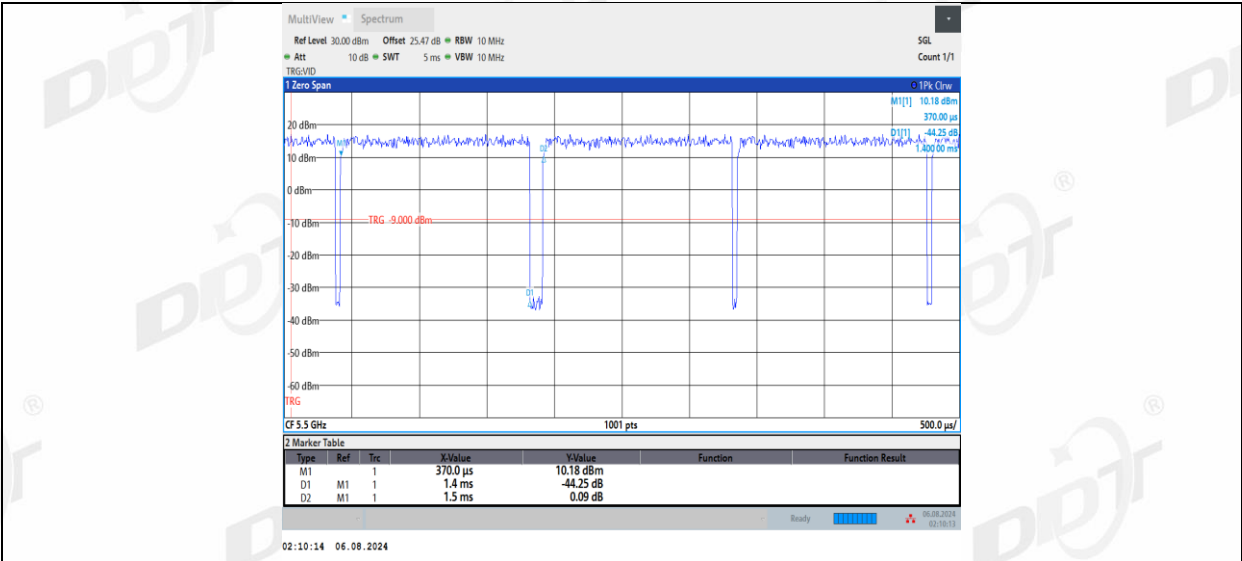
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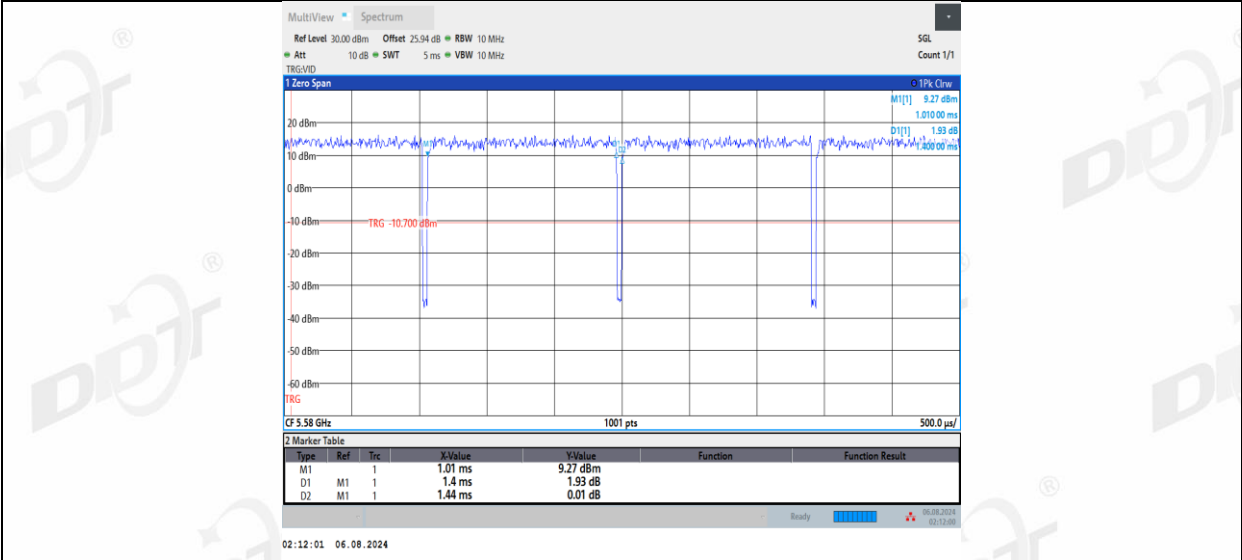
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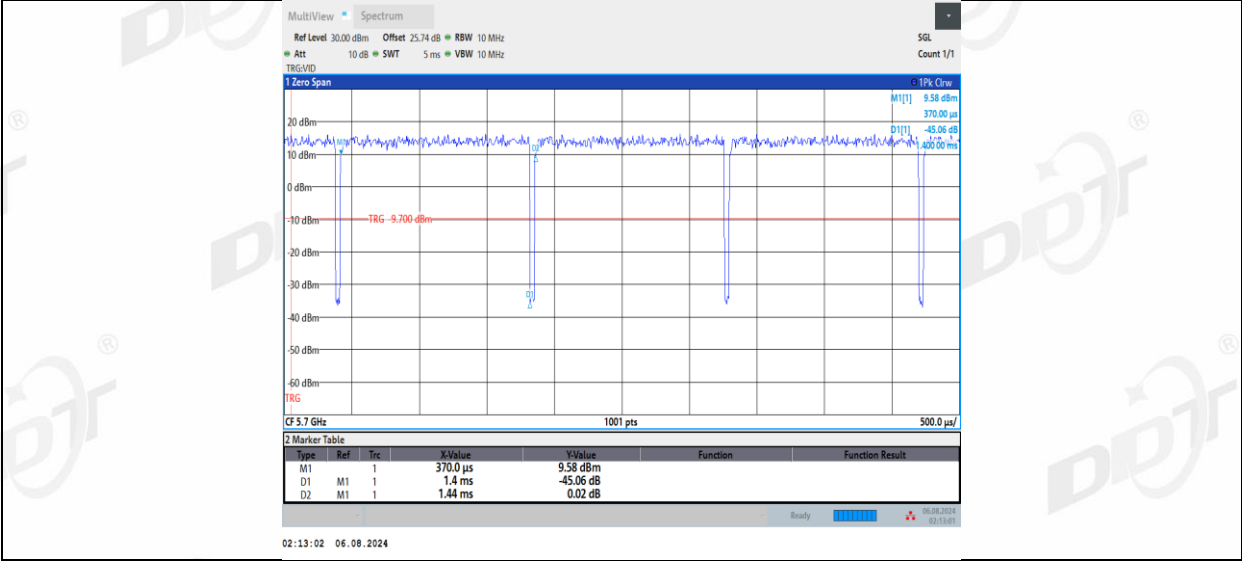
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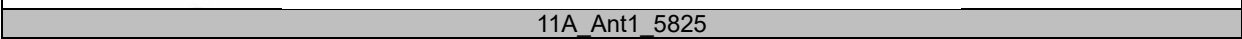
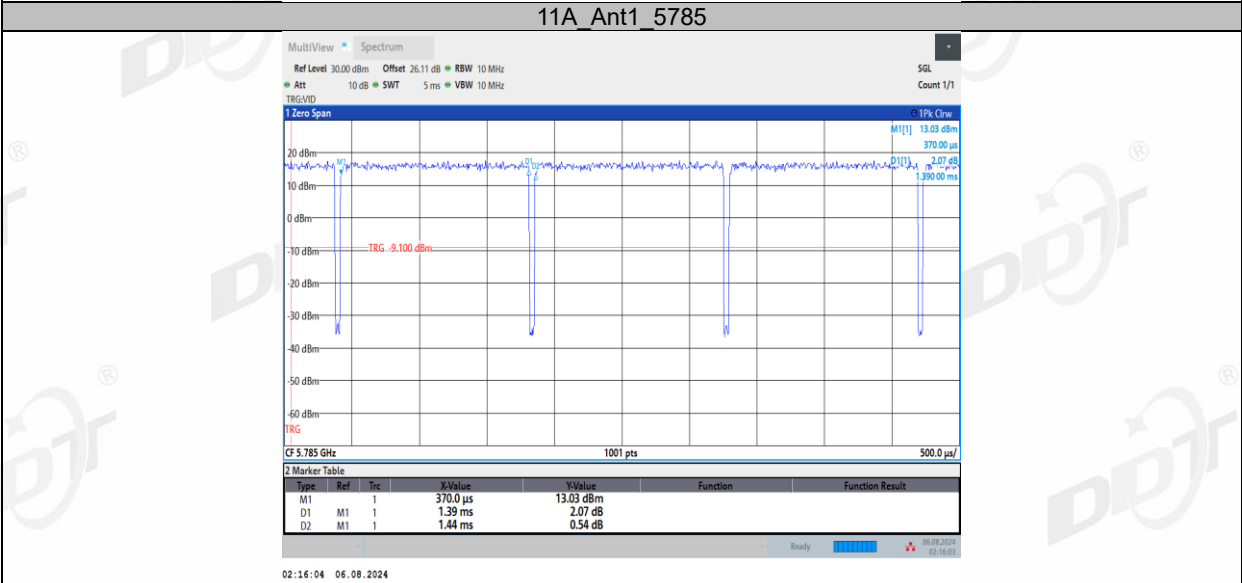
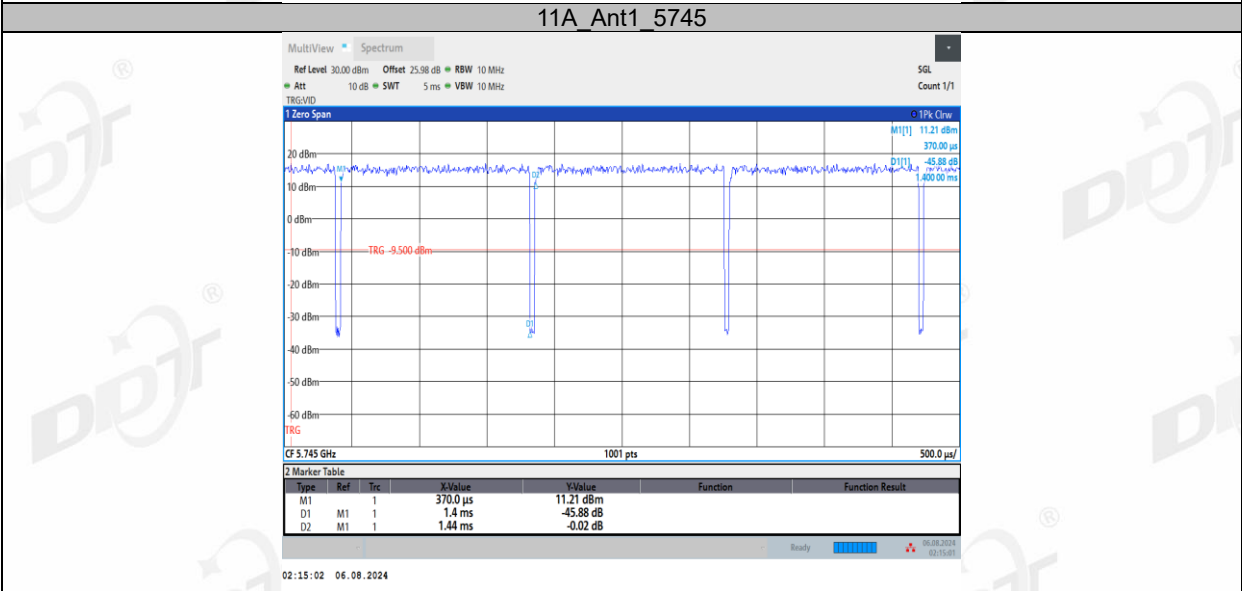
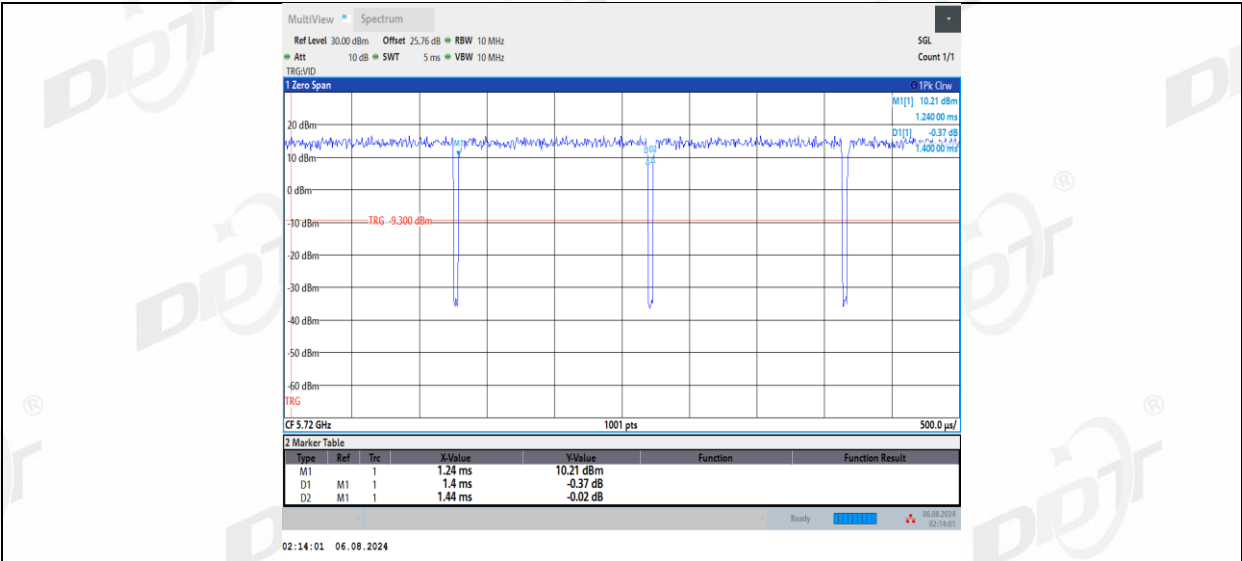
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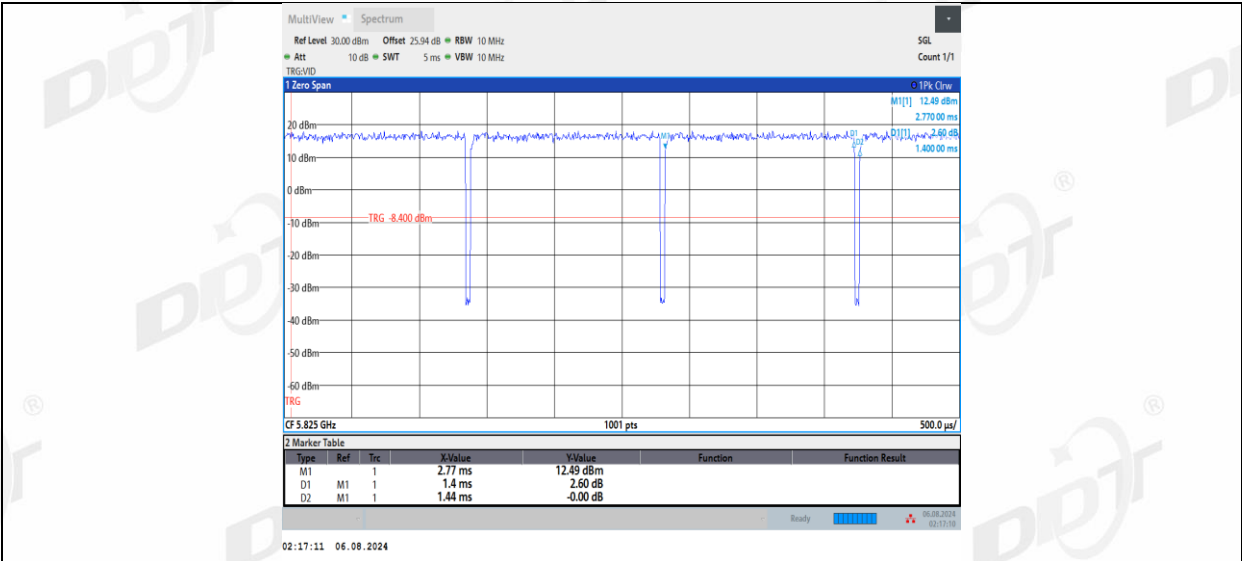


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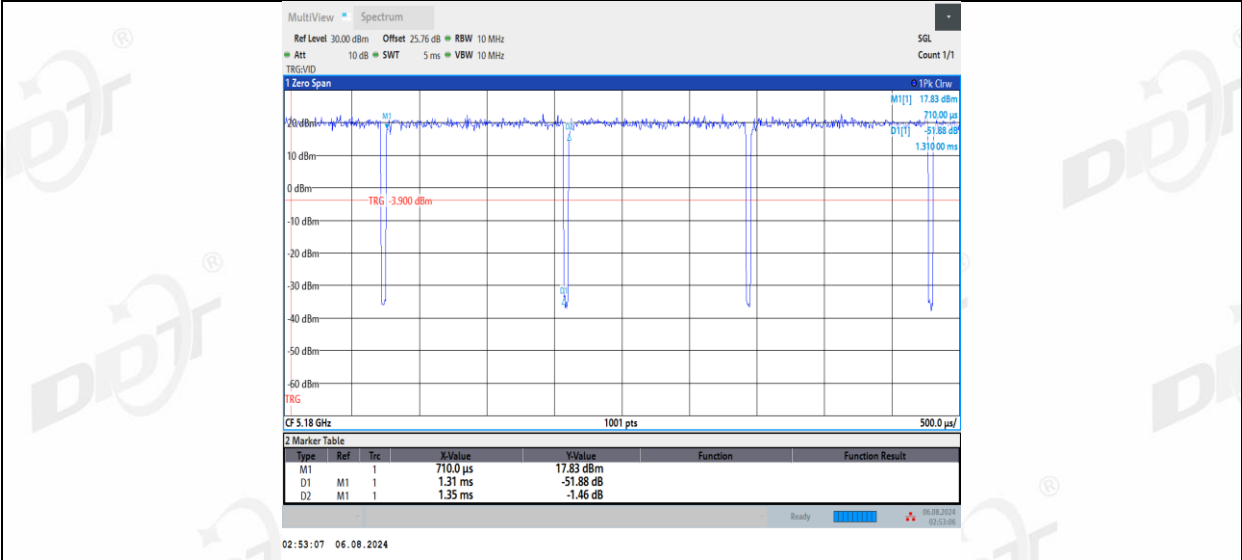


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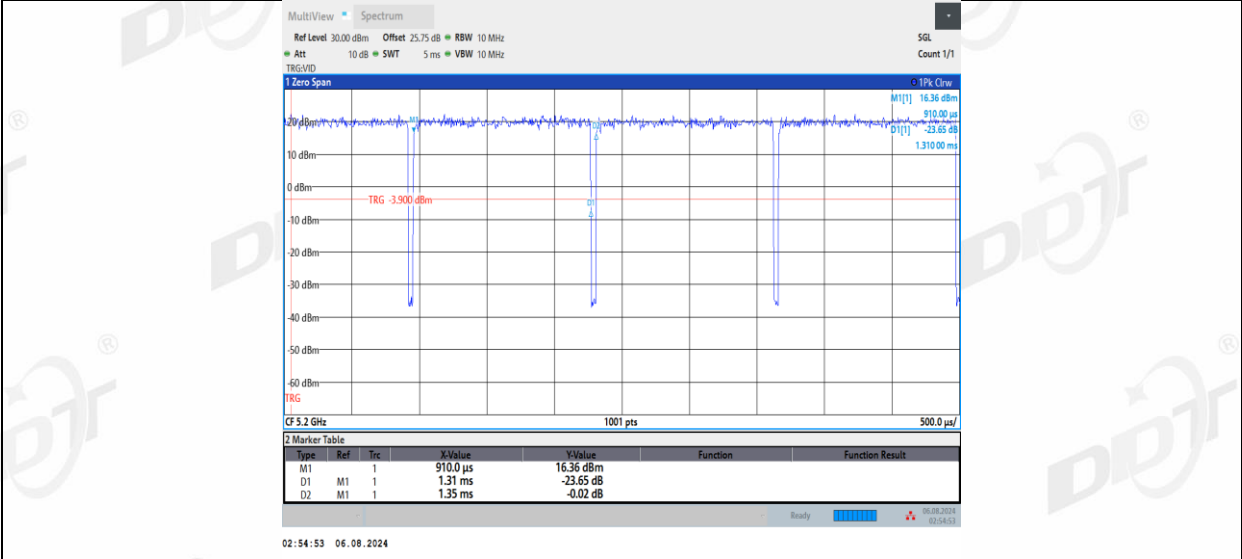




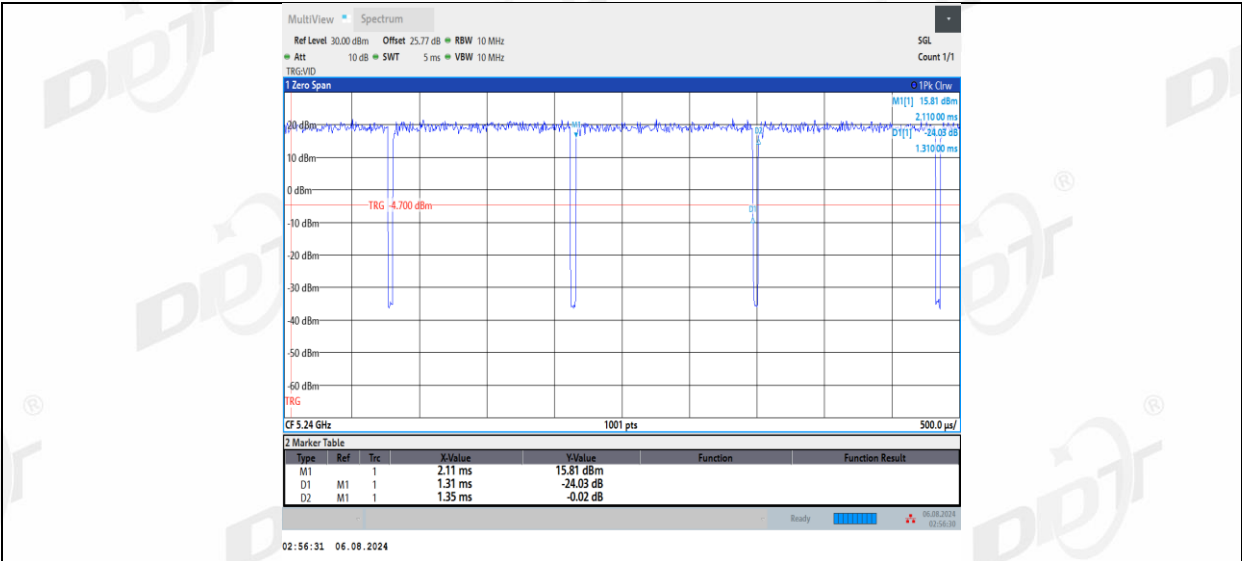
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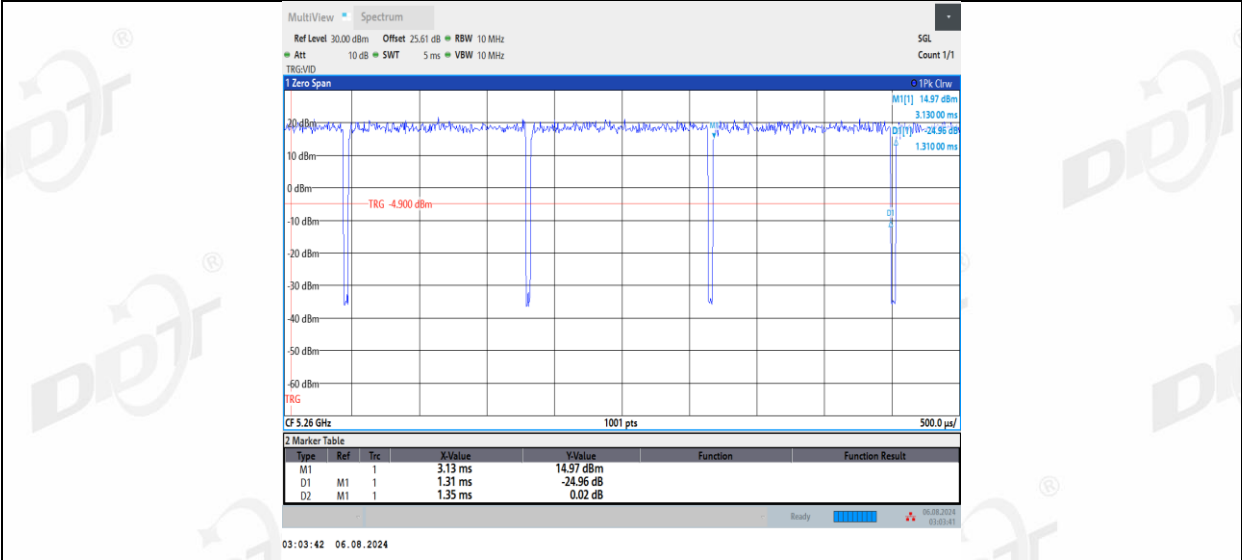
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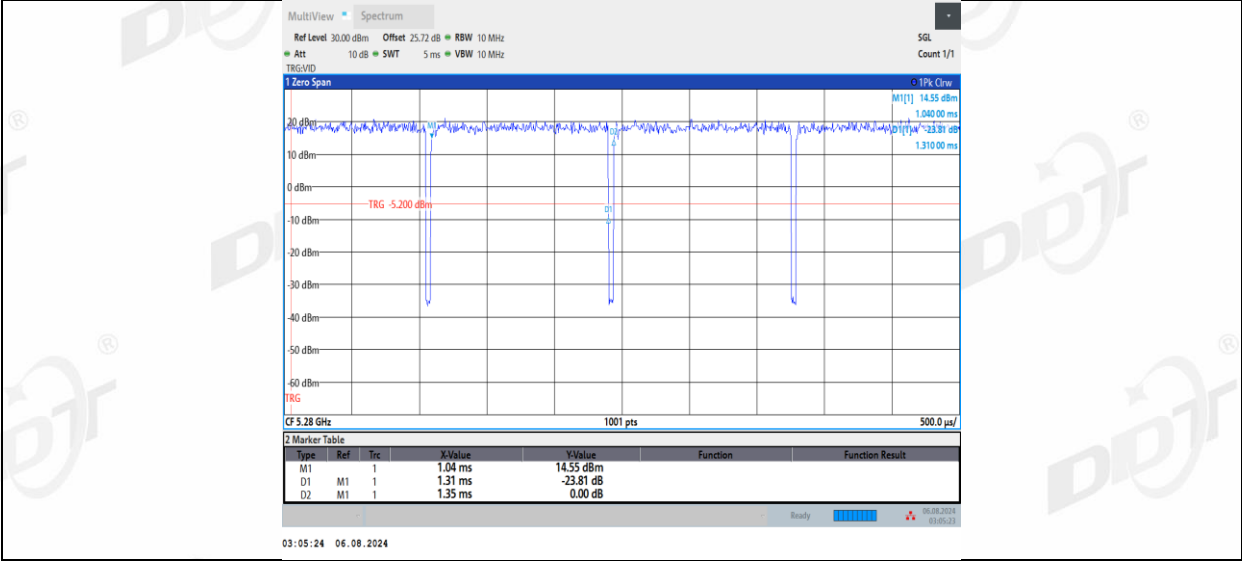
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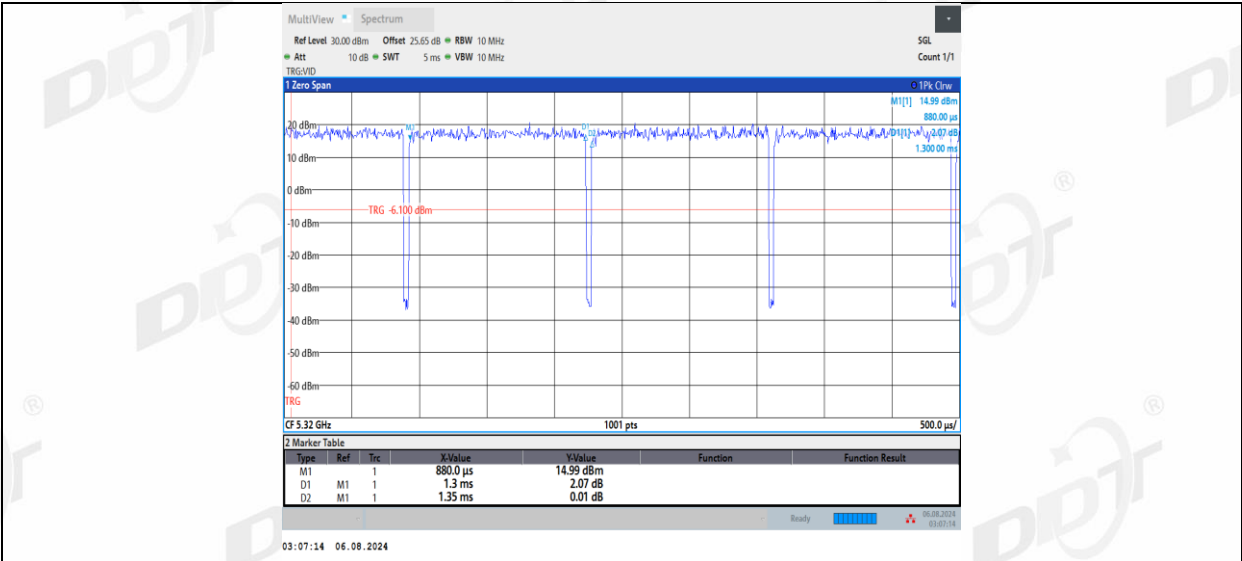
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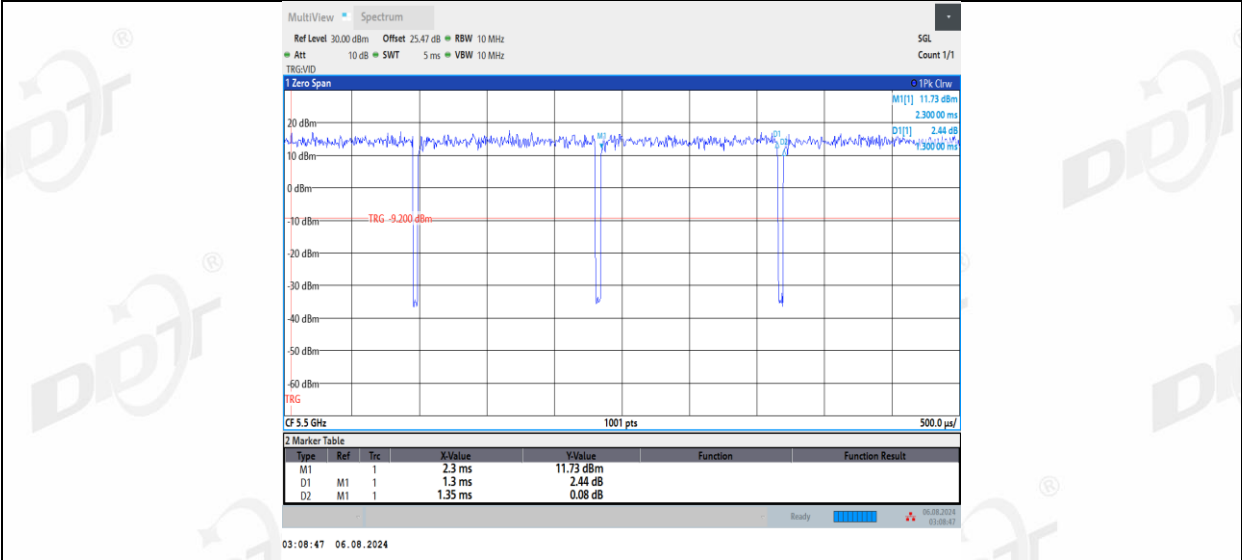
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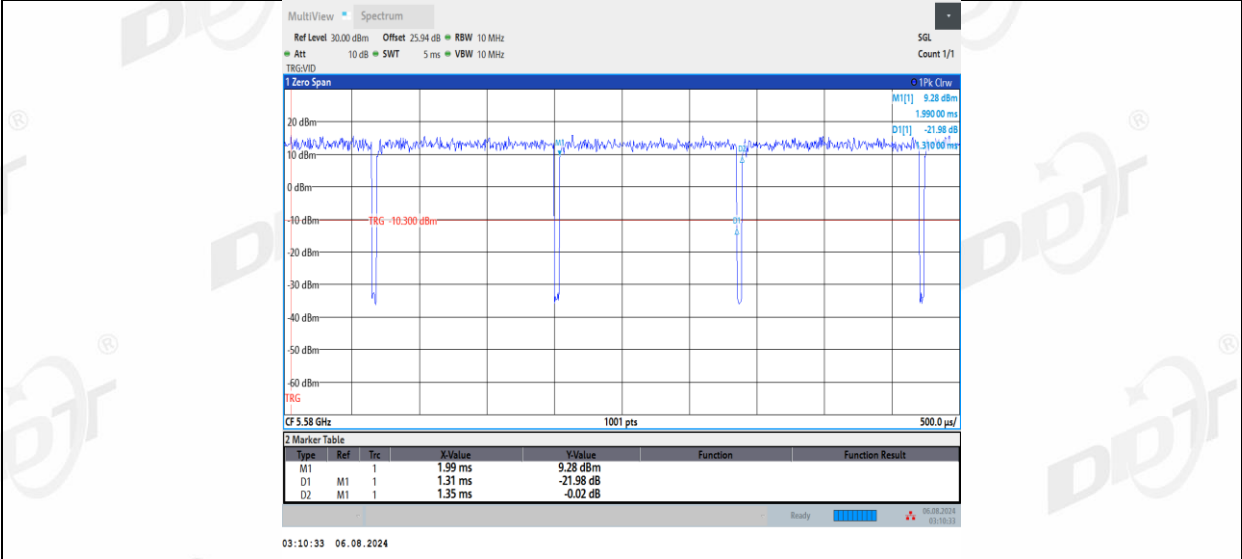
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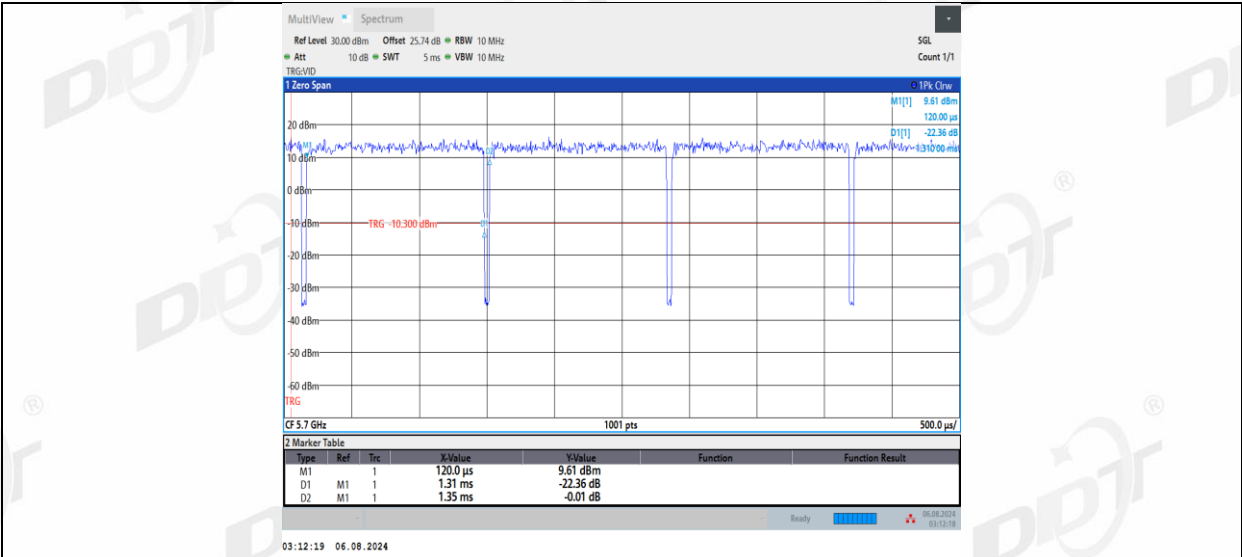
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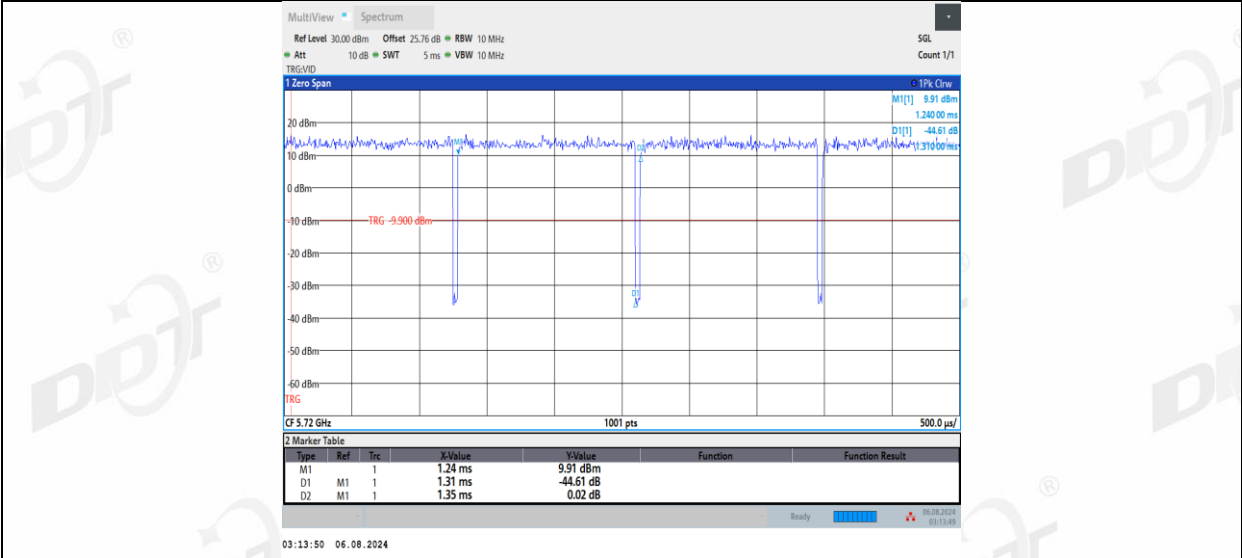
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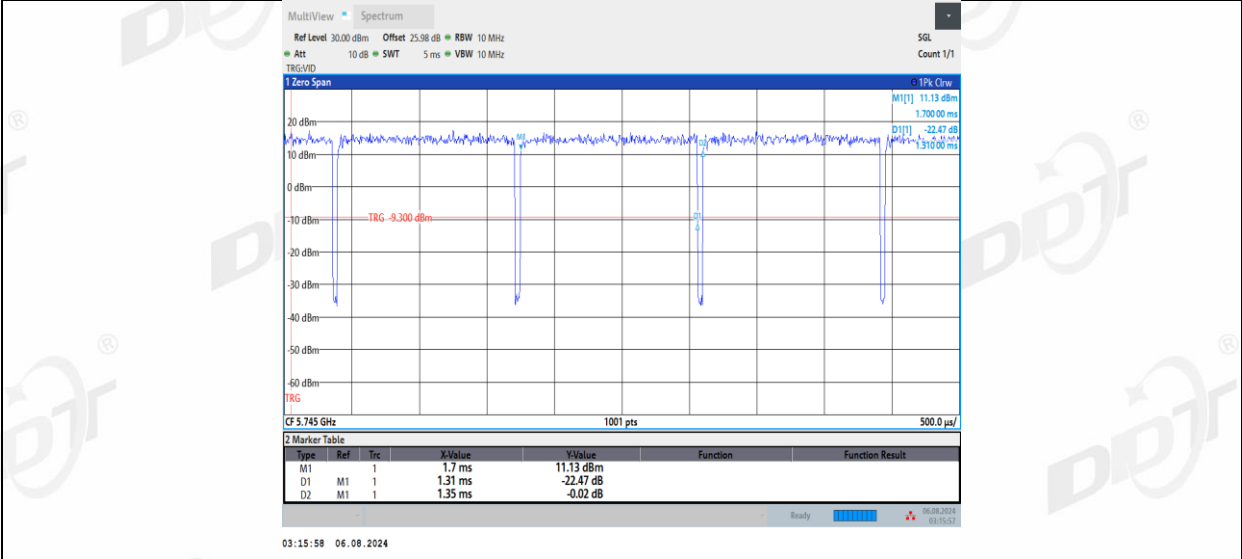
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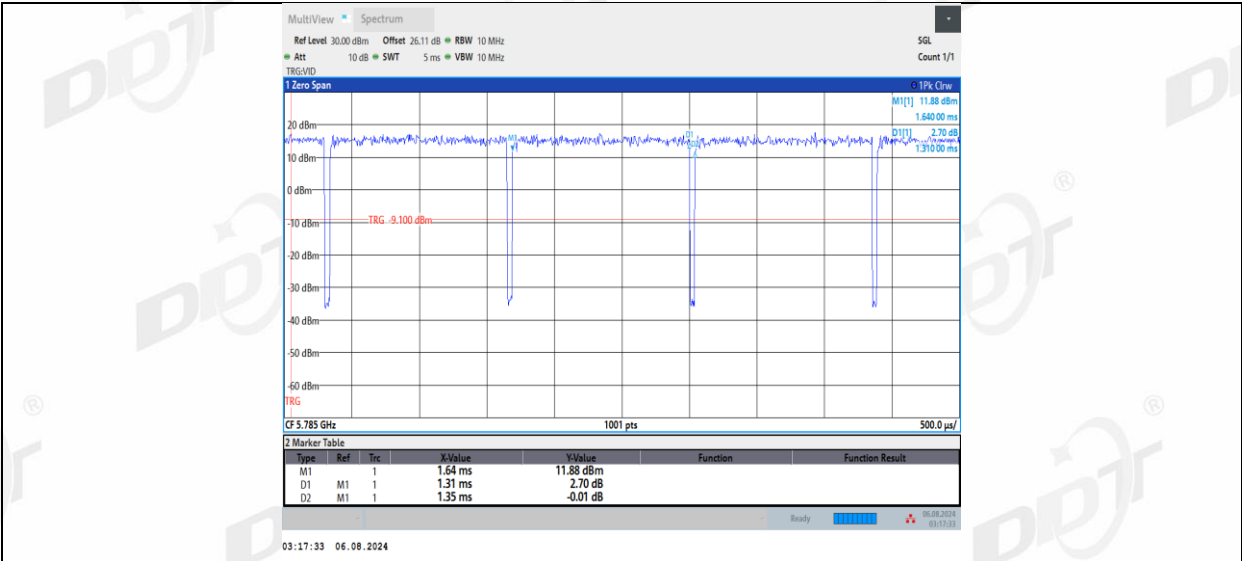
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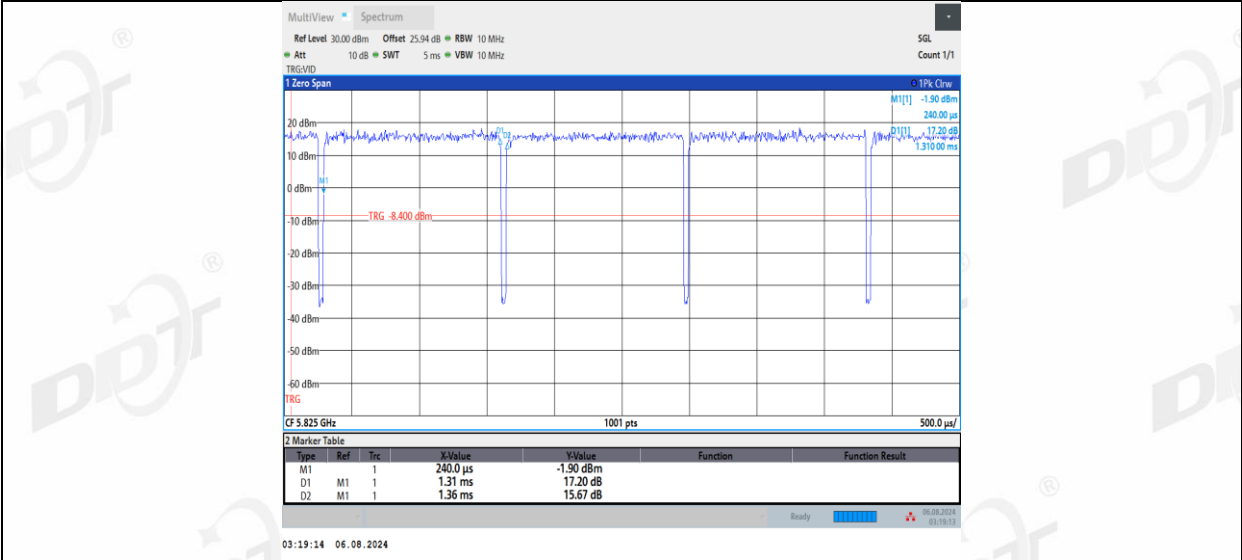
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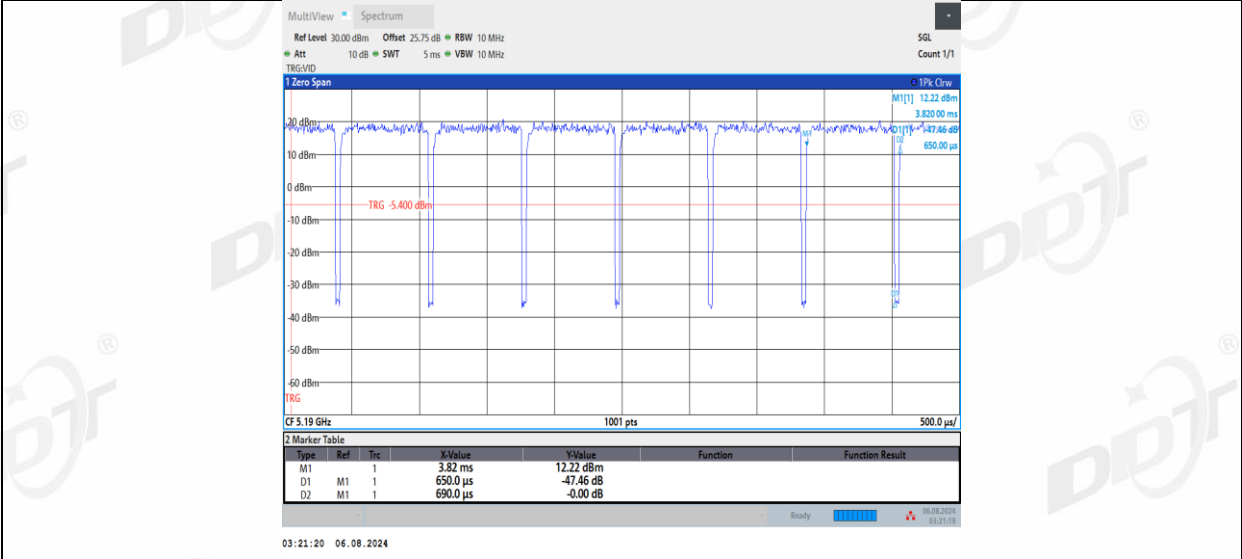
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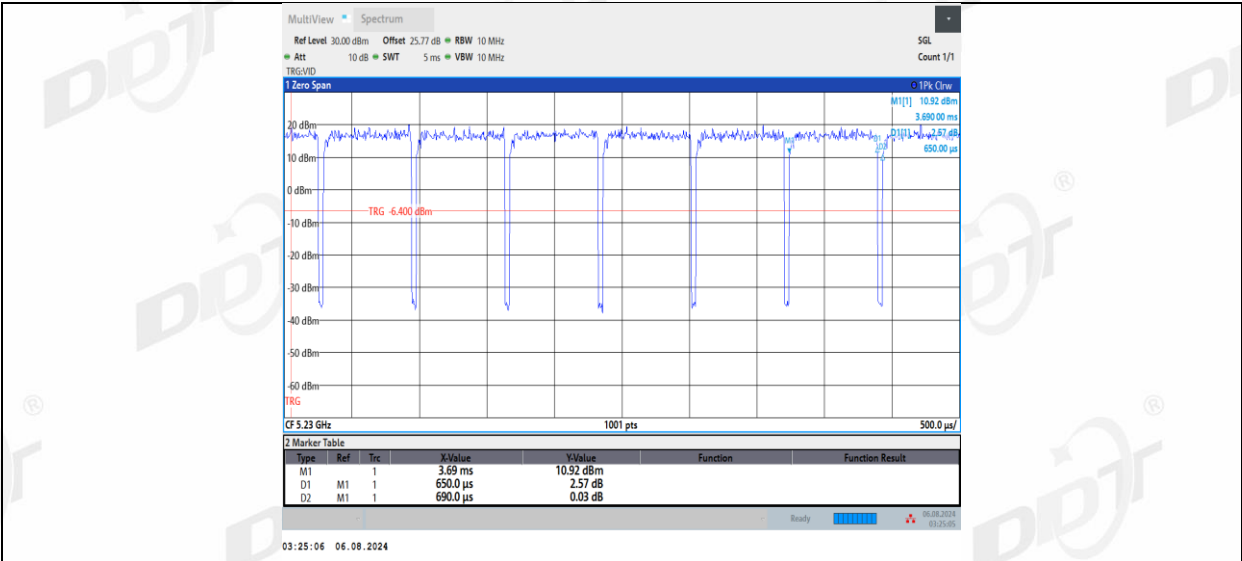
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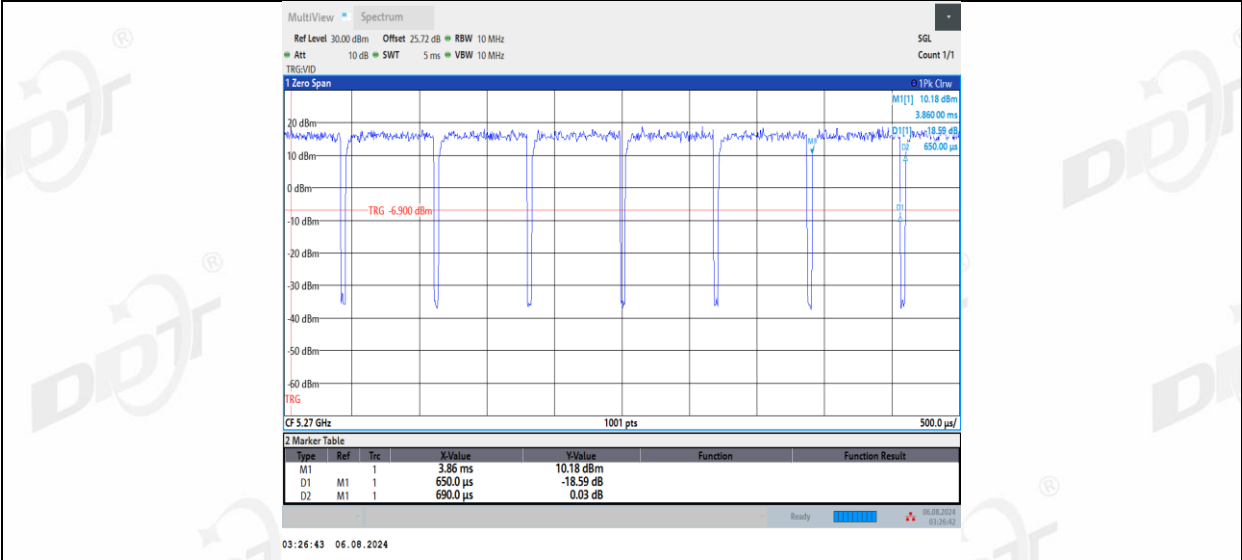
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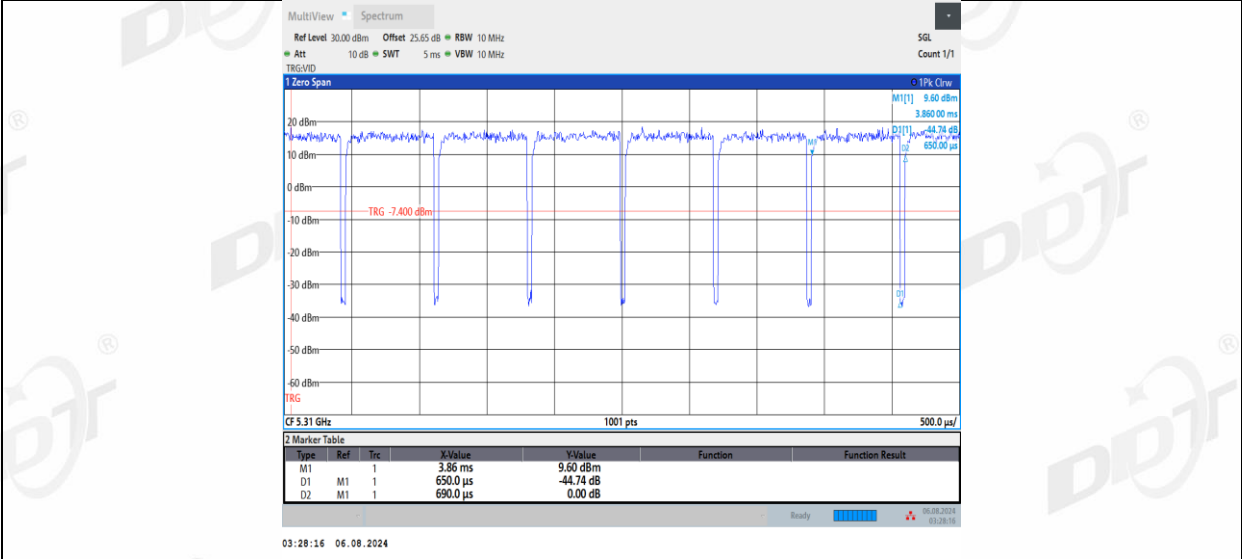
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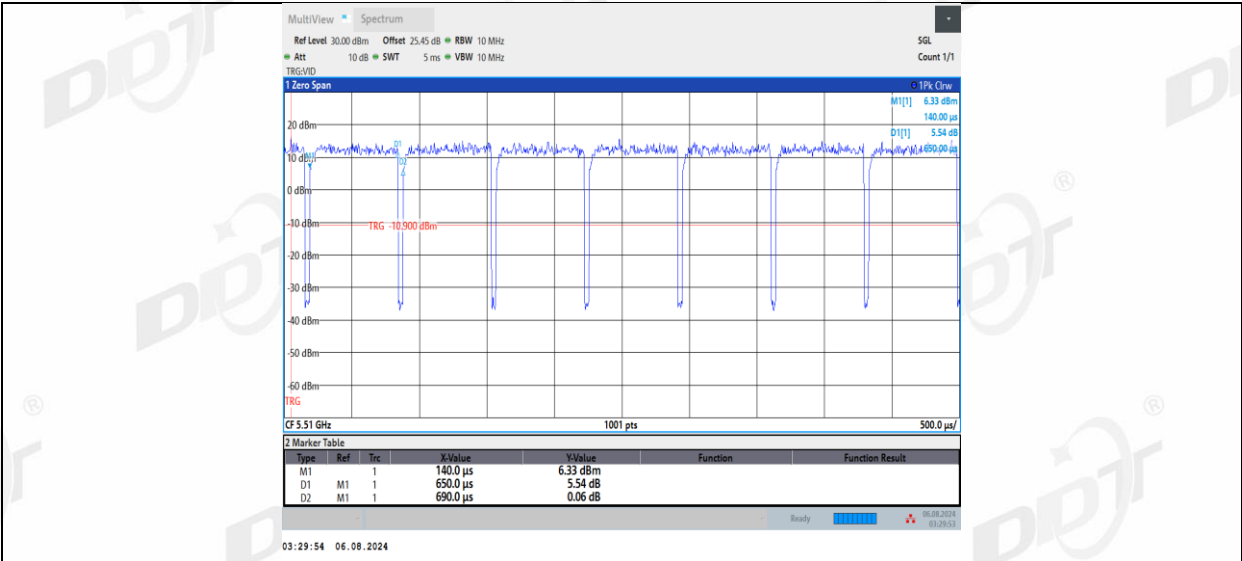
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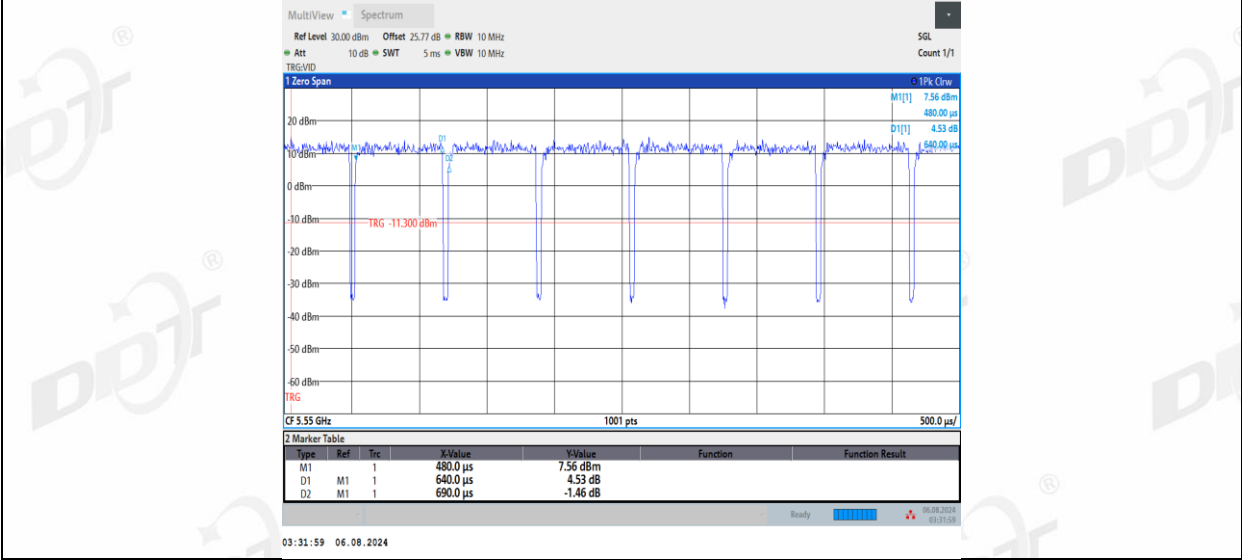
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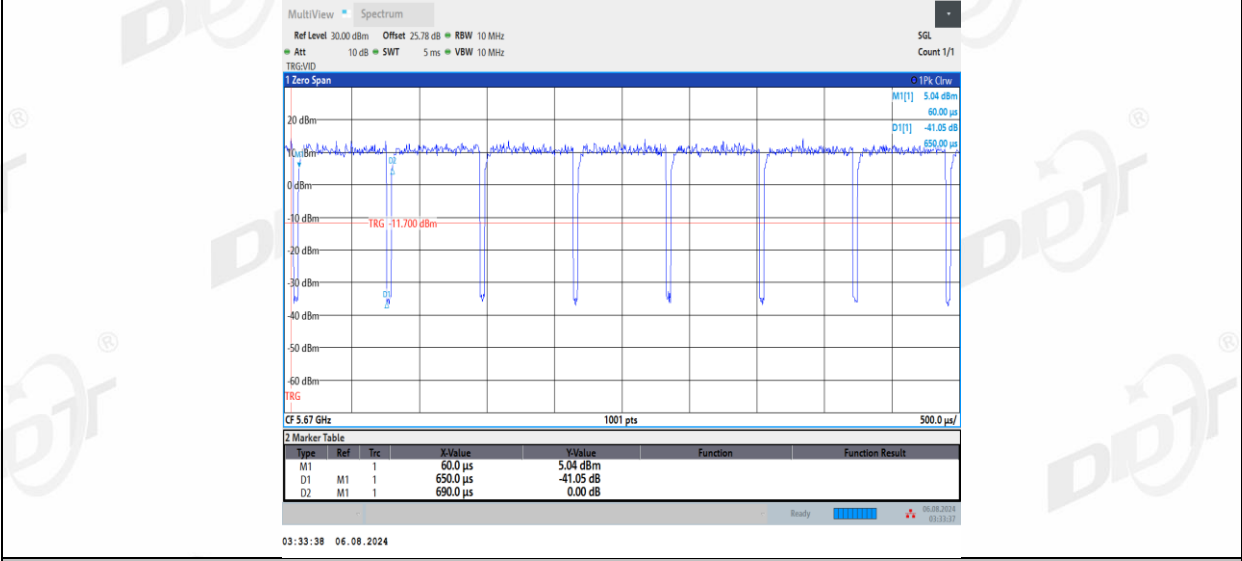
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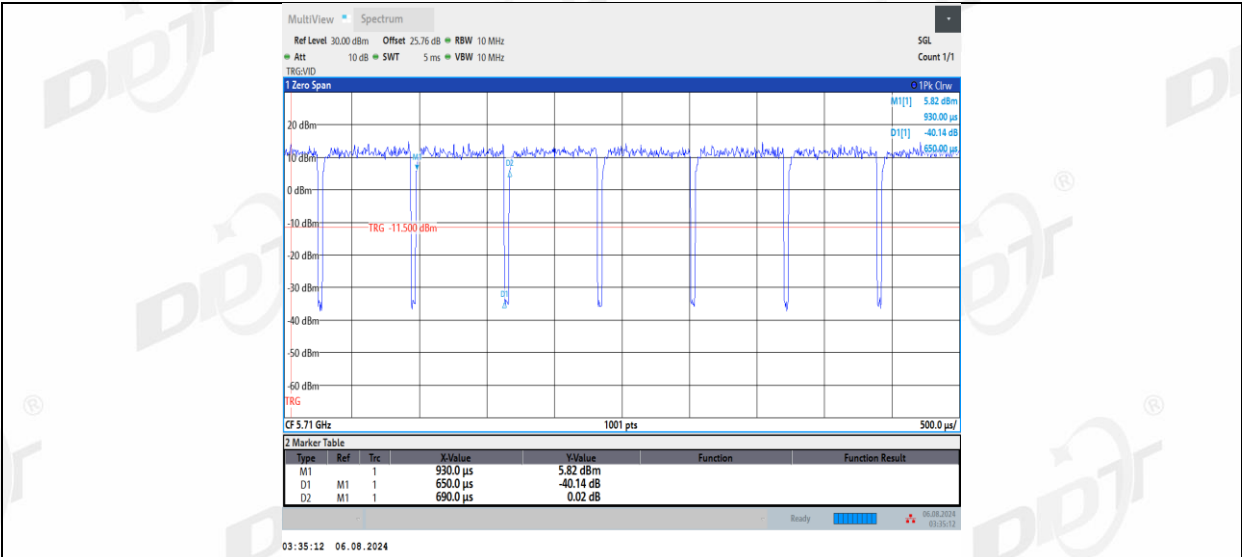
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11N40SiSO_Ant1_5670



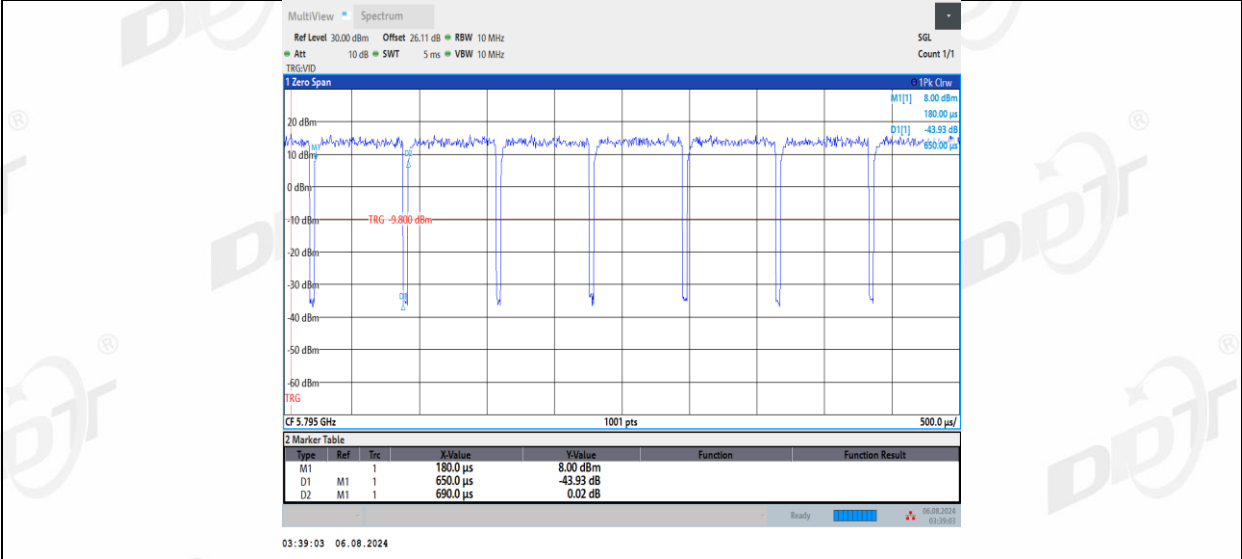
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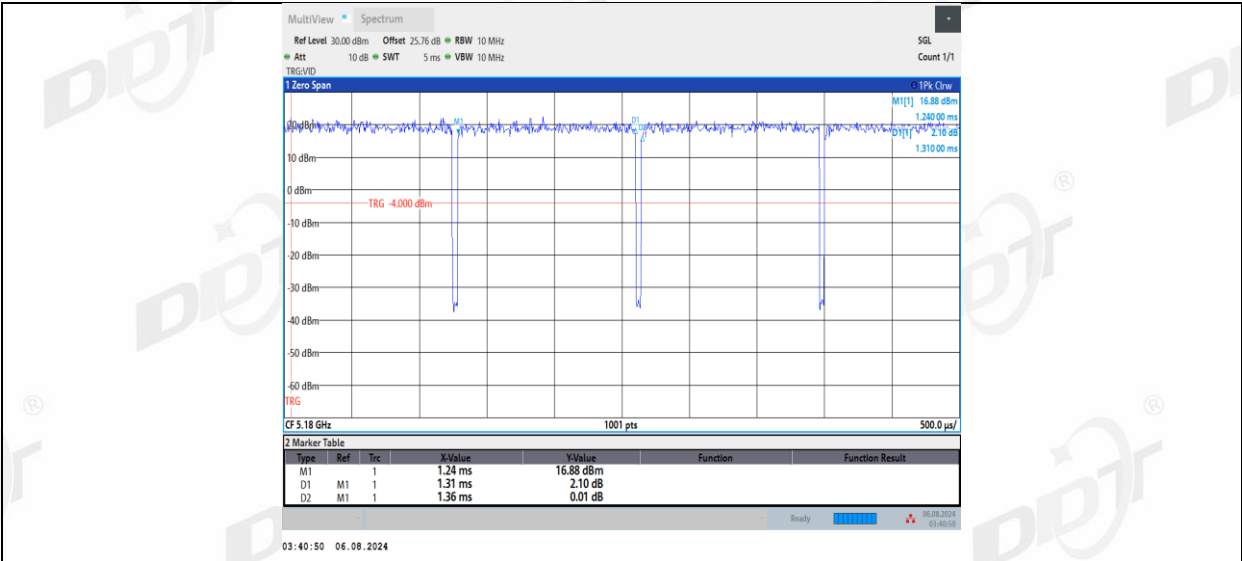
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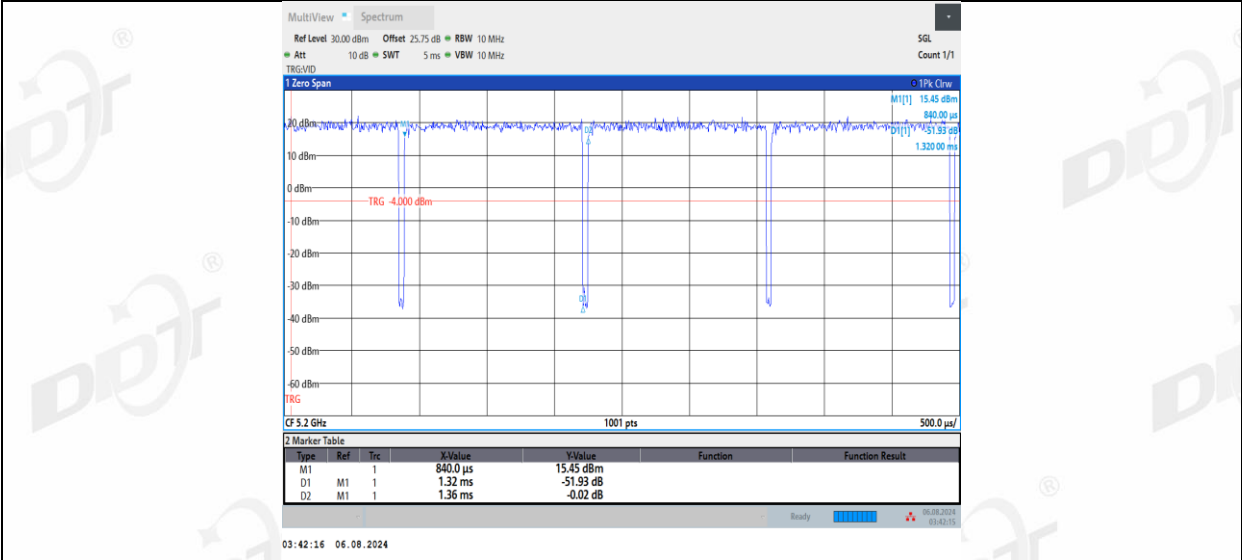
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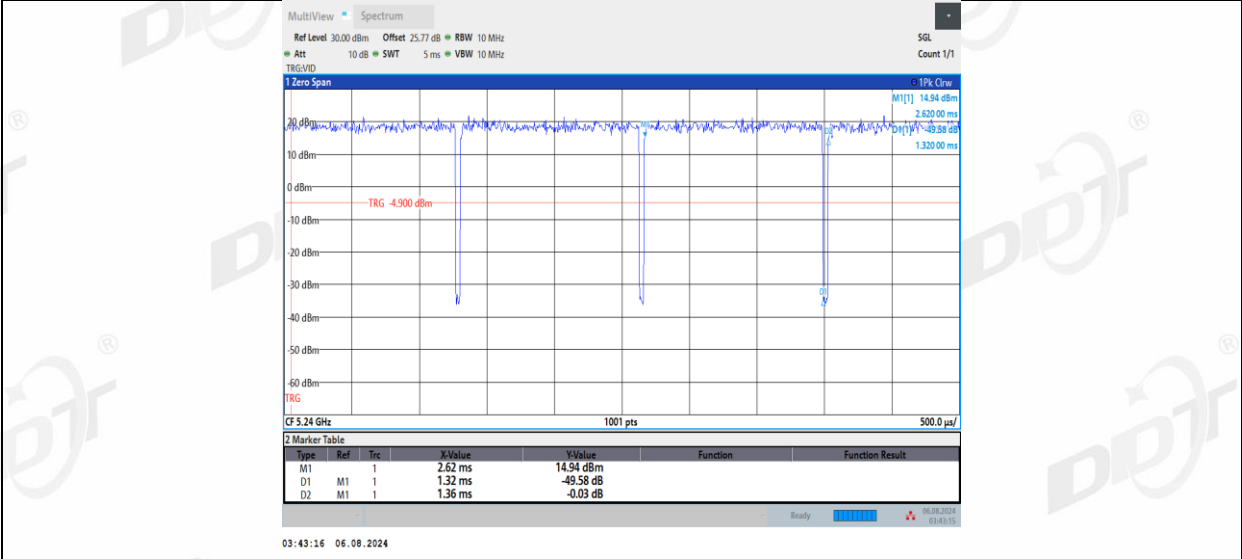
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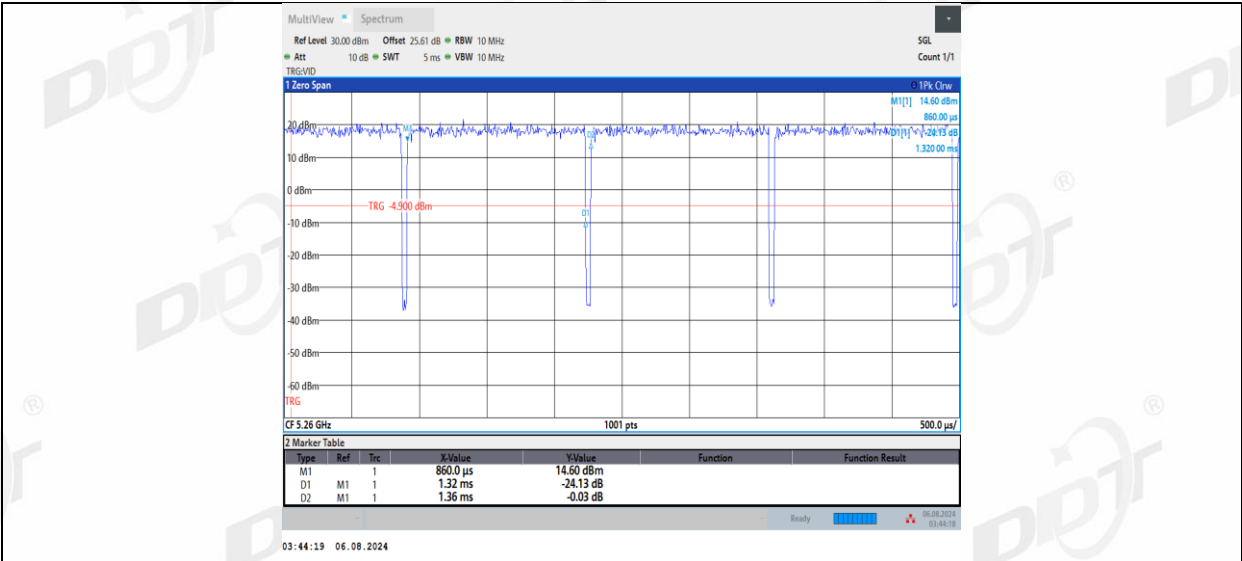
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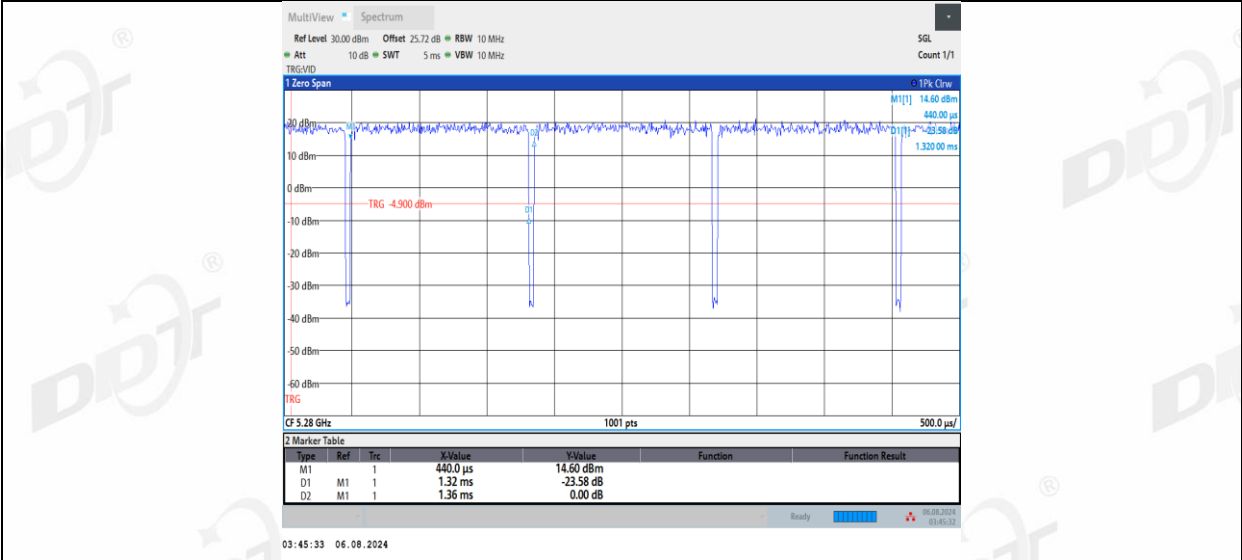
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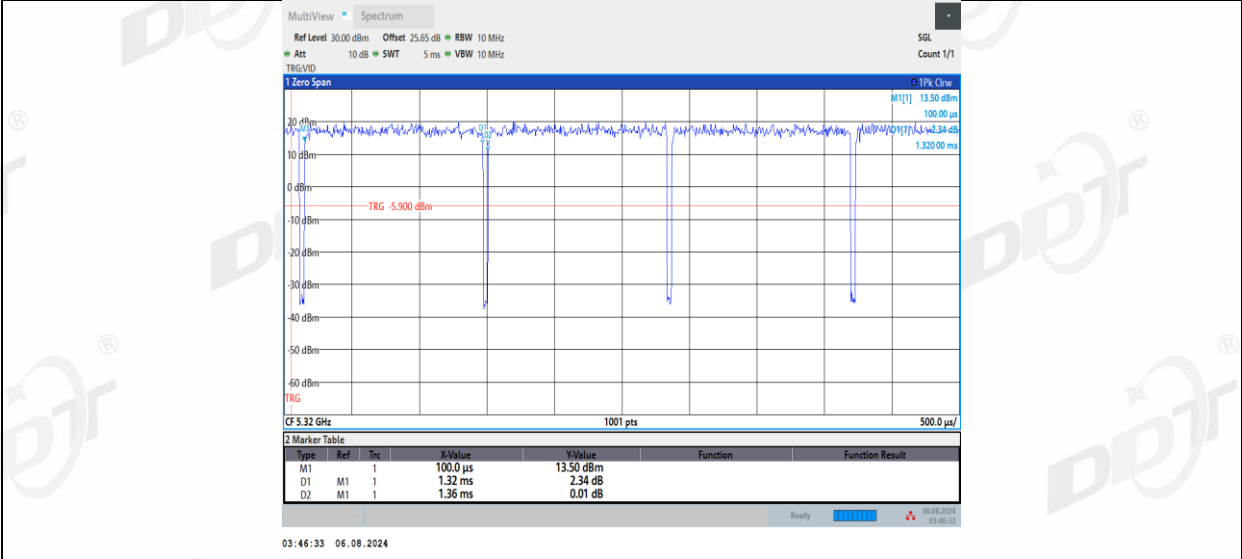
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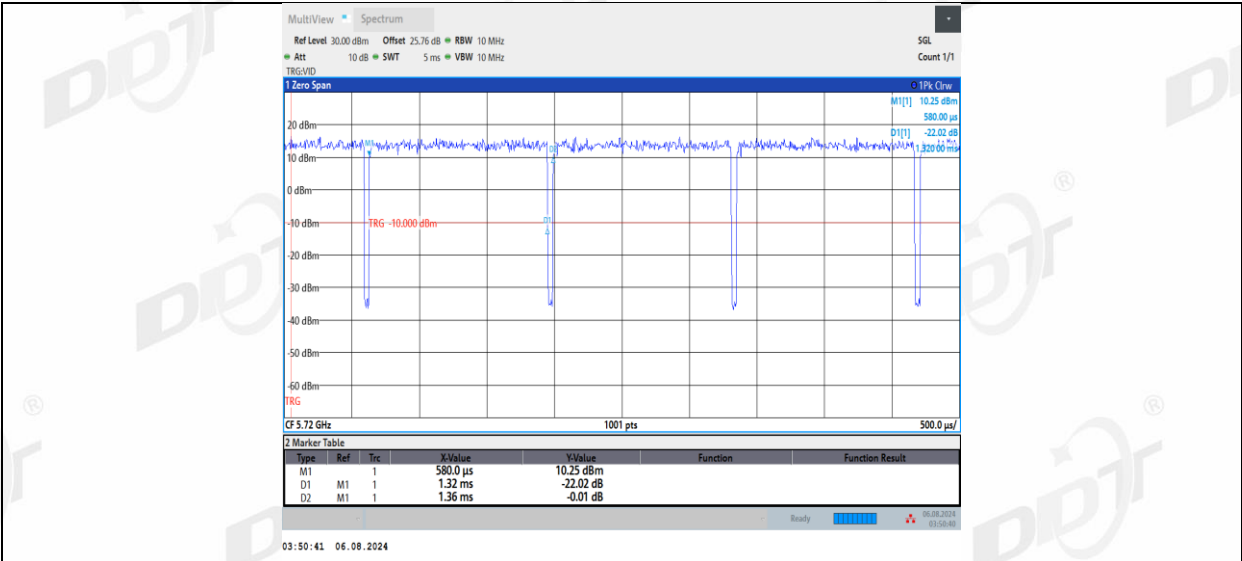


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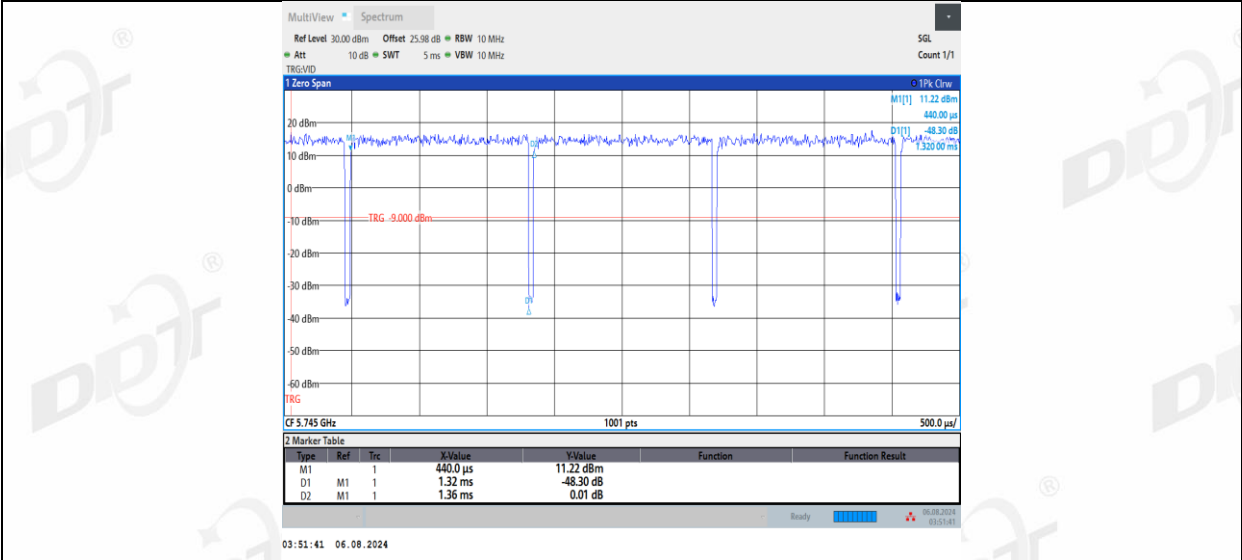


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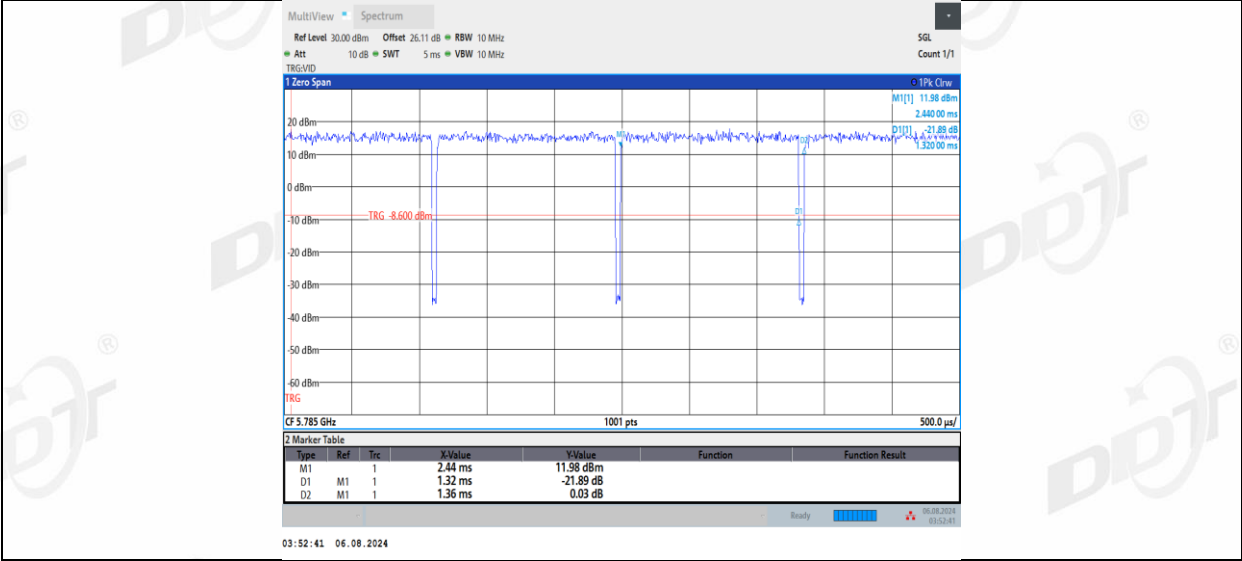




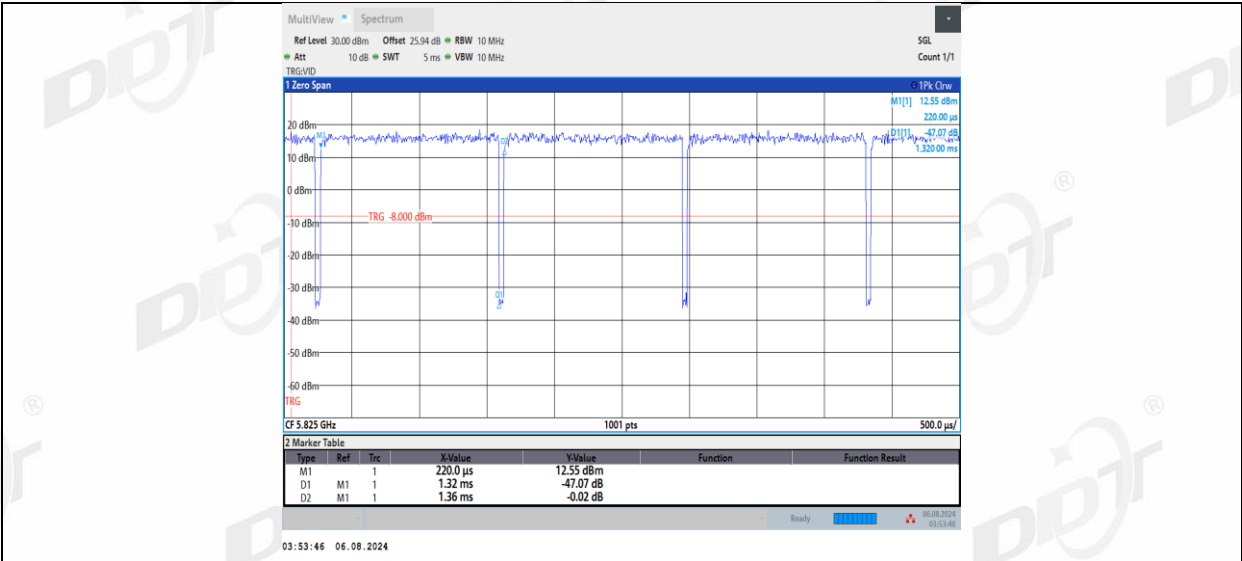
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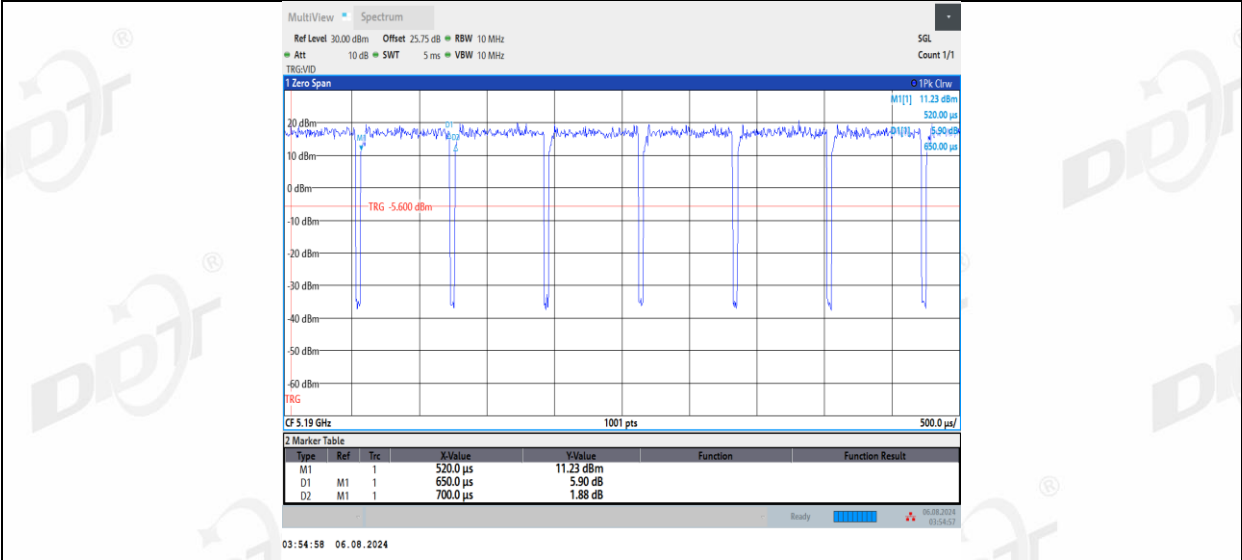
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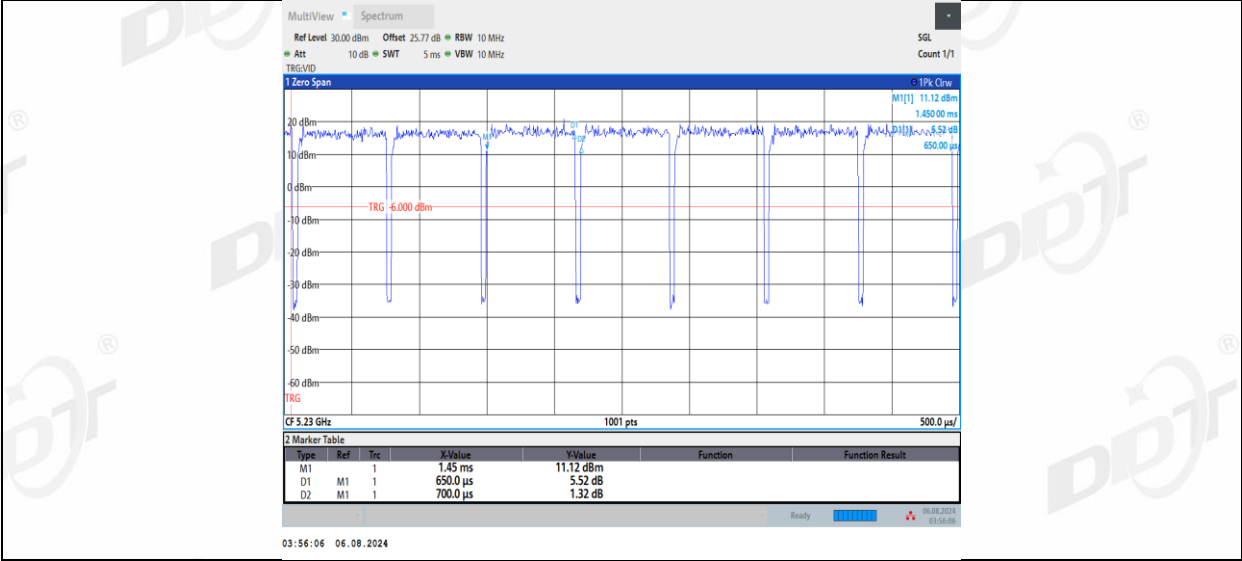
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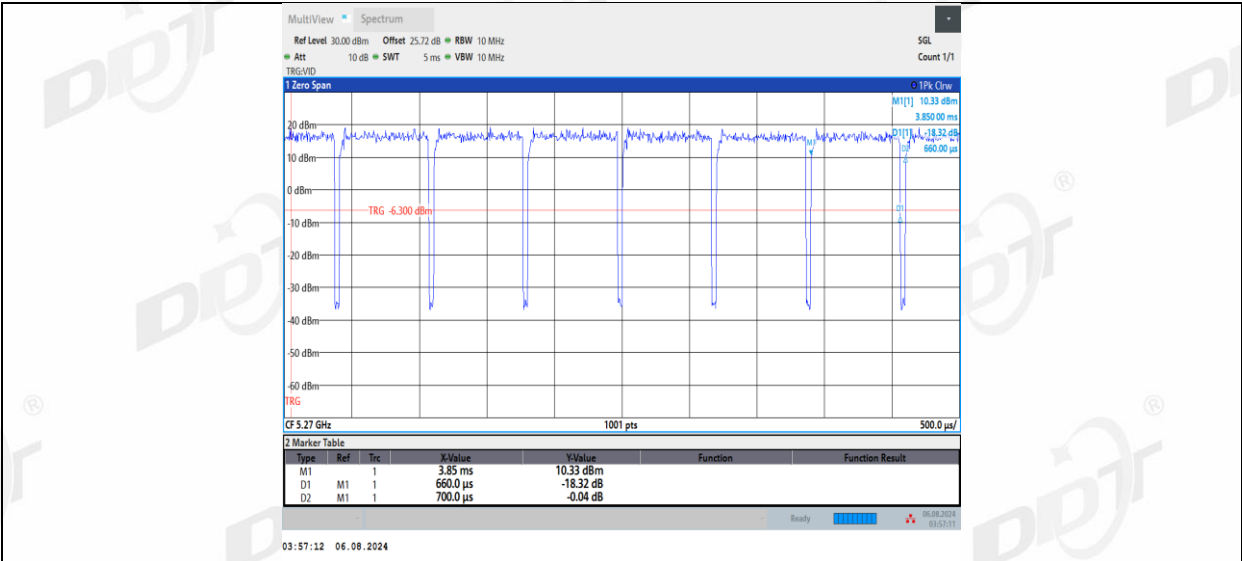
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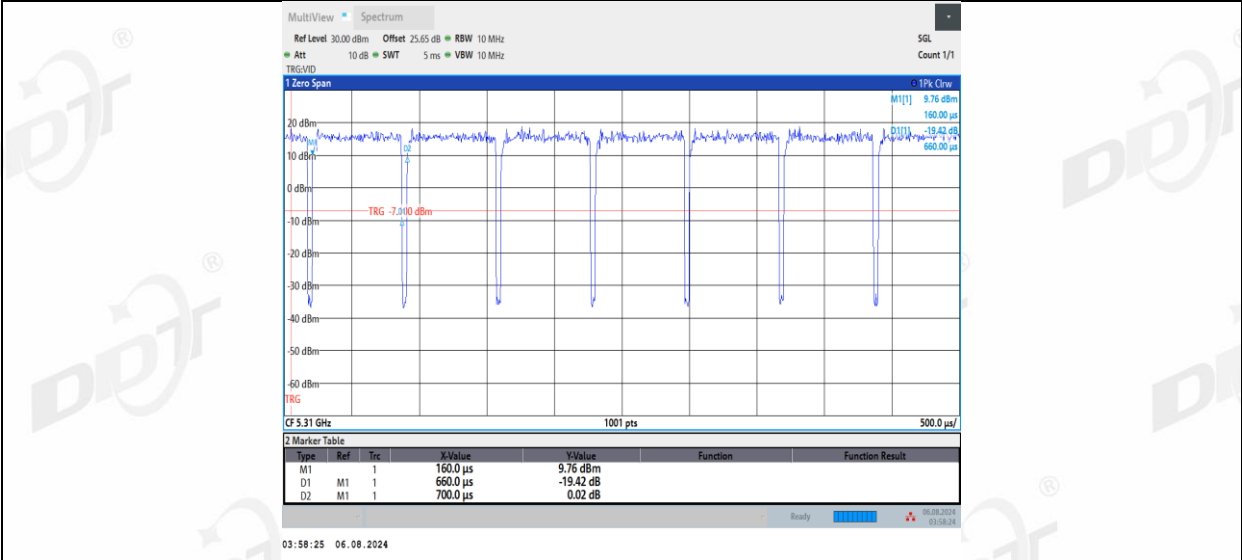
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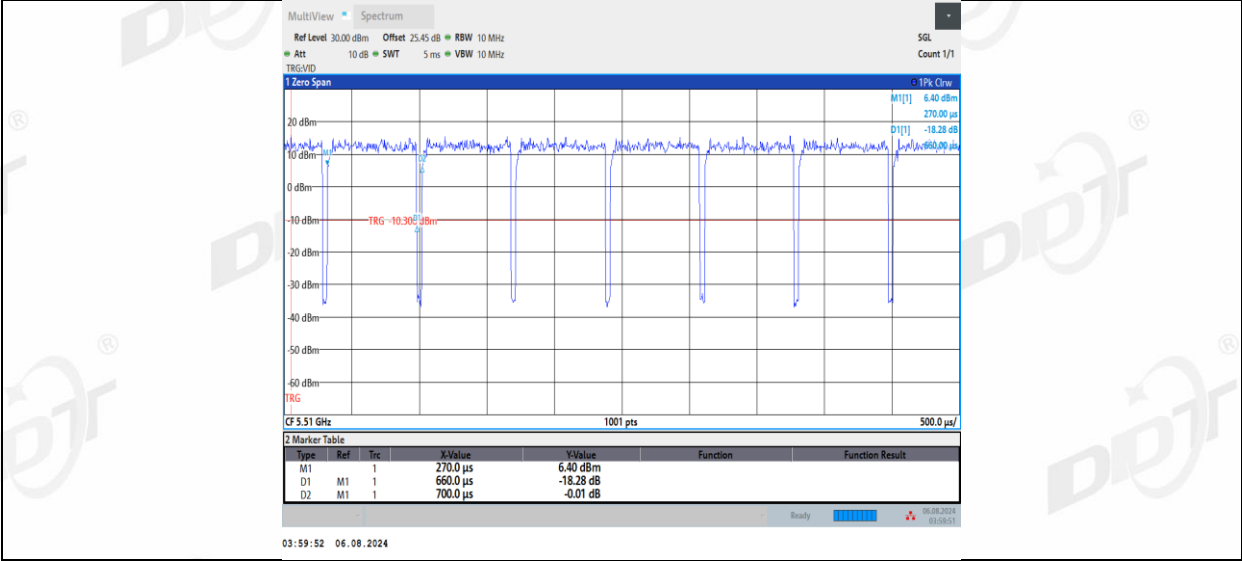
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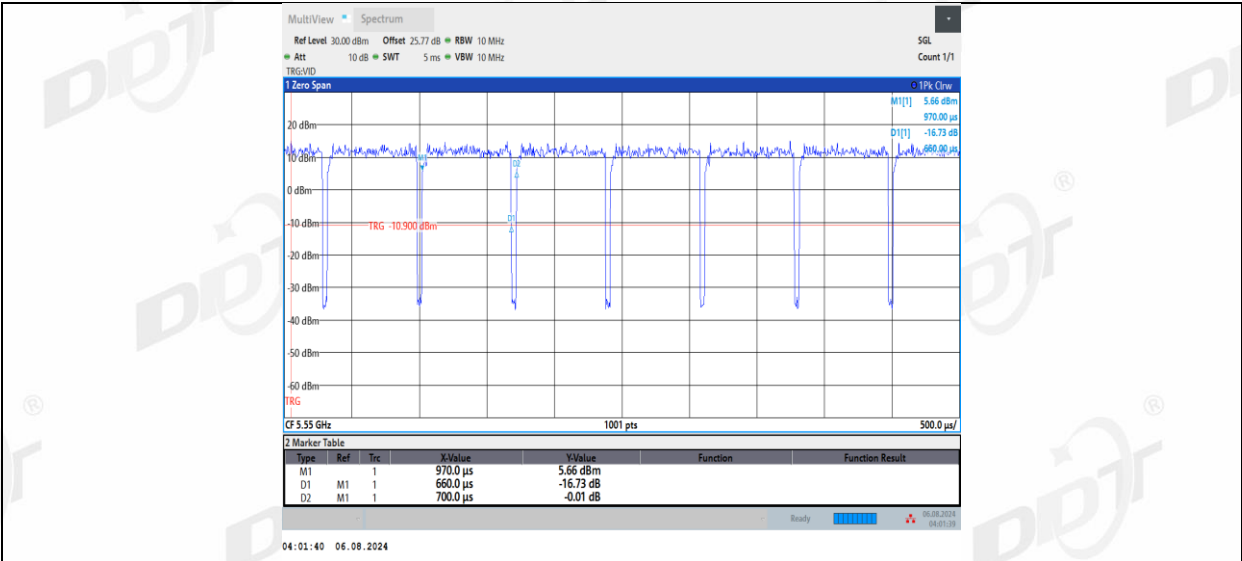
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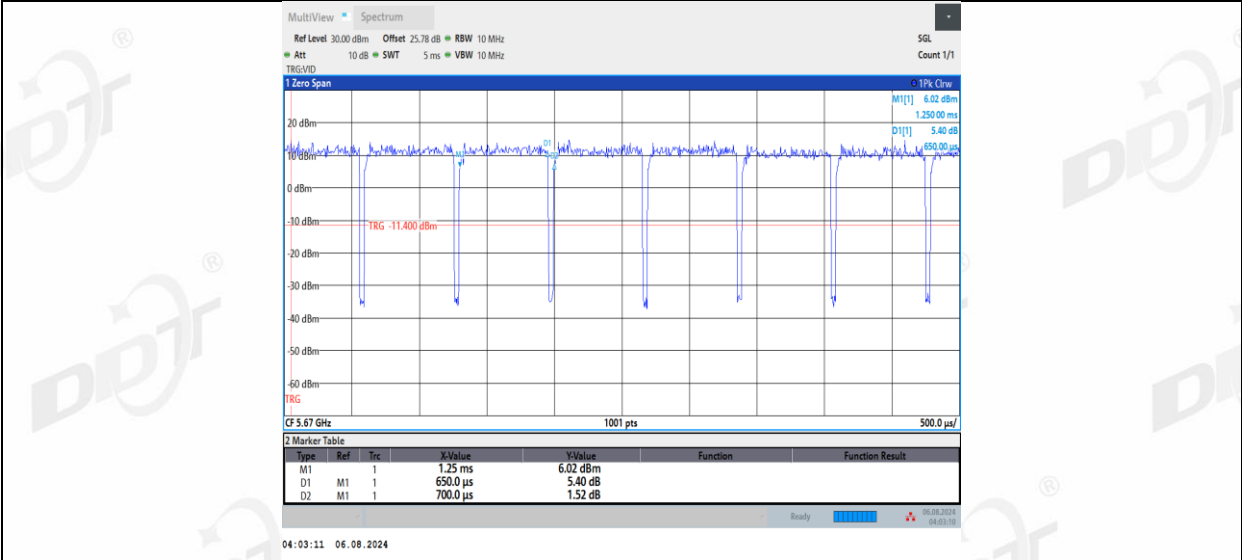
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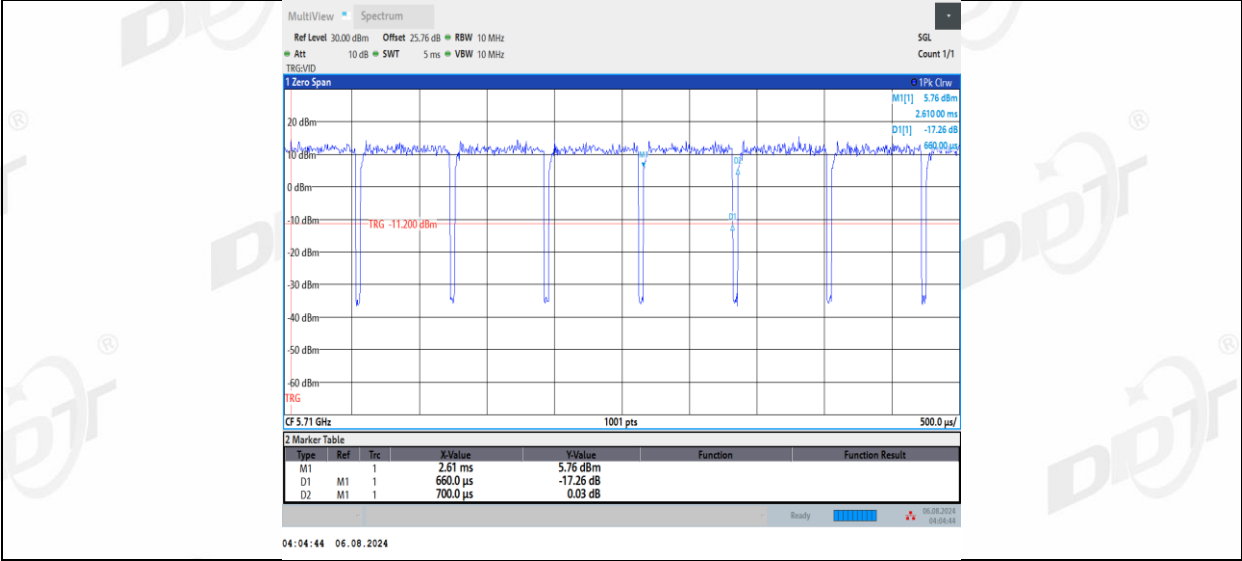
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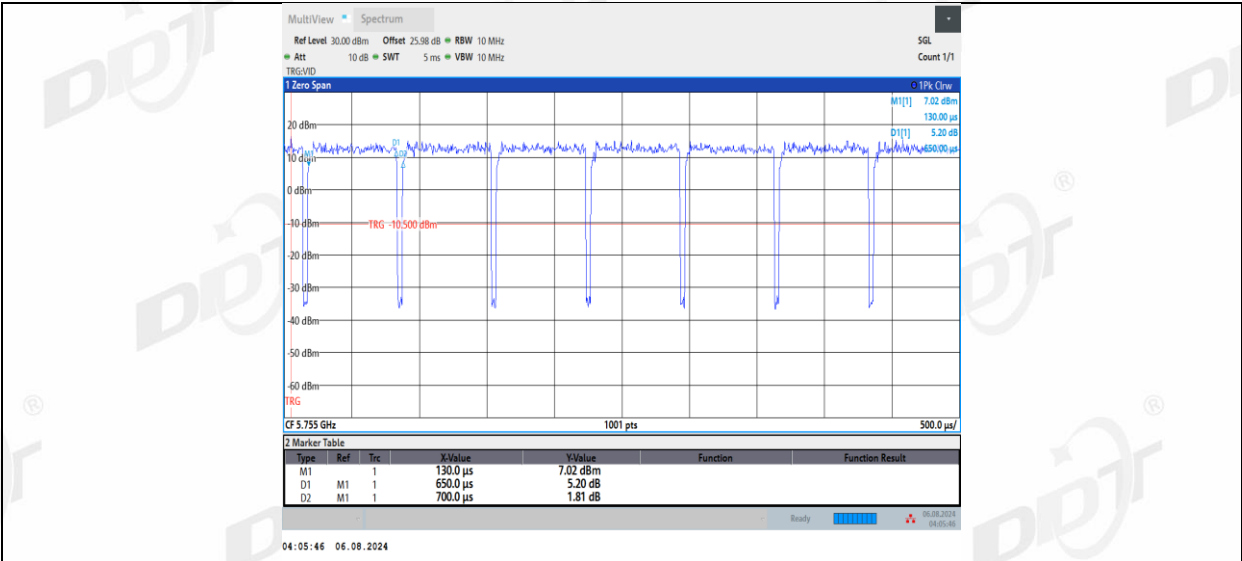
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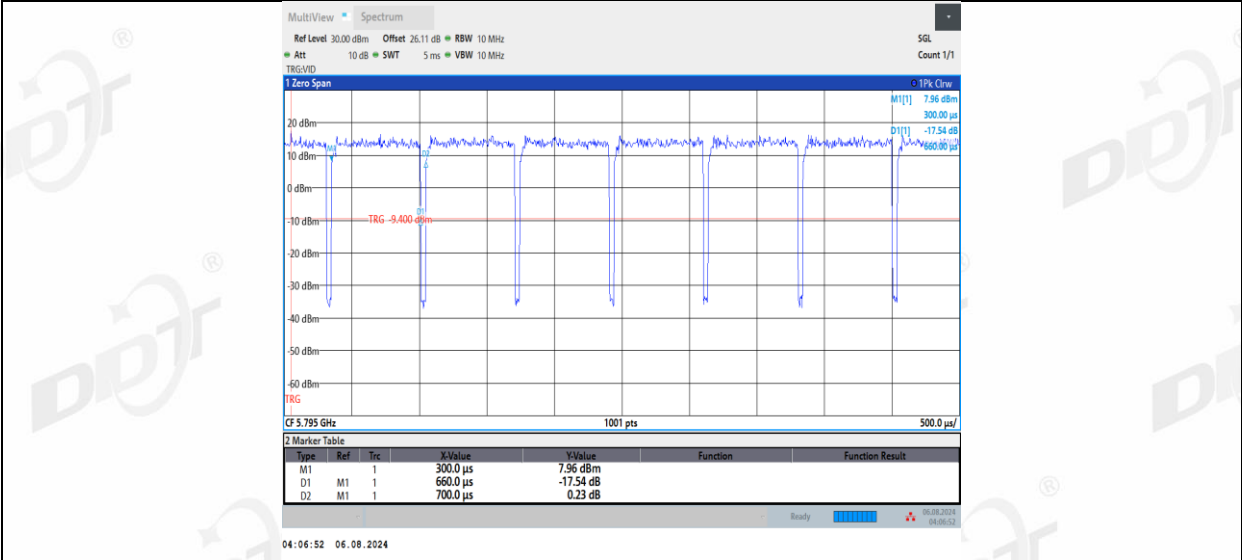
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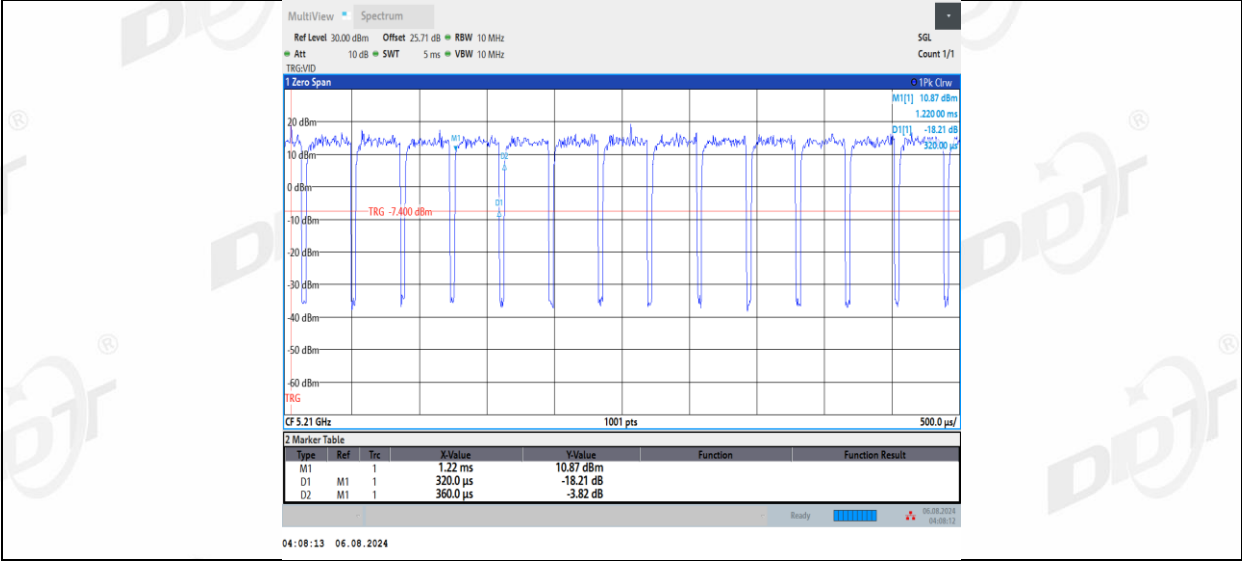
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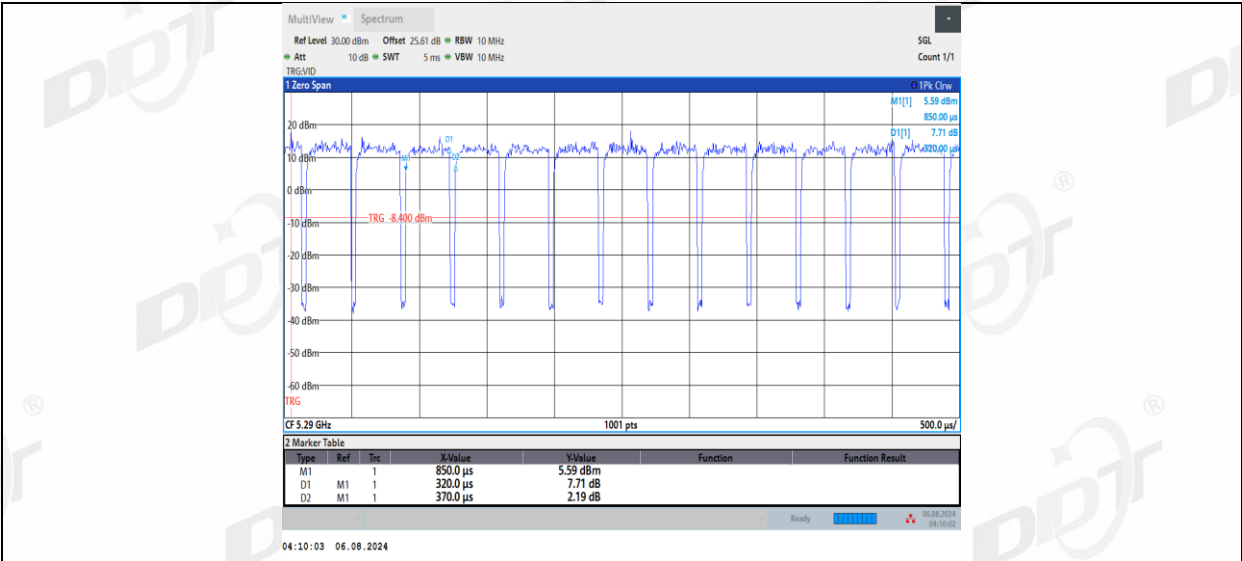
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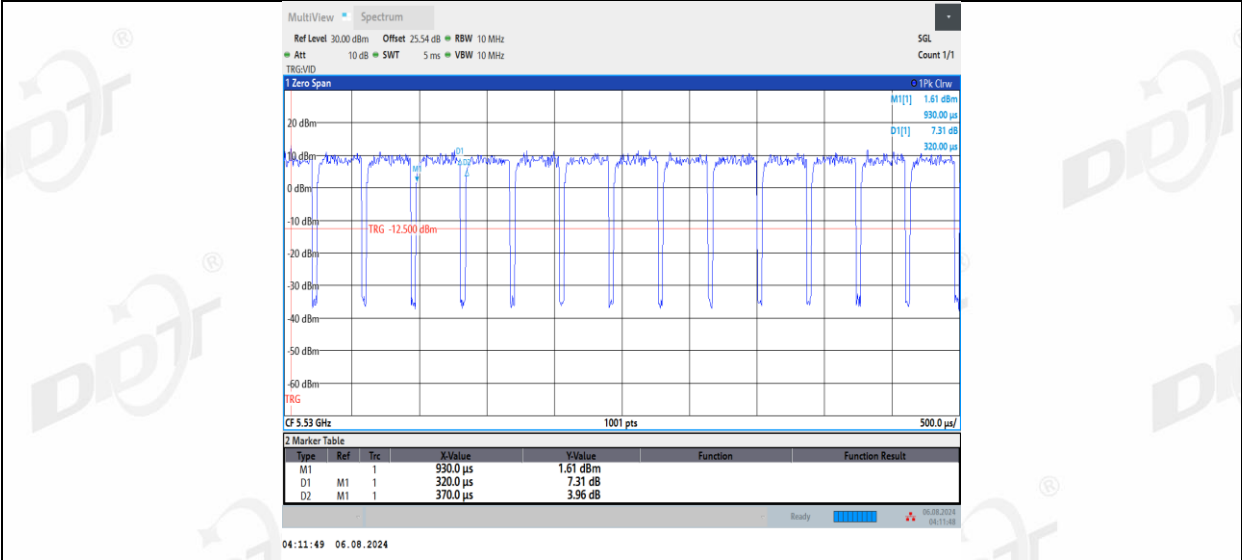
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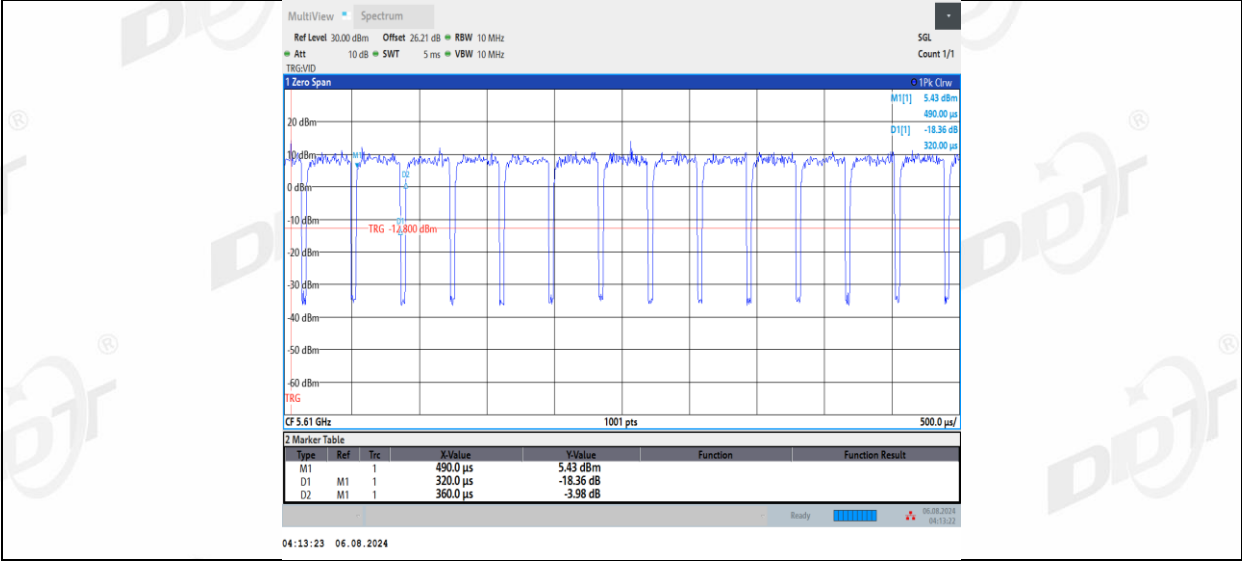
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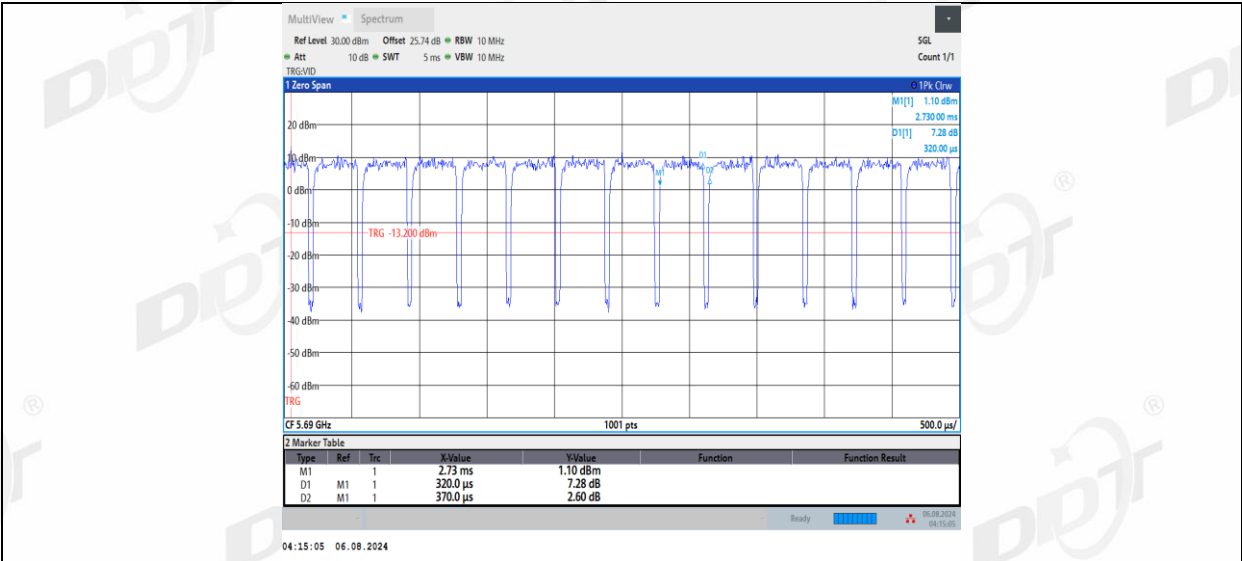
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11AC80SISO_Ant1_5610



11AC80SISO_Ant1_5690



11AC80SISO_Ant1_5775

