

Appendix for SA N78_3450-3550MHz

Appendix A: Average Power Output Data for SA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
N78-3450-3550	30	20	DFT-QPSK	L	Edge_1RB_Left	23.25	PC2	PASS
N78-3450-3550	30	20	DFT-QPSK	L	Edge_1RB_Right	24.14	PC2	PASS
N78-3450-3550	30	20	DFT-QPSK	L	Outer_Full	26.25	PC2	PASS
N78-3450-3550	30	20	DFT-QPSK	L	Inner_Full	27.22	PC2	PASS
N78-3450-3550	30	20	CP-QPSK	L	Edge_1RB_Left	23.20	PC2	PASS
N78-3450-3550	30	20	CP-QPSK	L	Edge_1RB_Right	24.18	PC2	PASS
N78-3450-3550	30	20	CP-QPSK	L	Outer_Full	24.29	PC2	PASS
N78-3450-3550	30	20	CP-QPSK	L	Inner_Full	25.88	PC2	PASS
N78-3450-3550	30	20	DFT-QPSK	M	Edge_1RB_Left	24.28	PC2	PASS
N78-3450-3550	30	20	DFT-QPSK	M	Edge_1RB_Right	23.68	PC2	PASS
N78-3450-3550	30	20	DFT-QPSK	M	Outer_Full	26.60	PC2	PASS
N78-3450-3550	30	20	DFT-QPSK	M	Inner_Full	27.40	PC2	PASS
N78-3450-3550	30	20	CP-QPSK	M	Edge_1RB_Left	24.31	PC2	PASS
N78-3450-3550	30	20	CP-QPSK	M	Edge_1RB_Right	23.61	PC2	PASS
N78-3450	30	20	CP-QPSK	M	Outer_Full	24.61	PC2	PASS

-3550								
N78-3450 -3550	30	20	CP-QPSK	M	Inner_Full	26.19	PC2	PASS
N78-3450 -3550	30	20	DFT-QPSK	H	Edge_1RB_Left	23.84	PC2	PASS
N78-3450 -3550	30	20	DFT-QPSK	H	Edge_1RB_Right	22.91	PC2	PASS
N78-3450 -3550	30	20	DFT-QPSK	H	Outer_Full	25.81	PC2	PASS
N78-3450 -3550	30	20	DFT-QPSK	H	Inner_Full	26.81	PC2	PASS
N78-3450 -3550	30	20	CP-QPSK	H	Edge_1RB_Left	23.97	PC2	PASS
N78-3450 -3550	30	20	CP-QPSK	H	Edge_1RB_Right	22.86	PC2	PASS
N78-3450 -3550	30	20	CP-QPSK	H	Outer_Full	23.84	PC2	PASS
N78-3450 -3550	30	20	CP-QPSK	H	Inner_Full	25.30	PC2	PASS
N78-3450 -3550	30	40	DFT-QPSK	L	Edge_1RB_Left	23.49	PC2	PASS
N78-3450 -3550	30	40	DFT-QPSK	L	Edge_1RB_Right	24.27	PC2	PASS
N78-3450 -3550	30	40	DFT-QPSK	L	Outer_Full	23.11	PC2	PASS
N78-3450 -3550	30	40	DFT-QPSK	L	Inner_Full	27.36	PC2	PASS
N78-3450 -3550	30	40	CP-QPSK	L	Edge_1RB_Left	23.31	PC2	PASS
N78-3450 -3550	30	40	CP-QPSK	L	Edge_1RB_Right	24.22	PC2	PASS
N78-3450 -3550	30	40	CP-QPSK	L	Outer_Full	24.48	PC2	PASS
N78-3450 -3550	30	40	CP-QPSK	L	Inner_Full	26.15	PC2	PASS
N78-3450 -3550	30	40	DFT-QPSK	M	Edge_1RB_Left	24.10	PC2	PASS
N78-3450 -3550	30	40	DFT-QPSK	M	Edge_1RB_Right	23.69	PC2	PASS
N78-3450 -3550	30	40	DFT-QPSK	M	Outer_Full	23.20	PC2	PASS
N78-3450	30	40	DFT-QPSK	M	Inner_Full	27.30	PC2	PASS

-3550								
N78-3450 -3550	30	40	CP-QPSK	M	Edge_1RB_Left	24.09	PC2	PASS
N78-3450 -3550	30	40	CP-QPSK	M	Edge_1RB_Right	23.71	PC2	PASS
N78-3450 -3550	30	40	CP-QPSK	M	Outer_Full	21.52	PC2	PASS
N78-3450 -3550	30	40	CP-QPSK	M	Inner_Full	24.56	PC2	PASS
N78-3450 -3550	30	40	DFT-QPSK	H	Edge_1RB_Left	23.67	PC2	PASS
N78-3450 -3550	30	40	DFT-QPSK	H	Edge_1RB_Right	22.16	PC2	PASS
N78-3450 -3550	30	40	DFT-QPSK	H	Outer_Full	26.06	PC2	PASS
N78-3450 -3550	30	40	DFT-QPSK	H	Inner_Full	23.77	PC2	PASS
N78-3450 -3550	30	40	CP-QPSK	H	Edge_1RB_Left	23.75	PC2	PASS
N78-3450 -3550	30	40	CP-QPSK	H	Edge_1RB_Right	23.02	PC2	PASS
N78-3450 -3550	30	40	CP-QPSK	H	Outer_Full	24.01	PC2	PASS
N78-3450 -3550	30	40	CP-QPSK	H	Inner_Full	25.68	PC2	PASS
N78-3450 -3550	30	50	DFT-QPSK	L	Edge_1RB_Left	23.16	PC2	PASS
N78-3450 -3550	30	50	DFT-QPSK	L	Edge_1RB_Right	24.09	PC2	PASS
N78-3450 -3550	30	50	DFT-QPSK	L	Outer_Full	23.82	PC2	PASS
N78-3450 -3550	30	50	DFT-QPSK	L	Inner_Full	27.41	PC2	PASS
N78-3450 -3550	30	50	CP-QPSK	L	Edge_1RB_Left	23.12	PC2	PASS
N78-3450 -3550	30	50	CP-QPSK	L	Edge_1RB_Right	24.08	PC2	PASS
N78-3450 -3550	30	50	CP-QPSK	L	Outer_Full	24.43	PC2	PASS
N78-3450 -3550	30	50	CP-QPSK	L	Inner_Full	26.01	PC2	PASS
N78-3450	30	50	DFT-QPSK	M	Edge_1RB_Left	23.89	PC2	PASS

-3550								
N78-3450 -3550	30	50	DFT-QPSK	M	Edge_1RB_Right	23.61	PC2	PASS
N78-3450 -3550	30	50	DFT-QPSK	M	Outer_Full	23.73	PC2	PASS
N78-3450 -3550	30	50	DFT-QPSK	M	Inner_Full	23.04	PC2	PASS
N78-3450 -3550	30	50	CP-QPSK	M	Edge_1RB_Left	24.75	PC2	PASS
N78-3450 -3550	30	50	CP-QPSK	M	Edge_1RB_Right	22.49	PC2	PASS
N78-3450 -3550	30	50	CP-QPSK	M	Outer_Full	24.32	PC2	PASS
N78-3450 -3550	30	50	CP-QPSK	M	Inner_Full	23.05	PC2	PASS
N78-3450 -3550	30	50	DFT-QPSK	H	Edge_1RB_Left	23.65	PC2	PASS
N78-3450 -3550	30	50	DFT-QPSK	H	Edge_1RB_Right	24.46	PC2	PASS
N78-3450 -3550	30	50	DFT-QPSK	H	Outer_Full	23.11	PC2	PASS
N78-3450 -3550	30	50	DFT-QPSK	H	Inner_Full	23.25	PC2	PASS
N78-3450 -3550	30	50	CP-QPSK	H	Edge_1RB_Left	23.72	PC2	PASS
N78-3450 -3550	30	50	CP-QPSK	H	Edge_1RB_Right	23.45	PC2	PASS
N78-3450 -3550	30	50	CP-QPSK	H	Outer_Full	24.03	PC2	PASS
N78-3450 -3550	30	50	CP-QPSK	H	Inner_Full	25.50	PC2	PASS
N78-3450 -3550	30	60	DFT-QPSK	L	Edge_1RB_Left	23.17	PC2	PASS
N78-3450 -3550	30	60	DFT-QPSK	L	Edge_1RB_Right	23.40	PC2	PASS
N78-3450 -3550	30	60	DFT-QPSK	L	Outer_Full	26.42	PC2	PASS
N78-3450 -3550	30	60	DFT-QPSK	L	Inner_Full	27.29	PC2	PASS
N78-3450 -3550	30	60	CP-QPSK	L	Edge_1RB_Left	23.15	PC2	PASS
N78-3450	30	60	CP-QPSK	L	Edge_1RB_Right	23.57	PC2	PASS

-3550								
N78-3450 -3550	30	60	CP-QPSK	L	Outer_Full	24.43	PC2	PASS
N78-3450 -3550	30	60	CP-QPSK	L	Inner_Full	25.97	PC2	PASS
N78-3450 -3550	30	60	DFT-QPSK	M	Edge_1RB_Left	23.96	PC2	PASS
N78-3450 -3550	30	60	DFT-QPSK	M	Edge_1RB_Right	23.62	PC2	PASS
N78-3450 -3550	30	60	DFT-QPSK	M	Outer_Full	26.34	PC2	PASS
N78-3450 -3550	30	60	DFT-QPSK	M	Inner_Full	27.16	PC2	PASS
N78-3450 -3550	30	60	CP-QPSK	M	Edge_1RB_Left	23.98	PC2	PASS
N78-3450 -3550	30	60	CP-QPSK	M	Edge_1RB_Right	23.54	PC2	PASS
N78-3450 -3550	30	60	CP-QPSK	M	Outer_Full	23.61	PC2	PASS
N78-3450 -3550	30	60	CP-QPSK	M	Inner_Full	23.65	PC2	PASS
N78-3450 -3550	30	60	DFT-QPSK	H	Edge_1RB_Left	24.08	PC2	PASS
N78-3450 -3550	30	60	DFT-QPSK	H	Edge_1RB_Right	24.41	PC2	PASS
N78-3450 -3550	30	60	DFT-QPSK	H	Outer_Full	24.37	PC2	PASS
N78-3450 -3550	30	60	DFT-QPSK	H	Inner_Full	24.33	PC2	PASS
N78-3450 -3550	30	60	CP-QPSK	H	Edge_1RB_Left	23.98	PC2	PASS
N78-3450 -3550	30	60	CP-QPSK	H	Edge_1RB_Right	23.52	PC2	PASS
N78-3450 -3550	30	60	CP-QPSK	H	Outer_Full	24.39	PC2	PASS
N78-3450 -3550	30	60	CP-QPSK	H	Inner_Full	24.29	PC2	PASS
N78-3450 -3550	30	80	DFT-QPSK	L	Edge_1RB_Left	25.58	PC2	PASS
N78-3450 -3550	30	80	DFT-QPSK	L	Edge_1RB_Right	24.82	PC2	PASS
N78-3450	30	80	DFT-QPSK	L	Outer_Full	24.56	PC2	PASS

-3550								
N78-3450 -3550	30	80	DFT-QPSK	L	Inner_Full	23.66	PC2	PASS
N78-3450 -3550	30	80	CP-QPSK	L	Edge_1RB_Left	24.64	PC2	PASS
N78-3450 -3550	30	80	CP-QPSK	L	Edge_1RB_Right	23.78	PC2	PASS
N78-3450 -3550	30	80	CP-QPSK	L	Outer_Full	23.45	PC2	PASS
N78-3450 -3550	30	80	CP-QPSK	L	Inner_Full	23.56	PC2	PASS
N78-3450 -3550	30	80	DFT-QPSK	M	Edge_1RB_Left	23.29	PC2	PASS
N78-3450 -3550	30	80	DFT-QPSK	M	Edge_1RB_Right	23.60	PC2	PASS
N78-3450 -3550	30	80	DFT-QPSK	M	Outer_Full	23.45	PC2	PASS
N78-3450 -3550	30	80	DFT-QPSK	M	Inner_Full	23.49	PC2	PASS
N78-3450 -3550	30	80	CP-QPSK	M	Edge_1RB_Left	23.37	PC2	PASS
N78-3450 -3550	30	80	CP-QPSK	M	Edge_1RB_Right	23.63	PC2	PASS
N78-3450 -3550	30	80	CP-QPSK	M	Outer_Full	24.48	PC2	PASS
N78-3450 -3550	30	80	CP-QPSK	M	Inner_Full	25.68	PC2	PASS
N78-3450 -3550	30	80	DFT-QPSK	H	Edge_1RB_Left	23.31	PC2	PASS
N78-3450 -3550	30	80	DFT-QPSK	H	Edge_1RB_Right	23.13	PC2	PASS
N78-3450 -3550	30	80	DFT-QPSK	H	Outer_Full	24.30	PC2	PASS
N78-3450 -3550	30	80	DFT-QPSK	H	Inner_Full	24.38	PC2	PASS
N78-3450 -3550	30	80	CP-QPSK	H	Edge_1RB_Left	24.46	PC2	PASS
N78-3450 -3550	30	80	CP-QPSK	H	Edge_1RB_Right	24.24	PC2	PASS
N78-3450 -3550	30	80	CP-QPSK	H	Outer_Full	23.32	PC2	PASS
N78-3450	30	80	CP-QPSK	H	Inner_Full	23.38	PC2	PASS

-3550								
N78-3450 -3550	30	90	DFT-QPSK	L	Edge_1RB_Left	24.26	PC2	PASS
N78-3450 -3550	30	90	DFT-QPSK	L	Edge_1RB_Right	23.11	PC2	PASS
N78-3450 -3550	30	90	DFT-QPSK	L	Outer_Full	26.03	PC2	PASS
N78-3450 -3550	30	90	DFT-QPSK	L	Inner_Full	27.21	PC2	PASS
N78-3450 -3550	30	90	CP-QPSK	L	Edge_1RB_Left	23.01	PC2	PASS
N78-3450 -3550	30	90	CP-QPSK	L	Edge_1RB_Right	23.30	PC2	PASS
N78-3450 -3550	30	90	CP-QPSK	L	Outer_Full	24.10	PC2	PASS
N78-3450 -3550	30	90	CP-QPSK	L	Inner_Full	25.76	PC2	PASS
N78-3450 -3550	30	90	DFT-QPSK	M	Edge_1RB_Left	23.35	PC2	PASS
N78-3450 -3550	30	90	DFT-QPSK	M	Edge_1RB_Right	22.85	PC2	PASS
N78-3450 -3550	30	90	DFT-QPSK	M	Outer_Full	26.19	PC2	PASS
N78-3450 -3550	30	90	DFT-QPSK	M	Inner_Full	27.23	PC2	PASS
N78-3450 -3550	30	90	CP-QPSK	M	Edge_1RB_Left	23.30	PC2	PASS
N78-3450 -3550	30	90	CP-QPSK	M	Edge_1RB_Right	23.09	PC2	PASS
N78-3450 -3550	30	90	CP-QPSK	M	Outer_Full	24.23	PC2	PASS
N78-3450 -3550	30	90	CP-QPSK	M	Inner_Full	25.70	PC2	PASS
N78-3450 -3550	30	90	DFT-QPSK	H	Edge_1RB_Left	23.62	PC2	PASS
N78-3450 -3550	30	90	DFT-QPSK	H	Edge_1RB_Right	22.79	PC2	PASS
N78-3450 -3550	30	90	DFT-QPSK	H	Outer_Full	25.96	PC2	PASS
N78-3450 -3550	30	90	DFT-QPSK	H	Inner_Full	27.12	PC2	PASS
N78-3450	30	90	CP-QPSK	H	Edge_1RB_Left	23.70	PC2	PASS

-3550								
N78-3450 -3550	30	90	CP-QPSK	H	Edge_1RB_Right	22.72	PC2	PASS
N78-3450 -3550	30	90	CP-QPSK	H	Outer_Full	24.04	PC2	PASS
N78-3450 -3550	30	90	CP-QPSK	H	Inner_Full	25.61	PC2	PASS
N78-3450 -3550	30	100	DFT-QPSK	L	Edge_1RB_Left	22.81	PC2	PASS
N78-3450 -3550	30	100	DFT-QPSK	L	Edge_1RB_Right	22.55	PC2	PASS
N78-3450 -3550	30	100	DFT-QPSK	L	Outer_Full	26.04	PC2	PASS
N78-3450 -3550	30	100	DFT-QPSK	L	Inner_Full	27.16	PC2	PASS
N78-3450 -3550	30	100	CP-QPSK	L	Edge_1RB_Left	22.92	PC2	PASS
N78-3450 -3550	30	100	CP-QPSK	L	Edge_1RB_Right	23.54	PC2	PASS
N78-3450 -3550	30	100	CP-QPSK	L	Outer_Full	23.33	PC2	PASS
N78-3450 -3550	30	100	CP-QPSK	L	Inner_Full	25.61	PC2	PASS
N78-3450 -3550	30	100	DFT-QPSK	M	Edge_1RB_Left	22.86	PC2	PASS
N78-3450 -3550	30	100	DFT-QPSK	M	Edge_1RB_Right	22.62	PC2	PASS
N78-3450 -3550	30	100	DFT-QPSK	M	Outer_Full	22.59	PC2	PASS
N78-3450 -3550	30	100	DFT-QPSK	M	Inner_Full	27.18	PC2	PASS
N78-3450 -3550	30	100	CP-QPSK	M	Edge_1RB_Left	22.83	PC2	PASS
N78-3450 -3550	30	100	CP-QPSK	M	Edge_1RB_Right	22.66	PC2	PASS
N78-3450 -3550	30	100	CP-QPSK	M	Outer_Full	24.15	PC2	PASS
N78-3450 -3550	30	100	CP-QPSK	M	Inner_Full	25.73	PC2	PASS
N78-3450 -3550	30	100	DFT-QPSK	H	Edge_1RB_Left	22.77	PC2	PASS
N78-3450	30	100	DFT-QPSK	H	Edge_1RB_Right	22.55	PC2	PASS

-3550								
N78-3450 -3550	30	100	DFT-QPSK	H	Outer_Full	26.11	PC2	PASS
N78-3450 -3550	30	100	DFT-QPSK	H	Inner_Full	27.12	PC2	PASS
N78-3450 -3550	30	100	CP-QPSK	H	Edge_1RB_Left	23.05	PC2	PASS
N78-3450 -3550	30	100	CP-QPSK	H	Edge_1RB_Right	22.66	PC2	PASS
N78-3450 -3550	30	100	CP-QPSK	H	Outer_Full	24.09	PC2	PASS
N78-3450 -3550	30	100	CP-QPSK	H	Inner_Full	25.57	PC2	PASS

Appendix B: Peak-to-Average Ratio for SA

Peak-to-Average Ratio(CCDF)

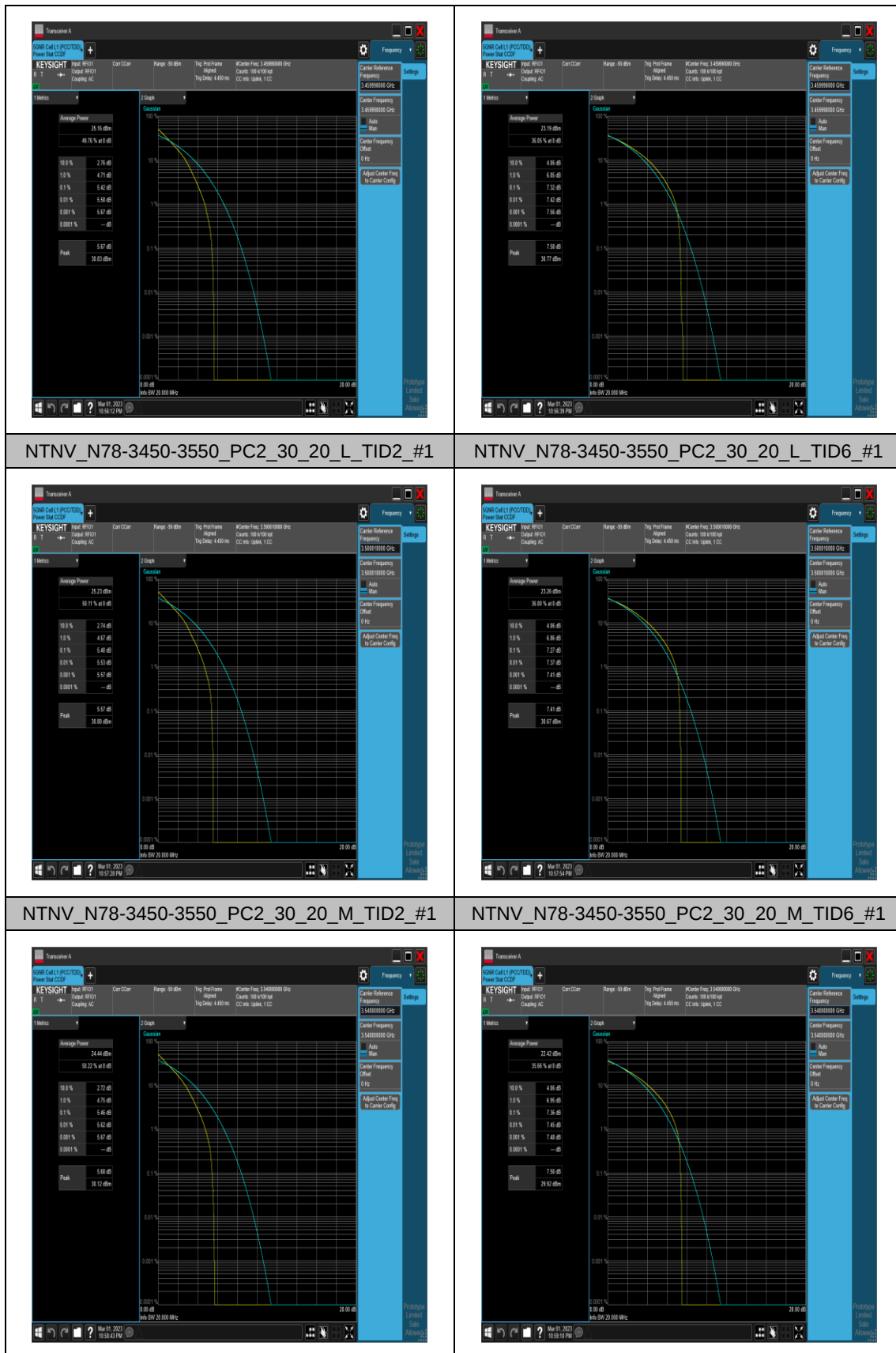
Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
N78-3450-3550	30	20	DFT-QPSK	L	Outer_Full	5.42	≤13	PASS
N78-3450-3550	30	20	CP-QPSK	L	Outer_Full	7.32	≤13	PASS
N78-3450-3550	30	20	DFT-QPSK	M	Outer_Full	5.40	≤13	PASS
N78-3450-3550	30	20	CP-QPSK	M	Outer_Full	7.27	≤13	PASS
N78-3450-3550	30	20	DFT-QPSK	H	Outer_Full	5.46	≤13	PASS
N78-3450-3550	30	20	CP-QPSK	H	Outer_Full	7.36	≤13	PASS
N78-3450-3550	30	40	DFT-QPSK	L	Outer_Full	5.49	≤13	PASS
N78-3450-3550	30	40	CP-QPSK	L	Outer_Full	7.52	≤13	PASS
N78-3450-3550	30	40	DFT-QPSK	M	Outer_Full	5.67	≤13	PASS
N78-3450-3550	30	40	CP-QPSK	M	Outer_Full	7.69	≤13	PASS
N78-3450-3550	30	40	DFT-QPSK	H	Outer_Full	5.81	≤13	PASS
N78-3450-3550	30	40	CP-QPSK	H	Outer_Full	7.91	≤13	PASS
N78-3450-3550	30	50	DFT-QPSK	L	Outer_Full	5.70	≤13	PASS
N78-3450-3550	30	50	CP-QPSK	L	Outer_Full	7.74	≤13	PASS
N78-3450-3550	30	50	DFT-QPSK	M	Outer_Full	5.82	≤13	PASS
N78-3450-3550	30	50	CP-QPSK	M	Outer_Full	7.81	≤13	PASS

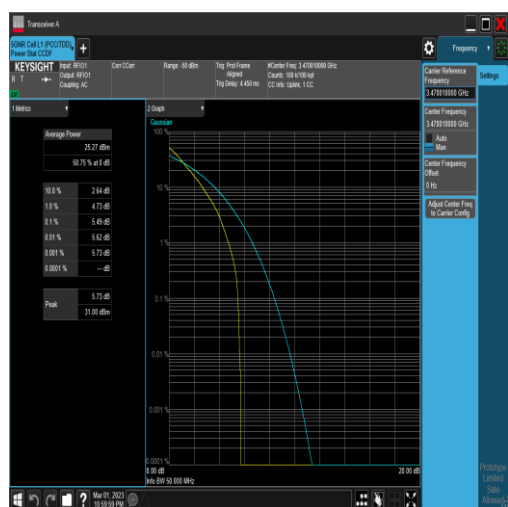
N78-3450-3550	30	50	DFT-QPSK	H	Outer_Full	5.30	≤13	PASS
N78-3450-3550	30	50	CP-QPSK	H	Outer_Full	7.97	≤13	PASS
N78-3450-3550	30	60	DFT-QPSK	L	Outer_Full	5.69	≤13	PASS
N78-3450-3550	30	60	CP-QPSK	L	Outer_Full	7.78	≤13	PASS
N78-3450-3550	30	60	DFT-QPSK	M	Outer_Full	5.90	≤13	PASS
N78-3450-3550	30	60	CP-QPSK	M	Outer_Full	7.82	≤13	PASS
N78-3450-3550	30	60	DFT-QPSK	H	Outer_Full	5.84	≤13	PASS
N78-3450-3550	30	60	CP-QPSK	H	Outer_Full	7.86	≤13	PASS
N78-3450-3550	30	80	DFT-QPSK	L	Outer_Full	5.20	≤13	PASS
N78-3450-3550	30	80	CP-QPSK	L	Outer_Full	7.99	≤13	PASS
N78-3450-3550	30	80	DFT-QPSK	M	Outer_Full	5.28	≤13	PASS
N78-3450-3550	30	80	CP-QPSK	M	Outer_Full	8.07	≤13	PASS
N78-3450-3550	30	80	DFT-QPSK	H	Outer_Full	5.48	≤13	PASS
N78-3450-3550	30	80	CP-QPSK	H	Outer_Full	7.36	≤13	PASS
N78-3450-3550	30	90	DFT-QPSK	L	Outer_Full	5.15	≤13	PASS
N78-3450-3550	30	90	CP-QPSK	L	Outer_Full	8.06	≤13	PASS
N78-3450-3550	30	90	DFT-QPSK	M	Outer_Full	5.17	≤13	PASS
N78-3450-3550	30	90	CP-QPSK	M	Outer_Full	8.00	≤13	PASS
N78-3450-3550	30	90	DFT-QPSK	H	Outer_Full	5.35	≤13	PASS
N78-3450-3550	30	90	CP-QPSK	H	Outer_Full	7.24	≤13	PASS
N78-3450-3550	30	100	DFT-QPSK	L	Outer_Full	5.22	≤13	PASS

N78-3450-3550	30	100	CP-QPSK	L	Outer_Full	7.29	≤13	PASS
N78-3450-3550	30	100	DFT-QPSK	M	Outer_Full	5.21	≤13	PASS
N78-3450-3550	30	100	CP-QPSK	M	Outer_Full	8.11	≤13	PASS
N78-3450-3550	30	100	DFT-QPSK	H	Outer_Full	5.24	≤13	PASS
N78-3450-3550	30	100	CP-QPSK	H	Outer_Full	8.02	≤13	PASS

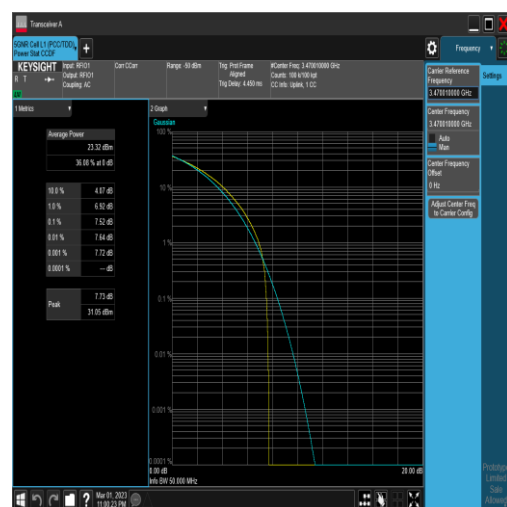
Test Graphs



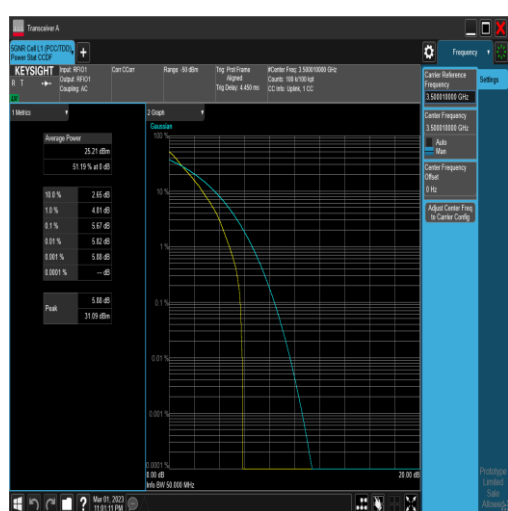
NTNV N78-3450-3550 PC2 30 20 H TID2 #1



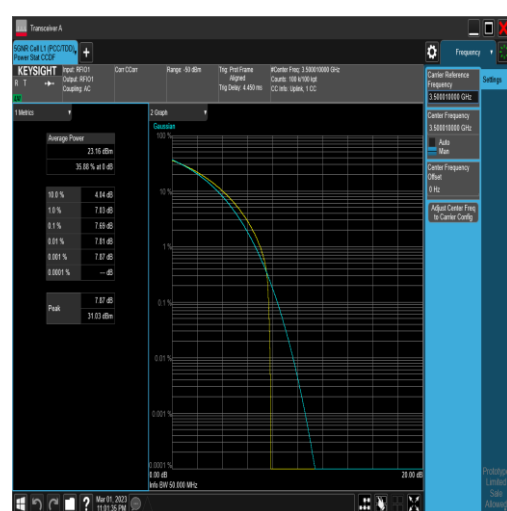
NTNV N78-3450-3550 PC2 30 20 H TID6 #1



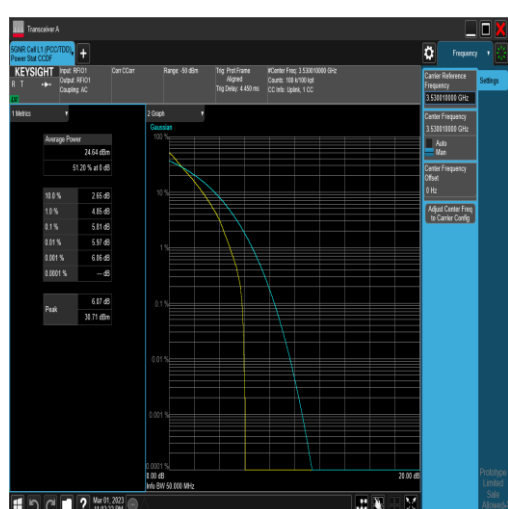
NTNV N78-3450-3550 PC2 30 40 L TID2 #1



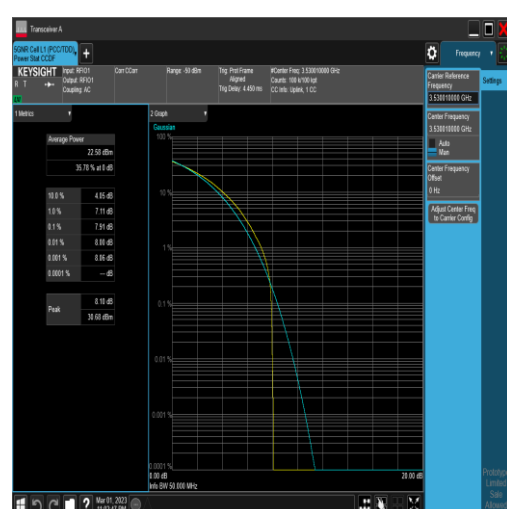
NTNV N78-3450-3550 PC2 30 40 L TID6 #1



NTNV N78-3450-3550 PC2 30 40 M TID2 #1

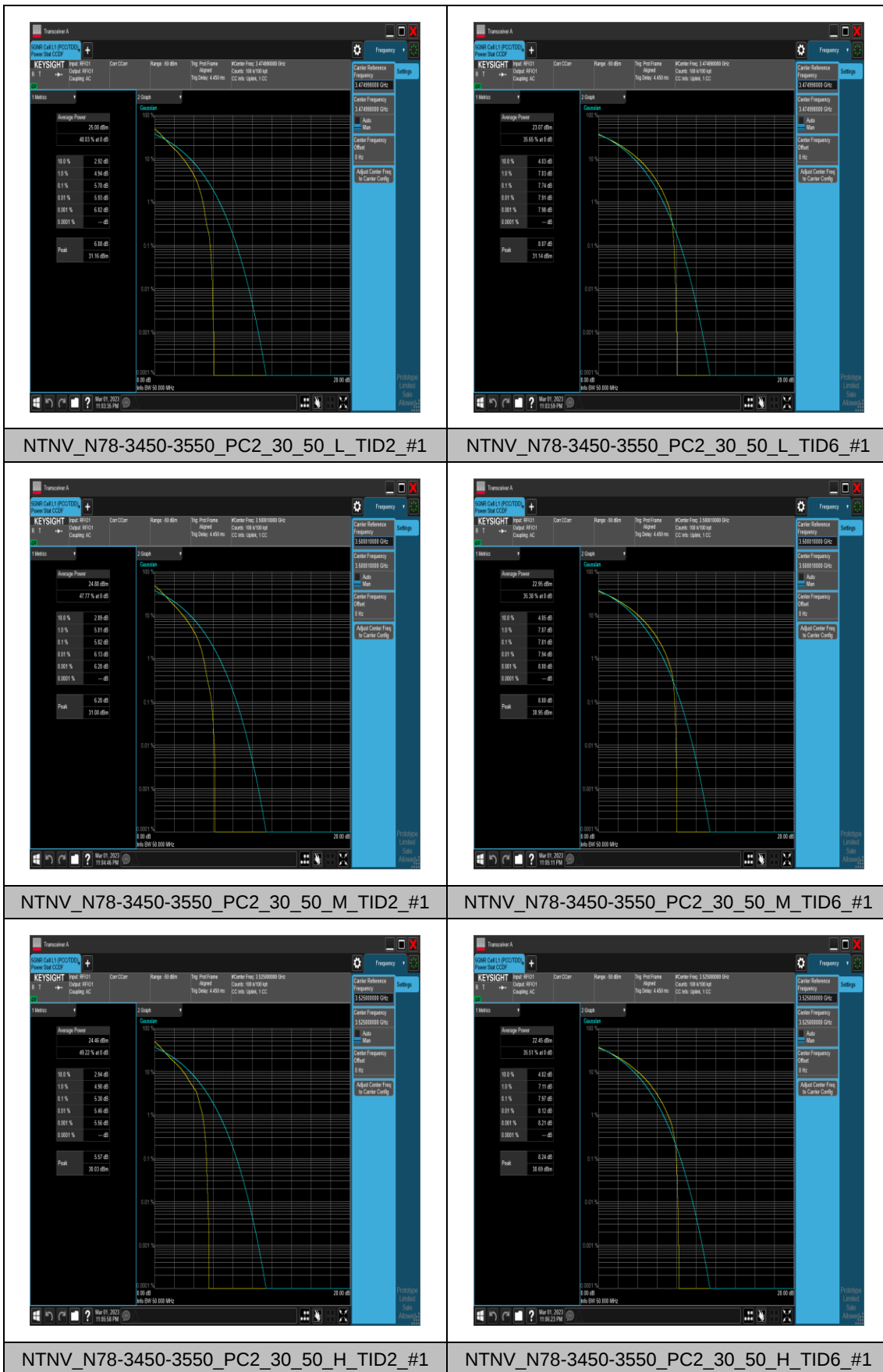


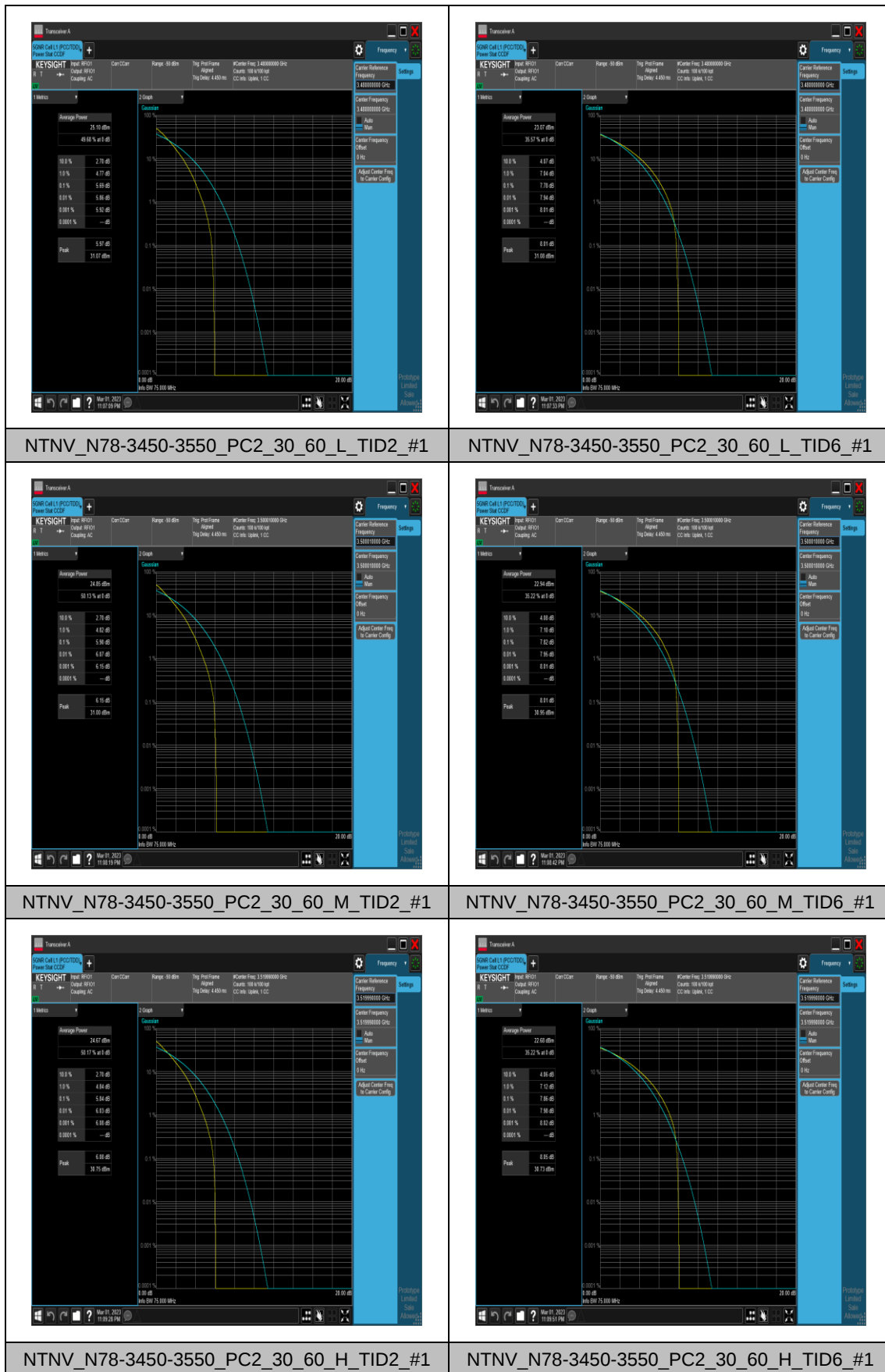
NTNV N78-3450-3550 PC2 30 40 M TID6 #1

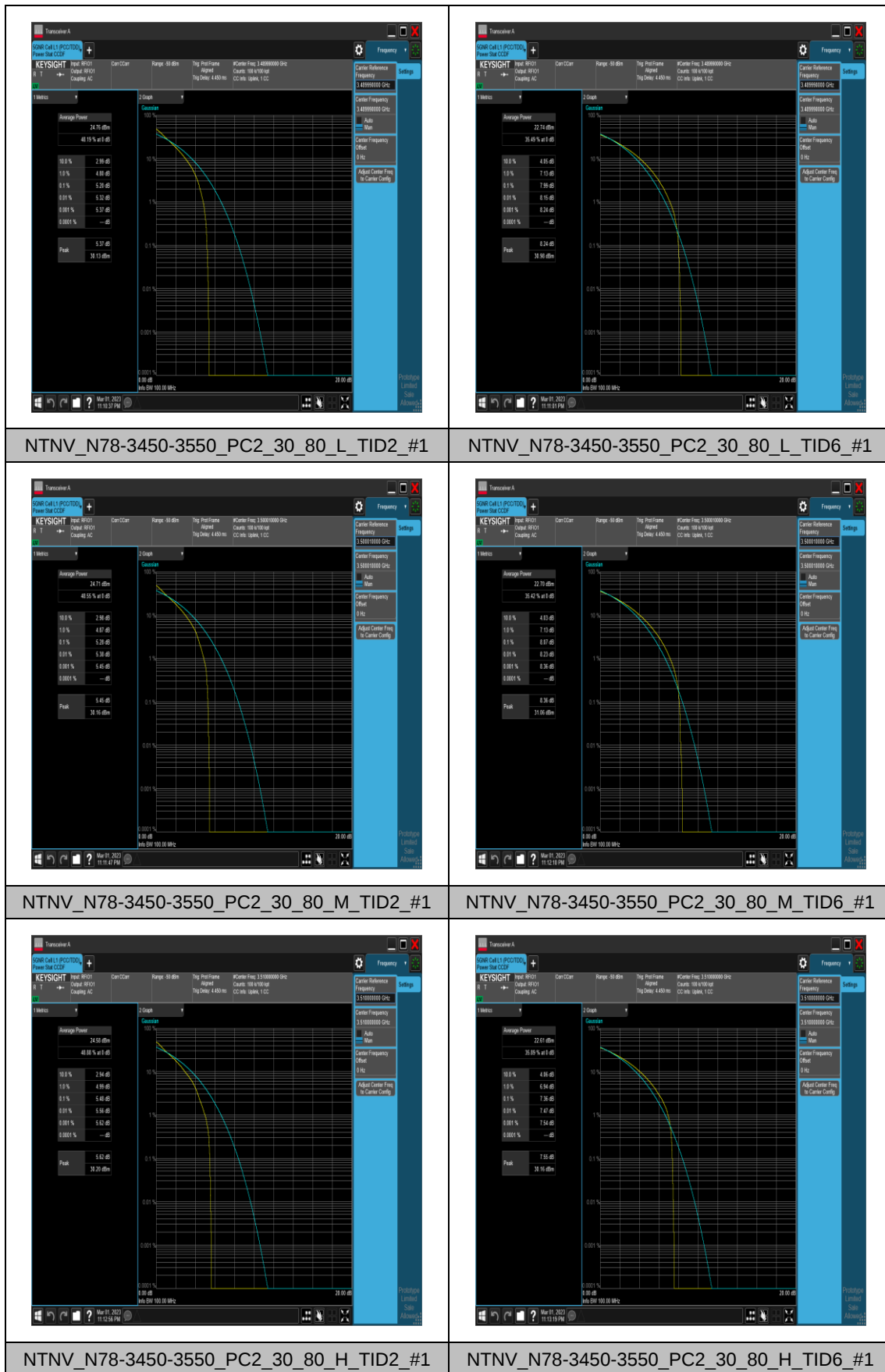


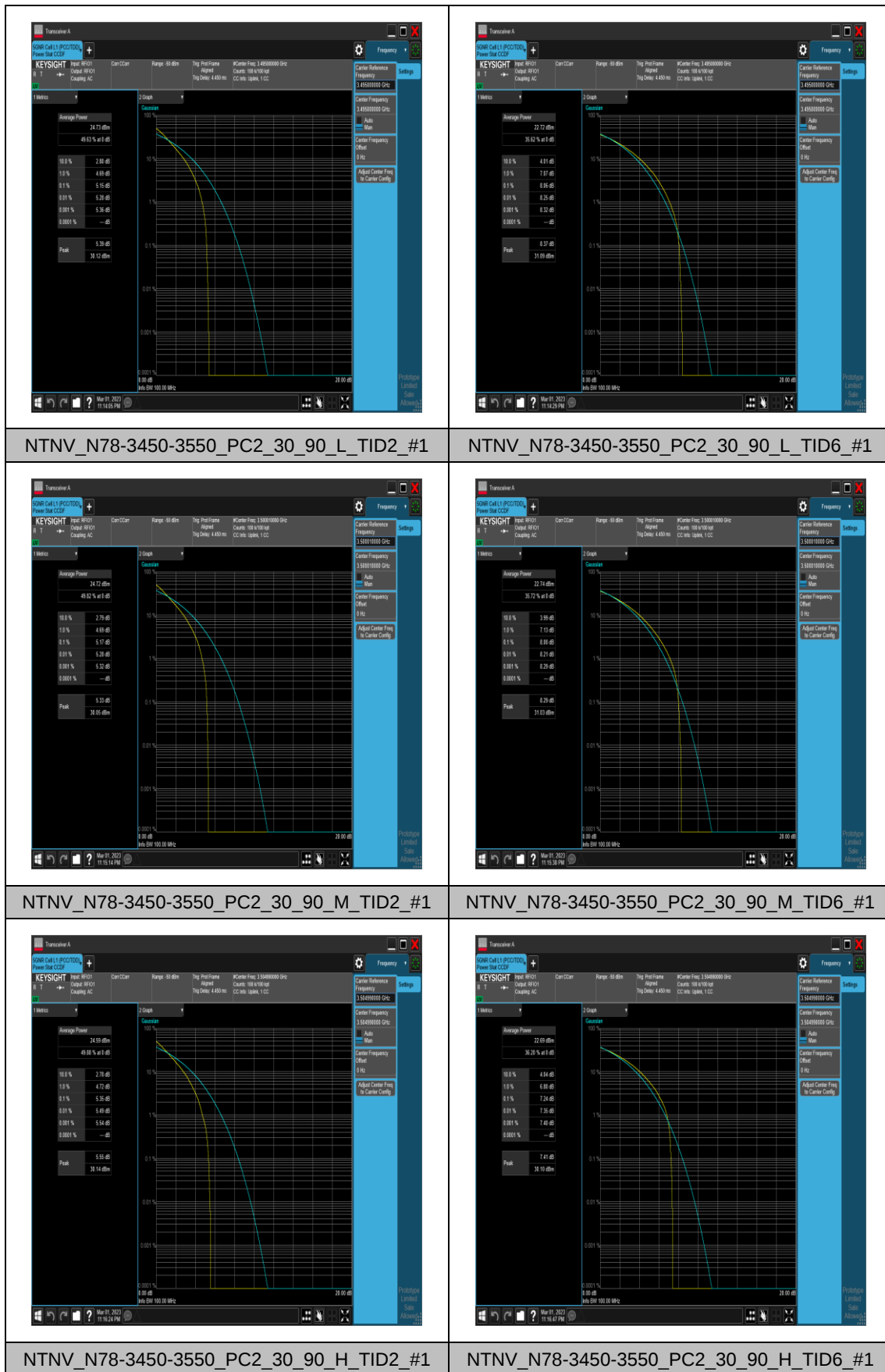
NTNV N78-3450-3550 PC2 30 40 H TID2 #1

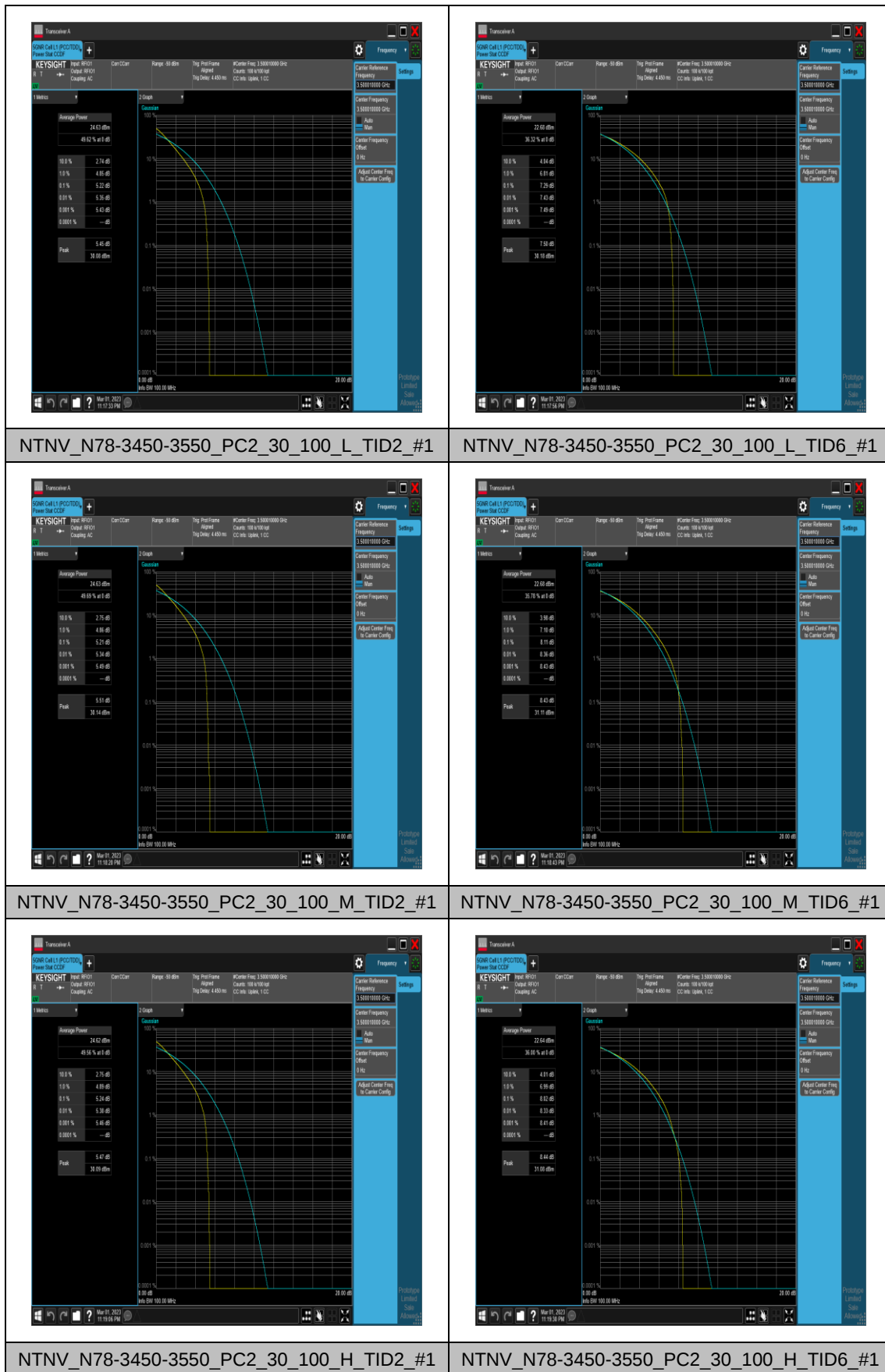
NTNV N78-3450-3550 PC2 30 40 H TID6 #1











Appendix C: 26dB Bandwidth and Occupied Bandwidth for SA

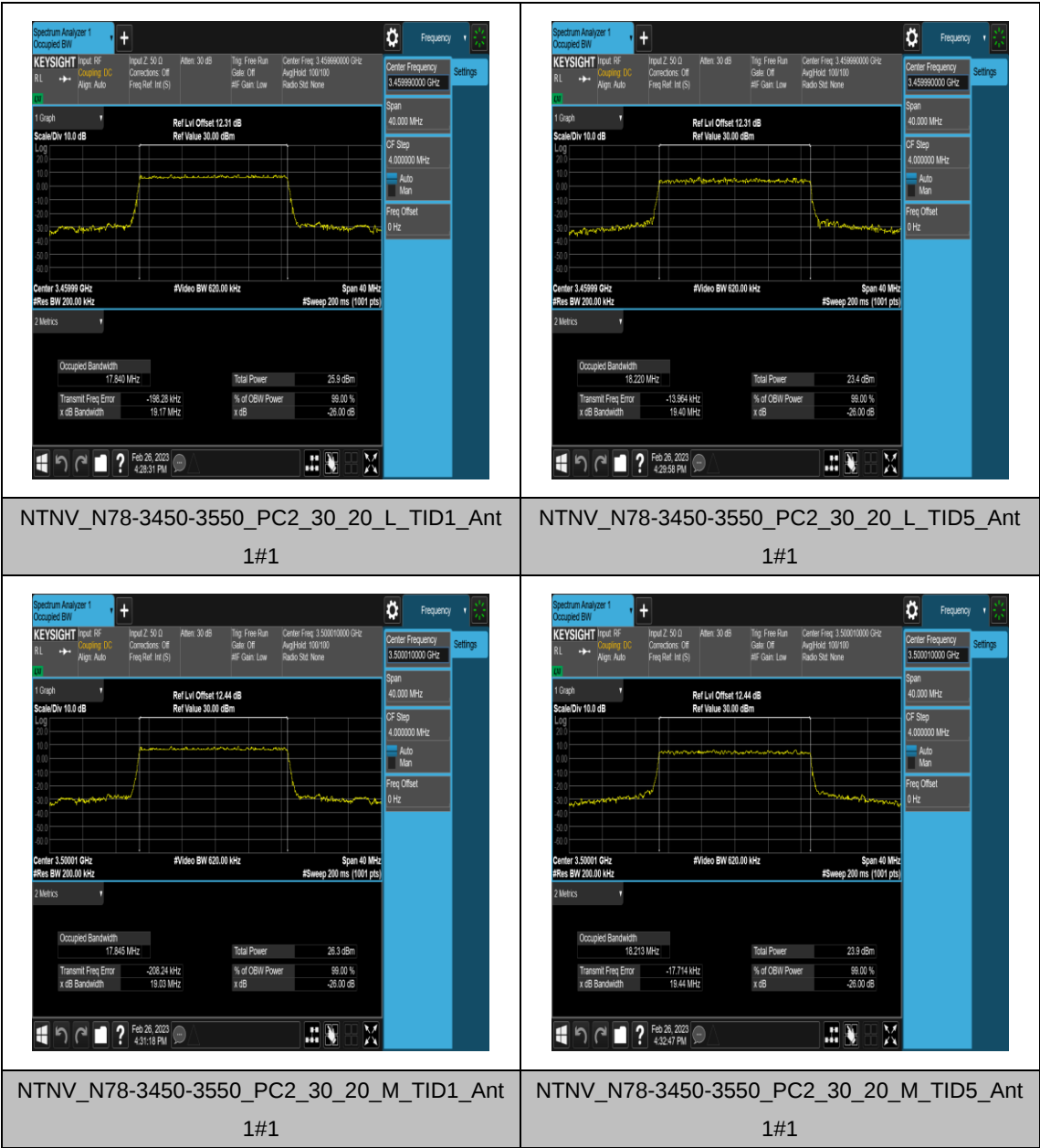
Test Result

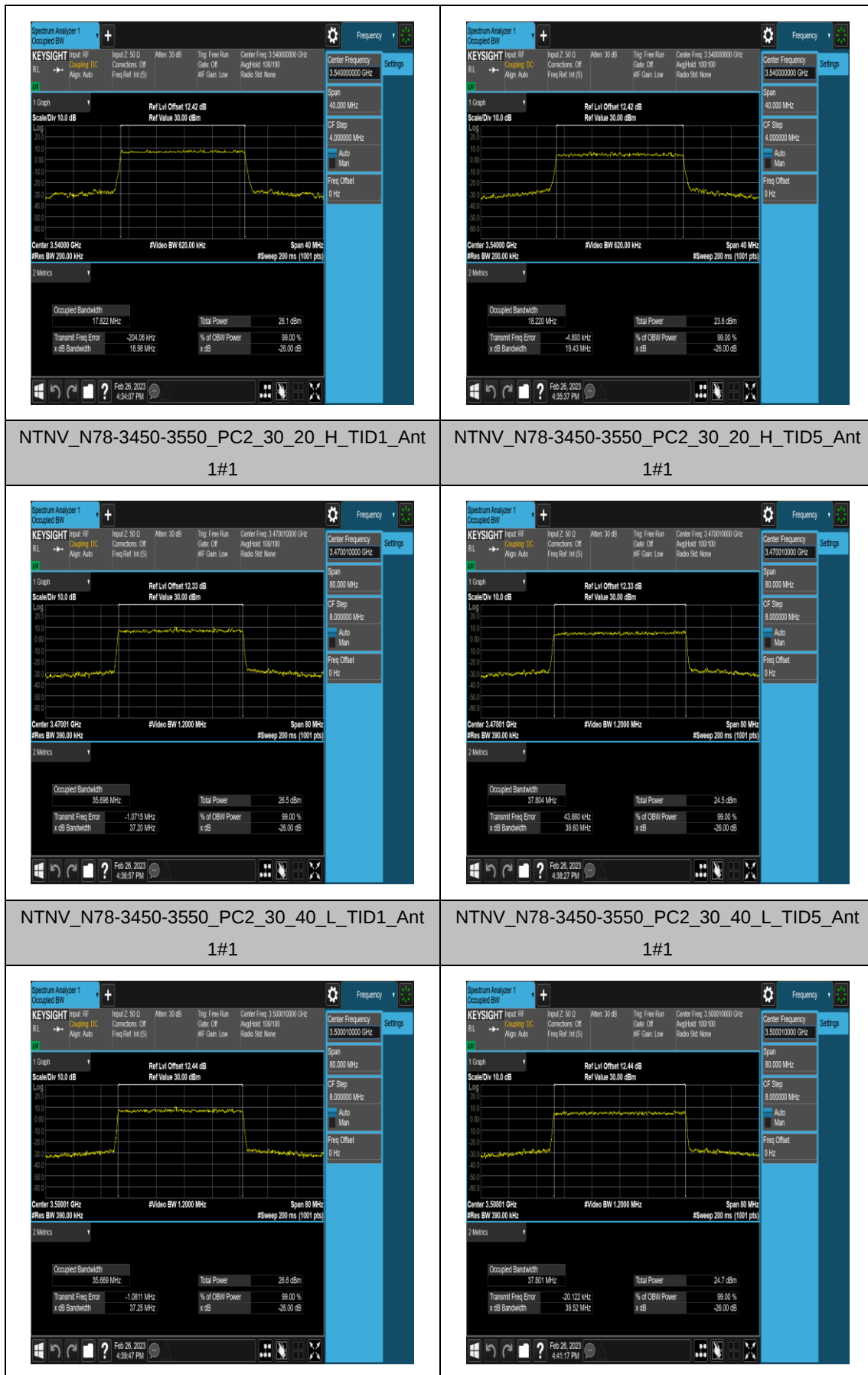
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
N78-3450 -3550	30	20	DFT-QPSK	L	Outer_Full	17.840	19.17	PASS
N78-3450 -3550	30	20	CP-QPSK	L	Outer_Full	18.220	19.40	PASS
N78-3450 -3550	30	20	DFT-QPSK	M	Outer_Full	17.845	19.03	PASS
N78-3450 -3550	30	20	CP-QPSK	M	Outer_Full	18.213	19.44	PASS
N78-3450 -3550	30	20	DFT-QPSK	H	Outer_Full	17.822	18.98	PASS
N78-3450 -3550	30	20	CP-QPSK	H	Outer_Full	18.220	19.43	PASS
N78-3450 -3550	30	40	DFT-QPSK	L	Outer_Full	35.696	37.20	PASS
N78-3450 -3550	30	40	CP-QPSK	L	Outer_Full	37.804	39.60	PASS
N78-3450 -3550	30	40	DFT-QPSK	M	Outer_Full	35.669	37.25	PASS
N78-3450 -3550	30	40	CP-QPSK	M	Outer_Full	37.801	39.52	PASS
N78-3450 -3550	30	40	DFT-QPSK	H	Outer_Full	35.677	37.36	PASS
N78-3450 -3550	30	40	CP-QPSK	H	Outer_Full	37.803	39.58	PASS
N78-3450 -3550	30	50	DFT-QPSK	L	Outer_Full	45.789	47.53	PASS
N78-3450 -3550	30	50	CP-QPSK	L	Outer_Full	47.397	49.47	PASS
N78-3450 -3550	30	50	DFT-QPSK	M	Outer_Full	45.789	47.67	PASS
N78-3450 -3550	30	50	CP-QPSK	M	Outer_Full	47.460	49.40	PASS
N78-3450 -3550	30	50	DFT-QPSK	H	Outer_Full	45.786	47.61	PASS
N78-3450 -3550	30	50	CP-QPSK	H	Outer_Full	47.365	49.42	PASS

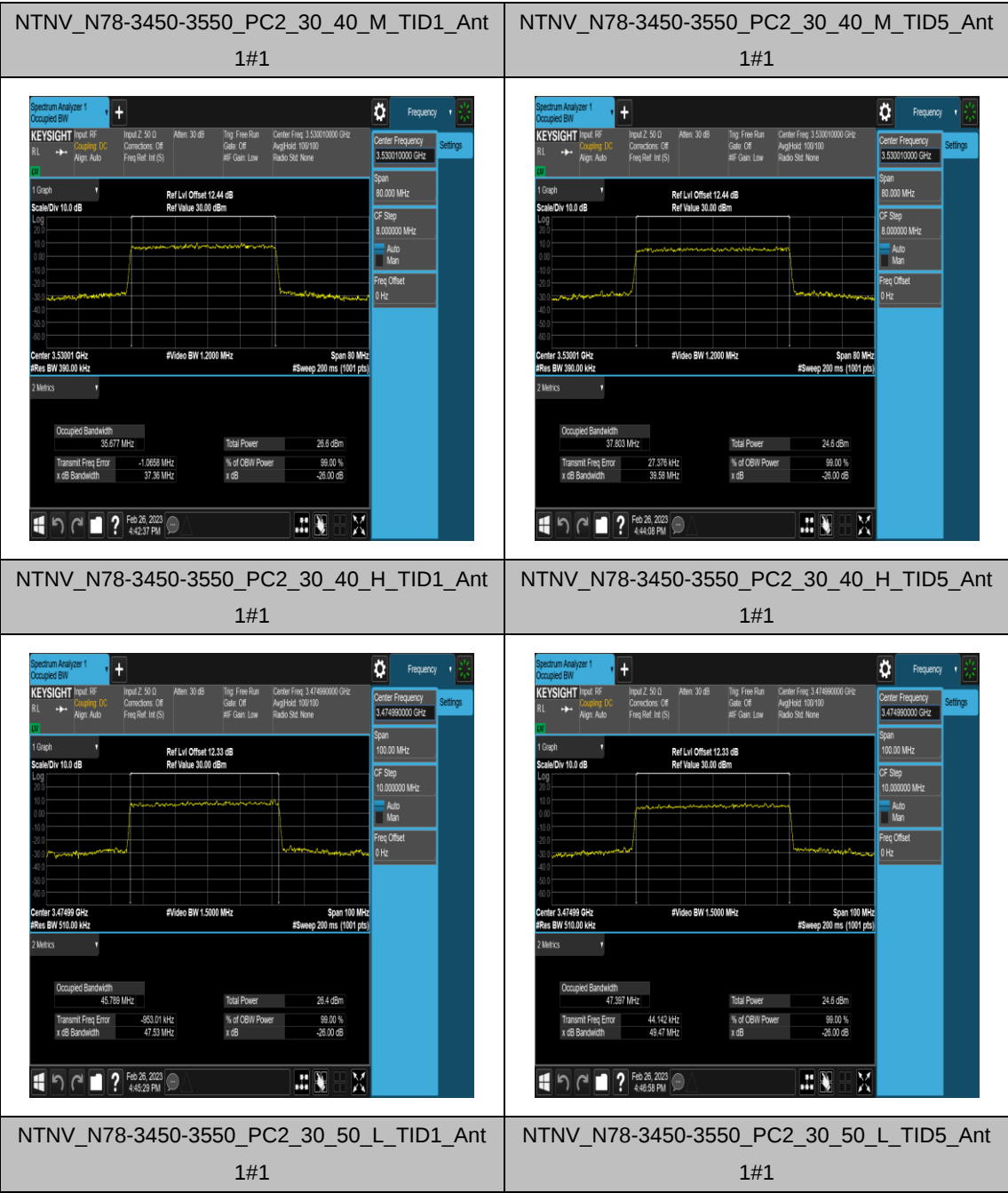
N78-3450 -3550	30	60	DFT-QPSK	L	Outer_Full	57.767	60.02	PASS
N78-3450 -3550	30	60	CP-QPSK	L	Outer_Full	57.677	60.09	PASS
N78-3450 -3550	30	60	DFT-QPSK	M	Outer_Full	57.789	60.14	PASS
N78-3450 -3550	30	60	CP-QPSK	M	Outer_Full	57.720	60.09	PASS
N78-3450 -3550	30	60	DFT-QPSK	H	Outer_Full	57.817	60.27	PASS
N78-3450 -3550	30	60	CP-QPSK	H	Outer_Full	57.758	60.16	PASS
N78-3450 -3550	30	80	DFT-QPSK	L	Outer_Full	76.860	79.79	PASS
N78-3450 -3550	30	80	CP-QPSK	L	Outer_Full	77.478	80.21	PASS
N78-3450 -3550	30	80	DFT-QPSK	M	Outer_Full	76.858	79.75	PASS
N78-3450 -3550	30	80	CP-QPSK	M	Outer_Full	77.498	80.29	PASS
N78-3450 -3550	30	80	DFT-QPSK	H	Outer_Full	76.822	79.95	PASS
N78-3450 -3550	30	80	CP-QPSK	H	Outer_Full	77.470	80.27	PASS
N78-3450 -3550	30	90	DFT-QPSK	L	Outer_Full	86.577	89.58	PASS
N78-3450 -3550	30	90	CP-QPSK	L	Outer_Full	87.194	90.41	PASS
N78-3450 -3550	30	90	DFT-QPSK	M	Outer_Full	86.620	89.72	PASS
N78-3450 -3550	30	90	CP-QPSK	M	Outer_Full	87.172	90.34	PASS
N78-3450 -3550	30	90	DFT-QPSK	H	Outer_Full	86.611	89.75	PASS
N78-3450 -3550	30	90	CP-QPSK	H	Outer_Full	87.337	90.30	PASS
N78-3450 -3550	30	100	DFT-QPSK	L	Outer_Full	96.293	99.60	PASS
N78-3450 -3550	30	100	CP-QPSK	L	Outer_Full	97.122	100.7	PASS
N78-3450 -3550	30	100	DFT-QPSK	M	Outer_Full	96.193	99.64	PASS

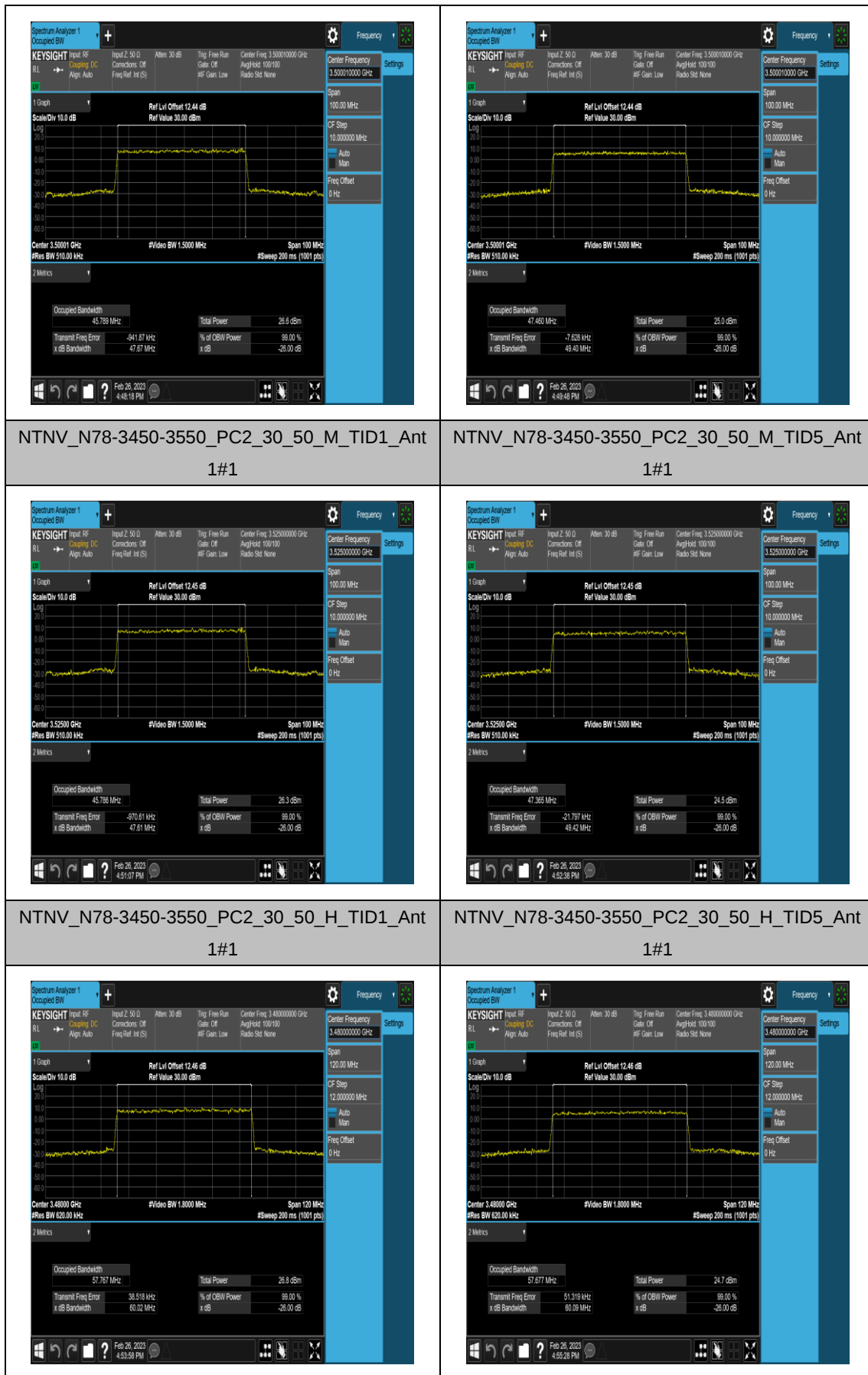
N78-3450 -3550	30	100	CP-QPSK	M	Outer_Full	97.074	100.6	PASS
N78-3450 -3550	30	100	DFT-QPSK	H	Outer_Full	96.168	99.56	PASS
N78-3450 -3550	30	100	CP-QPSK	H	Outer_Full	97.112	100.6	PASS

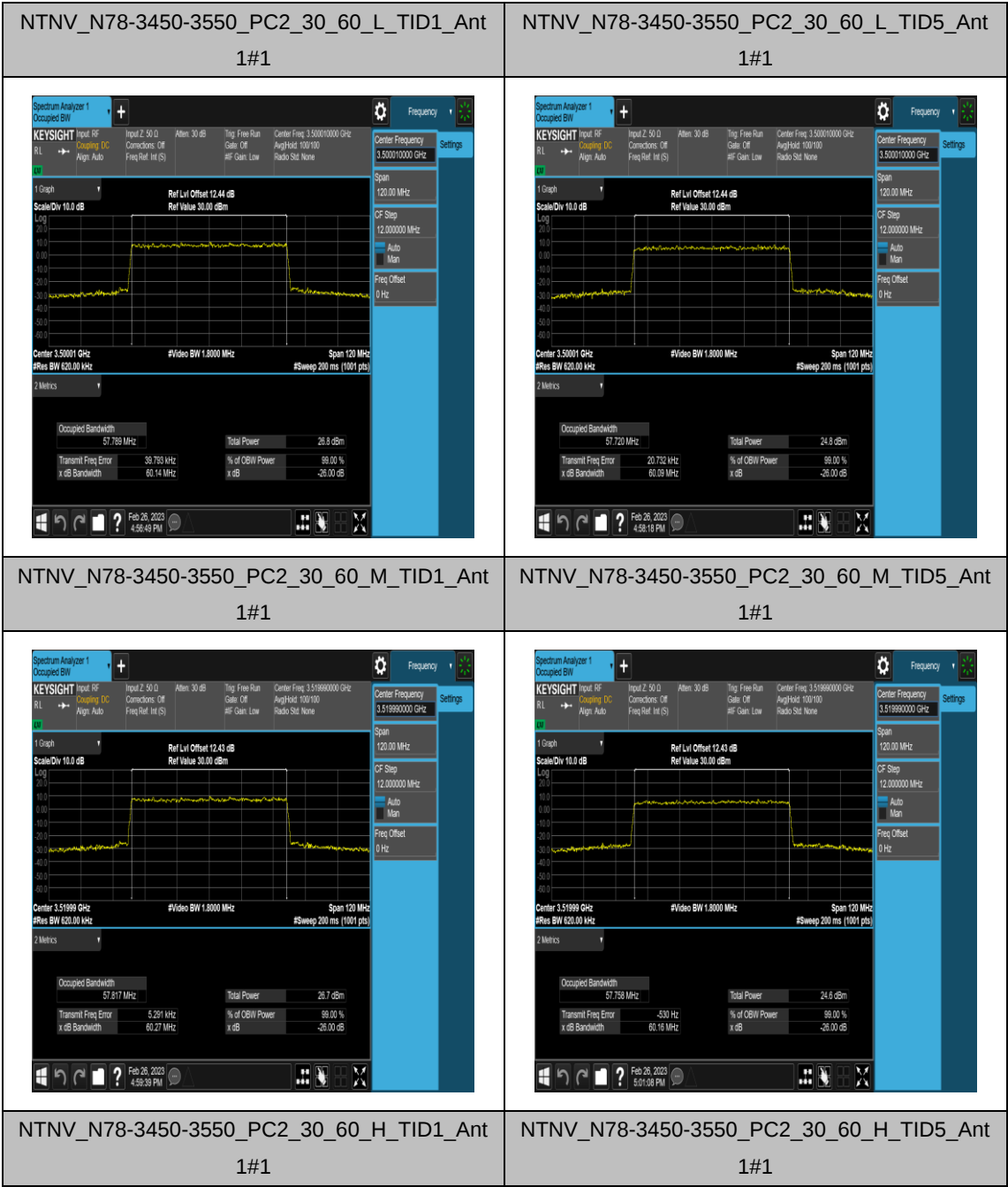
Test Graphs

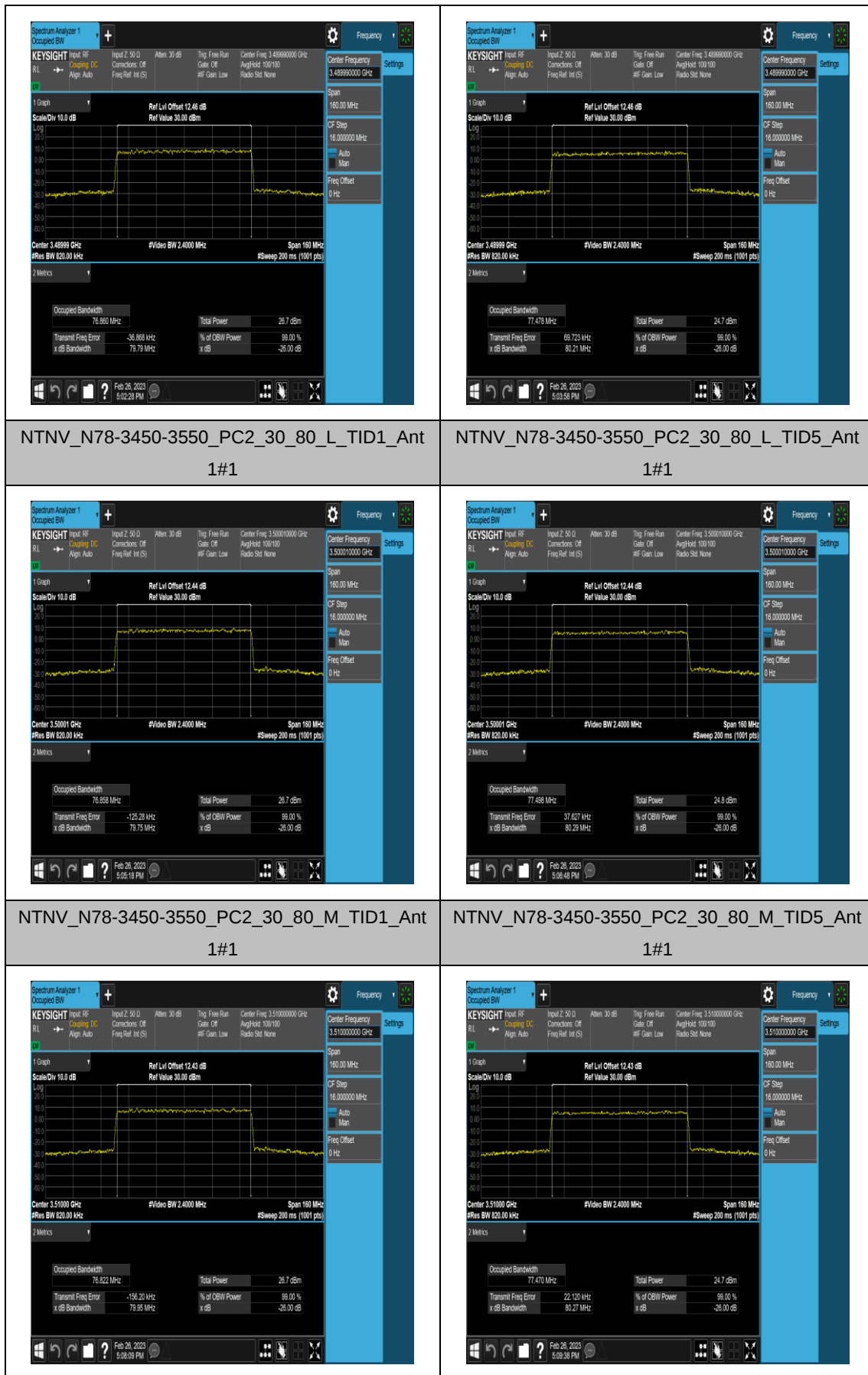




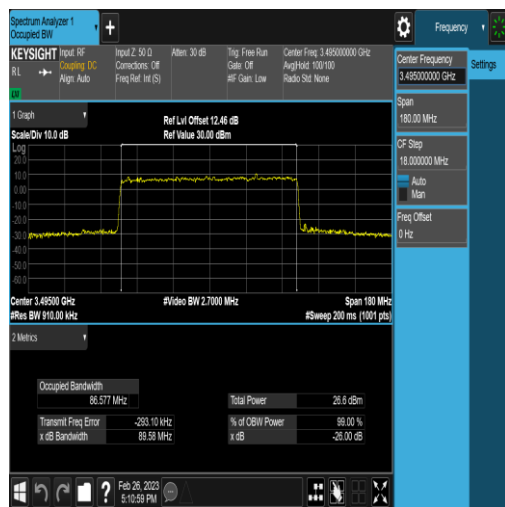








NTNV_N78-3450-3550_PC2_30_80_H_TID1_Ant
1#1



NTNV_N78-3450-3550_PC2_30_80_H_TID5_Ant
1#1



NTNV_N78-3450-3550_PC2_30_90_L_TID1_Ant
1#1

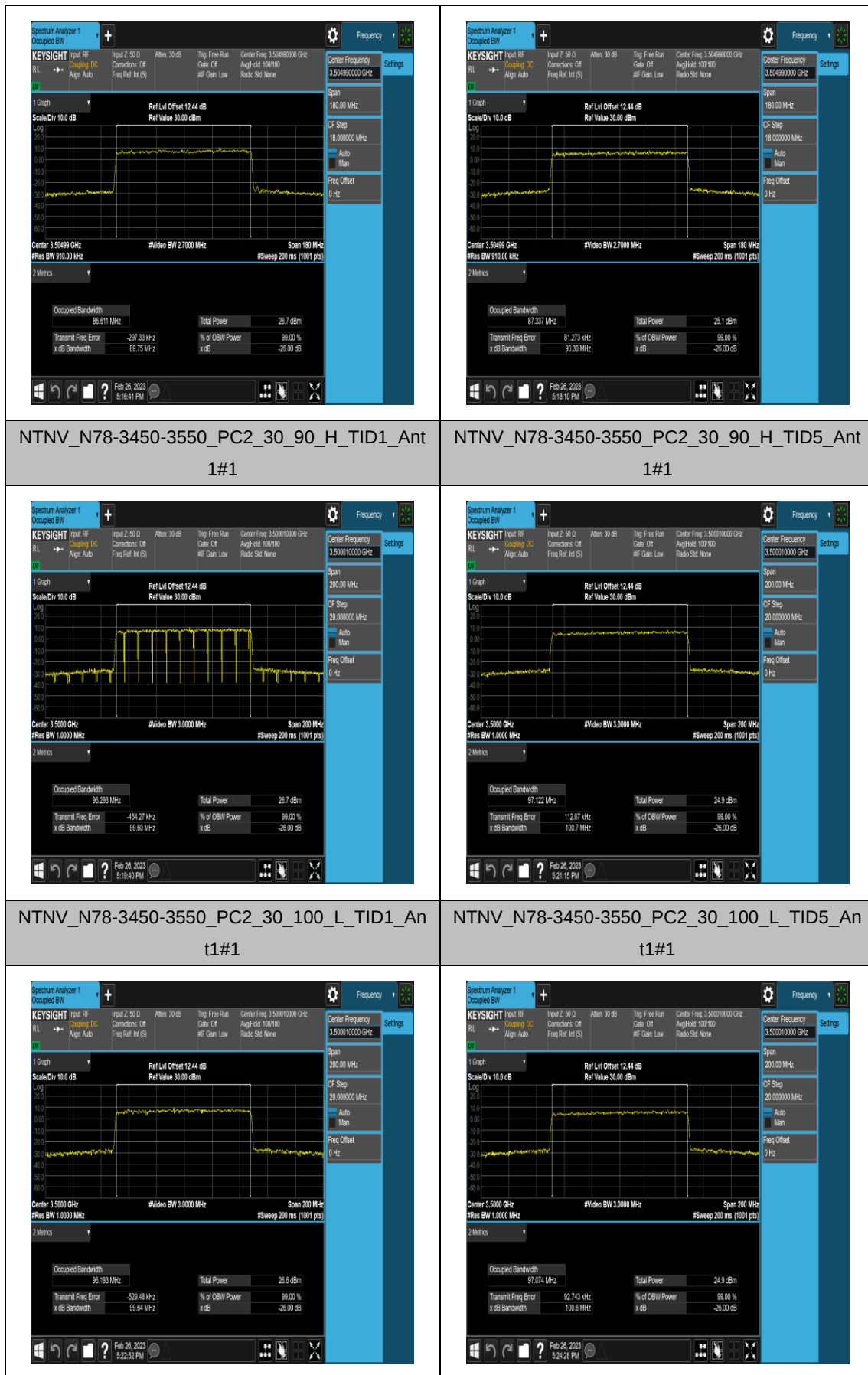


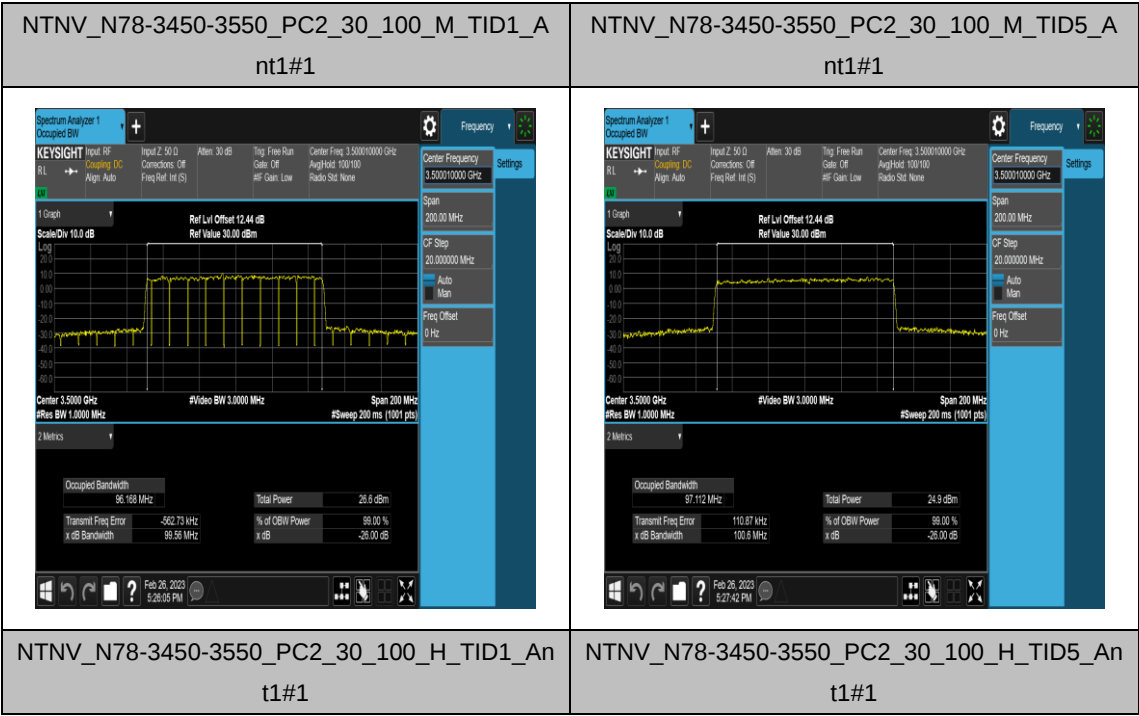
NTNV_N78-3450-3550_PC2_30_90_L_TID5_Ant
1#1



NTNV_N78-3450-3550_PC2_30_90_M_TID1_Ant
1#1

NTNV_N78-3450-3550_PC2_30_90_M_TID5_Ant
1#1





Appendix D: Band Edge for SA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Verdict
N78-3450-3550	30	20	CP-QPSK	L	Edge_1RB_Left	see graph	PASS
N78-3450-3550	30	20	CP-QPSK	L	Outer_Full	see graph	PASS
N78-3450-3550	30	40	CP-QPSK	L	Edge_1RB_Left	see graph	PASS
N78-3450-3550	30	40	CP-QPSK	L	Outer_Full	see graph	PASS
N78-3450-3550	30	50	CP-QPSK	L	Edge_1RB_Left	see graph	PASS
N78-3450-3550	30	50	CP-QPSK	L	Outer_Full	see graph	PASS
N78-3450-3550	30	60	CP-QPSK	L	Edge_1RB_Left	see graph	PASS
N78-3450-3550	30	60	CP-QPSK	L	Outer_Full	see graph	PASS
N78-3450-3550	30	80	CP-QPSK	L	Edge_1RB_Left	see graph	PASS
N78-3450-3550	30	80	CP-QPSK	L	Outer_Full	see graph	PASS
N78-3450-3550	30	90	CP-QPSK	L	Edge_1RB_Left	see graph	PASS
N78-3450-3550	30	90	CP-QPSK	L	Outer_Full	see graph	PASS
N78-3450-3550	30	100	CP-QPSK	L	Edge_1RB_Left	see graph	PASS
N78-3450-3550	30	100	CP-QPSK	L	Outer_Full	see graph	PASS
N78-3450-3550	30	20	CP-QPSK	H	Edge_1RB_Right	see graph	PASS