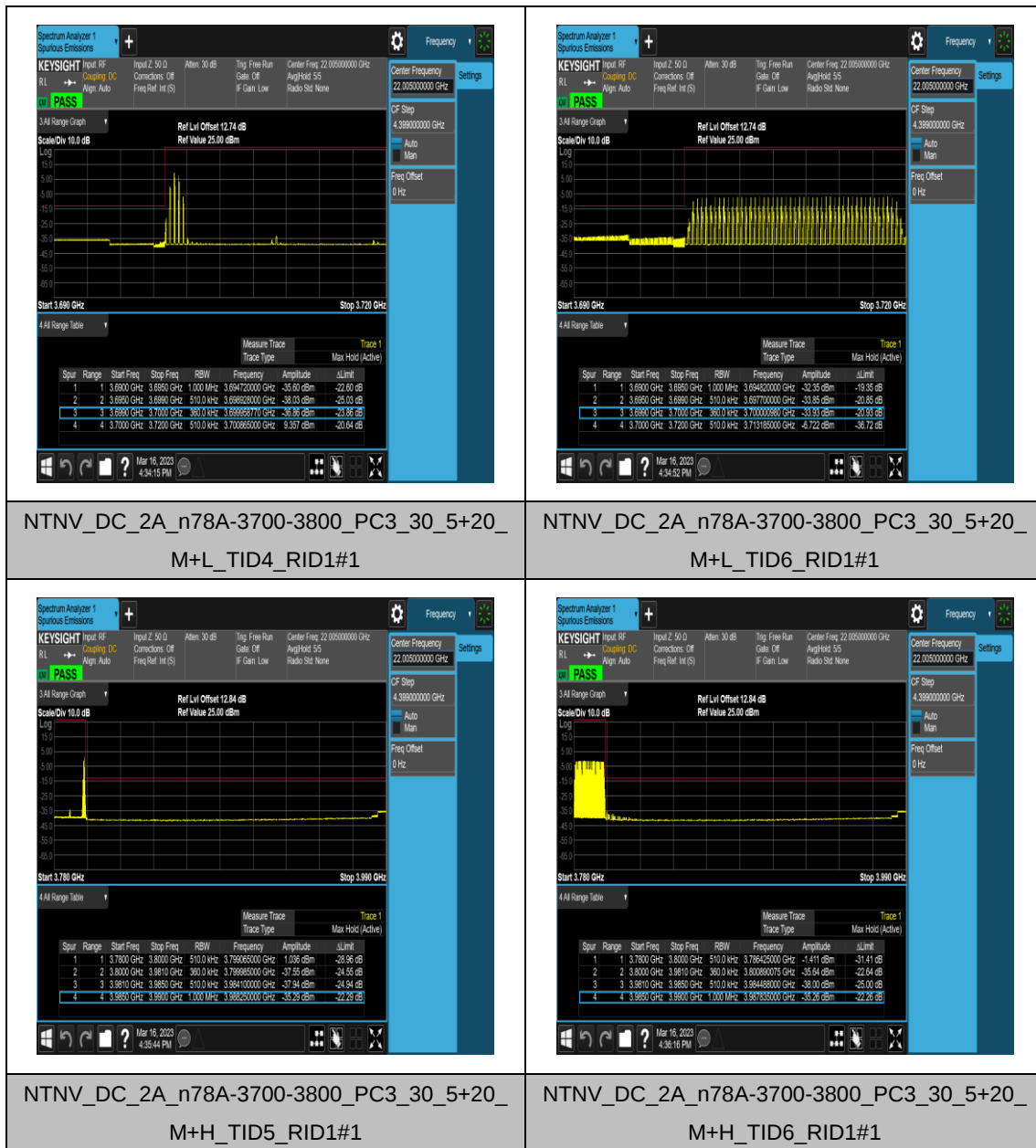
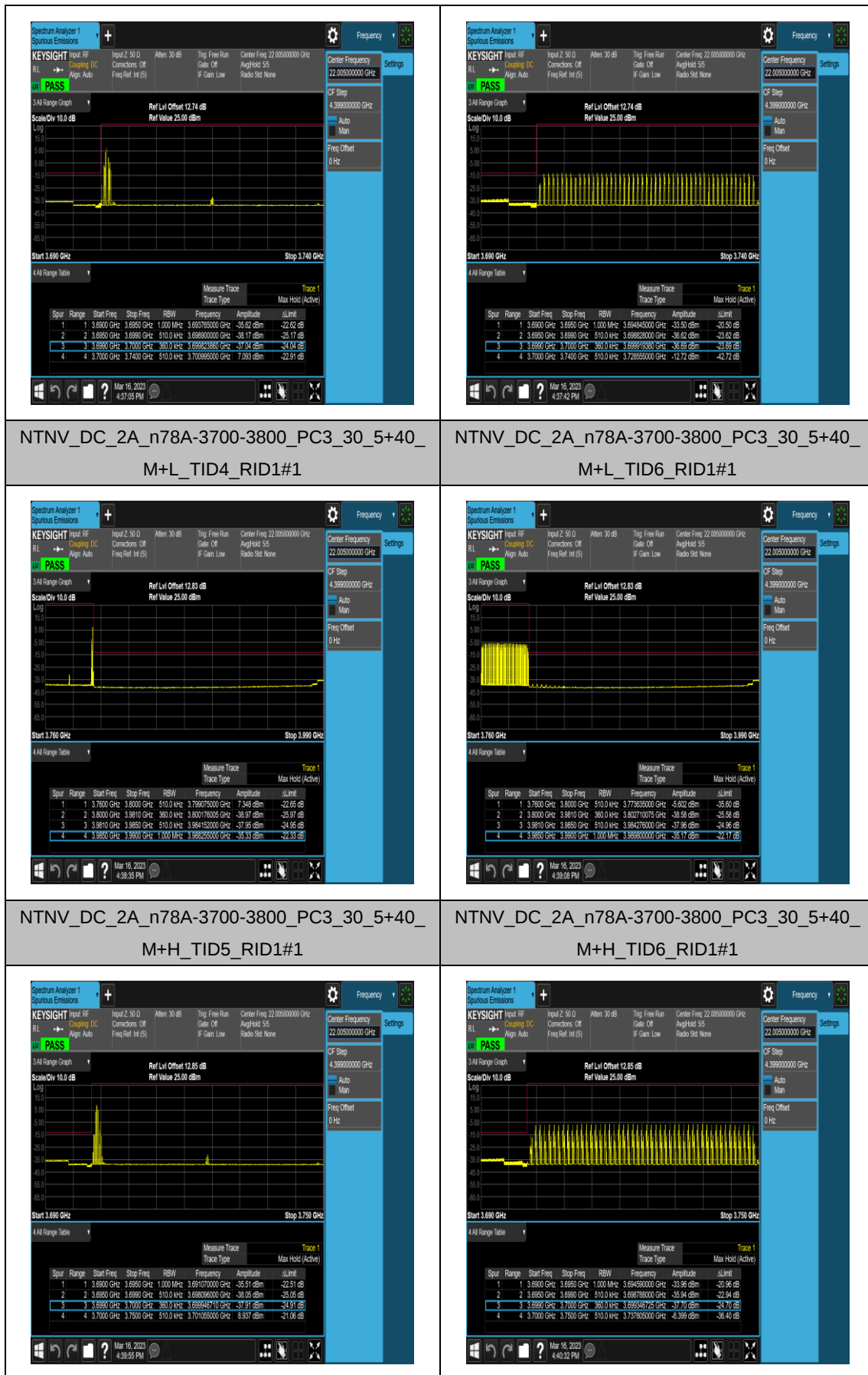
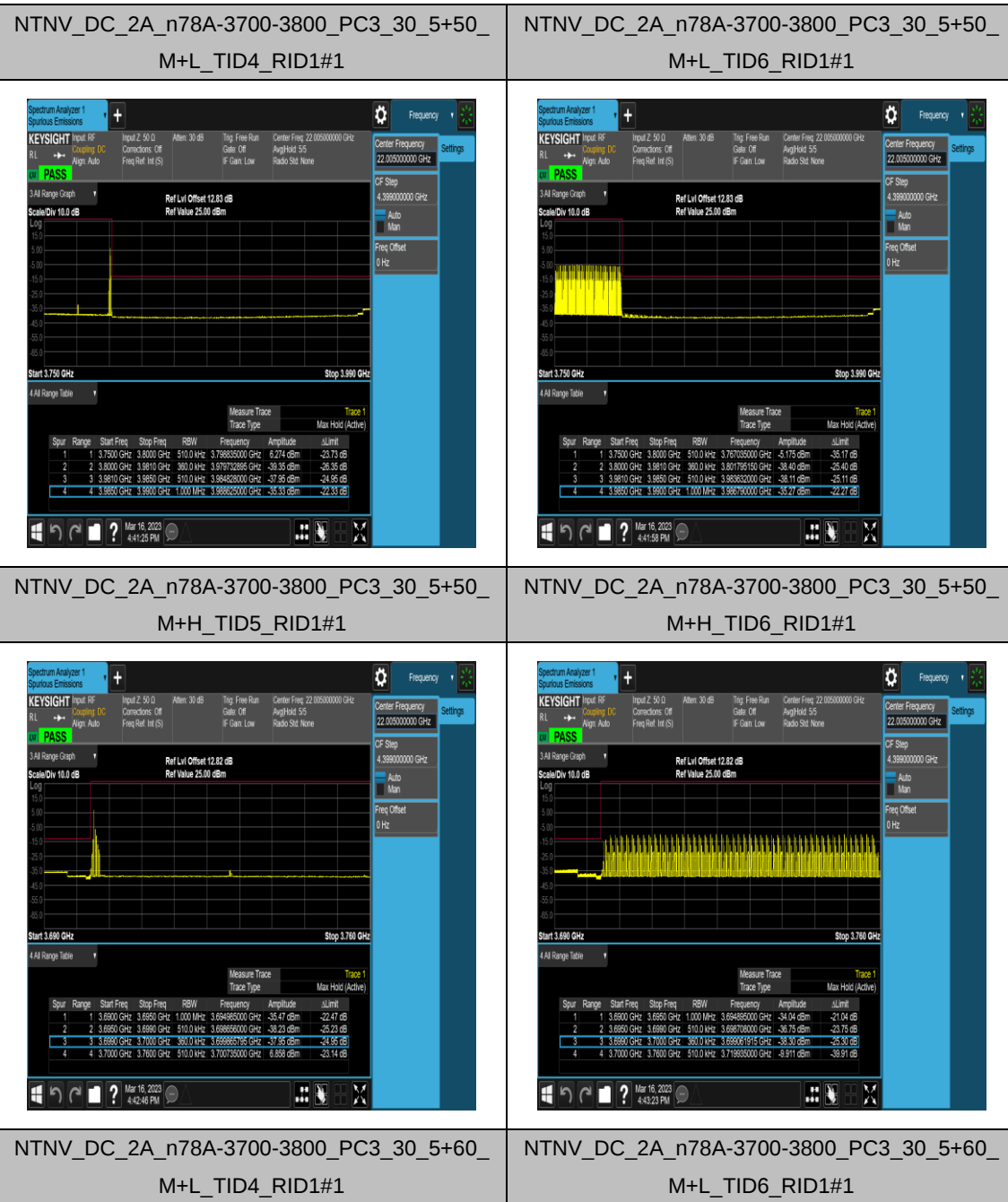
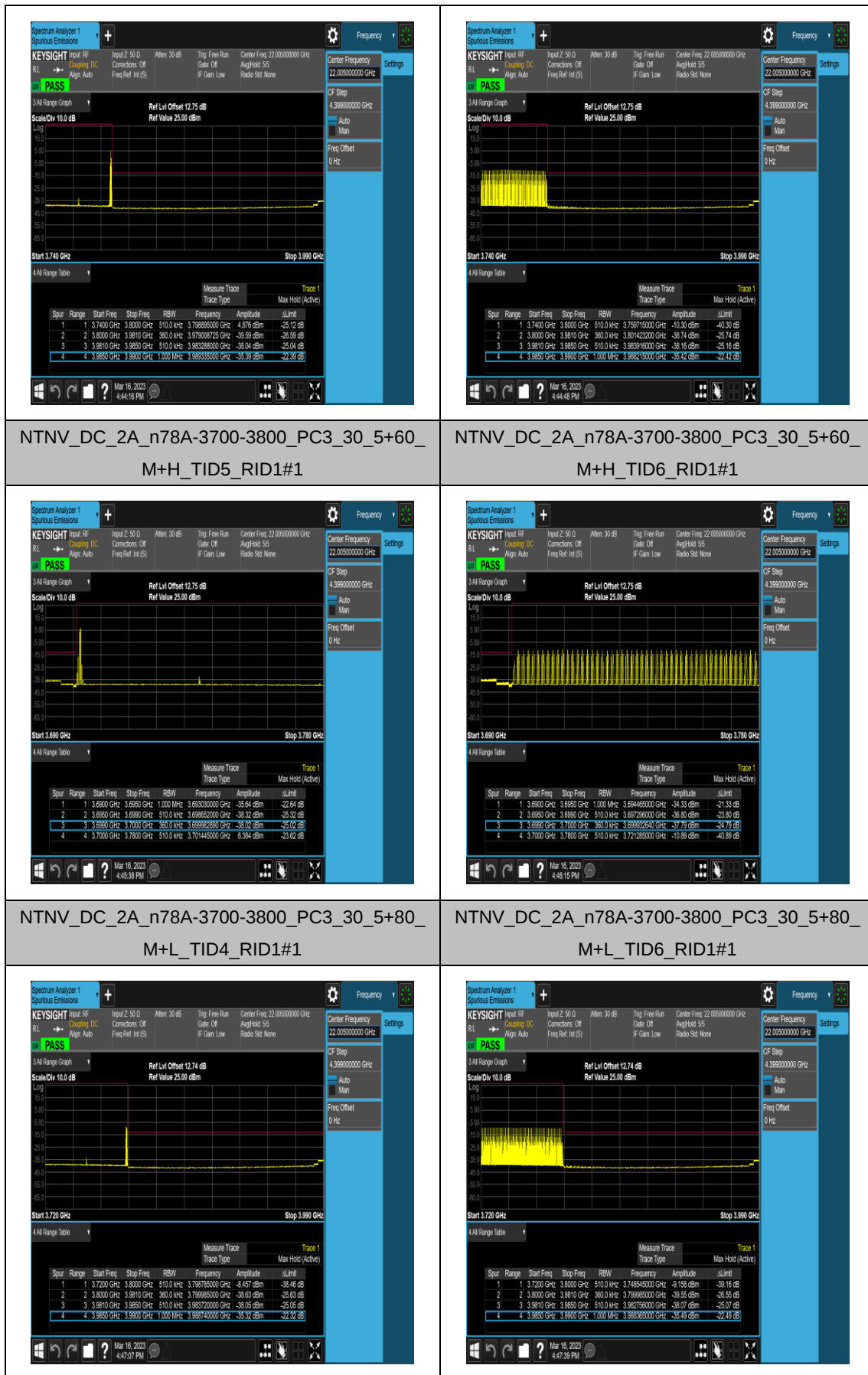


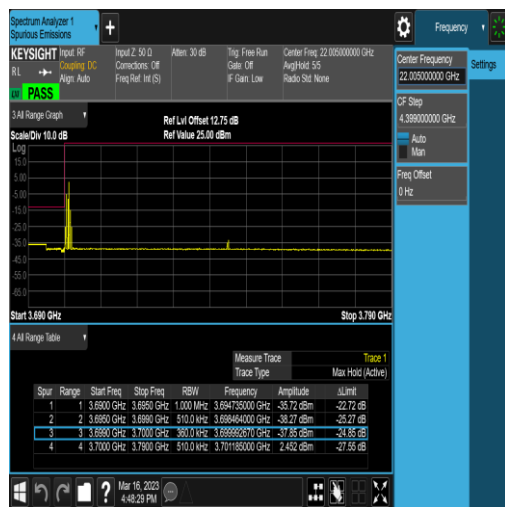
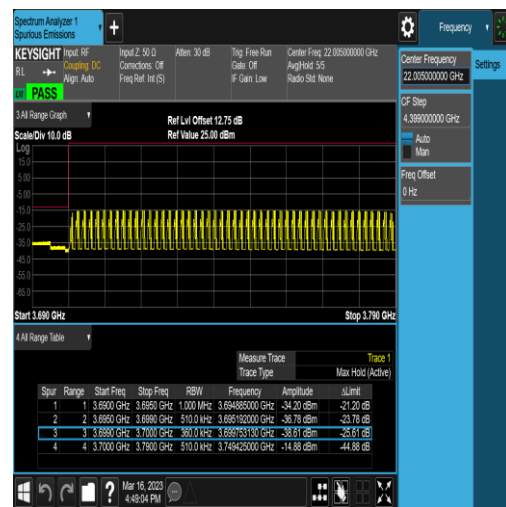
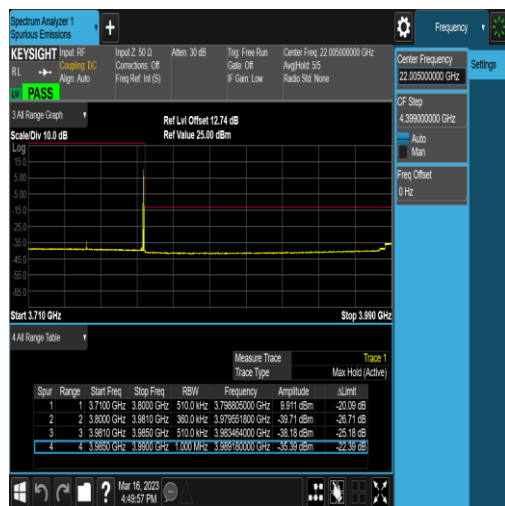
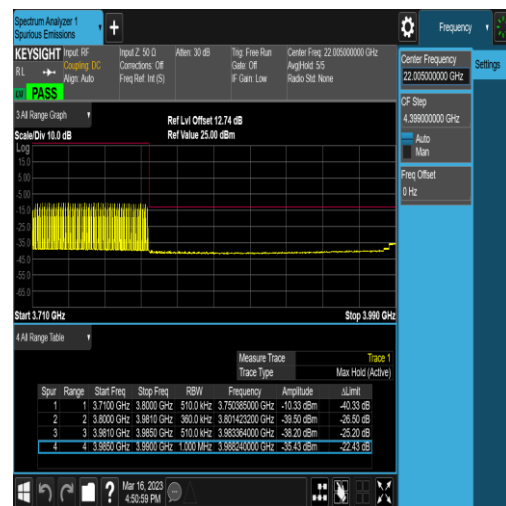
Test Graphs

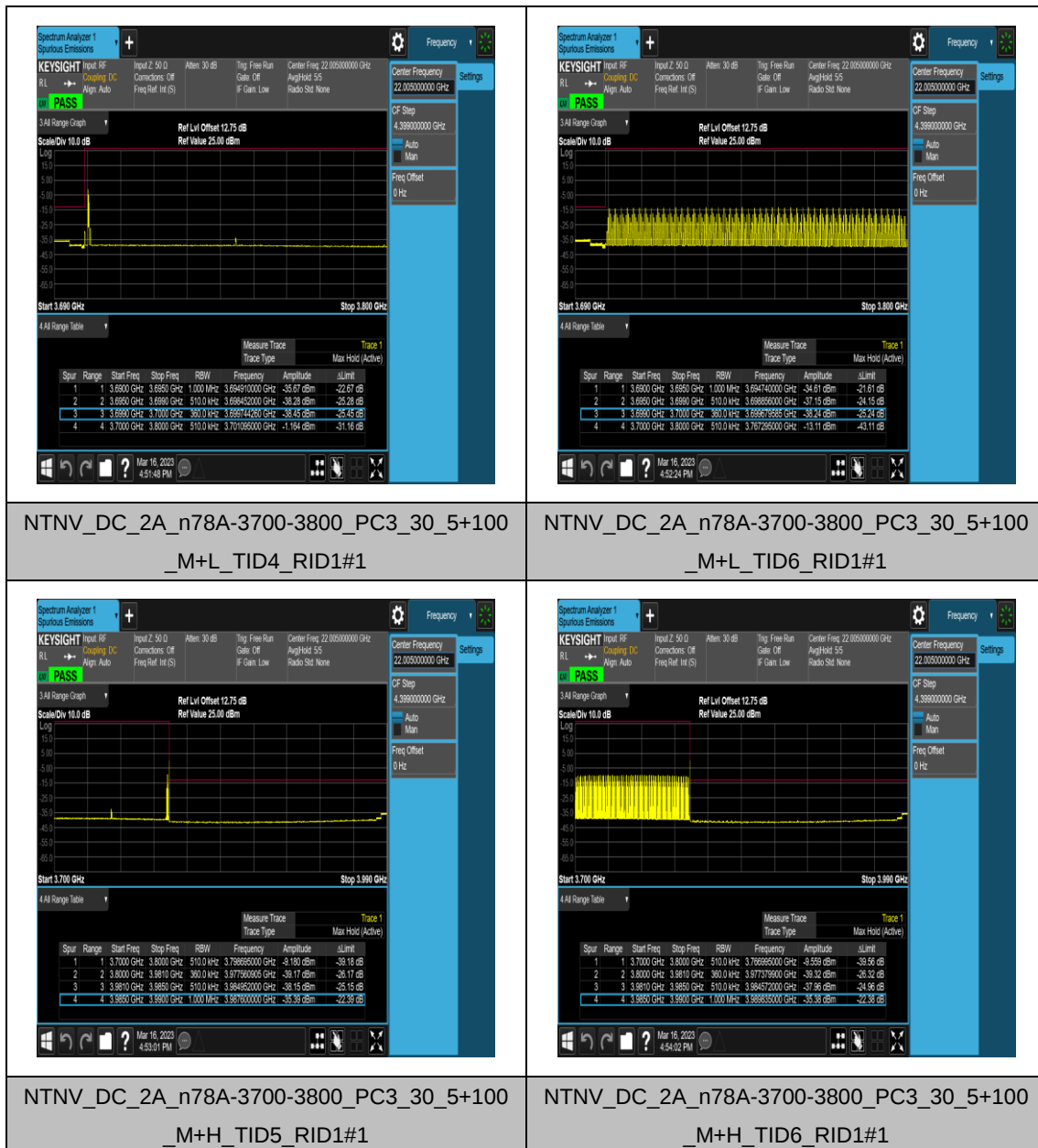








NTNV_DC_2A_n78A-3700-3800_PC3_30_5+80_
M+H_TID5_RID1#1NTNV_DC_2A_n78A-3700-3800_PC3_30_5+80_
M+H_TID6_RID1#1NTNV_DC_2A_n78A-3700-3800_PC3_30_5+90_
M+L_TID4_RID1#1NTNV_DC_2A_n78A-3700-3800_PC3_30_5+90_
M+L_TID6_RID1#1NTNV_DC_2A_n78A-3700-3800_PC3_30_5+90_
M+H_TID5_RID1#1NTNV_DC_2A_n78A-3700-3800_PC3_30_5+90_
M+H_TID6_RID1#1



Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SC S	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-67.24	-43	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-69.93	-33	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-70.64	-23	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-37.63	-13	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.52	-13	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-30.07	-13	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.36	-13	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-71.64	-43	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-70.49	-33	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-70.69	-23	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-42.78	-13	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.45	-13	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.79	-13	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.20	-13	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-67.19	-43	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	0.15	30	-73.26	-33	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	30	1000	-70.51	-23	PASS
DC_2A_n78A- 3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	1000	3000	-42.06	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	3000	6000	-48.75	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	6000	26000	-29.98	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	26000	40000	-33.33	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-66.39	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-73.01	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-70.66	-23	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.62	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.73	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.92	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.58	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-68.37	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-70.13	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-70.84	-23	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-41.37	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.61	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-30.06	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.35	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Outer_Full	0.009	0.15	-66.51	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Outer_Full	0.15	30	-73.77	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Outer_Full	30	1000	-70.37	-23	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Outer_Full	1000	3000	-39.13	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Outer_Full	3000	6000	-43.83	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Outer_Full	6000	26000	-29.72	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Outer_Full	26000	40000	-33.02	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-57.68	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-68.99	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-69.85	-23	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-42.81	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.59	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-30.12	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.31	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-59.86	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-70.06	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-70.24	-23	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-25.64	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.55	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.86	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.47	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-58.88	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	0.15	30	-69.53	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	30	1000	-70.48	-23	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	1000	3000	-40.51	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	3000	6000	-47.85	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	6000	26000	-29.78	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	26000	40000	-33.48	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-55.91	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-72.21	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-70.45	-23	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-38.75	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.66	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-30.07	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.33	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-59.53	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-69.14	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-70.69	-23	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-39.12	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.66	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.96	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.38	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Outer_Full	0.009	0.15	-57.68	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Outer_Full	0.15	30	-70.00	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Outer_Full	30	1000	-70.51	-23	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Outer_Full	1000	3000	-42.06	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Outer_Full	3000	6000	-49.33	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Outer_Full	6000	26000	-30.05	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+M	Outer_Full	26000	40000	-33.43	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-58.82	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-70.22	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-70.17	-23	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-39.34	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.35	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.93	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.57	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-55.30	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-68.89	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-70.31	-23	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.39	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.49	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.84	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.39	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-58.44	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	0.15	30	-72.04	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	30	1000	-70.59	-23	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	1000	3000	-41.11	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	3000	6000	-49.38	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	6000	26000	-29.95	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	26000	40000	-33.02	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-58.23	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-70.71	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-70.16	-23	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-42.56	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.49	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-30.01	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.16	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-56.88	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-67.41	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-70.68	-23	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-38.81	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.59	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.82	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.13	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Outer_Full	0.009	0.15	-57.81	-43	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Outer_Full	0.15	30	-69.18	-33	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Outer_Full	30	1000	-70.55	-23	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Outer_Full	1000	3000	-38.60	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Outer_Full	3000	6000	-48.20	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Outer_Full	6000	26000	-29.84	-13	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Outer_Full	26000	40000	-33.47	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-58.38	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-70.10	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-70.51	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-39.28	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.68	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.92	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.52	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-56.90	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-71.65	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-70.29	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-41.11	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.86	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.86	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.37	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-57.64	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	0.15	30	-66.20	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	30	1000	-70.41	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	1000	3000	-42.77	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	3000	6000	-47.65	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	6000	26000	-29.81	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	26000	40000	-33.38	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-58.89	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-72.31	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-70.63	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-39.14	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.53	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.85	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.44	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-56.57	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-71.98	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-69.95	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.18	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.00	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.91	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.29	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Outer_Full	0.009	0.15	-57.65	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Outer_Full	0.15	30	-68.55	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Outer_Full	30	1000	-70.46	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Outer_Full	1000	3000	-42.87	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Outer_Full	3000	6000	-44.67	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Outer_Full	6000	26000	-30.01	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Outer_Full	26000	40000	-33.28	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-55.18	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-62.90	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-70.44	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.43	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.43	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.97	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-32.87	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-58.02	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-70.97	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-70.15	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.82	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.63	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.71	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.40	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-57.01	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	0.15	30	-74.46	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	30	1000	-70.83	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	1000	3000	-43.09	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	3000	6000	-48.16	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	6000	26000	-30.01	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	26000	40000	-33.58	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-72.38	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-70.86	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-69.35	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.14	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.51	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.85	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.03	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-57.50	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-70.60	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-70.33	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-41.43	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.69	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.98	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.37	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Outer_Full	0.009	0.15	-57.09	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Outer_Full	0.15	30	-70.40	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Outer_Full	30	1000	-70.10	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Outer_Full	1000	3000	-38.72	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Outer_Full	3000	6000	-43.38	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Outer_Full	6000	26000	-29.94	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+M	Outer_Full	26000	40000	-33.25	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-56.96	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-67.02	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-70.64	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-38.89	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.52	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.88	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.15	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-57.66	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-68.27	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-70.69	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-39.43	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.32	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.88	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.30	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-56.88	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	0.15	30	-69.47	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	30	1000	-70.64	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	1000	3000	-35.15	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	3000	6000	-47.84	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	6000	26000	-30.00	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	26000	40000	-33.46	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-57.38	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-69.20	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-70.17	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-41.91	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.70	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.69	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.23	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-71.88	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-74.17	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-70.28	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-38.91	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.62	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-30.00	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.35	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Outer_Full	0.009	0.15	-58.86	-43	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Outer_Full	0.15	30	-69.22	-33	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Outer_Full	30	1000	-70.37	-23	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Outer_Full	1000	3000	-42.05	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Outer_Full	3000	6000	-45.69	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Outer_Full	6000	26000	-29.67	-13	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Outer_Full	26000	40000	-33.31	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-57.15	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-71.65	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-70.47	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-38.31	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.71	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-30.07	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.37	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-57.32	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-69.12	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-70.53	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.80	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.33	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.97	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-32.98	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-57.21	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	0.15	30	-70.70	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	30	1000	-70.29	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	1000	3000	-39.42	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	3000	6000	-47.94	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	6000	26000	-29.95	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	26000	40000	-33.32	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-58.50	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-72.72	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-70.00	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.52	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.19	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-30.15	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.37	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-57.22	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-70.48	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-70.16	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-41.63	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.78	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.91	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.45	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Outer_Full	0.009	0.15	-56.96	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Outer_Full	0.15	30	-70.66	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Outer_Full	30	1000	-70.50	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Outer_Full	1000	3000	-38.86	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Outer_Full	3000	6000	-43.98	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Outer_Full	6000	26000	-29.81	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Outer_Full	26000	40000	-33.11	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-58.11	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-68.68	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-70.34	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-37.92	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.30	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.73	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-32.97	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-57.63	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-71.14	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-70.31	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-41.24	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.46	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.76	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.30	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-57.50	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	0.15	30	-72.90	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	30	1000	-70.22	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	1000	3000	-42.68	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	3000	6000	-48.04	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	6000	26000	-30.19	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	26000	40000	-33.25	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-59.49	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-71.15	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-70.53	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-41.62	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.65	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.73	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.16	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-58.98	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-73.79	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-70.84	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-41.14	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.09	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.88	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.38	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Outer_Full	0.009	0.15	-58.89	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Outer_Full	0.15	30	-68.73	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Outer_Full	30	1000	-70.13	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Outer_Full	1000	3000	-43.01	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Outer_Full	3000	6000	-44.72	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Outer_Full	6000	26000	-30.04	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+M	Outer_Full	26000	40000	-33.49	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-58.59	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-71.81	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-70.34	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-41.55	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.62	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.87	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.38	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-57.71	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-70.41	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-70.70	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.18	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.41	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.87	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.49	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-58.84	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	0.15	30	-70.22	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	30	1000	-70.82	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	1000	3000	-41.13	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	3000	6000	-48.89	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	6000	26000	-29.98	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	26000	40000	-33.17	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-56.79	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-70.89	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-70.55	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.32	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.53	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.98	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.22	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-58.11	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-74.28	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-70.50	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.33	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.56	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.71	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.28	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Outer_Full	0.009	0.15	-59.71	-43	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Outer_Full	0.15	30	-71.28	-33	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Outer_Full	30	1000	-70.68	-23	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Outer_Full	1000	3000	-39.68	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Outer_Full	3000	6000	-44.10	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Outer_Full	6000	26000	-29.64	-13	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Outer_Full	26000	40000	-33.28	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-57.48	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-71.81	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-70.42	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-43.03	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.31	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-30.00	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.25	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-57.41	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-71.82	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-70.44	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-40.97	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.58	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-30.08	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.58	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-55.64	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	0.15	30	-71.10	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	30	1000	-70.75	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	1000	3000	-39.27	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	3000	6000	-48.58	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	6000	26000	-30.00	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	26000	40000	-33.42	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-58.81	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-71.77	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-70.50	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-38.43	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.67	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-30.29	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.20	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-57.51	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-71.94	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-70.42	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-42.87	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.52	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-30.05	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.31	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Outer_Full	0.009	0.15	-58.27	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Outer_Full	0.15	30	-73.20	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Outer_Full	30	1000	-70.22	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Outer_Full	1000	3000	-40.13	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Outer_Full	3000	6000	-44.26	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Outer_Full	6000	26000	-29.78	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Outer_Full	26000	40000	-33.33	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-57.76	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-70.51	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-70.29	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-42.72	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.33	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-30.01	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.21	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-58.63	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-69.79	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-70.28	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-36.92	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.66	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.69	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.17	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-57.61	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	0.15	30	-73.09	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	30	1000	-70.67	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	1000	3000	-41.90	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	3000	6000	-47.80	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	6000	26000	-29.91	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	26000	40000	-33.16	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-57.88	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-73.38	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-70.65	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-42.65	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.76	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.80	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.23	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-58.68	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-74.09	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-70.11	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-39.73	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.55	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-30.08	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.57	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Outer_Full	0.009	0.15	-58.47	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Outer_Full	0.15	30	-73.21	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Outer_Full	30	1000	-70.34	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Outer_Full	1000	3000	-41.01	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Outer_Full	3000	6000	-43.82	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Outer_Full	6000	26000	-30.00	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+M	Outer_Full	26000	40000	-33.49	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-56.99	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-72.96	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-70.65	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-39.14	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.38	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.73	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.53	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-57.71	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-65.22	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-70.86	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-41.14	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.31	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-30.07	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.33	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-58.22	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	0.15	30	-73.21	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	30	1000	-70.45	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	1000	3000	-37.70	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	3000	6000	-46.86	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	6000	26000	-29.93	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	26000	40000	-33.28	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-57.95	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-74.25	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-70.84	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-38.62	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.12	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-30.06	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.00	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-58.08	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-72.95	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-70.21	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-42.49	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.61	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-30.02	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.35	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Outer_Full	0.009	0.15	-57.08	-43	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Outer_Full	0.15	30	-73.89	-33	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Outer_Full	30	1000	-70.53	-23	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Outer_Full	1000	3000	-41.85	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Outer_Full	3000	6000	-43.15	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Outer_Full	6000	26000	-29.97	-13	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Outer_Full	26000	40000	-33.19	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-57.41	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-72.68	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-70.45	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-42.03	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.65	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.70	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.33	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-57.76	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-74.49	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-70.39	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.21	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.35	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.77	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.21	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-57.55	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	0.15	30	-73.14	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	30	1000	-70.68	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	1000	3000	-39.10	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	3000	6000	-47.12	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	6000	26000	-29.96	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+L	Outer_Full	26000	40000	-33.23	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-56.47	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-71.66	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-70.32	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-41.08	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.74	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.93	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.45	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-56.96	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-72.58	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-70.28	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-41.01	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.67	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.61	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.24	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Outer_Full	0.009	0.15	-58.18	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Outer_Full	0.15	30	-72.40	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Outer_Full	30	1000	-70.55	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Outer_Full	1000	3000	-42.17	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Outer_Full	3000	6000	-44.31	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Outer_Full	6000	26000	-29.86	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Outer_Full	26000	40000	-33.31	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-55.40	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-72.29	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-69.71	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.85	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.44	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-30.11	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.28	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-55.35	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-71.27	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-70.42	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-40.18	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.44	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.75	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.39	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-58.06	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	0.15	30	-71.15	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	30	1000	-70.38	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	1000	3000	-39.27	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	3000	6000	-47.91	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	6000	26000	-29.40	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	26000	40000	-33.17	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-57.57	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-72.84	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-70.46	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-39.70	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.68	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.56	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.22	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-56.76	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-68.37	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-70.30	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-42.66	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.77	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.96	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.37	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Outer_Full	0.009	0.15	-57.50	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Outer_Full	0.15	30	-71.08	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Outer_Full	30	1000	-70.30	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Outer_Full	1000	3000	-38.77	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Outer_Full	3000	6000	-43.64	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Outer_Full	6000	26000	-29.85	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+M	Outer_Full	26000	40000	-33.34	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-58.45	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-71.11	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-70.45	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-41.90	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.38	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.87	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.60	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-59.25	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-72.22	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-70.54	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.76	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.65	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.69	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.31	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-56.88	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	0.15	30	-72.15	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	30	1000	-70.35	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	1000	3000	-41.76	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	3000	6000	-1000.00	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	6000	26000	-1000.00	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	26000	40000	-33.21	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-57.12	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-69.95	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-70.53	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-38.93	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.29	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.71	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.37	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-58.55	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-71.43	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-70.54	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-41.04	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.59	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-30.01	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.44	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Outer_Full	0.009	0.15	-57.80	-43	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Outer_Full	0.15	30	-71.16	-33	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Outer_Full	30	1000	-70.15	-23	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Outer_Full	1000	3000	-42.80	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Outer_Full	3000	6000	-44.46	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Outer_Full	6000	26000	-29.92	-13	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Outer_Full	26000	40000	-33.15	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-57.47	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-73.03	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-70.41	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-40.85	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.38	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.98	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.24	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-57.92	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-70.53	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-70.62	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-38.76	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.72	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.65	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.12	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-55.73	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	0.15	30	-72.62	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	30	1000	-70.21	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	1000	3000	-43.07	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	3000	6000	-48.54	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	6000	26000	-29.94	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	26000	40000	-33.14	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-57.70	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-72.76	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-70.63	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-41.04	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.59	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.97	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.24	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-58.43	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-73.24	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-70.62	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.40	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.78	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.68	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.33	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Outer_Full	0.009	0.15	-60.96	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Outer_Full	0.15	30	-70.45	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Outer_Full	30	1000	-70.13	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Outer_Full	1000	3000	-41.48	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Outer_Full	3000	6000	-43.14	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Outer_Full	6000	26000	-29.87	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+L	Outer_Full	26000	40000	-33.27	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-53.99	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-72.87	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-70.15	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-40.32	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.48	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-30.00	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.25	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-57.38	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-73.66	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-70.23	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-39.19	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.67	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-30.05	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-32.96	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-58.77	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	0.15	30	-74.21	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	30	1000	-70.24	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	1000	3000	-41.23	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	3000	6000	-47.48	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	6000	26000	-29.93	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	26000	40000	-32.83	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-57.62	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-71.07	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-70.69	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-38.84	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.38	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.82	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.38	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-58.60	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-72.10	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-70.40	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-42.65	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.33	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.71	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.37	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Outer_Full	0.009	0.15	-57.98	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Outer_Full	0.15	30	-72.44	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Outer_Full	30	1000	-70.85	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Outer_Full	1000	3000	-41.75	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Outer_Full	3000	6000	-44.57	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Outer_Full	6000	26000	-29.97	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+M	Outer_Full	26000	40000	-33.42	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-58.25	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-72.92	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-70.51	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-43.11	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.69	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.54	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.17	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-56.38	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-72.29	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-70.57	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-42.75	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.50	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.97	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-32.99	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-64.63	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	0.15	30	-72.15	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	30	1000	-70.72	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	1000	3000	-38.94	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	3000	6000	-49.64	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	6000	26000	-29.82	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	26000	40000	-33.44	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-57.67	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.27	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-70.28	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-42.79	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.62	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.95	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.02	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-57.94	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-72.70	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-70.54	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.83	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.68	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.47	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.22	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Outer_Full	0.009	0.15	-75.95	-43	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Outer_Full	0.15	30	-71.55	-33	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Outer_Full	30	1000	-70.05	-23	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Outer_Full	1000	3000	-40.73	-13	PASS

3700-3800										
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Outer_Full	3000	6000	-44.32	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Outer_Full	6000	26000	-29.99	-13	PASS
DC_2A_n78A-3700-3800	30	5+90	CP-QPSK	M+H	Outer_Full	26000	40000	-33.25	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-56.57	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-68.97	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-70.63	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-41.42	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.42	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.84	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.26	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-58.22	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-72.52	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-70.59	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-41.66	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.43	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.52	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.04	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-54.68	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.15	30	-74.94	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	30	1000	-70.35	-23	PASS
DC_2A_n78A-3700-3800	30	5+10	DFT-QPSK	M+L	Outer_Full	1000	3000	-40.80	-13	PASS

3700-3800		0								
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	3000	6000	-48.49	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	6000	26000	-29.82	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+L	Outer_Full	26000	40000	-33.28	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-56.83	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-73.23	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-70.12	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-42.91	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.69	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.47	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.18	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-57.81	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-74.51	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-70.62	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-39.17	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.62	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.93	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.27	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Outer_Full	0.009	0.15	-56.32	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Outer_Full	0.15	30	-72.82	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Outer_Full	30	1000	-70.85	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Outer_Full	1000	3000	-38.89	-13	PASS

3700-3800		0								
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Outer_Full	3000	6000	-45.61	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Outer_Full	6000	26000	-29.75	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+L	Outer_Full	26000	40000	-32.88	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-74.10	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-72.92	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-63.72	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-38.45	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.28	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.93	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.39	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-74.02	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-73.64	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-70.32	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-42.01	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.62	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.91	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.25	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-72.90	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.15	30	-72.04	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	30	1000	-69.97	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	1000	3000	-41.74	-13	PASS

3700-3800		0								
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	3000	6000	-48.03	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	6000	26000	-29.94	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+M	Outer_Full	26000	40000	-33.35	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-72.32	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-72.47	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-70.68	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-43.12	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.67	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-30.02	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.44	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-73.75	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-72.97	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-70.74	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-38.68	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.68	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.94	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.49	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Outer_Full	0.009	0.15	-72.61	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Outer_Full	0.15	30	-72.84	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Outer_Full	30	1000	-63.53	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Outer_Full	1000	3000	-41.04	-13	PASS

3700-3800		0								
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Outer_Full	3000	6000	-45.01	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Outer_Full	6000	26000	-29.97	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+M	Outer_Full	26000	40000	-33.20	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-74.03	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-74.23	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-67.28	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-40.75	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.17	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.98	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.39	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-73.26	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-71.98	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-64.14	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-40.79	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.75	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.92	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.62	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-73.64	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.15	30	-74.45	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	30	1000	-69.99	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	1000	3000	-41.30	-13	PASS

3700-3800		0								
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	3000	6000	-47.28	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	6000	26000	-30.08	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	DFT-QPSK	M+H	Outer_Full	26000	40000	-33.49	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-73.81	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-73.47	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-70.28	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-42.21	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.36	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.92	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.33	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-72.77	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-71.98	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-70.66	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-42.26	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.80	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-30.07	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.44	-13	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Outer_Full	0.009	0.15	-57.15	-43	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Outer_Full	0.15	30	-70.77	-33	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Outer_Full	30	1000	-70.61	-23	PASS
DC_2A_n78A-3700-3800	30	5+10 0	CP-QPSK	M+H	Outer_Full	1000	3000	-39.19	-13	PASS

3700-3800		0								
DC_2A_n78A- 3700-3800	30	5+10 0	CP-QPSK	M+H	Outer_Full	3000	6000	-44.67	-13	PASS
DC_2A_n78A- 3700-3800	30	5+10 0	CP-QPSK	M+H	Outer_Full	6000	26000	-30.07	-13	PASS
DC_2A_n78A- 3700-3800	30	5+10 0	CP-QPSK	M+H	Outer_Full	26000	40000	-33.29	-13	PASS

Test Graphs

