

Appendix for NSA 2A_N78A-3700-3800MHz

Appendix A: Average Power Output Data for NSA

Test Result

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	EIRP (dBm)	Power Class	Verdict
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	23.14	23.14	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	23.25	23.25	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Outer_Full	23.65	23.65	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+L	Inner_Full	23.65	23.65	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	23.17	23.17	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	23.12	23.12	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	CP-QPSK	M+L	Outer_Full	23.65	23.65	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	CP-QPSK	M+L	Inner_Full	23.64	23.64	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	24.20	24.20	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	24.37	24.37	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Outer_Full	24.78	24.78	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+M	Inner_Full	24.67	24.67	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	20.65	20.65	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	24.60	24.60	PC3	PASS
DC_2A_n78A-37	30	5+20	CP-QPSK	M+M	Outer_Full	24.61	24.61	PC3	PASS

00-3800									
DC_2A_n78A-37 00-3800	30	5+20	CP-QPSK	M+M	Inner_Full	24.76	24.76	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	24.23	24.23	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	20.24	20.24	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Outer_Full	24.60	24.60	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	DFT-QPSK	M+H	Inner_Full	24.49	24.49	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	24.16	24.16	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	23.79	23.79	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	CP-QPSK	M+H	Outer_Full	24.60	24.60	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+20	CP-QPSK	M+H	Inner_Full	24.70	24.70	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	22.91	22.91	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	23.62	23.62	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Outer_Full	23.67	23.67	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+L	Inner_Full	23.74	23.74	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	23.03	23.03	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	23.43	23.43	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	CP-QPSK	M+L	Outer_Full	23.68	23.68	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	CP-QPSK	M+L	Inner_Full	23.73	23.73	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	23.85	23.85	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	24.38	24.38	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+M	Outer_Full	24.54	24.54	PC3	PASS
DC_2A_n78A-37	30	5+40	DFT-QPSK	M+M	Inner_Full	24.65	24.65	PC3	PASS

00-3800									
DC_2A_n78A-37 00-3800	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	23.58	23.58	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	24.42	24.42	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	CP-QPSK	M+M	Outer_Full	24.57	24.57	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	CP-QPSK	M+M	Inner_Full	24.66	24.66	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	24.01	24.01	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	23.91	23.91	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Outer_Full	24.54	24.54	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	DFT-QPSK	M+H	Inner_Full	24.63	24.63	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	23.80	23.80	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	23.64	23.64	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	CP-QPSK	M+H	Outer_Full	24.44	24.44	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+40	CP-QPSK	M+H	Inner_Full	24.59	24.59	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	23.14	23.14	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	23.86	23.86	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Outer_Full	23.93	23.93	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+L	Inner_Full	24.00	24.00	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	23.06	23.06	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	23.66	23.66	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	CP-QPSK	M+L	Outer_Full	23.93	23.93	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	CP-QPSK	M+L	Inner_Full	23.92	23.92	PC3	PASS
DC_2A_n78A-37	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	23.85	23.85	PC3	PASS

00-3800									
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	24.32	24.32	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Outer_Full	24.67	24.67	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+M	Inner_Full	24.71	24.71	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	23.67	23.67	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	24.52	24.52	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	CP-QPSK	M+M	Outer_Full	24.63	24.63	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	CP-QPSK	M+M	Inner_Full	24.74	24.74	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	23.98	23.98	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	23.95	23.95	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Outer_Full	24.65	24.65	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	DFT-QPSK	M+H	Inner_Full	24.67	24.67	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	23.83	23.83	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	23.67	23.67	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	CP-QPSK	M+H	Outer_Full	24.52	24.52	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+50	CP-QPSK	M+H	Inner_Full	24.63	24.63	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	22.93	22.93	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	23.95	23.95	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Outer_Full	24.00	24.00	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+L	Inner_Full	23.96	23.96	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	22.77	22.77	PC3	PASS
DC_2A_n78A-37	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	23.71	23.71	PC3	PASS

00-3800									
DC_2A_n78A-37 00-3800	30	5+60	CP-QPSK	M+L	Outer_Full	23.99	23.99	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	CP-QPSK	M+L	Inner_Full	24.02	24.02	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	23.59	23.59	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	24.30	24.30	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Outer_Full	24.68	24.68	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+M	Inner_Full	24.69	24.69	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	23.60	23.60	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	24.43	24.43	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	CP-QPSK	M+M	Outer_Full	24.62	24.62	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	CP-QPSK	M+M	Inner_Full	24.69	24.69	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	23.61	23.61	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	23.97	23.97	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Outer_Full	24.51	24.51	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	DFT-QPSK	M+H	Inner_Full	24.73	24.73	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	23.87	23.87	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	23.60	23.60	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	CP-QPSK	M+H	Outer_Full	24.41	24.41	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+60	CP-QPSK	M+H	Inner_Full	24.67	24.67	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	23.33	23.33	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	24.28	24.28	PC3	PASS
DC_2A_n78A-37	30	5+80	DFT-QPSK	M+L	Outer_Full	24.32	24.32	PC3	PASS

00-3800									
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+L	Inner_Full	24.40	24.40	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	23.25	23.25	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	24.34	24.34	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	CP-QPSK	M+L	Outer_Full	24.31	24.31	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	CP-QPSK	M+L	Inner_Full	24.52	24.52	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	23.28	23.28	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	24.21	24.21	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Outer_Full	24.53	24.53	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+M	Inner_Full	24.67	24.67	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	23.10	23.10	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	24.25	24.25	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	CP-QPSK	M+M	Outer_Full	24.52	24.52	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	CP-QPSK	M+M	Inner_Full	24.66	24.66	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	23.62	23.62	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	23.99	23.99	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Outer_Full	24.65	24.65	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	DFT-QPSK	M+H	Inner_Full	24.70	24.70	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	23.50	23.50	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	23.84	23.84	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+80	CP-QPSK	M+H	Outer_Full	24.60	24.60	PC3	PASS
DC_2A_n78A-37	30	5+80	CP-QPSK	M+H	Inner_Full	24.74	24.74	PC3	PASS

00-3800									
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	23.15	23.15	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	24.20	24.20	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Outer_Full	24.46	24.46	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+L	Inner_Full	24.53	24.53	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	23.08	23.08	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	24.07	24.07	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	CP-QPSK	M+L	Outer_Full	24.28	24.28	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	CP-QPSK	M+L	Inner_Full	24.49	24.49	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	23.10	23.10	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	23.99	23.99	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Outer_Full	24.53	24.53	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+M	Inner_Full	24.67	24.67	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	22.97	22.97	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	24.16	24.16	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	CP-QPSK	M+M	Outer_Full	24.49	24.49	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	CP-QPSK	M+M	Inner_Full	24.62	24.62	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	23.24	23.24	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	24.02	24.02	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Outer_Full	24.52	24.52	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	DFT-QPSK	M+H	Inner_Full	24.66	24.66	PC3	PASS
DC_2A_n78A-37	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	23.41	23.41	PC3	PASS

00-3800									
DC_2A_n78A-37 00-3800	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	24.13	24.13	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	CP-QPSK	M+H	Outer_Full	24.48	24.48	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+90	CP-QPSK	M+H	Inner_Full	24.69	24.69	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	23.12	23.12	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	23.88	23.88	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Outer_Full	24.49	24.49	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+L	Inner_Full	24.68	24.68	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	23.24	23.24	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	CP-QPSK	M+L	Edge_1RB_Right	23.69	23.69	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	CP-QPSK	M+L	Outer_Full	24.50	24.50	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	CP-QPSK	M+L	Inner_Full	24.65	24.65	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	23.09	23.09	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	23.93	23.93	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Outer_Full	24.49	24.49	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+M	Inner_Full	24.85	24.85	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	CP-QPSK	M+M	Edge_1RB_Left	23.24	23.24	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	CP-QPSK	M+M	Edge_1RB_Right	23.70	23.70	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	CP-QPSK	M+M	Outer_Full	24.47	24.47	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	CP-QPSK	M+M	Inner_Full	24.64	24.64	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	23.08	23.08	PC3	PASS
DC_2A_n78A-37	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	23.87	23.87	PC3	PASS

00-3800									
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Outer_Full	23.75	23.75	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	DFT-QPSK	M+H	Inner_Full	24.67	24.67	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	CP-QPSK	M+H	Edge_1RB_Left	23.20	23.20	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	23.76	23.76	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	CP-QPSK	M+H	Outer_Full	24.43	24.43	PC3	PASS
DC_2A_n78A-37 00-3800	30	5+100	CP-QPSK	M+H	Inner_Full	24.61	24.61	PC3	PASS

Appendix B: Peak-to-Average Ratio for NSA

Peak-to-Average Ratio(CCDF)

Test Result

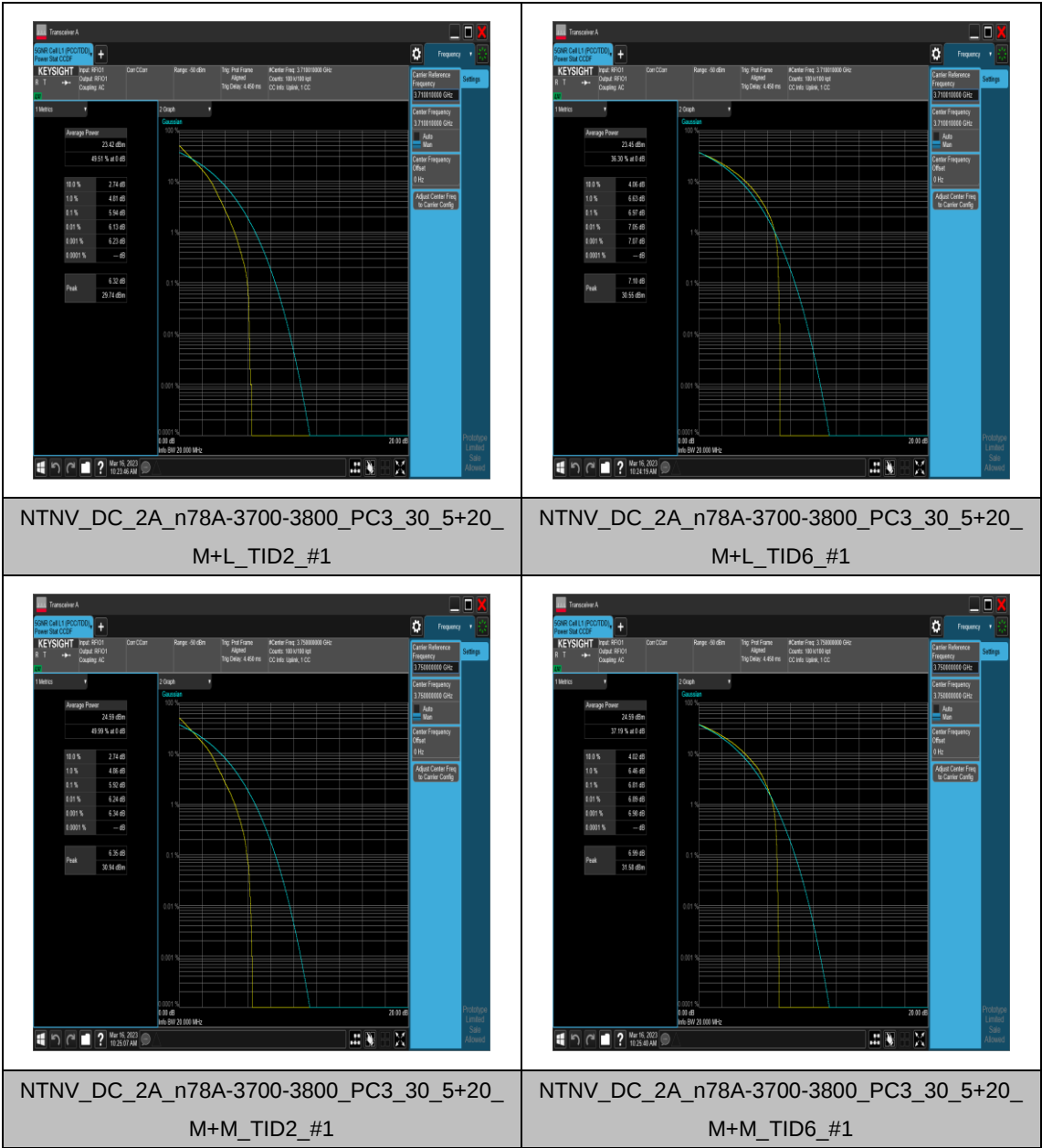
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_2A_n7 8A-3700-38 00	30	5+20	DFT-QPSK	M+L	Outer_Full	5.94	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+20	CP-QPSK	M+L	Outer_Full	6.97	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+20	DFT-QPSK	M+M	Outer_Full	5.92	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+20	CP-QPSK	M+M	Outer_Full	6.81	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+20	DFT-QPSK	M+H	Outer_Full	5.27	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+20	CP-QPSK	M+H	Outer_Full	7.00	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+40	DFT-QPSK	M+L	Outer_Full	5.90	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+40	CP-QPSK	M+L	Outer_Full	7.68	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+40	DFT-QPSK	M+M	Outer_Full	5.27	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+40	CP-QPSK	M+M	Outer_Full	7.13	≤13	PASS
DC_2A_n7	30	5+40	DFT-QPSK	M+H	Outer_Full	6.05	≤13	PASS

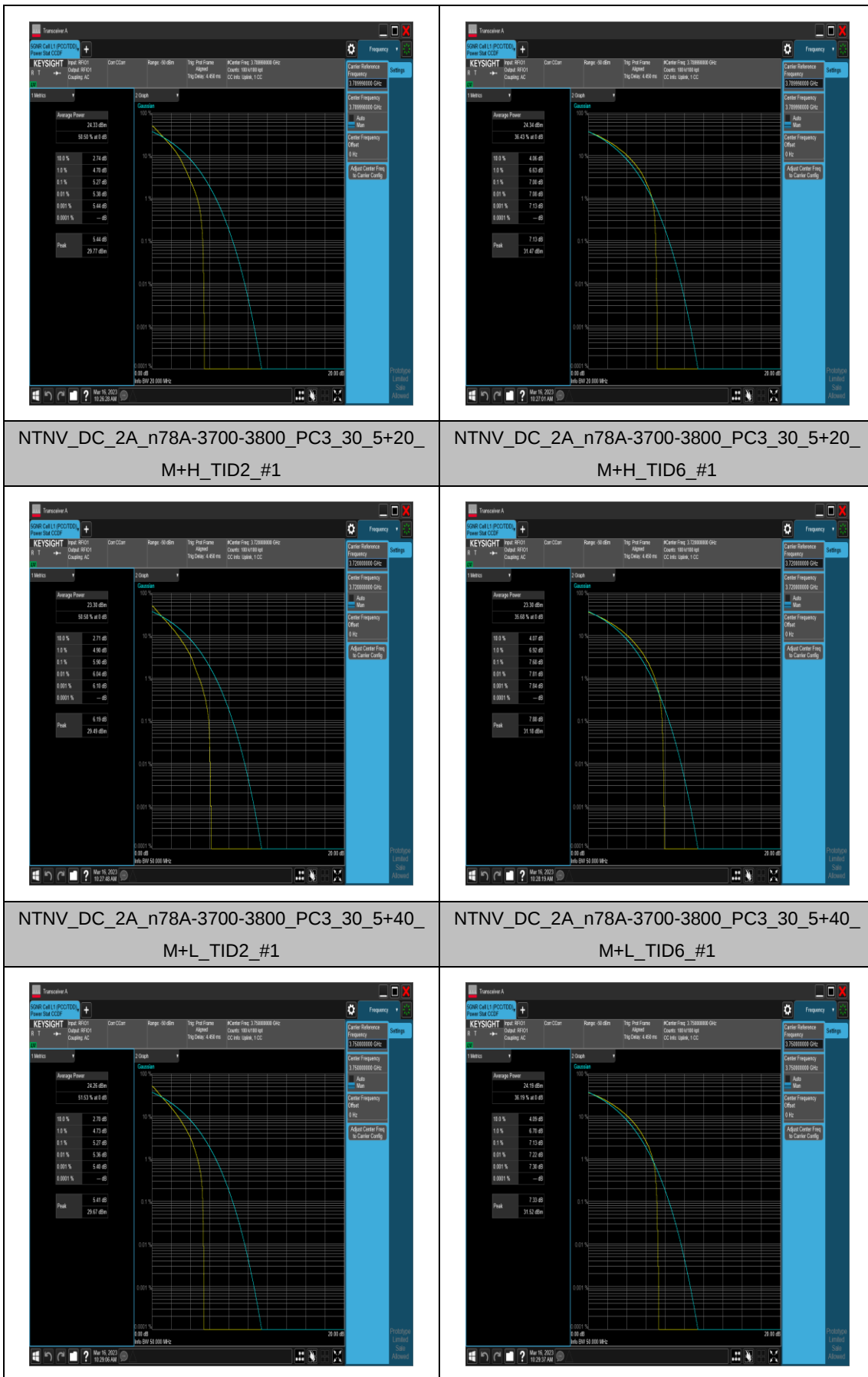
8A-3700-38 00								
DC_2A_n7 8A-3700-38 00	30	5+40	CP-QPSK	M+H	Outer_Full	7.06	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+50	DFT-QPSK	M+L	Outer_Full	5.73	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+50	CP-QPSK	M+L	Outer_Full	7.43	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+50	DFT-QPSK	M+M	Outer_Full	5.34	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+50	CP-QPSK	M+M	Outer_Full	7.02	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+50	DFT-QPSK	M+H	Outer_Full	6.00	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+50	CP-QPSK	M+H	Outer_Full	6.87	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+60	DFT-QPSK	M+L	Outer_Full	5.56	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+60	CP-QPSK	M+L	Outer_Full	7.29	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+60	DFT-QPSK	M+M	Outer_Full	5.26	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+60	CP-QPSK	M+M	Outer_Full	6.93	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+60	DFT-QPSK	M+H	Outer_Full	6.01	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+60	CP-QPSK	M+H	Outer_Full	6.93	≤13	PASS
DC_2A_n7 8A-3700-38	30	5+80	DFT-QPSK	M+L	Outer_Full	5.71	≤13	PASS

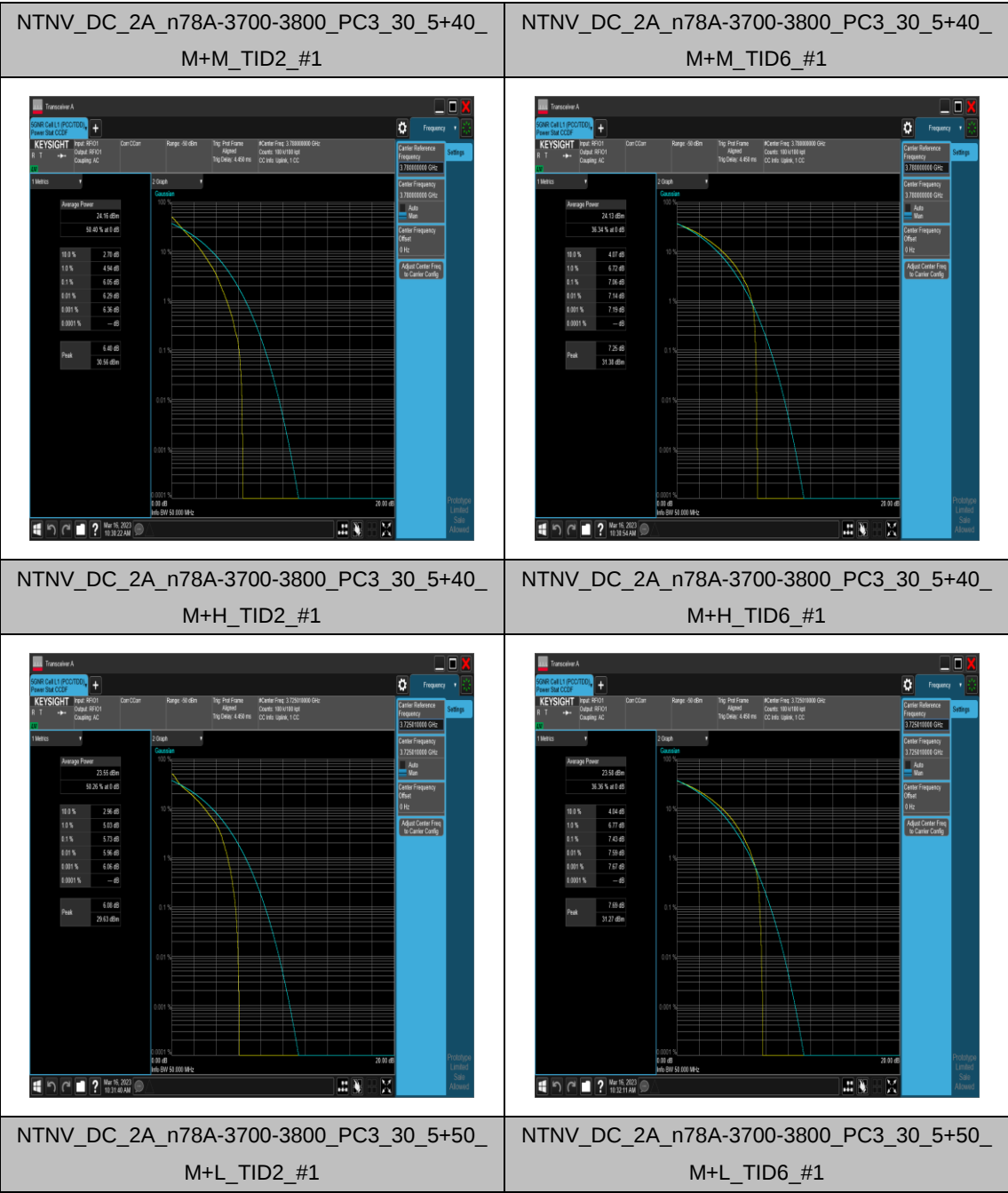
00								
DC_2A_n7 8A-3700-38 00	30	5+80	CP-QPSK	M+L	Outer_Full	7.47	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+80	DFT-QPSK	M+M	Outer_Full	5.45	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+80	CP-QPSK	M+M	Outer_Full	7.21	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+80	DFT-QPSK	M+H	Outer_Full	5.44	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+80	CP-QPSK	M+H	Outer_Full	7.22	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+90	DFT-QPSK	M+L	Outer_Full	5.52	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+90	CP-QPSK	M+L	Outer_Full	7.32	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+90	DFT-QPSK	M+M	Outer_Full	5.45	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+90	CP-QPSK	M+M	Outer_Full	7.26	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+90	DFT-QPSK	M+H	Outer_Full	5.36	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+90	CP-QPSK	M+H	Outer_Full	7.26	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+100	DFT-QPSK	M+L	Outer_Full	5.50	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+100	CP-QPSK	M+L	Outer_Full	7.30	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+100	DFT-QPSK	M+M	Outer_Full	5.45	≤13	PASS

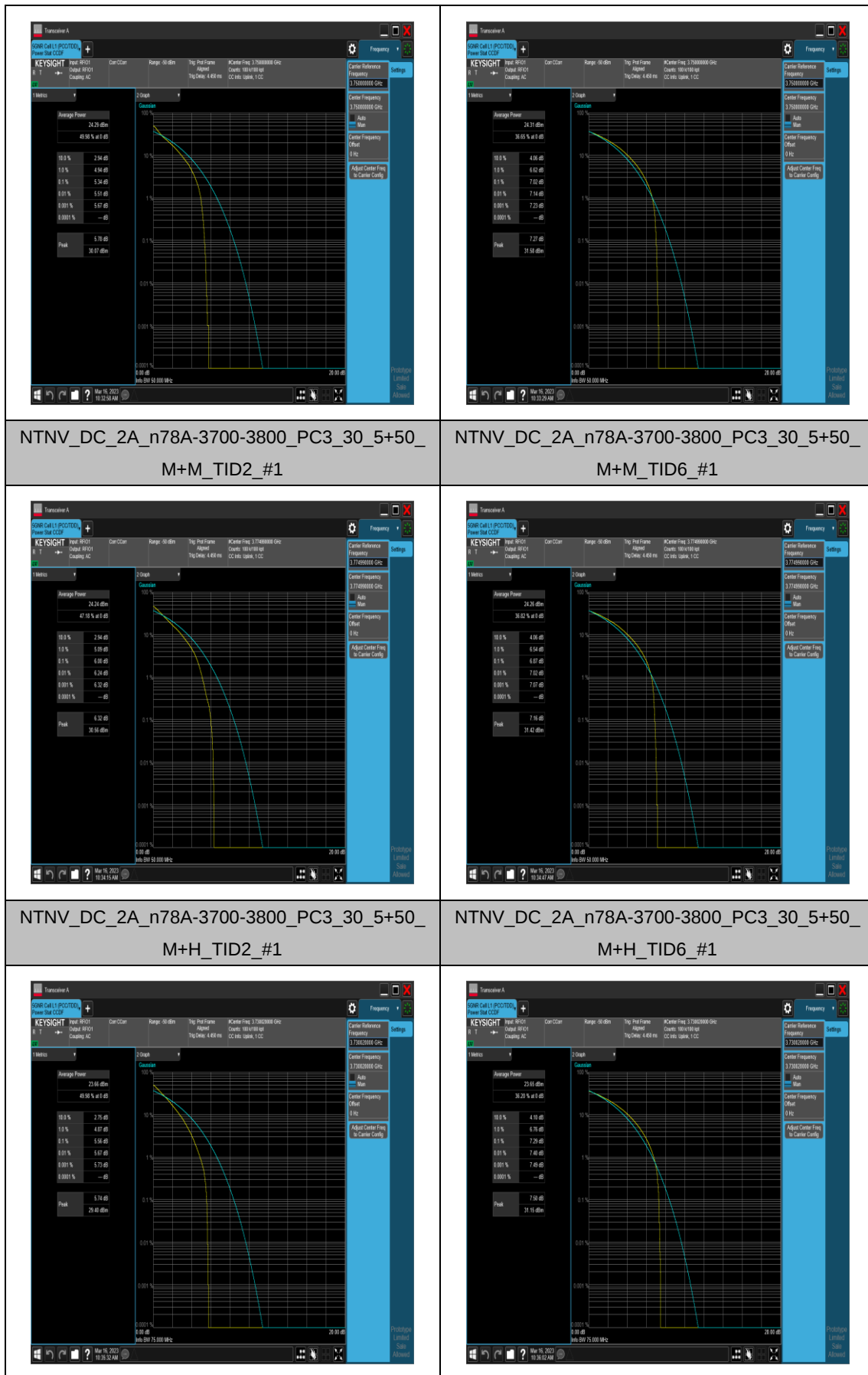
DC_2A_n7 8A-3700-38 00	30	5+100	CP-QPSK	M+M	Outer_Full	7.31	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+100	DFT-QPSK	M+H	Outer_Full	5.45	≤13	PASS
DC_2A_n7 8A-3700-38 00	30	5+100	CP-QPSK	M+H	Outer_Full	7.32	≤13	PASS

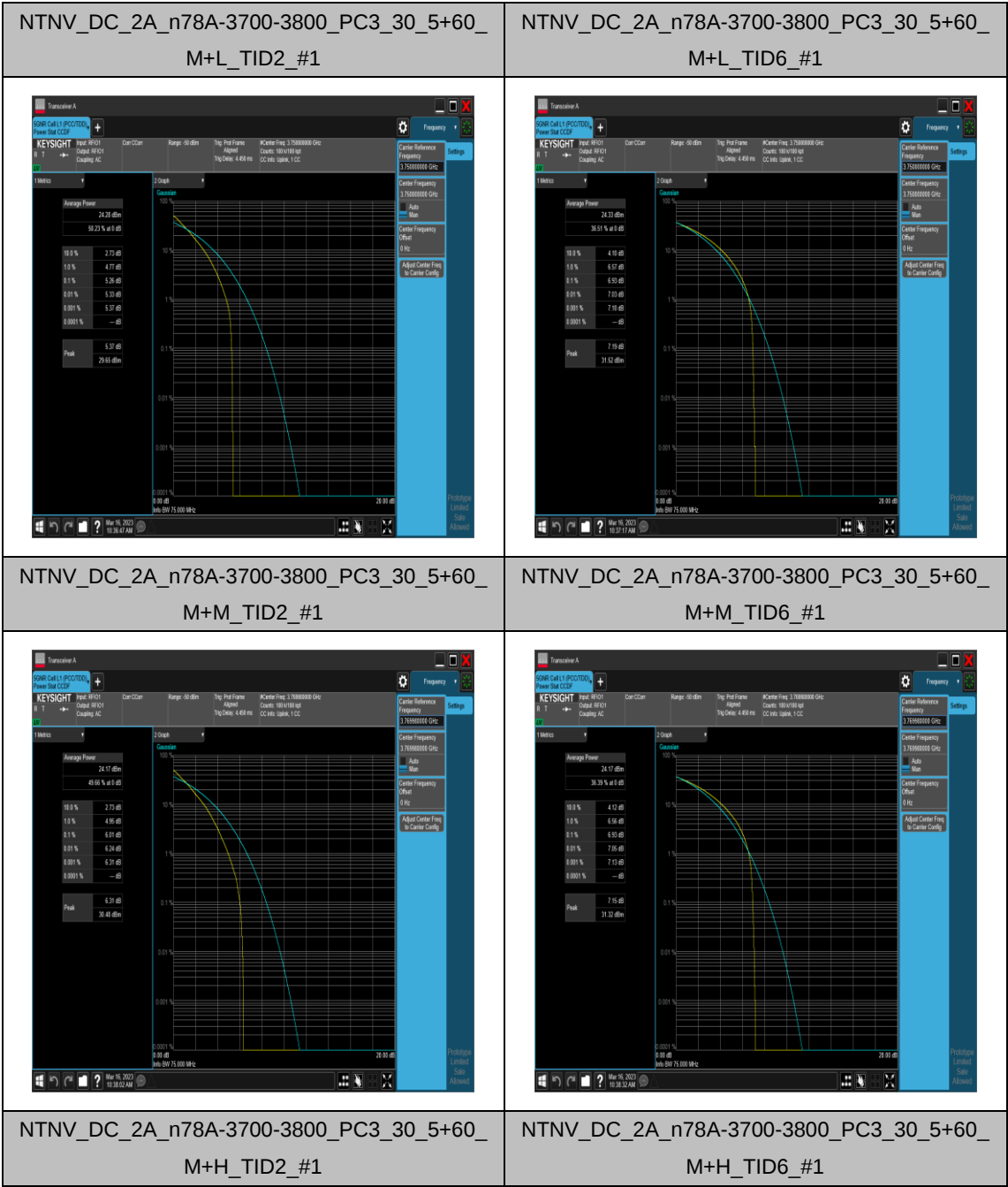
Test Graphs

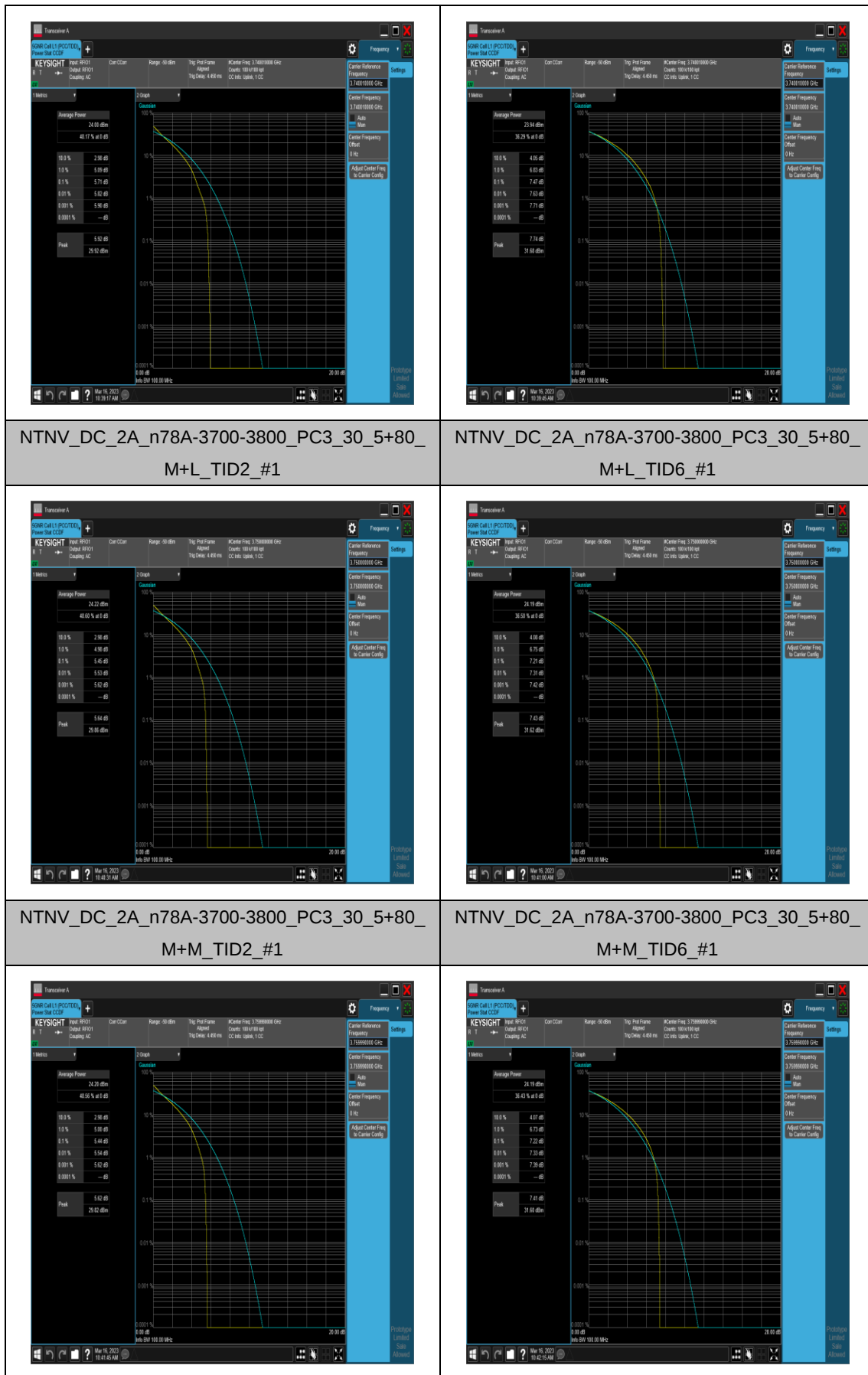


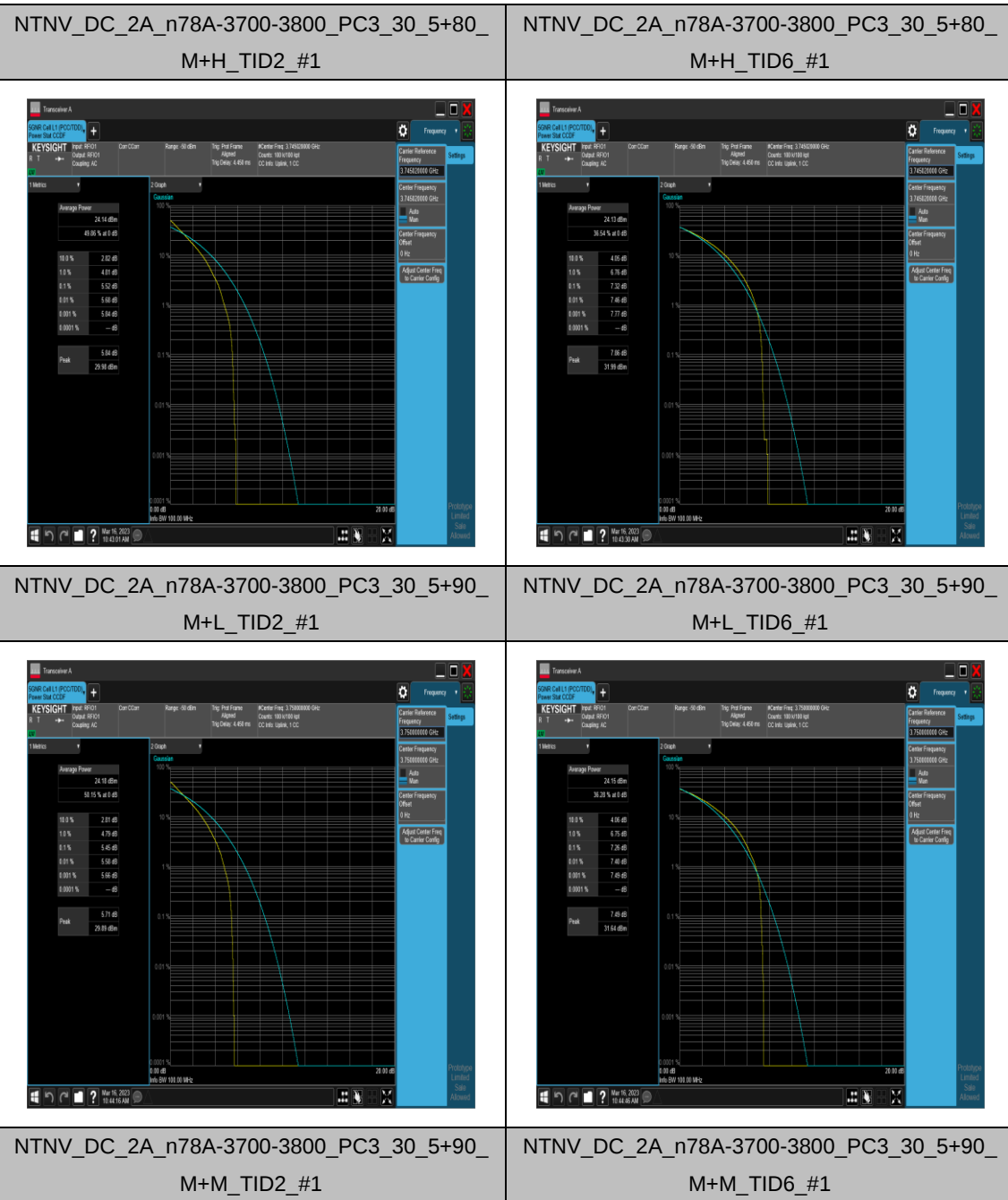


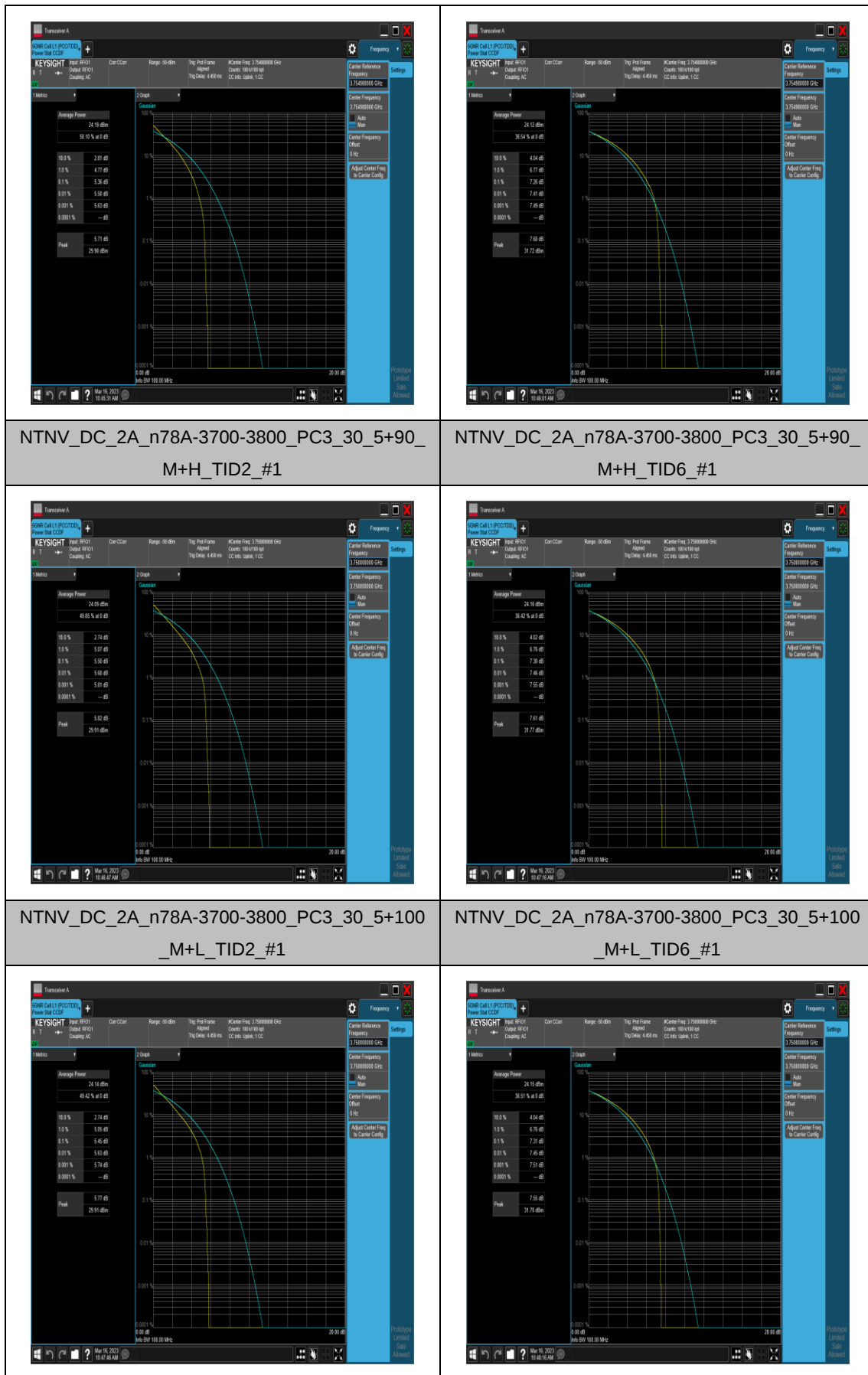


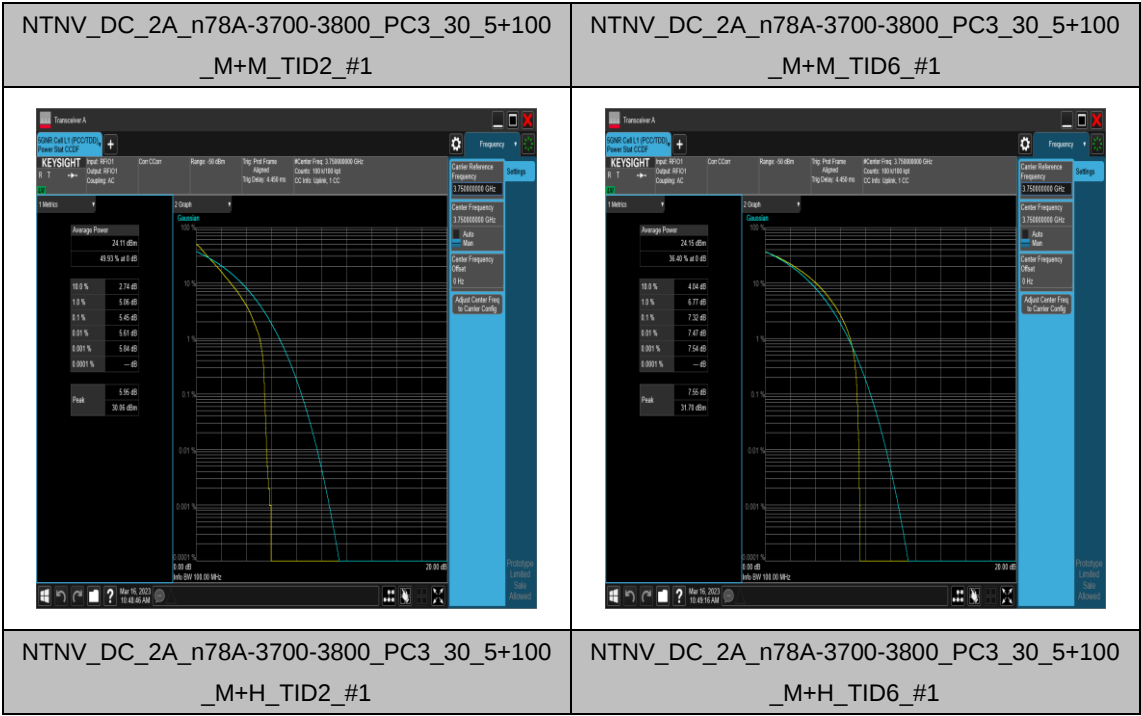












Appendix C: 26dB Bandwidth and Occupied Bandwidth for NSA

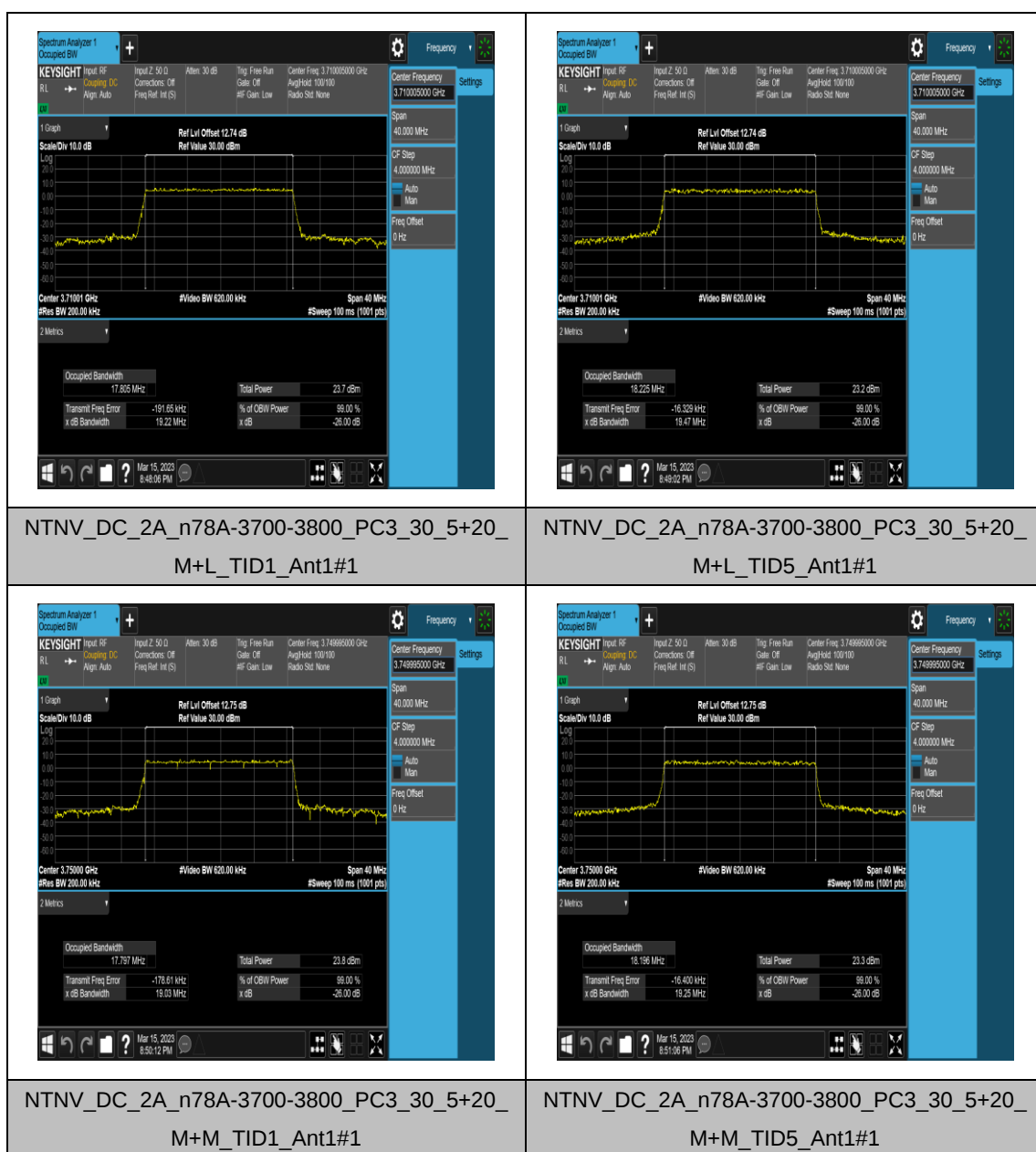
Test Result

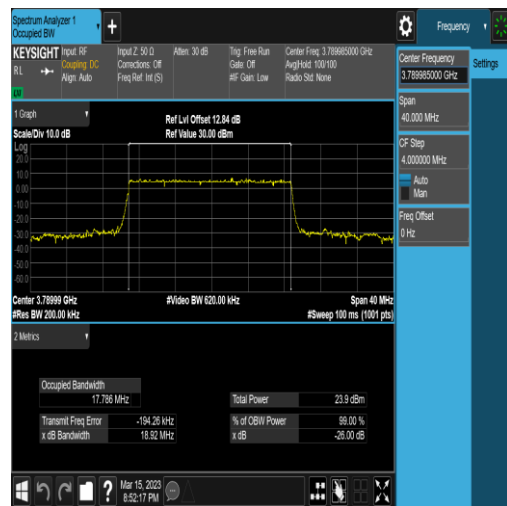
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_2A_n78A-3700-38 00	30	5+20	DFT-QPSK	M+L	Outer_Full	17.805	19.22	PASS
DC_2A_n78A-3700-38 00	30	5+20	CP-QPSK	M+L	Outer_Full	18.225	19.47	PASS
DC_2A_n78A-3700-38 00	30	5+20	DFT-QPSK	M+M	Outer_Full	17.797	19.03	PASS
DC_2A_n78A-3700-38 00	30	5+20	CP-QPSK	M+M	Outer_Full	18.196	19.25	PASS
DC_2A_n78A-3700-38 00	30	5+20	DFT-QPSK	M+H	Outer_Full	17.786	18.92	PASS
DC_2A_n78A-3700-38 00	30	5+20	CP-QPSK	M+H	Outer_Full	18.239	19.59	PASS
DC_2A_n78A-3700-38 00	30	5+40	DFT-QPSK	M+L	Outer_Full	35.681	37.48	PASS
DC_2A_n78A-3700-38 00	30	5+40	CP-QPSK	M+L	Outer_Full	37.779	39.59	PASS
DC_2A_n78A-3700-38 00	30	5+40	DFT-QPSK	M+M	Outer_Full	35.742	37.45	PASS
DC_2A_n78A-3700-38 00	30	5+40	CP-QPSK	M+M	Outer_Full	37.838	39.58	PASS
DC_2A_n78A-3700-38 00	30	5+40	DFT-QPSK	M+H	Outer_Full	35.740	37.34	PASS
DC_2A_n78A-3700-38 00	30	5+40	CP-QPSK	M+H	Outer_Full	37.796	39.54	PASS
DC_2A_n78A-3700-38 00	30	5+50	DFT-QPSK	M+L	Outer_Full	45.642	47.47	PASS
DC_2A_n78A-3700-38 00	30	5+50	CP-QPSK	M+L	Outer_Full	47.410	49.34	PASS
DC_2A_n78A-3700-38 00	30	5+50	DFT-QPSK	M+M	Outer_Full	45.663	47.57	PASS
DC_2A_n78A-3700-38 00	30	5+50	CP-QPSK	M+M	Outer_Full	47.394	49.38	PASS
DC_2A_n78A-3700-38	30	5+50	DFT-QPSK	M+H	Outer_Full	45.642	47.43	PASS

00								
DC_2A_n78A-3700-38 00	30	5+50	CP-QPSK	M+H	Outer_Full	47.361	49.43	PASS
DC_2A_n78A-3700-38 00	30	5+60	DFT-QPSK	M+L	Outer_Full	57.769	60.10	PASS
DC_2A_n78A-3700-38 00	30	5+60	CP-QPSK	M+L	Outer_Full	57.902	60.16	PASS
DC_2A_n78A-3700-38 00	30	5+60	DFT-QPSK	M+M	Outer_Full	57.723	60.13	PASS
DC_2A_n78A-3700-38 00	30	5+60	CP-QPSK	M+M	Outer_Full	57.838	60.17	PASS
DC_2A_n78A-3700-38 00	30	5+60	DFT-QPSK	M+H	Outer_Full	57.699	59.97	PASS
DC_2A_n78A-3700-38 00	30	5+60	CP-QPSK	M+H	Outer_Full	57.793	59.93	PASS
DC_2A_n78A-3700-38 00	30	5+80	DFT-QPSK	M+L	Outer_Full	77.036	79.76	PASS
DC_2A_n78A-3700-38 00	30	5+80	CP-QPSK	M+L	Outer_Full	77.346	80.12	PASS
DC_2A_n78A-3700-38 00	30	5+80	DFT-QPSK	M+M	Outer_Full	77.137	79.89	PASS
DC_2A_n78A-3700-38 00	30	5+80	CP-QPSK	M+M	Outer_Full	77.363	80.31	PASS
DC_2A_n78A-3700-38 00	30	5+80	DFT-QPSK	M+H	Outer_Full	76.964	79.88	PASS
DC_2A_n78A-3700-38 00	30	5+80	CP-QPSK	M+H	Outer_Full	77.305	80.14	PASS
DC_2A_n78A-3700-38 00	30	5+90	DFT-QPSK	M+L	Outer_Full	86.462	89.70	PASS
DC_2A_n78A-3700-38 00	30	5+90	CP-QPSK	M+L	Outer_Full	87.333	90.49	PASS
DC_2A_n78A-3700-38 00	30	5+90	DFT-QPSK	M+M	Outer_Full	86.607	89.72	PASS
DC_2A_n78A-3700-38 00	30	5+90	CP-QPSK	M+M	Outer_Full	87.429	90.49	PASS
DC_2A_n78A-3700-38 00	30	5+90	DFT-QPSK	M+H	Outer_Full	86.445	89.55	PASS
DC_2A_n78A-3700-38 00	30	5+90	CP-QPSK	M+H	Outer_Full	87.405	90.55	PASS
DC_2A_n78A-3700-38 00	30	5+100	DFT-QPSK	M+L	Outer_Full	96.101	99.64	PASS
DC_2A_n78A-3700-38	30	5+100	CP-QPSK	M+L	Outer_Full	97.204	100.7	PASS

00								
DC_2A_n78A-3700-38 00	30	5+100	DFT-QPSK	M+M	Outer_Full	96.135	99.58	PASS
DC_2A_n78A-3700-38 00	30	5+100	CP-QPSK	M+M	Outer_Full	97.271	100.7	PASS
DC_2A_n78A-3700-38 00	30	5+100	DFT-QPSK	M+H	Outer_Full	96.036	99.55	PASS
DC_2A_n78A-3700-38 00	30	5+100	CP-QPSK	M+H	Outer_Full	97.289	100.7	PASS

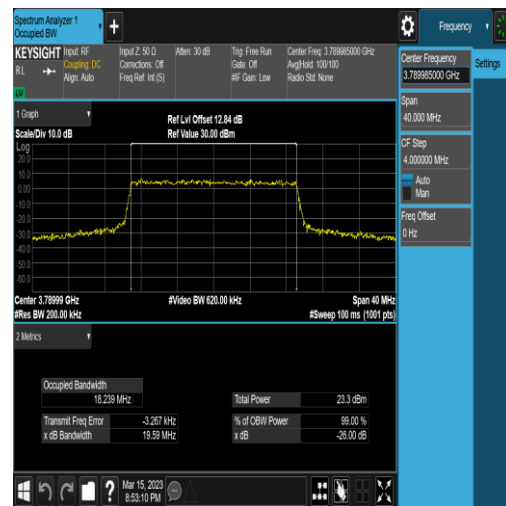
Test Graphs





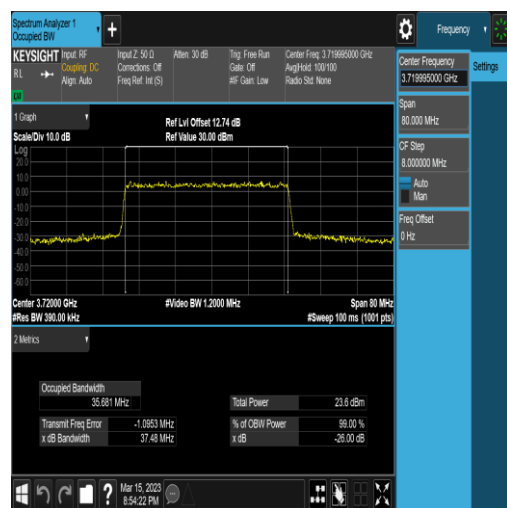
NTNV_DC_2A_n78A-3700-3800_PC3_30_5+20_

M+H_TID1_Ant1#1



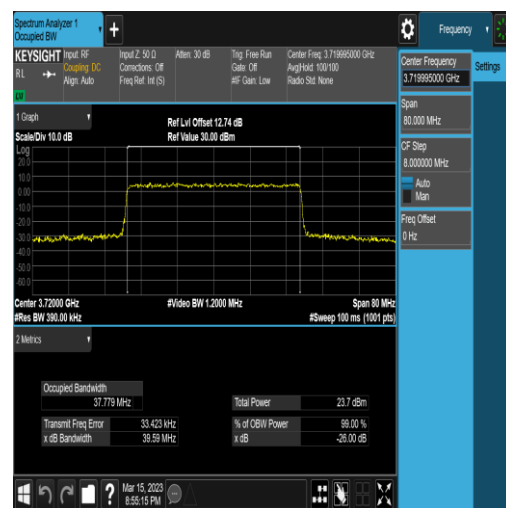
NTNV_DC_2A_n78A-3700-3800_PC3_30_5+20_

M+H_TID5_Ant1#1



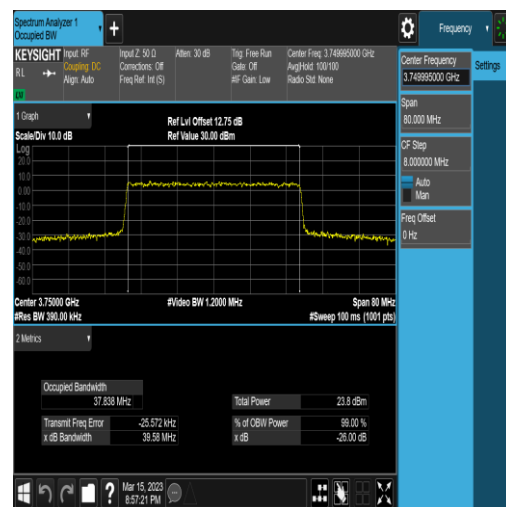
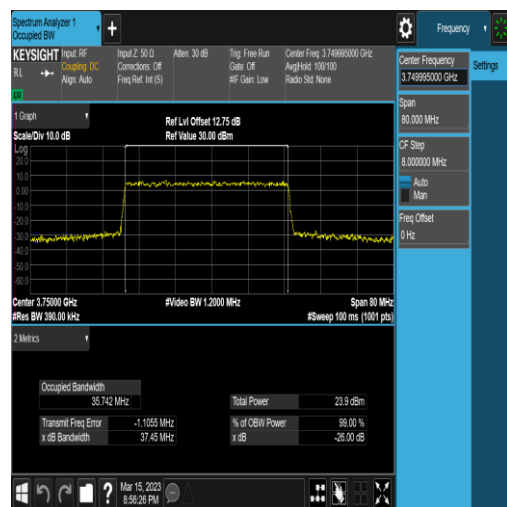
NTNV_DC_2A_n78A-3700-3800_PC3_30_5+40_

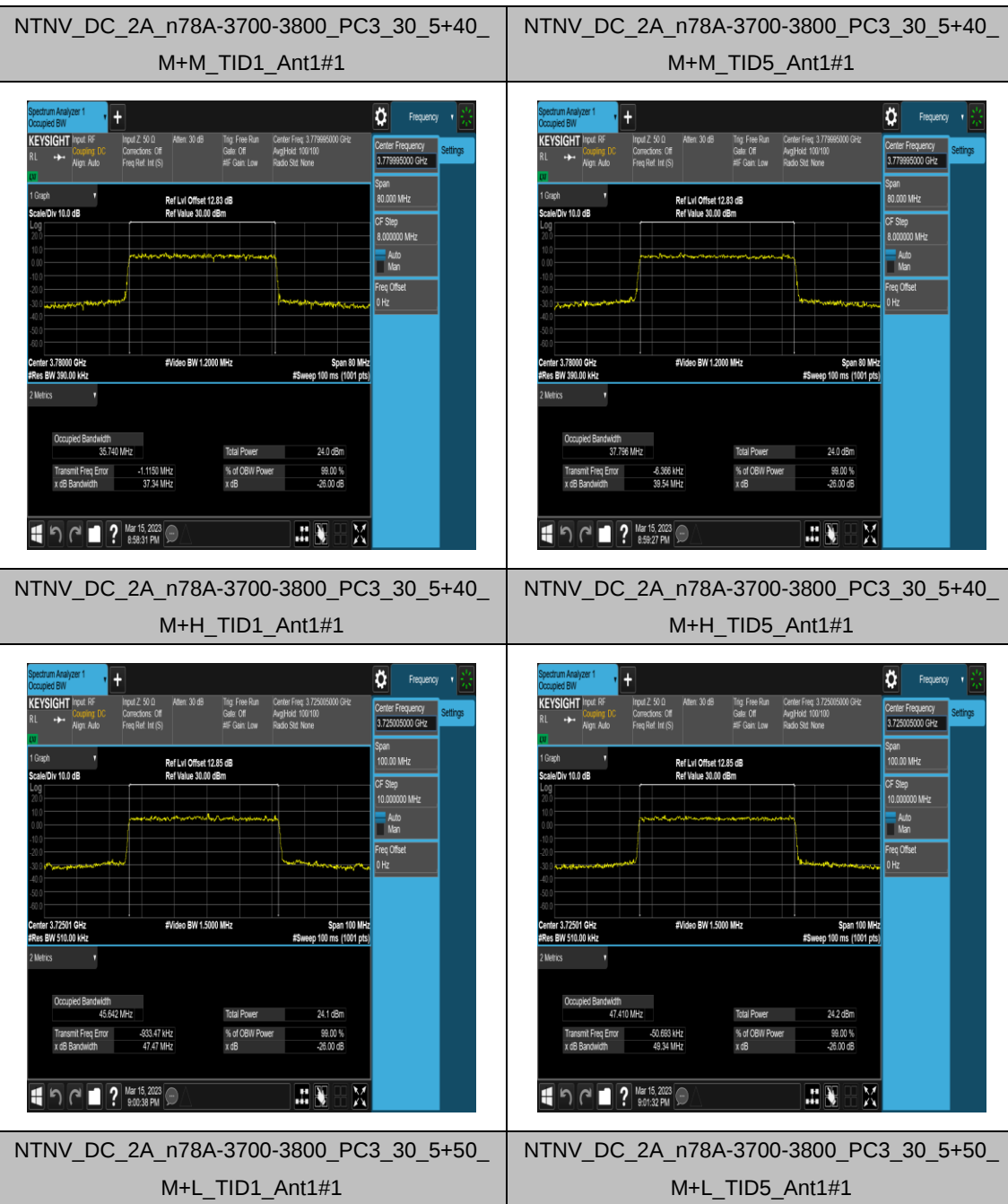
M+L_TID1_Ant1#1

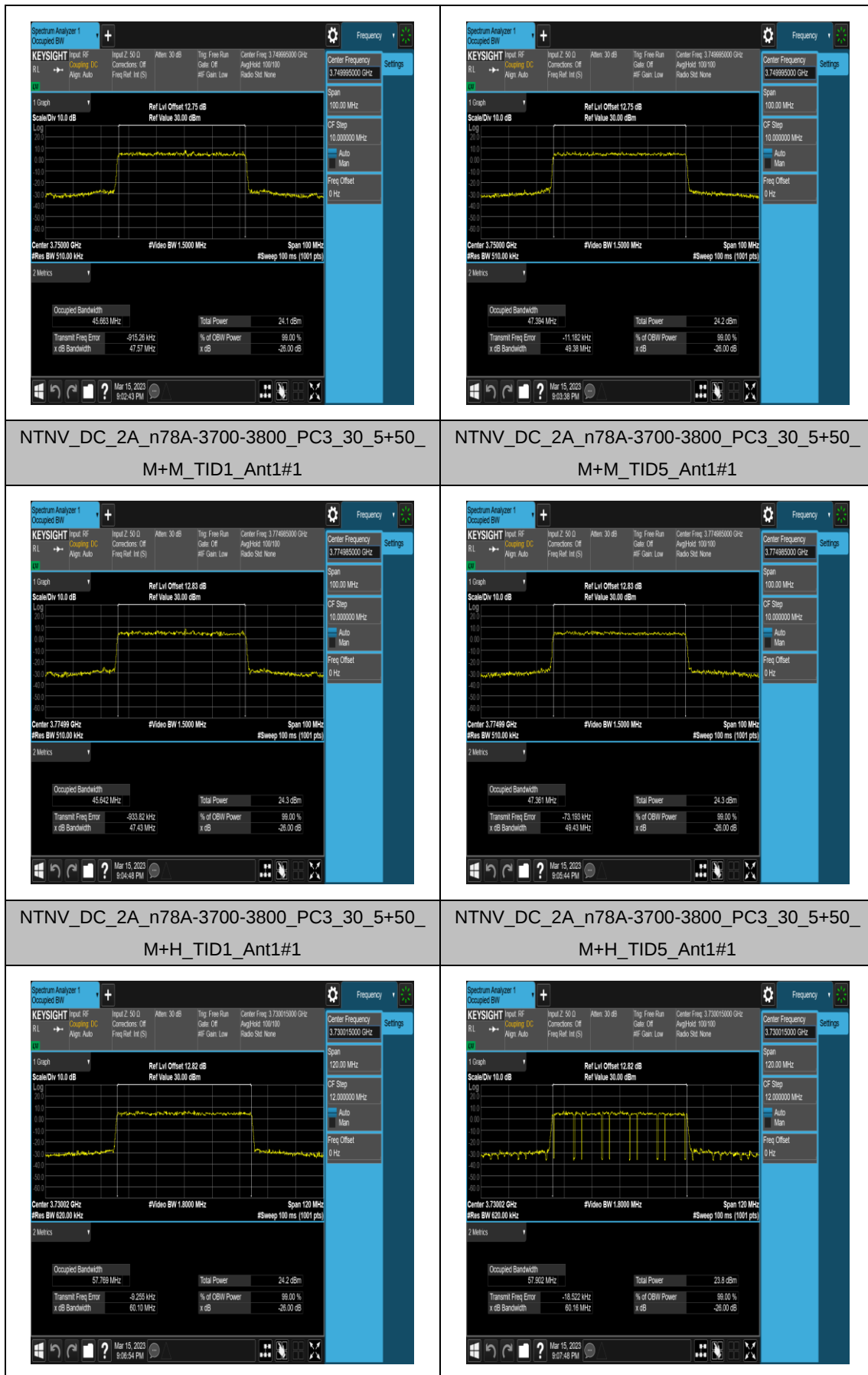


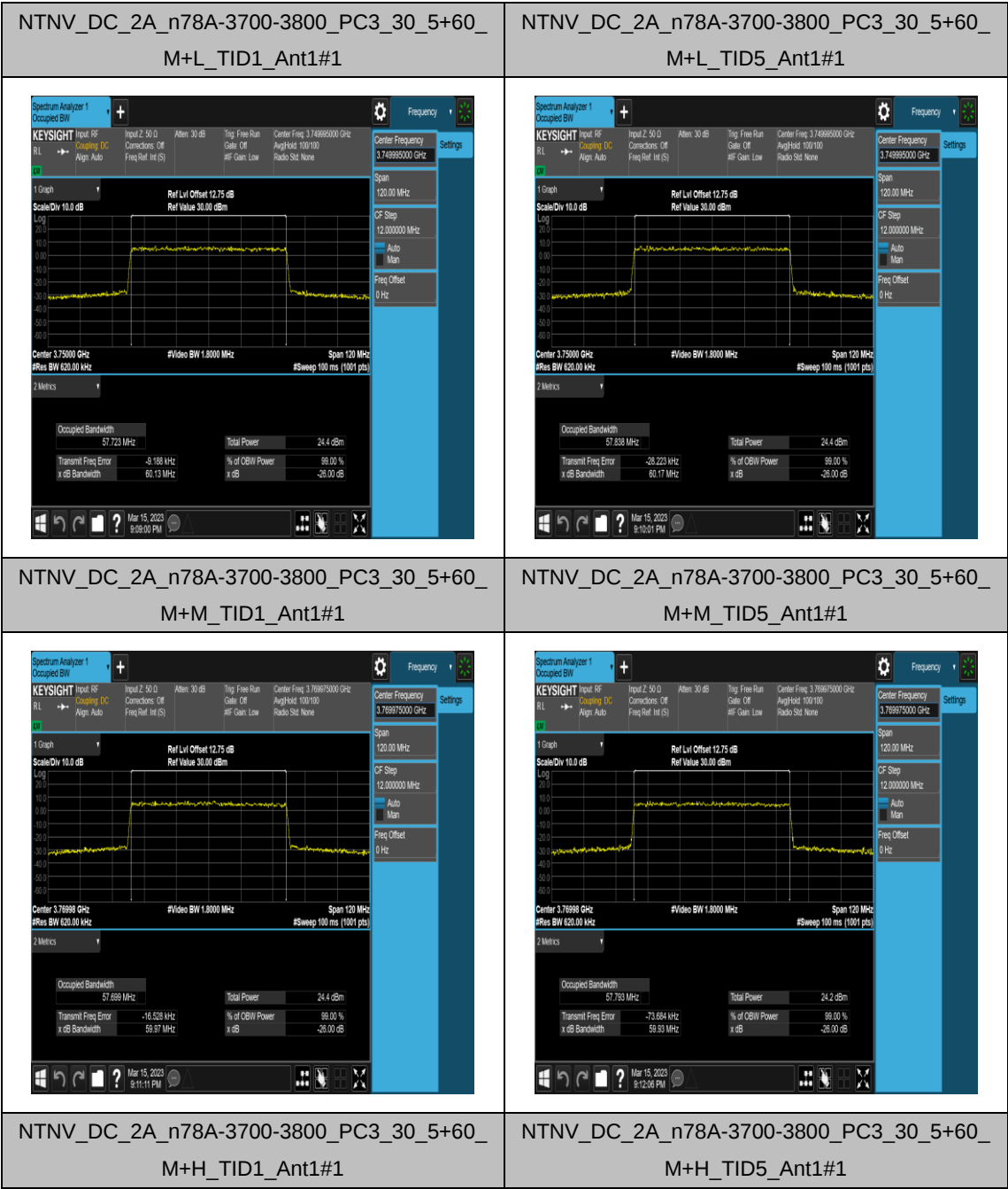
NTNV_DC_2A_n78A-3700-3800_PC3_30_5+40_

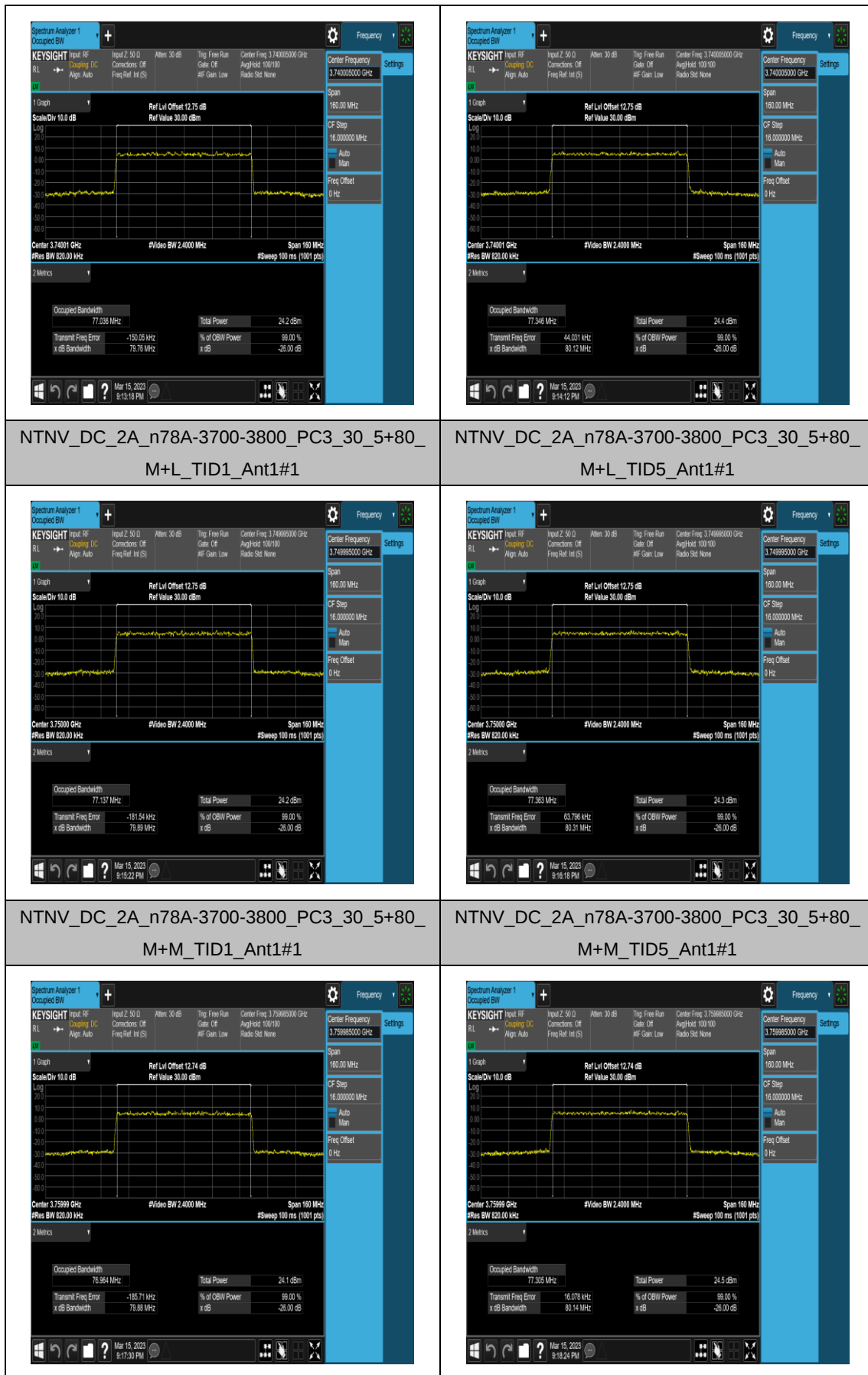
M+L_TID5_Ant1#1

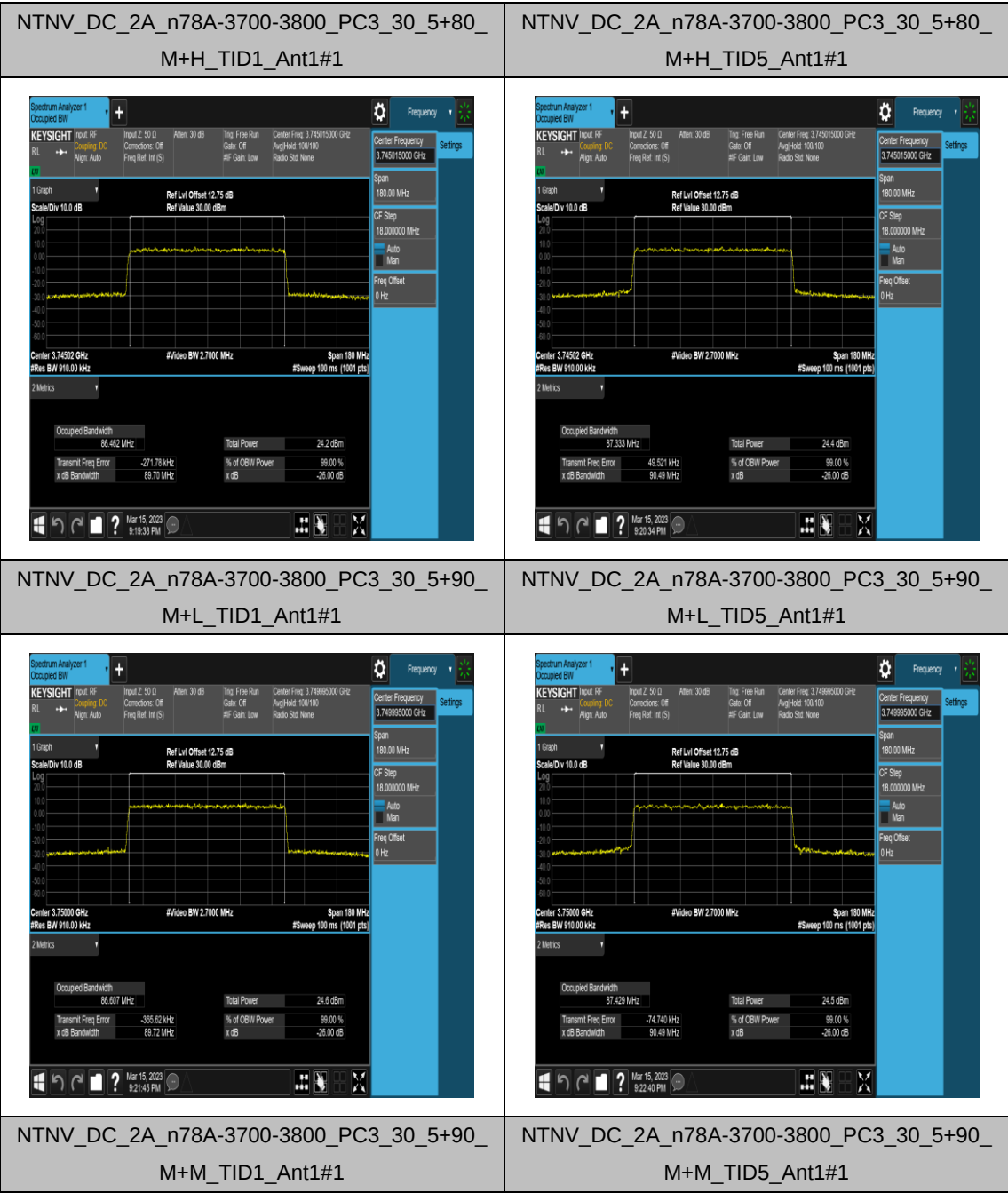


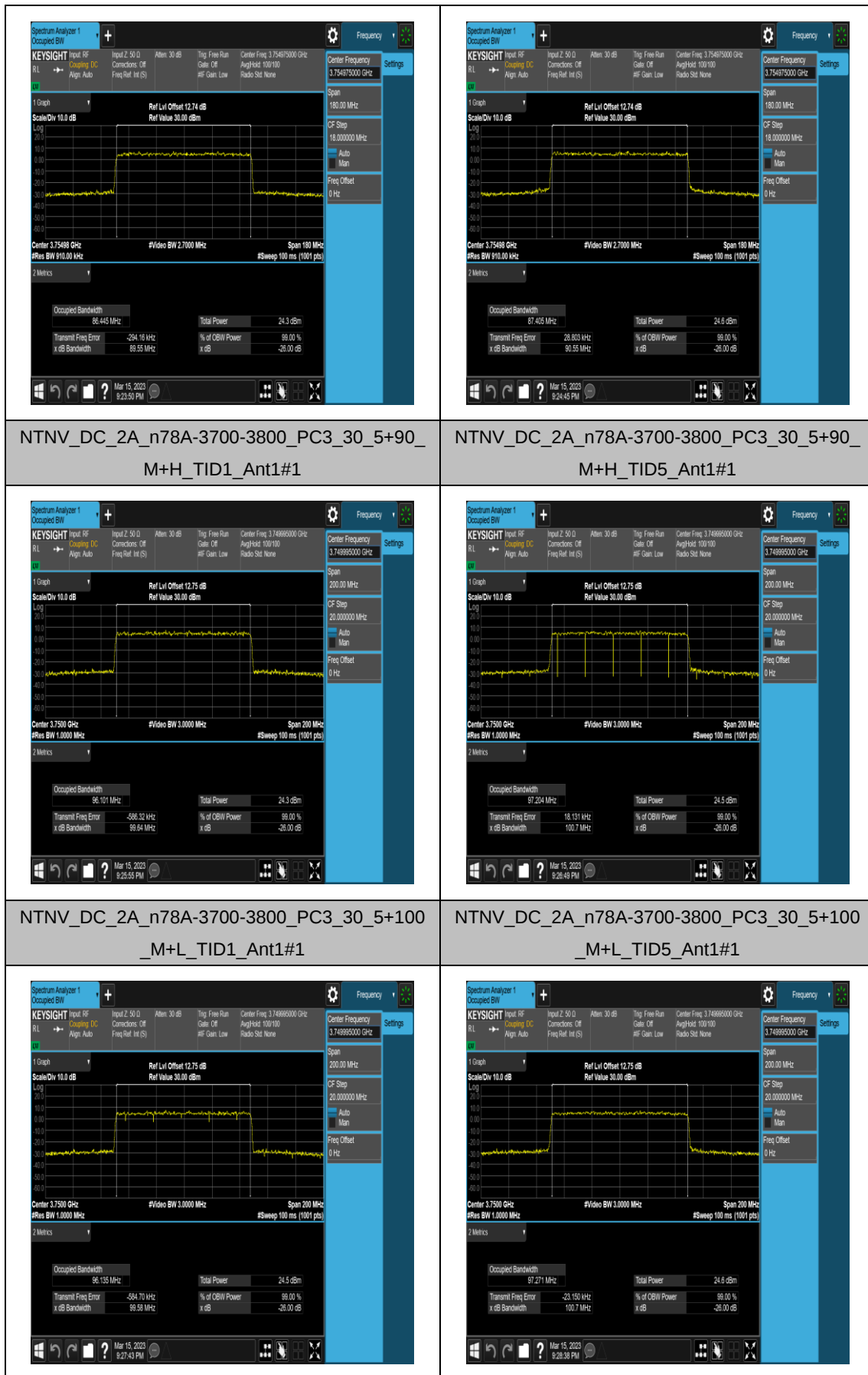


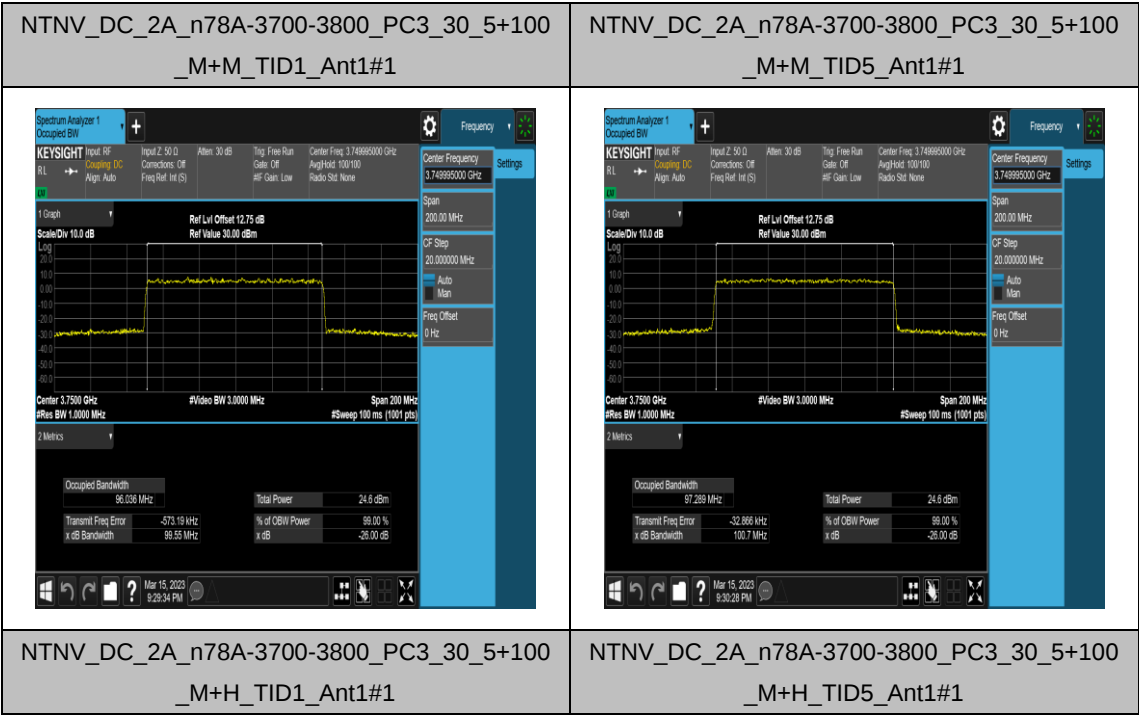












Appendix D: Band Edge for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Verdict
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n78A-3700-3800	30	5+20	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n78A-3700-3800	30	5+40	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n78A-3700-3800	30	5+50	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n78A-3700-3800	30	5+60	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3700-3800	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS

00							
DC_2A_n78A-3700-38 00	30	5+80	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3700-38 00	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3700-38 00	30	5+90	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3700-38 00	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n78A-3700-38 00	30	5+90	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3700-38 00	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3700-38 00	30	5+100	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3700-38 00	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n78A-3700-38 00	30	5+100	CP-QPSK	M+H	Outer_Full	see graph	PASS