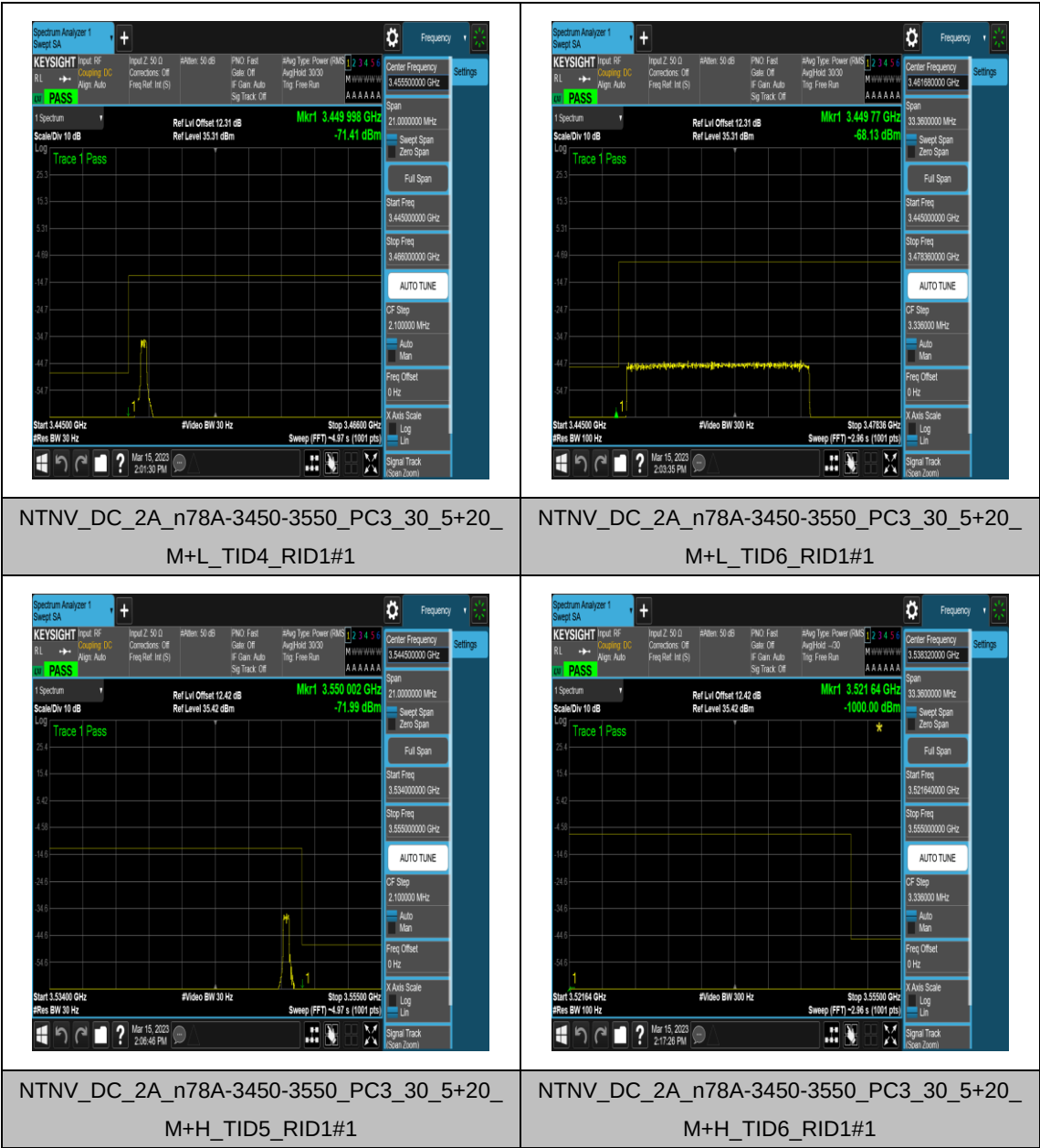
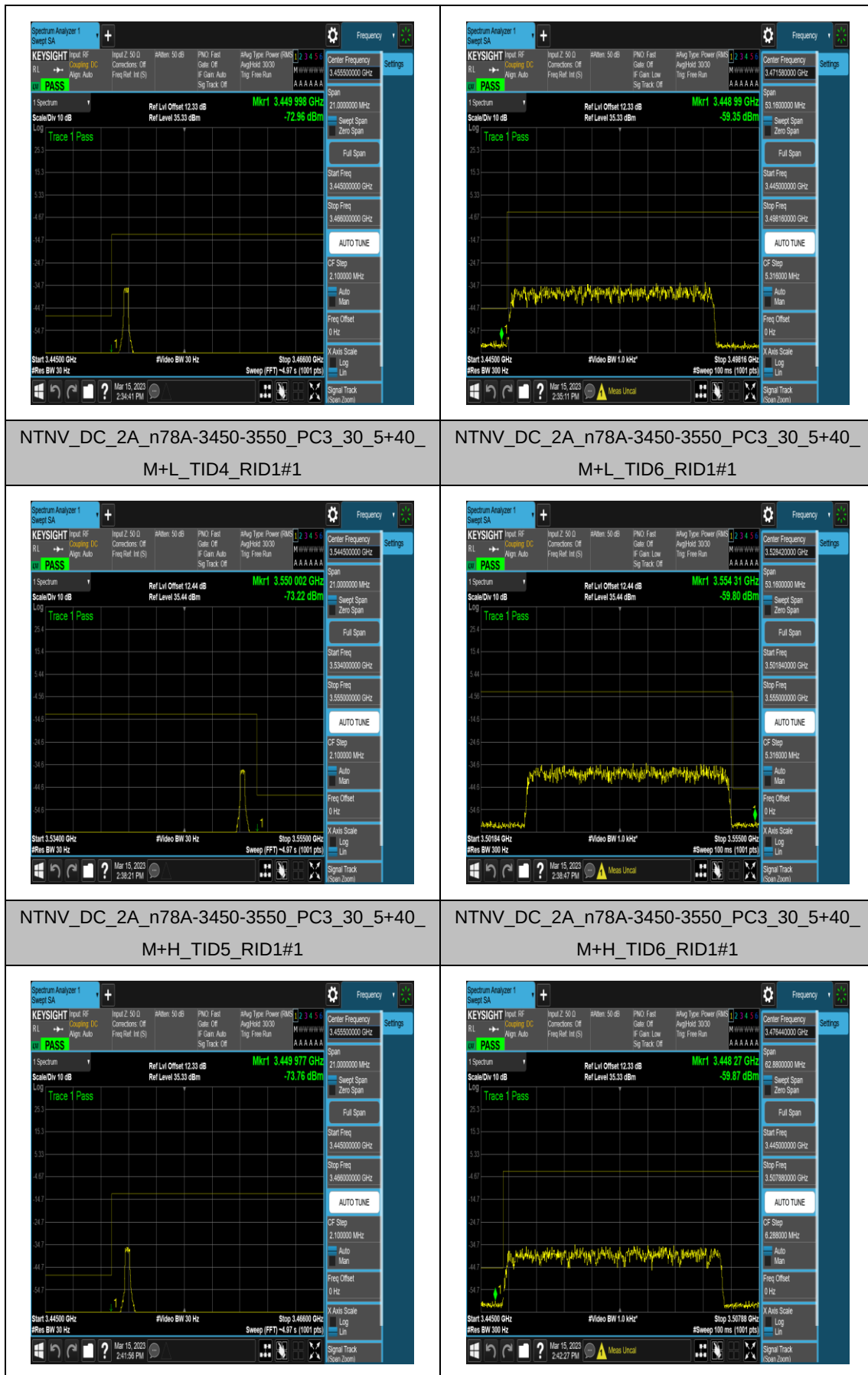
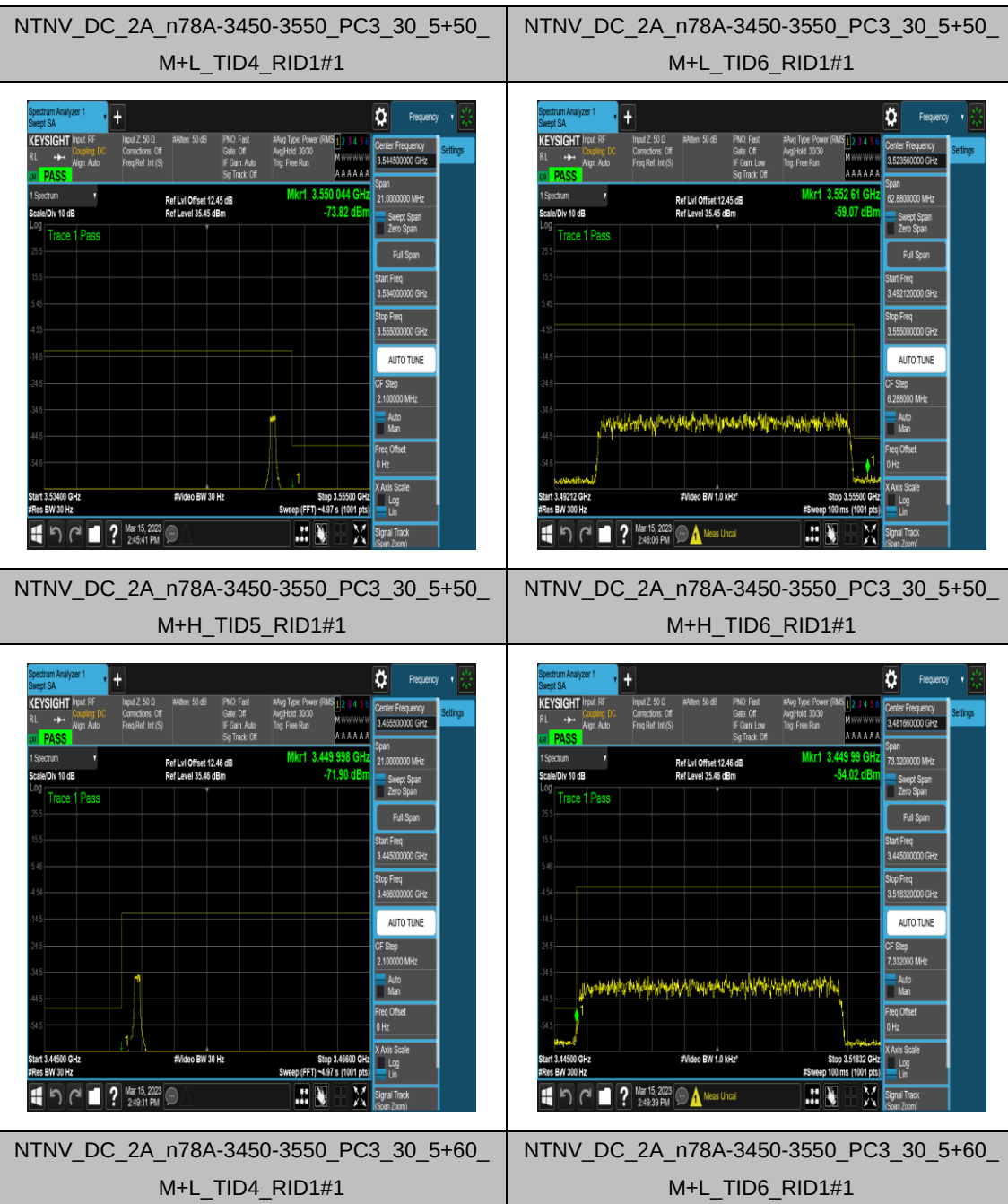
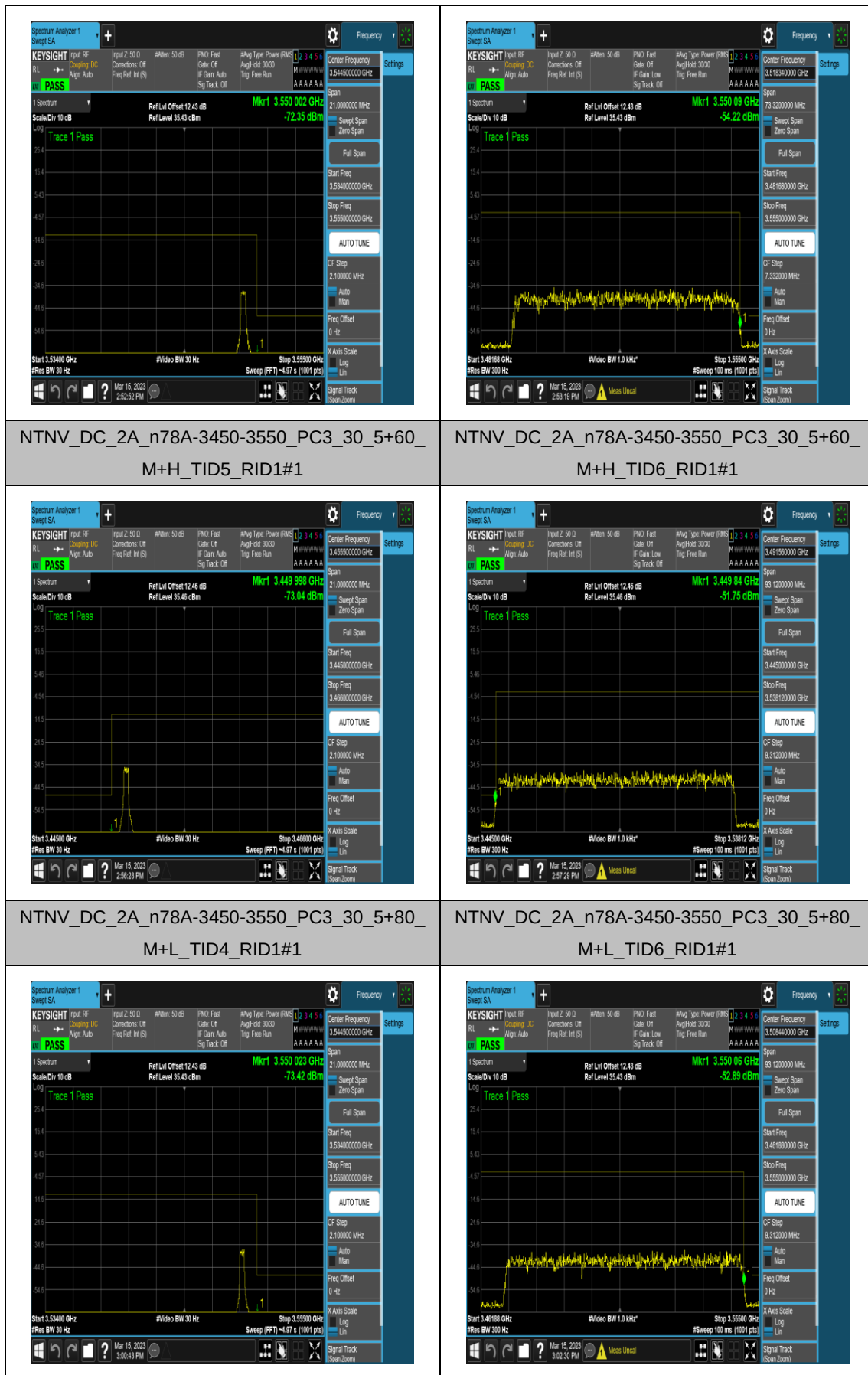


Test Graphs





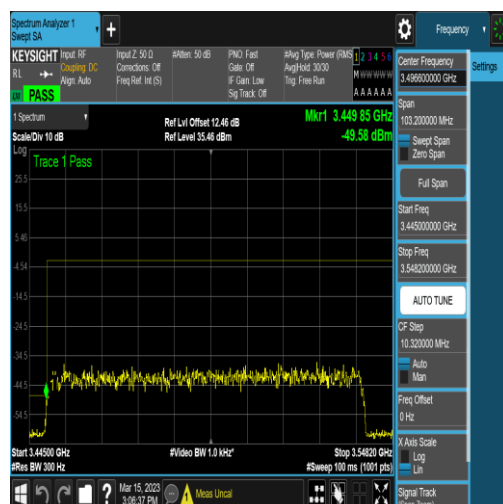




NTNV_DC_2A_n78A-3450-3550_PC3_30_5+80_
M+H TID5 RID1#1



NTNV_DC_2A_n78A-3450-3550_PC3_30_5+80_
M+H TID6 RID1#1



NTNV_DC_2A_n78A-3450-3550_PC3_30_5+90_
M+L TID4 RID1#1

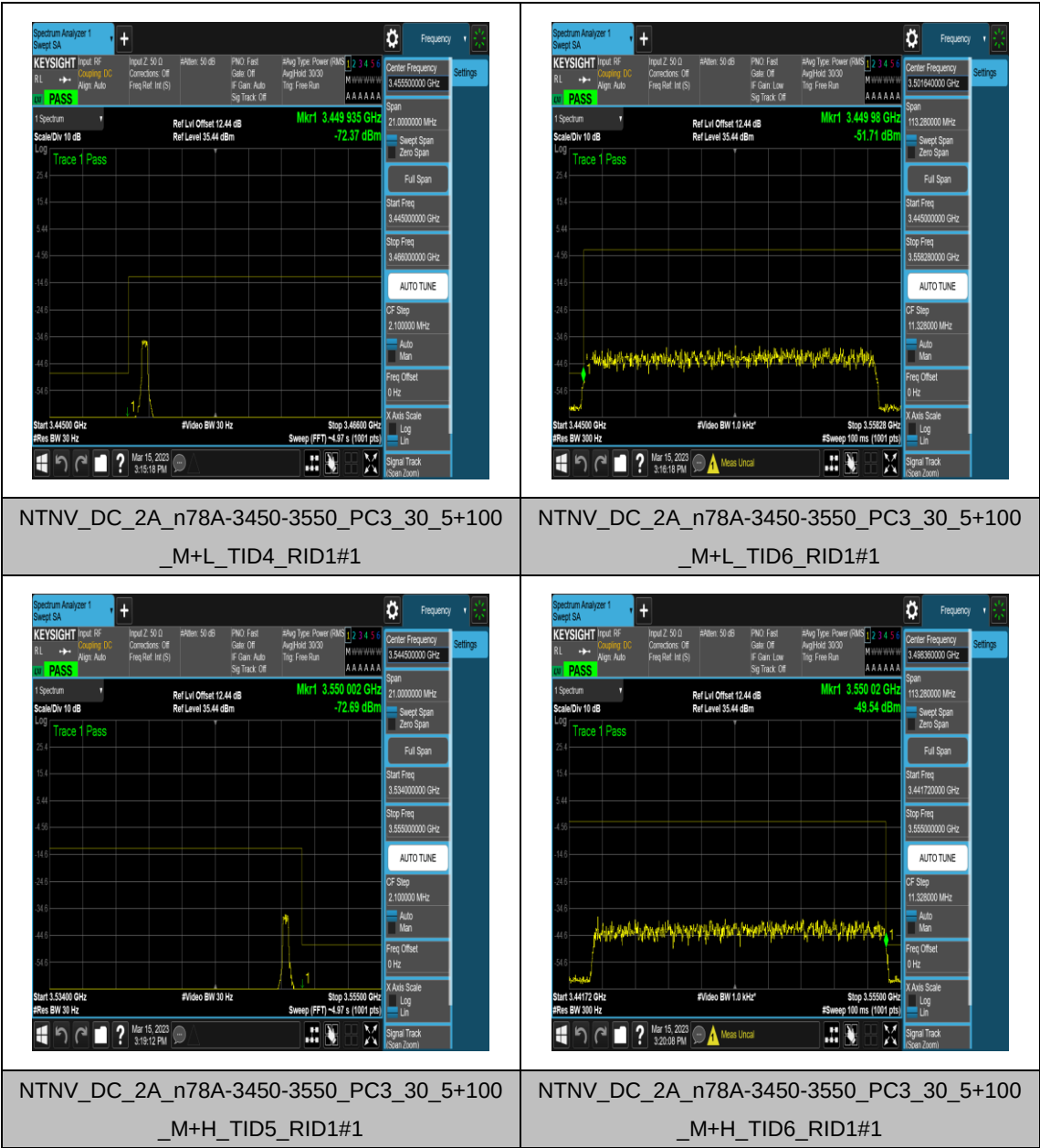


NTNV_DC_2A_n78A-3450-3550_PC3_30_5+90_
M+L_TID6 RID1#1



NTNV_DC_2A_n78A-3450-3550_PC3_30_5+90_
M+H TID5 RID1#1

NTNV_DC_2A_n78A-3450-3550_PC3_30_5+90_
M+H TID6 RID1#1



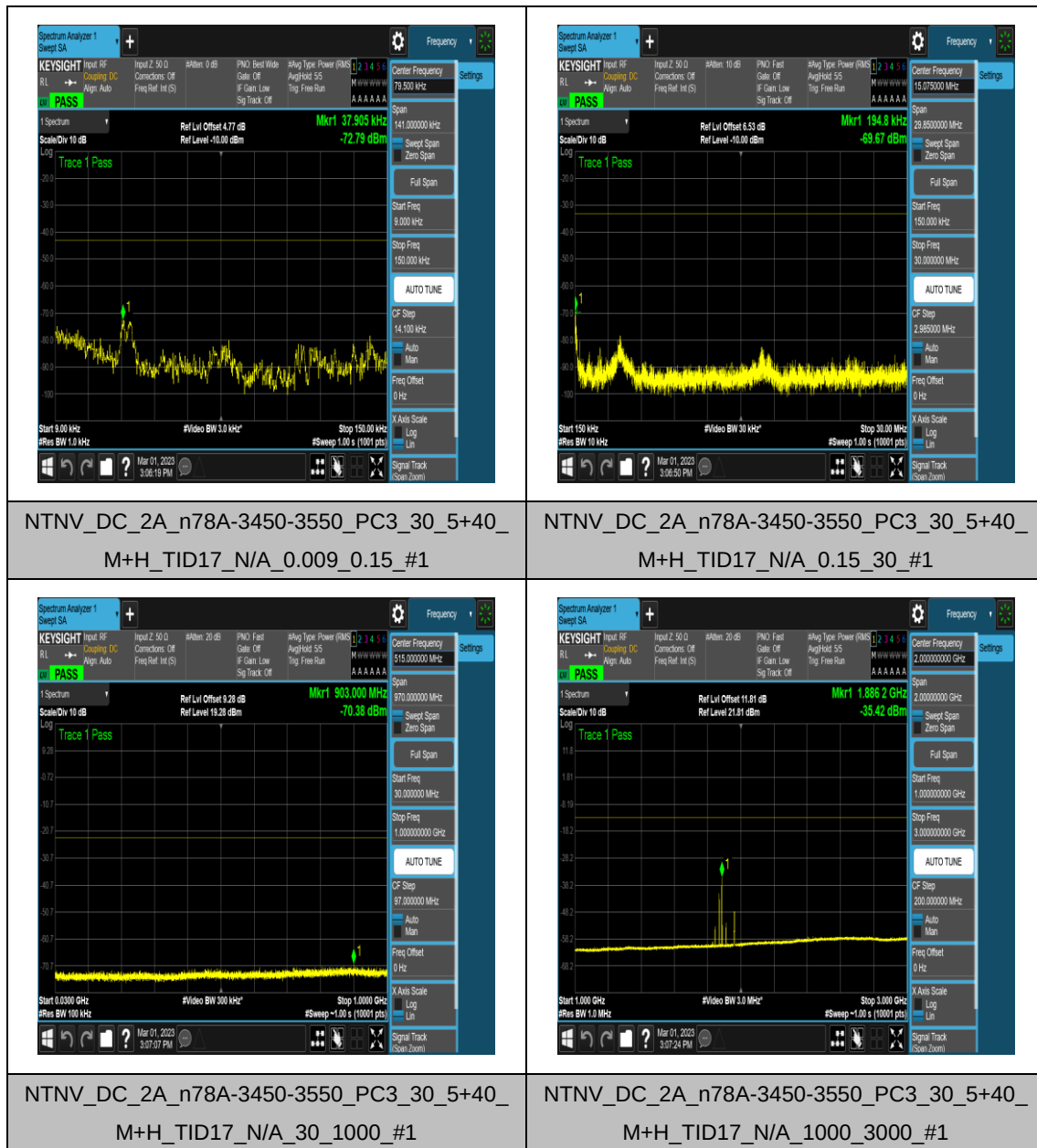
Appendix E: Conducted Spurious Emission for NSA

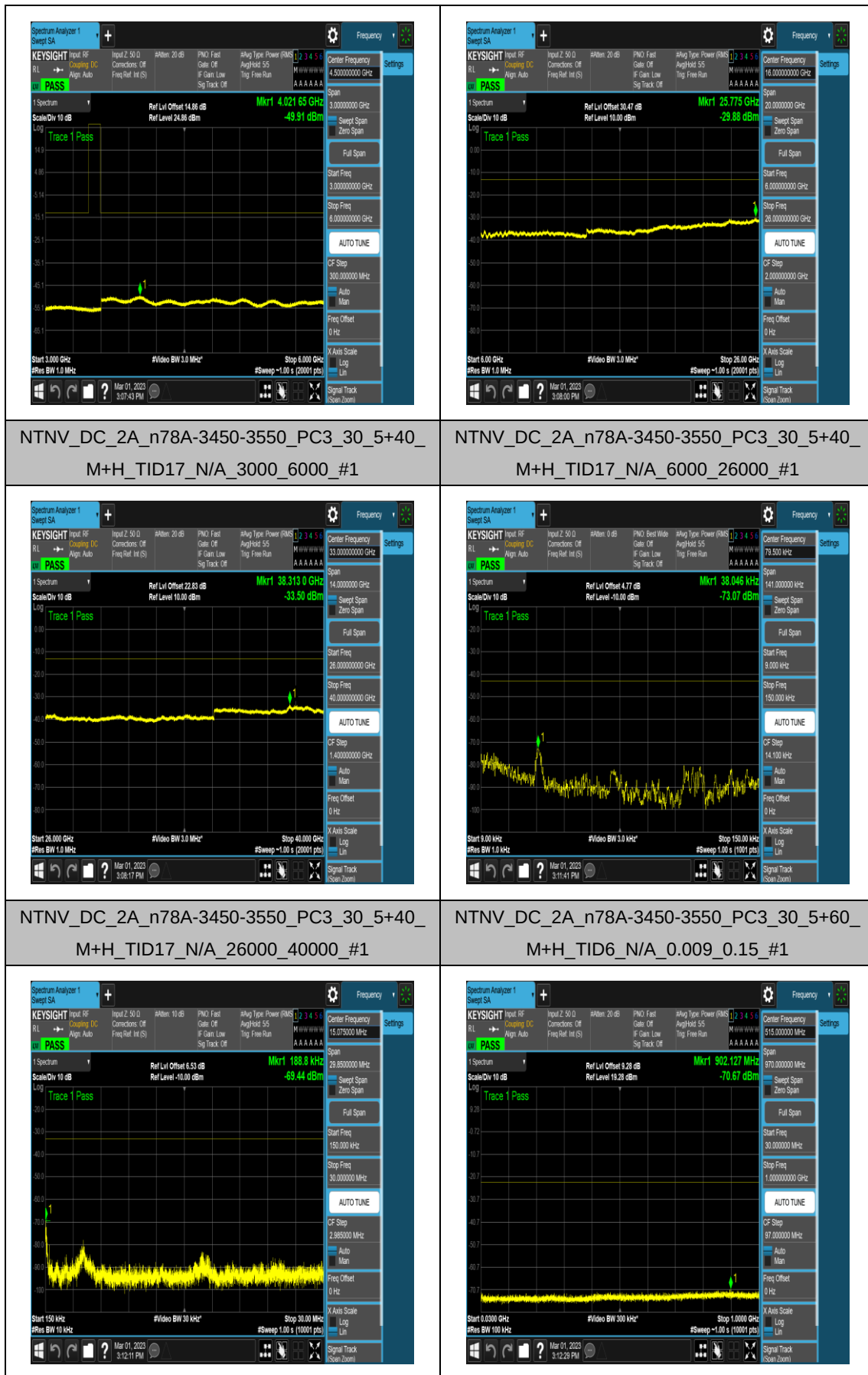
Test Result

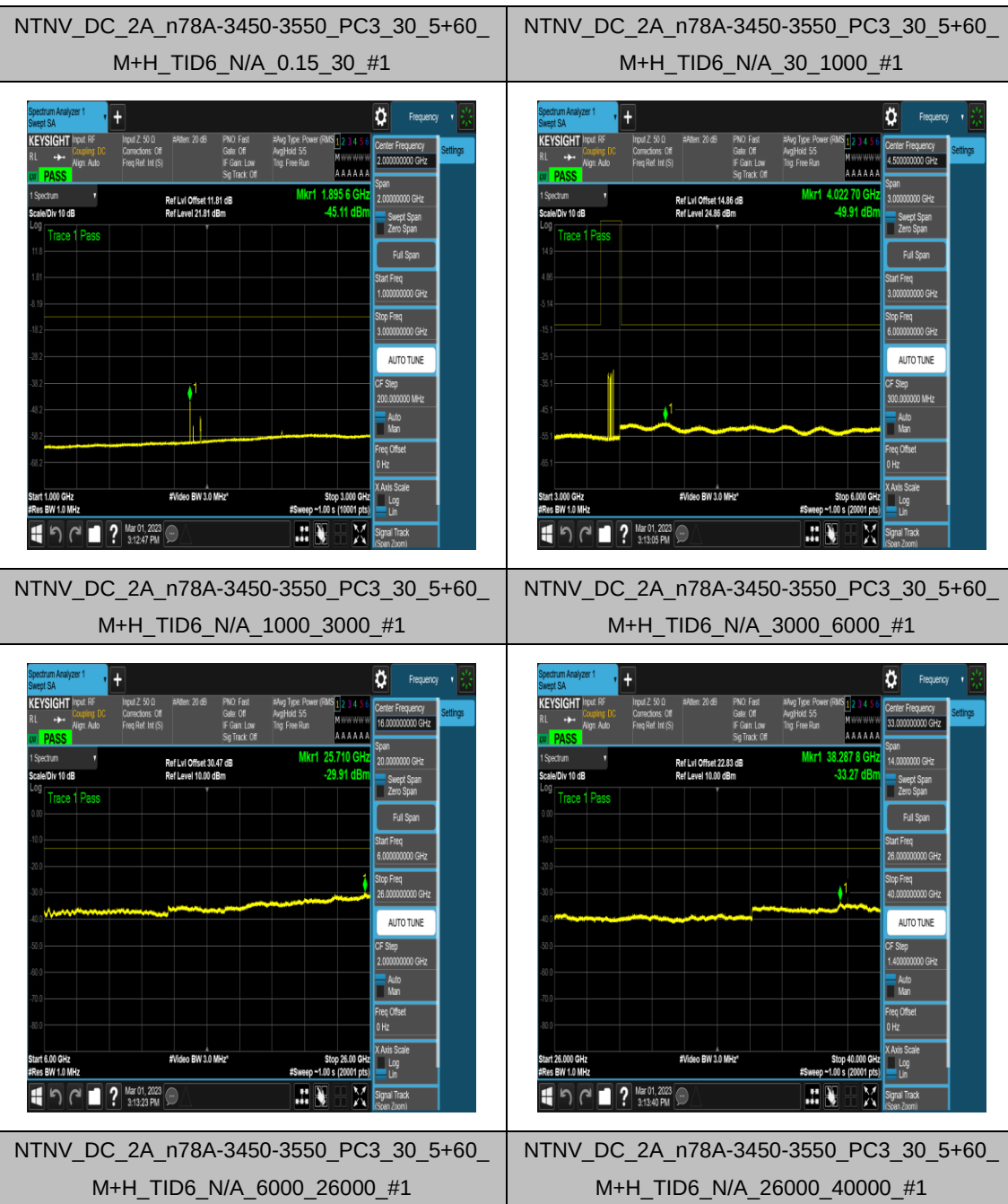
Band	SC S	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_2A_n78A- 3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-72.79	-43	PASS
DC_2A_n78A- 3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-69.67	-33	PASS
DC_2A_n78A- 3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-70.38	-23	PASS
DC_2A_n78A- 3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-35.42	-13	PASS
DC_2A_n78A- 3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.91	-13	PASS
DC_2A_n78A- 3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.88	-13	PASS
DC_2A_n78A- 3450-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.50	-13	PASS
DC_2A_n78A- 3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-73.07	-43	PASS
DC_2A_n78A- 3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	0.15	30	-69.44	-33	PASS
DC_2A_n78A- 3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	30	1000	-70.67	-23	PASS
DC_2A_n78A- 3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	1000	3000	-45.11	-13	PASS
DC_2A_n78A- 3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	3000	6000	-49.91	-13	PASS
DC_2A_n78A- 3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	6000	26000	-29.91	-13	PASS
DC_2A_n78A- 3450-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	26000	40000	-33.27	-13	PASS
DC_2A_n78A- 3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-72.84	-43	PASS
DC_2A_n78A- 3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-67.96	-33	PASS
DC_2A_n78A- 3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-70.77	-23	PASS
DC_2A_n78A- 3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-46.26	-13	PASS

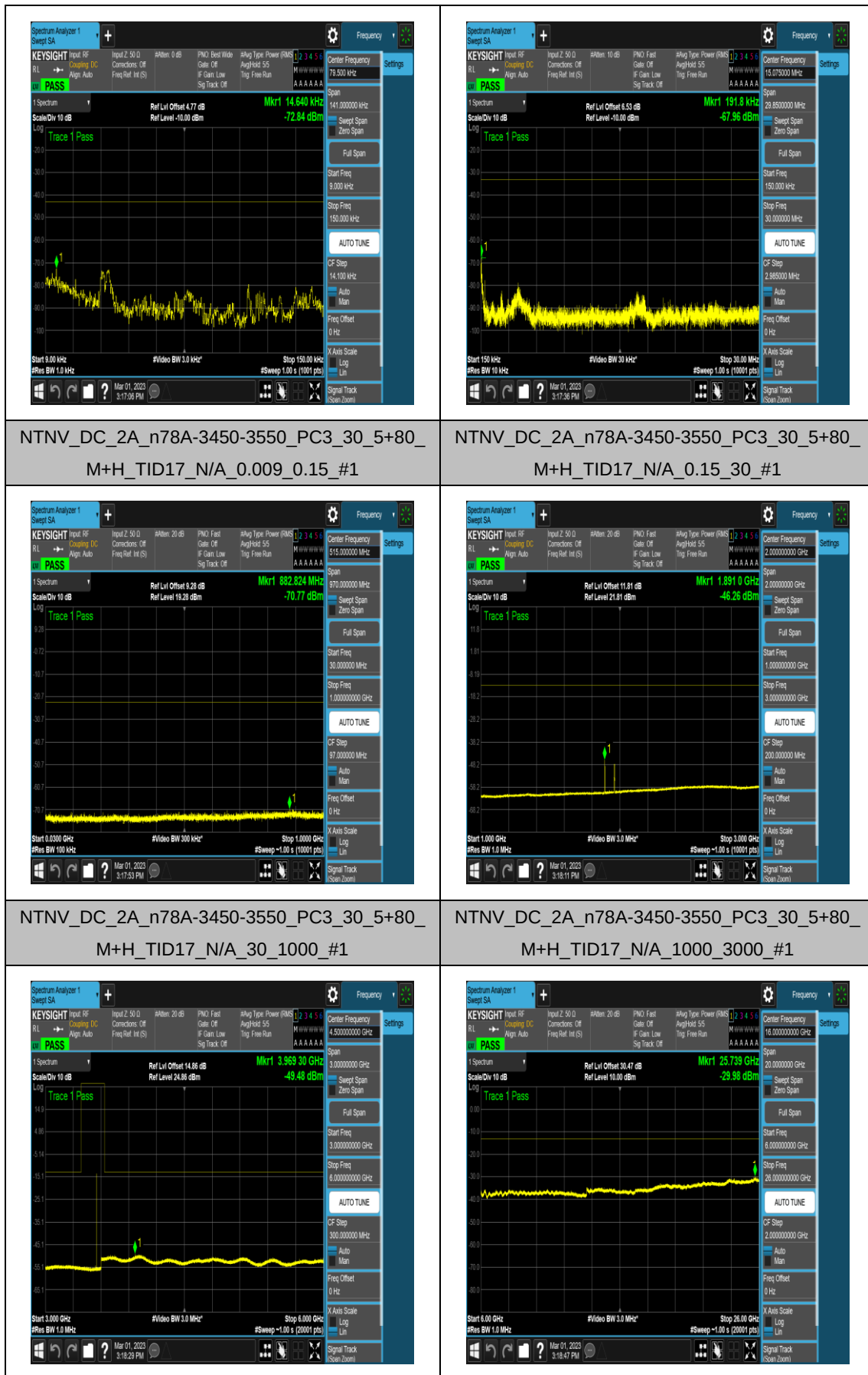
3450-3550										
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.48	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.98	-13	PASS
DC_2A_n78A-3450-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.75	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Outer_Full	0.009	0.15	-72.80	-43	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Outer_Full	0.15	30	-69.10	-33	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Outer_Full	30	1000	-69.98	-23	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Outer_Full	1000	3000	-48.90	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Outer_Full	3000	6000	-49.74	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Outer_Full	6000	26000	-30.14	-13	PASS
DC_2A_n78A-3450-3550	30	5+90	CP-QPSK	M+H	Outer_Full	26000	40000	-33.65	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-72.99	-43	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-67.44	-33	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-70.61	-23	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-48.66	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.69	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-30.20	-13	PASS
DC_2A_n78A-3450-3550	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.71	-13	PASS

Test Graphs





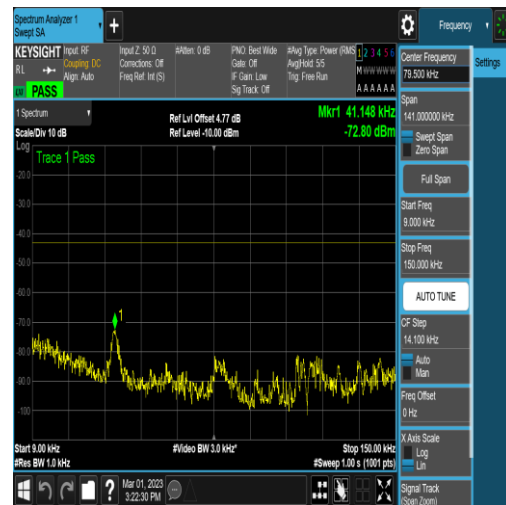




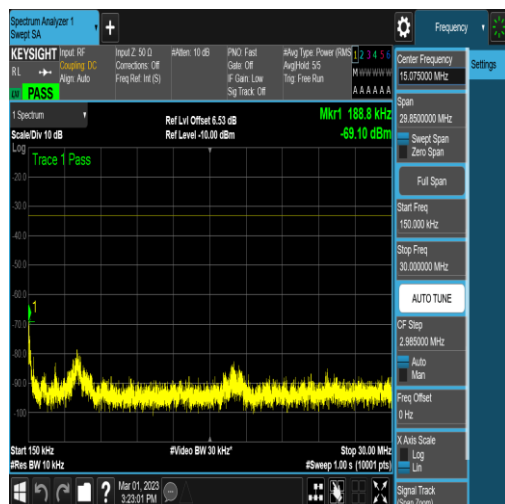
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M+H_TID17_N/A_3000_6000_#1



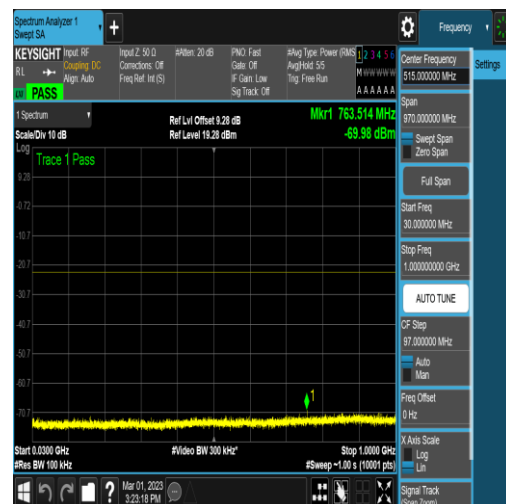
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M+H_TID18_N/A_0.009_0.15_#1

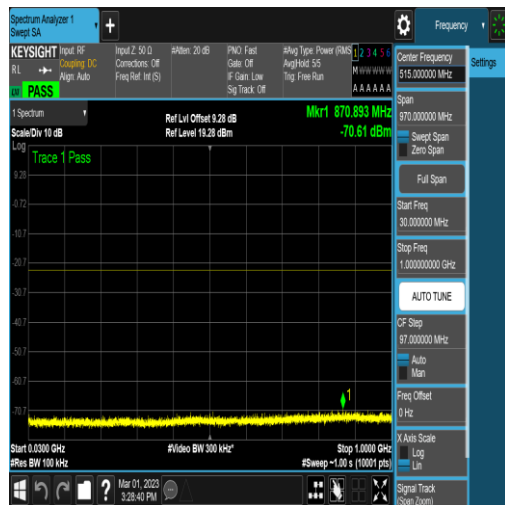


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M+H_TID18_N/A_0.15_30_#1

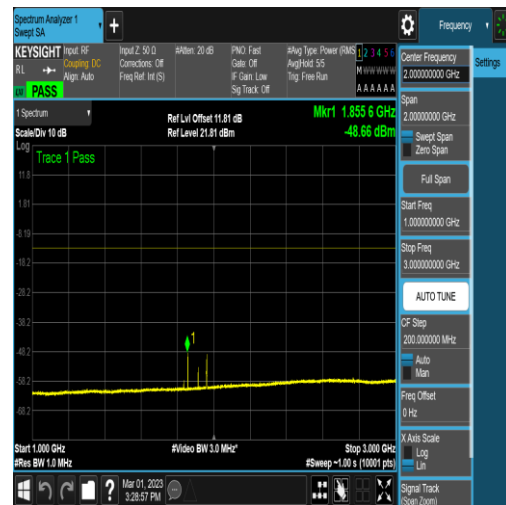
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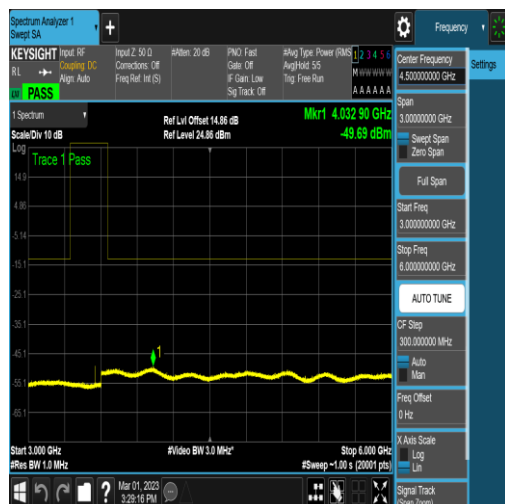
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_M+L_TID17_N/A_0.009_0.15_#1



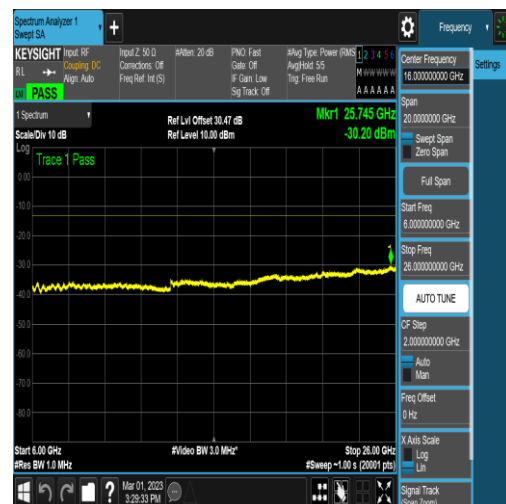
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_M+L_TID17_N/A_0.15_30_#1



NTNV_DC_2A_n78A-3450-3550_PC3_30_5+100
_M+L_TID17_N/A_30_1000_#1

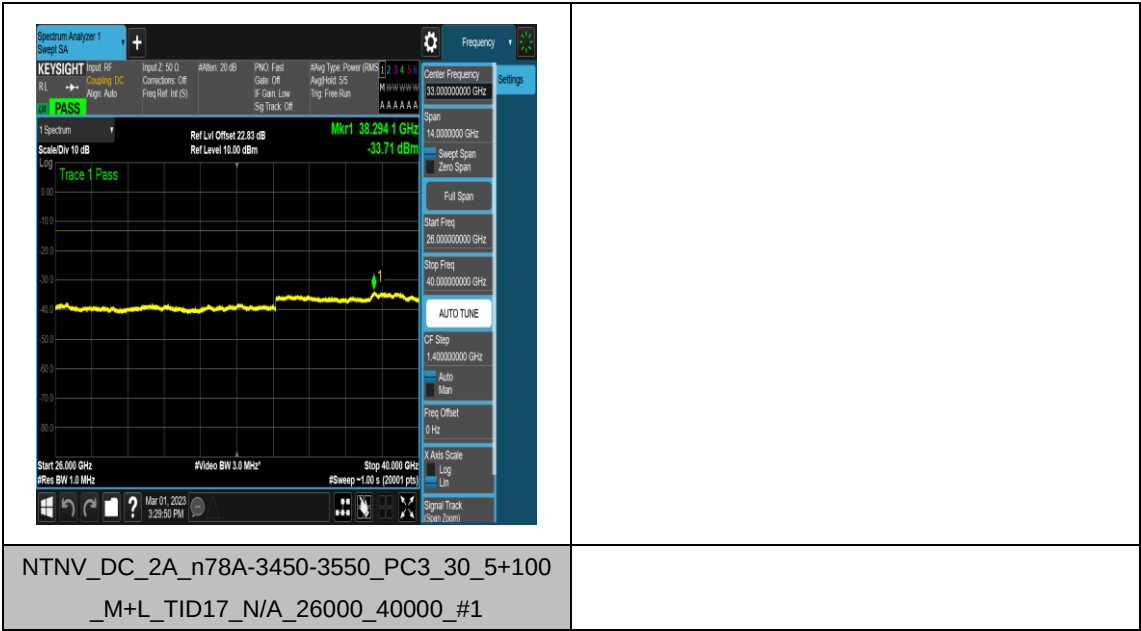


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_M+L_TID17_N/A_1000_3000_#1



NTNV_DC_2A_n78A-3450-3550_PC3_30_5+100
_M+L_TID17_N/A_3000_6000_#1

NTNV_DC_2A_n78A-3450-3550_PC3_30_5+100
_M+L_TID17_N/A_6000_26000_#1



Appendix F: Frequency Stability for NSA

Test Result

Frequency Error VS. Voltage

Voltage										
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Voltage	Temperature	Deviation(Hz)	Deviation (ppm)	Verdict
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	VN	NT	-2.55	0.00	PASS
DC_2A_n78A-34 50-3550	30	5+20	CP-QPSK	M+L	Outer_Full	VN	NT	-2.07	0.00	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	VN	NT	-3.21	0.00	PASS
DC_2A_n78A-34 50-3550	30	5+20	CP-QPSK	M+M	Outer_Full	VN	NT	-3.82	0.00	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	VN	NT	5.94	0.00	PASS
DC_2A_n78A-34 50-3550	30	5+20	CP-QPSK	M+H	Outer_Full	VN	NT	-4.00	0.00	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	VN	NT	-1.65	0.00	PASS
DC_2A_n78A-34 50-3550	30	5+40	CP-QPSK	M+L	Outer_Full	VN	NT	-7.93	0.00	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	VN	NT	-3.75	0.00	PASS
DC_2A_n78A-34 50-3550	30	5+40	CP-QPSK	M+M	Outer_Full	VN	NT	-11.65	0.00	PASS
DC_2A_n78A-34	30	5+40	DFT-QPSK	M+H	Outer_Full	VN	NT	-12.06	0.00	PASS

50-3550										
DC_2A_ n78A-34 50-3550	30	5+40	CP-QPSK	M+H	Outer_Full	VN	NT	-9.07	0.00	PASS
DC_2A_ n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	VN	NT	-8.83	0.00	PASS
DC_2A_ n78A-34 50-3550	30	5+50	CP-QPSK	M+L	Outer_Full	VN	NT	-4.50	0.00	PASS
DC_2A_ n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	VN	NT	-4.39	0.00	PASS
DC_2A_ n78A-34 50-3550	30	5+50	CP-QPSK	M+M	Outer_Full	VN	NT	3.30	0.00	PASS
DC_2A_ n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	VN	NT	-14.73	0.00	PASS
DC_2A_ n78A-34 50-3550	30	5+50	CP-QPSK	M+H	Outer_Full	VN	NT	-4.23	0.00	PASS
DC_2A_ n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	VN	NT	-0.20	0.00	PASS
DC_2A_ n78A-34 50-3550	30	5+60	CP-QPSK	M+L	Outer_Full	VN	NT	1.95	0.00	PASS
DC_2A_ n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	VN	NT	-12.97	0.00	PASS
DC_2A_ n78A-34 50-3550	30	5+60	CP-QPSK	M+M	Outer_Full	VN	NT	2.04	0.00	PASS
DC_2A_ n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	VN	NT	1.68	0.00	PASS
DC_2A_ n78A-34 50-3550	30	5+60	CP-QPSK	M+H	Outer_Full	VN	NT	7.48	0.00	PASS
DC_2A_ n78A-34	30	5+80	DFT-QPSK	M+L	Outer_Full	VN	NT	-8.99	0.00	PASS

50-3550										
DC_2A_ n78A-34 50-3550	30	5+80	CP-QPSK	M+L	Outer_Full	VN	NT	-14.20	0.00	PASS
DC_2A_ n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	VN	NT	1.78	0.00	PASS
DC_2A_ n78A-34 50-3550	30	5+80	CP-QPSK	M+M	Outer_Full	VN	NT	0.07	0.00	PASS
DC_2A_ n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	VN	NT	-18.98	-0.01	PASS
DC_2A_ n78A-34 50-3550	30	5+80	CP-QPSK	M+H	Outer_Full	VN	NT	-10.16	0.00	PASS
DC_2A_ n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	VN	NT	-5.57	0.00	PASS
DC_2A_ n78A-34 50-3550	30	5+90	CP-QPSK	M+L	Outer_Full	VN	NT	-10.07	0.00	PASS
DC_2A_ n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	VN	NT	5.27	0.00	PASS
DC_2A_ n78A-34 50-3550	30	5+90	CP-QPSK	M+M	Outer_Full	VN	NT	3.98	0.00	PASS
DC_2A_ n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	VN	NT	-4.77	0.00	PASS
DC_2A_ n78A-34 50-3550	30	5+90	CP-QPSK	M+H	Outer_Full	VN	NT	3.89	0.00	PASS
DC_2A_ n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	VN	NT	-5.04	0.00	PASS
DC_2A_ n78A-34 50-3550	30	5+100	CP-QPSK	M+L	Outer_Full	VN	NT	-0.57	0.00	PASS
DC_2A_ n78A-34	30	5+100	DFT-QPSK	M+M	Outer_Full	VN	NT	-4.29	0.00	PASS

50-3550										
DC_2A_ n78A-34 50-3550	30	5+100	CP-QPSK	M+M	Outer_Full	VN	NT	-8.00	0.00	PASS
DC_2A_ n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	VN	NT	-7.11	0.00	PASS
DC_2A_ n78A-34 50-3550	30	5+100	CP-QPSK	M+H	Outer_Full	VN	NT	-7.24	0.00	PASS

Frequency Error VS. Temperature

Voltage										
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Voltage	Temperature	Deviation(Hz)	Deviation (ppm)	Verdict
DC_2A_ n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	NV	TN	-7.839239	-0.002266	PASS
DC_2A_ n78A-34 50-3550	30	5+20	CP-QPSK	M+L	Outer_Full	NV	TN	-10.382575	-0.003001	PASS
DC_2A_ n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	NV	TN	-8.664624	-0.002476	PASS
DC_2A_ n78A-34 50-3550	30	5+20	CP-QPSK	M+M	Outer_Full	NV	TN	-11.182175	-0.003195	PASS
DC_2A_ n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	NV	TN	-14.089616	-0.003980	PASS
DC_2A_ n78A-34 50-3550	30	5+20	CP-QPSK	M+H	Outer_Full	NV	TN	-9.319066	-0.002633	PASS
DC_2A_ n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	NV	TN	-4.704968	-0.001356	PASS
DC_2A_ n78A-34 50-3550	30	5+40	CP-QPSK	M+L	Outer_Full	NV	TN	3.013869	0.000869	PASS
DC_2A_ n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	NV	TN	-6.744492	-0.001927	PASS

DC_2A_ n78A-34 50-3550	30	5+40	CP-QPSK	M+M	Outer_Full	NV	TN	-5.665034	-0.001619	PASS
DC_2A_ n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	NV	TN	-9.478163	-0.002685	PASS
DC_2A_ n78A-34 50-3550	30	5+40	CP-QPSK	M+H	Outer_Full	NV	TN	-10.753030	-0.003046	PASS
DC_2A_ n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	NV	TN	-7.118439	-0.002048	PASS
DC_2A_ n78A-34 50-3550	30	5+50	CP-QPSK	M+L	Outer_Full	NV	TN	-20.992102	-0.006041	PASS
DC_2A_ n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	NV	TN	-2.614294	-0.000747	PASS
DC_2A_ n78A-34 50-3550	30	5+50	CP-QPSK	M+M	Outer_Full	NV	TN	-16.239180	-0.004640	PASS
DC_2A_ n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	NV	TN	-14.594136	-0.004140	PASS
DC_2A_ n78A-34 50-3550	30	5+50	CP-QPSK	M+H	Outer_Full	NV	TN	0.108794	0.000031	PASS
DC_2A_ n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	NV	TN	-8.857456	-0.002545	PASS
DC_2A_ n78A-34 50-3550	30	5+60	CP-QPSK	M+L	Outer_Full	NV	TN	9.666872	0.002778	PASS
DC_2A_ n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	NV	TN	0.615448	0.000176	PASS
DC_2A_ n78A-34 50-3550	30	5+60	CP-QPSK	M+M	Outer_Full	NV	TN	-9.308449	-0.002660	PASS
DC_2A_ n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	NV	TN	-5.948653	-0.001690	PASS

DC_2A_ n78A-34 50-3550	30	5+60	CP-QPSK	M+H	Outer_Full	NV	TN	5.936722	0.001687	PASS
DC_2A_ n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Outer_Full	NV	TN	1.795246	0.000514	PASS
DC_2A_ n78A-34 50-3550	30	5+80	CP-QPSK	M+L	Outer_Full	NV	TN	-20.100582	-0.005759	PASS
DC_2A_ n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	NV	TN	-1.760823	-0.000503	PASS
DC_2A_ n78A-34 50-3550	30	5+80	CP-QPSK	M+M	Outer_Full	NV	TN	7.799660	0.002228	PASS
DC_2A_ n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	NV	TN	-2.044207	-0.000582	PASS
DC_2A_ n78A-34 50-3550	30	5+80	CP-QPSK	M+H	Outer_Full	NV	TN	4.313148	0.001229	PASS
DC_2A_ n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	NV	TN	-6.463716	-0.001849	PASS
DC_2A_ n78A-34 50-3550	30	5+90	CP-QPSK	M+L	Outer_Full	NV	TN	-6.995545	-0.002002	PASS
DC_2A_ n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	NV	TN	-6.001614	-0.001715	PASS
DC_2A_ n78A-34 50-3550	30	5+90	CP-QPSK	M+M	Outer_Full	NV	TN	1.970174	0.000563	PASS
DC_2A_ n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	NV	TN	0.549674	0.000157	PASS
DC_2A_ n78A-34 50-3550	30	5+90	CP-QPSK	M+H	Outer_Full	NV	TN	-8.458356	-0.002413	PASS
DC_2A_ n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	NV	TN	4.054958	0.001159	PASS

DC_2A_ n78A-34 50-3550	30	5+100	CP-QPSK	M+L	Outer_Full	NV	TN	-2.459742	-0.000703	PASS
DC_2A_ n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	NV	TN	-5.221427	-0.001492	PASS
DC_2A_ n78A-34 50-3550	30	5+100	CP-QPSK	M+M	Outer_Full	NV	TN	6.922469	0.001978	PASS
DC_2A_ n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	NV	TN	2.279754	0.000651	PASS
DC_2A_ n78A-34 50-3550	30	5+100	CP-QPSK	M+H	Outer_Full	NV	TN	-7.286384	-0.002082	PASS

Appendix F: Modulation characteristics for NSA

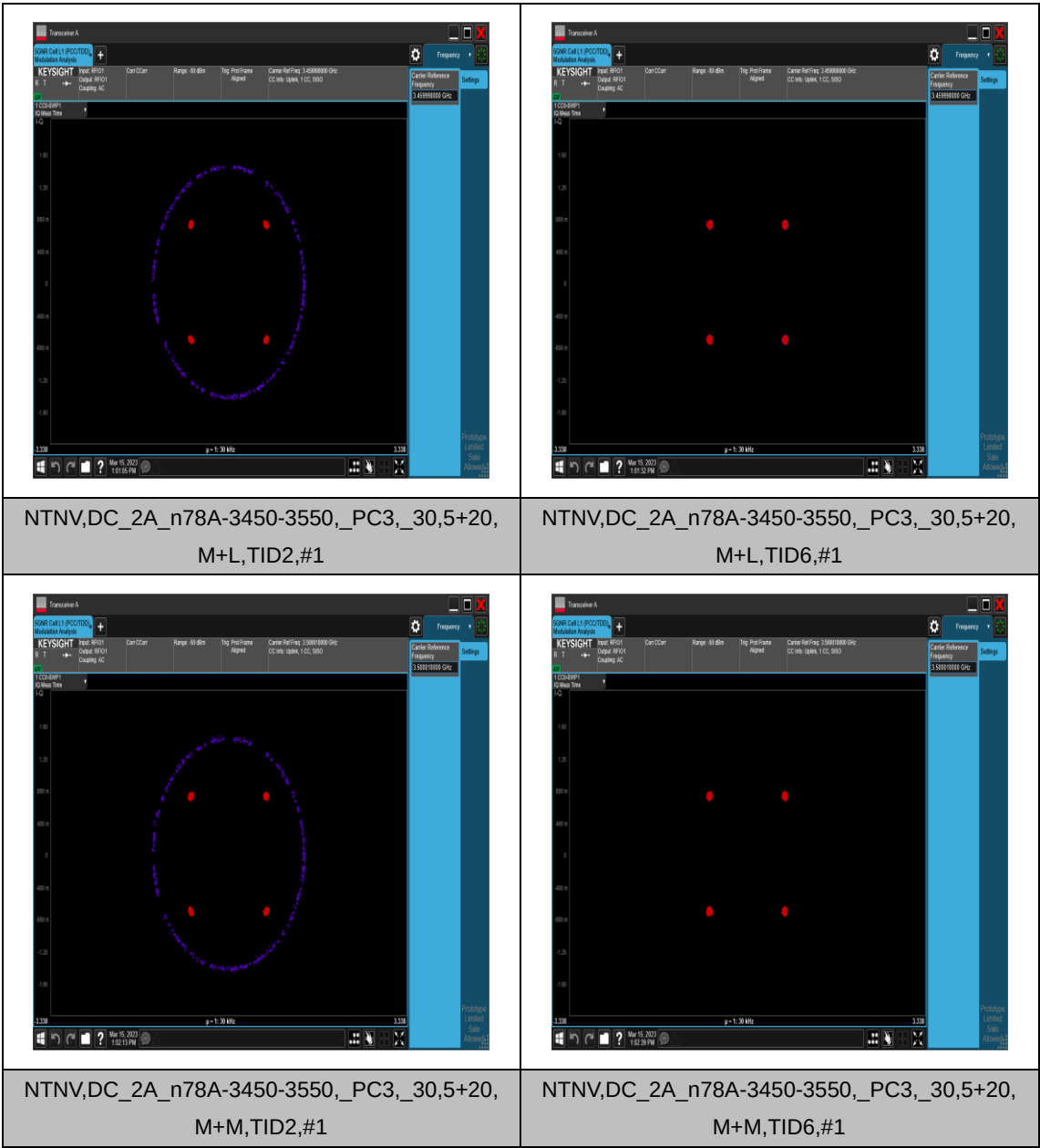
Test Result

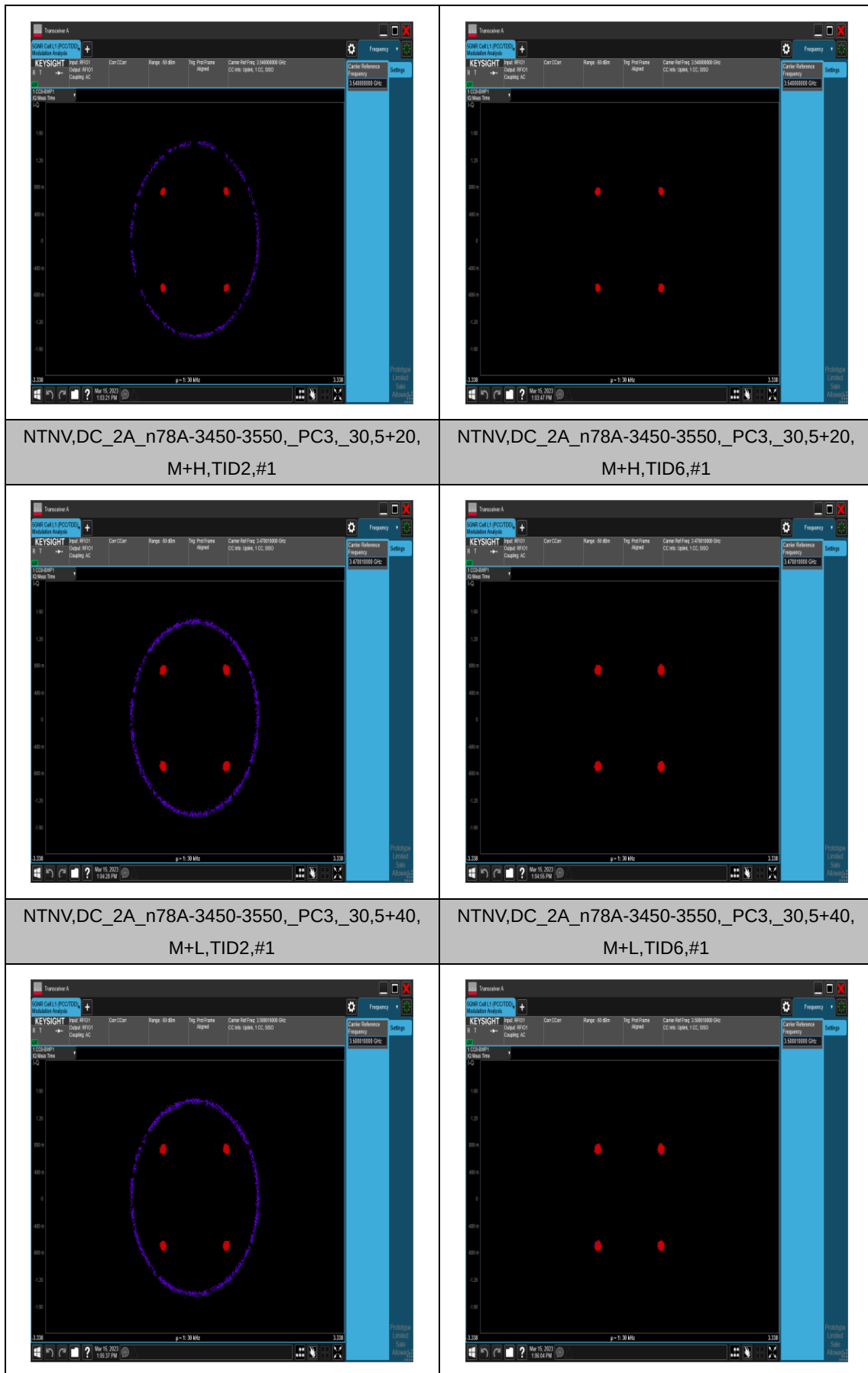
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Verdict
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+M	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3550	30	5+20	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+M	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3550	30	5+40	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+M	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3550	30	5+50	CP-QPSK	M+H	Outer_Full	see graph	PASS

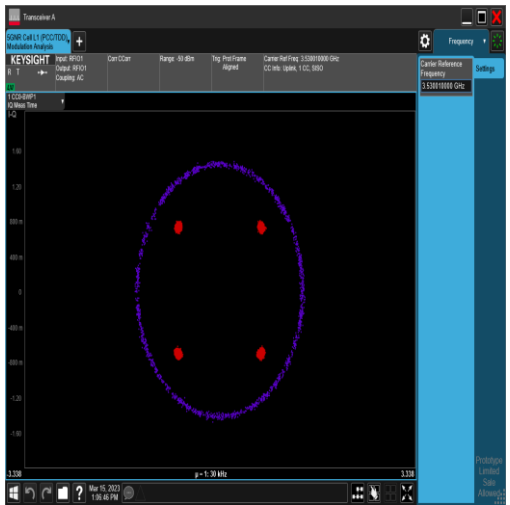
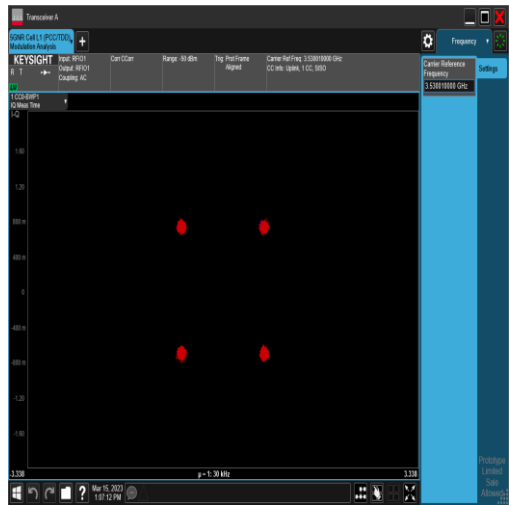
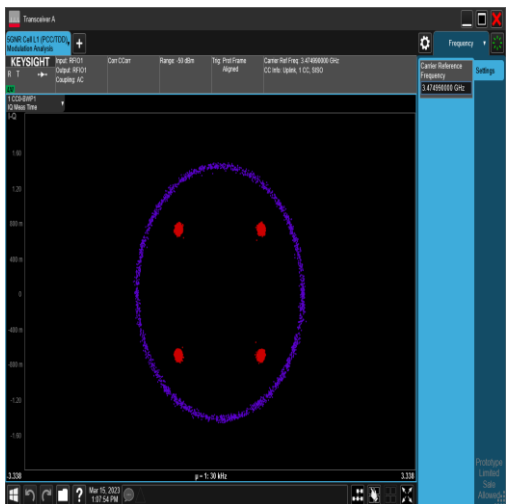
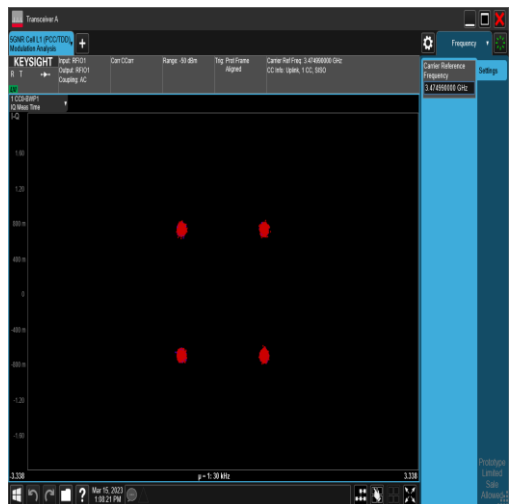
DC_2A_n78A-3450-3 550	30	5+60	DFT-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3 550	30	5+60	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3 550	30	5+60	DFT-QPSK	M+M	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3 550	30	5+60	CP-QPSK	M+M	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3 550	30	5+60	DFT-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3 550	30	5+60	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3 550	30	5+80	DFT-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3 550	30	5+80	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3 550	30	5+80	DFT-QPSK	M+M	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3 550	30	5+80	CP-QPSK	M+M	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3 550	30	5+80	DFT-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3 550	30	5+80	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3 550	30	5+90	DFT-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3 550	30	5+90	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3 550	30	5+90	DFT-QPSK	M+M	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3 550	30	5+90	CP-QPSK	M+M	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3 550	30	5+90	DFT-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3 550	30	5+90	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3 550	30	5+100	DFT-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3 550	30	5+100	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3 550	30	5+100	DFT-QPSK	M+M	Outer_Full	see graph	PASS

DC_2A_n78A-3450-3 550	30	5+100	CP-QPSK	M+M	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3 550	30	5+100	DFT-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3450-3 550	30	5+100	CP-QPSK	M+H	Outer_Full	see graph	PASS

Test Graphs





NTNV,DC_2A_n78A-3450-3550,_PC3,_30,5+40, M+M,TID2,#1	NTNV,DC_2A_n78A-3450-3550,_PC3,_30,5+40, M+M,TID6,#1
	
NTNV,DC_2A_n78A-3450-3550,_PC3,_30,5+40, M+H,TID2,#1	NTNV,DC_2A_n78A-3450-3550,_PC3,_30,5+40, M+H,TID6,#1
	
NTNV,DC_2A_n78A-3450-3550,_PC3,_30,5+50, M+L,TID2,#1	NTNV,DC_2A_n78A-3450-3550,_PC3,_30,5+50, M+L,TID6,#1