

Appendix for NSA 2A_N78A-3450-3550MHz

Appendix A: Average Power Output Data for NSA

Test Result

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	24.20	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	24.38	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Outer_Full	24.82	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+L	Inner_Full	24.61	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	23.98	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	24.24	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	CP-QPSK	M+L	Outer_Full	24.65	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	CP-QPSK	M+L	Inner_Full	24.83	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	24.62	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	23.62	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Outer_Full	24.51	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+M	Inner_Full	24.60	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	24.40	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	23.69	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	CP-QPSK	M+M	Outer_Full	24.55	PC3	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+20	CP-QPSK	M+M	Inner_Full	24.52	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	23.67	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	23.07	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Outer_Full	23.62	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	DFT-QPSK	M+H	Inner_Full	23.55	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	23.61	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	22.83	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	CP-QPSK	M+H	Outer_Full	23.63	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+20	CP-QPSK	M+H	Inner_Full	20.76	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	24.35	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	24.72	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Outer_Full	24.50	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+L	Inner_Full	24.8	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	24.15	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	24.56	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	CP-QPSK	M+L	Outer_Full	24.67	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	CP-QPSK	M+L	Inner_Full	19.16	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	24.66	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	24.15	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+M	Outer_Full	24.77	PC3	PASS
DC_2A_n78A-34	30	5+40	DFT-QPSK	M+M	Inner_Full	24.77	PC3	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	24.47	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	23.85	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	CP-QPSK	M+M	Outer_Full	24.72	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	CP-QPSK	M+M	Inner_Full	24.69	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	23.71	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	23.09	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Outer_Full	24.18	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	DFT-QPSK	M+H	Inner_Full	24.29	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	23.59	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	23.01	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	CP-QPSK	M+H	Outer_Full	24.05	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+40	CP-QPSK	M+H	Inner_Full	24.21	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	24.01	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	23.95	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Outer_Full	24.81	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+L	Inner_Full	24.84	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	23.84	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	24.07	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	CP-QPSK	M+L	Outer_Full	24.76	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	CP-QPSK	M+L	Inner_Full	24.81	PC3	PASS
DC_2A_n78A-34	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	24.13	PC3	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	23.96	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Outer_Full	24.48	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+M	Inner_Full	24.64	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	23.89	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	23.75	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	CP-QPSK	M+M	Outer_Full	24.55	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	CP-QPSK	M+M	Inner_Full	24.48	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	23.80	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	22.89	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Outer_Full	23.95	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	DFT-QPSK	M+H	Inner_Full	24.13	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	23.99	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	22.85	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	CP-QPSK	M+H	Outer_Full	23.93	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+50	CP-QPSK	M+H	Inner_Full	24.18	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	24.07	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	23.34	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Outer_Full	24.70	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+L	Inner_Full	24.66	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	24.25	PC3	PASS
DC_2A_n78A-34	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	23.17	PC3	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+60	CP-QPSK	M+L	Outer_Full	24.53	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	CP-QPSK	M+L	Inner_Full	24.81	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	24.23	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	23.68	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Outer_Full	24.46	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+M	Inner_Full	24.50	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	24.30	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	23.78	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	CP-QPSK	M+M	Outer_Full	24.43	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	CP-QPSK	M+M	Inner_Full	24.50	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	24.45	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	22.84	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Outer_Full	24.05	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	DFT-QPSK	M+H	Inner_Full	24.13	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	24.48	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	22.75	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	CP-QPSK	M+H	Outer_Full	24.04	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+60	CP-QPSK	M+H	Inner_Full	24.19	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	23.88	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	23.51	PC3	PASS
DC_2A_n78A-34	30	5+80	DFT-QPSK	M+L	Outer_Full	24.36	PC3	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+L	Inner_Full	24.50	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	23.72	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	23.38	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	CP-QPSK	M+L	Outer_Full	24.34	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	CP-QPSK	M+L	Inner_Full	24.39	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	24.27	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	22.74	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Outer_Full	24.24	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+M	Inner_Full	24.42	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	24.10	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	21.08	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	CP-QPSK	M+M	Outer_Full	24.32	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	CP-QPSK	M+M	Inner_Full	24.20	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	23.95	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	22.83	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Outer_Full	24.02	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	DFT-QPSK	M+H	Inner_Full	24.13	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	24.18	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	22.86	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+80	CP-QPSK	M+H	Outer_Full	24.08	PC3	PASS
DC_2A_n78A-34	30	5+80	CP-QPSK	M+H	Inner_Full	24.14	PC3	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	23.84	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	22.79	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Outer_Full	24.35	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+L	Inner_Full	24.34	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	23.62	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	22.65	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	CP-QPSK	M+L	Outer_Full	24.33	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	CP-QPSK	M+L	Inner_Full	24.44	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	24.12	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	22.63	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Outer_Full	24.31	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+M	Inner_Full	24.32	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	24.04	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	22.77	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	CP-QPSK	M+M	Outer_Full	24.17	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	CP-QPSK	M+M	Inner_Full	24.29	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	24.21	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	22.75	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Outer_Full	24.10	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	DFT-QPSK	M+H	Inner_Full	24.26	PC3	PASS
DC_2A_n78A-34	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	24.46	PC3	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	22.93	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	CP-QPSK	M+H	Outer_Full	24.12	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+90	CP-QPSK	M+H	Inner_Full	24.21	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	23.59	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	22.84	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Outer_Full	24.19	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+L	Inner_Full	24.86	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	23.85	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	CP-QPSK	M+L	Edge_1RB_Right	22.82	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	CP-QPSK	M+L	Outer_Full	24.09	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	CP-QPSK	M+L	Inner_Full	24.33	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	23.64	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	22.62	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Outer_Full	24.18	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+M	Inner_Full	24.61	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	CP-QPSK	M+M	Edge_1RB_Left	23.47	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	CP-QPSK	M+M	Edge_1RB_Right	22.86	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	CP-QPSK	M+M	Outer_Full	24.19	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	CP-QPSK	M+M	Inner_Full	24.34	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	22.49	PC3	PASS
DC_2A_n78A-34	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	22.61	PC3	PASS

50-3550								
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Outer_Full	24.19	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	DFT-QPSK	M+H	Inner_Full	24.71	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	CP-QPSK	M+H	Edge_1RB_Left	23.47	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	22.59	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	CP-QPSK	M+H	Outer_Full	24.09	PC3	PASS
DC_2A_n78A-34 50-3550	30	5+100	CP-QPSK	M+H	Inner_Full	24.32	PC3	PASS

Appendix B: Peak-to-Average Ratio for NSA

Peak-to-Average Ratio(CCDF)

Test Result

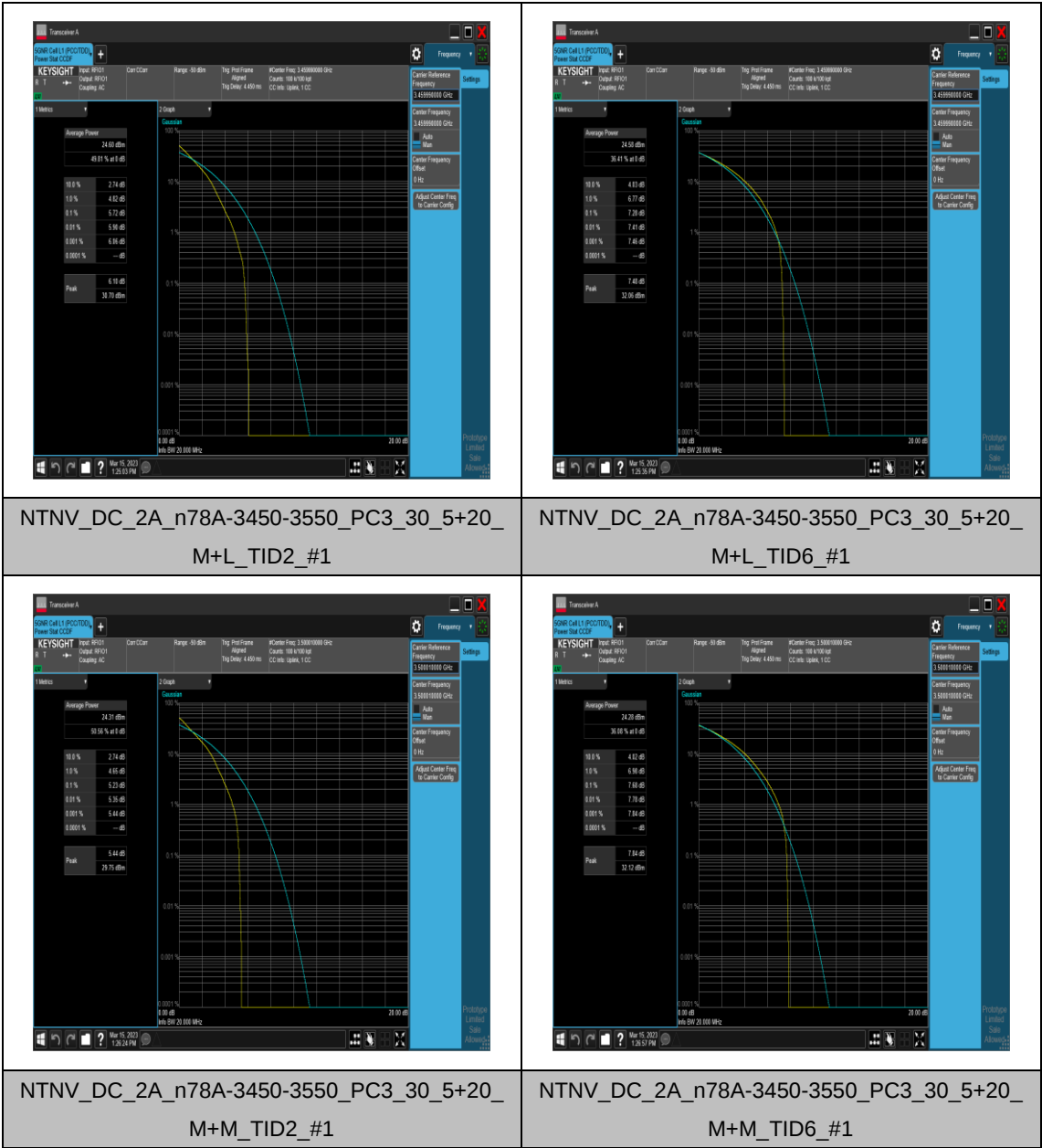
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_2A_n7 8A-3450-35 50	30	5+20	DFT-QPSK	M+L	Outer_Full	5.72	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+20	CP-QPSK	M+L	Outer_Full	7.28	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+20	DFT-QPSK	M+M	Outer_Full	5.23	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+20	CP-QPSK	M+M	Outer_Full	7.68	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+20	DFT-QPSK	M+H	Outer_Full	5.35	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+20	CP-QPSK	M+H	Outer_Full	7.76	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+40	DFT-QPSK	M+L	Outer_Full	5.81	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+40	CP-QPSK	M+L	Outer_Full	7.06	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+40	DFT-QPSK	M+M	Outer_Full	5.36	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+40	CP-QPSK	M+M	Outer_Full	7.29	≤13	PASS
DC_2A_n7 8A-3450-35	30	5+40	DFT-QPSK	M+H	Outer_Full	5.54	≤13	PASS

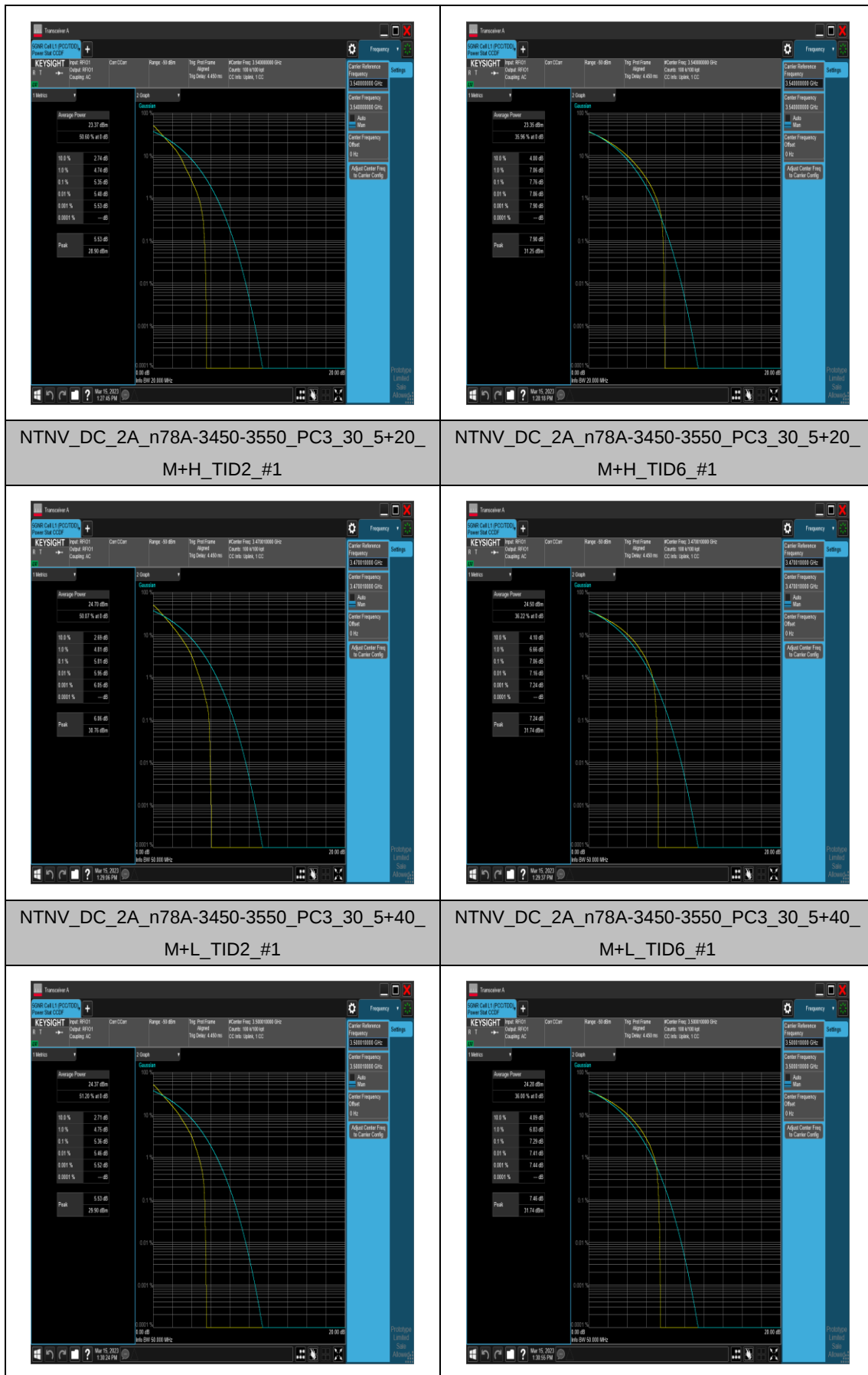
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DC_2A_n7 8A-3450-35 50	30	5+40	CP-QPSK	M+H	Outer_Full	7.50	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+50	DFT-QPSK	M+L	Outer_Full	5.35	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+50	CP-QPSK	M+L	Outer_Full	7.15	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+50	DFT-QPSK	M+M	Outer_Full	5.54	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+50	CP-QPSK	M+M	Outer_Full	7.33	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+50	DFT-QPSK	M+H	Outer_Full	5.25	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+50	CP-QPSK	M+H	Outer_Full	7.61	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+60	DFT-QPSK	M+L	Outer_Full	5.36	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+60	CP-QPSK	M+L	Outer_Full	7.31	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+60	DFT-QPSK	M+M	Outer_Full	5.55	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+60	CP-QPSK	M+M	Outer_Full	7.37	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+60	DFT-QPSK	M+H	Outer_Full	5.70	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+60	CP-QPSK	M+H	Outer_Full	7.49	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+80	DFT-QPSK	M+L	Outer_Full	5.59	≤13	PASS

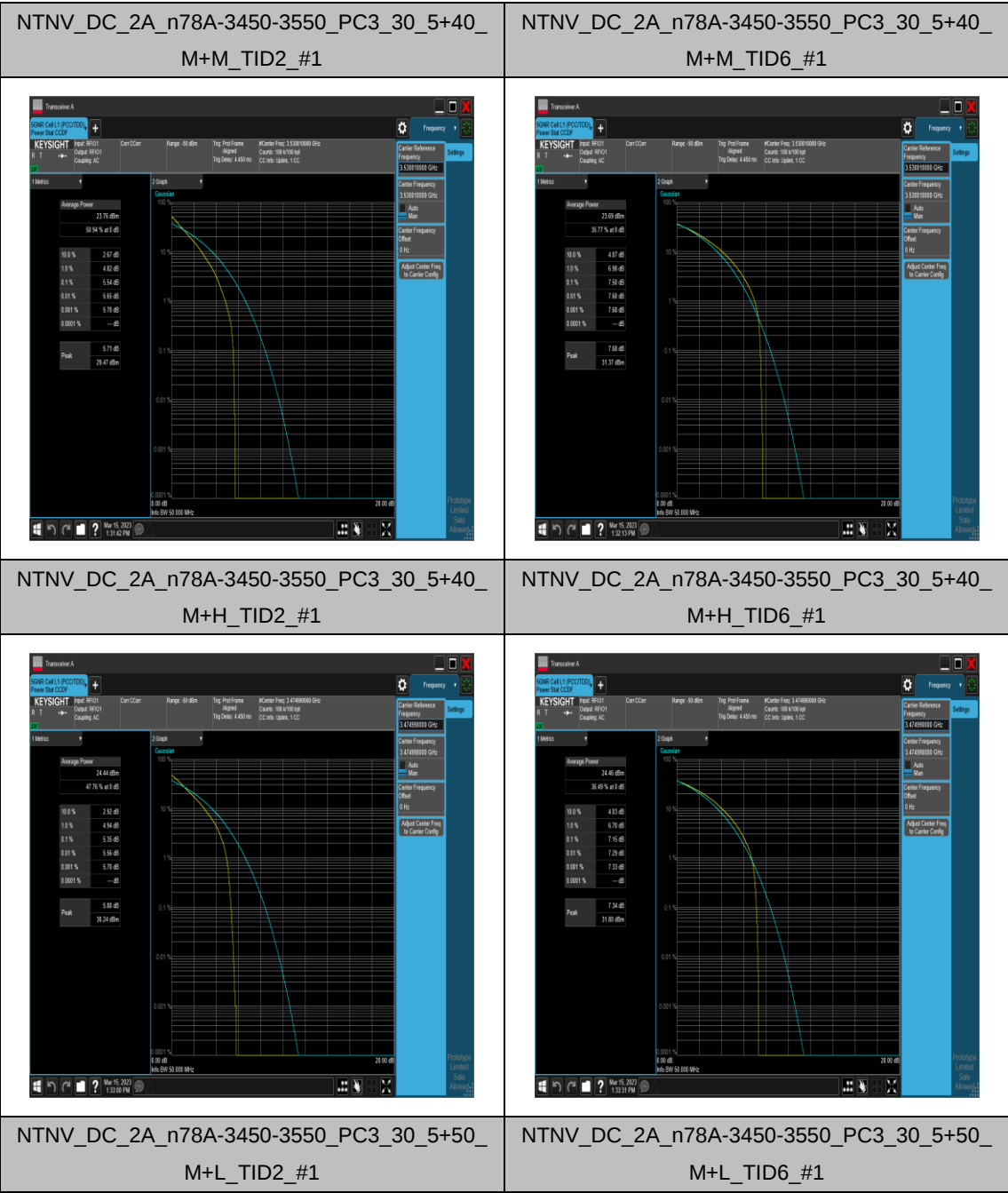
DC_2A_n7 8A-3450-35 50	30	5+80	CP-QPSK	M+L	Outer_Full	7.45	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+80	DFT-QPSK	M+M	Outer_Full	5.75	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+80	CP-QPSK	M+M	Outer_Full	7.61	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+80	DFT-QPSK	M+H	Outer_Full	5.40	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+80	CP-QPSK	M+H	Outer_Full	7.77	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+90	DFT-QPSK	M+L	Outer_Full	5.64	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+90	CP-QPSK	M+L	Outer_Full	7.61	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+90	DFT-QPSK	M+M	Outer_Full	5.63	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+90	CP-QPSK	M+M	Outer_Full	7.57	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+90	DFT-QPSK	M+H	Outer_Full	5.18	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+90	CP-QPSK	M+H	Outer_Full	7.73	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+100	DFT-QPSK	M+L	Outer_Full	5.62	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+100	CP-QPSK	M+L	Outer_Full	7.67	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+100	DFT-QPSK	M+M	Outer_Full	5.60	≤13	PASS
DC_2A_n7	30	5+100	CP-QPSK	M+M	Outer_Full	7.68	≤13	PASS

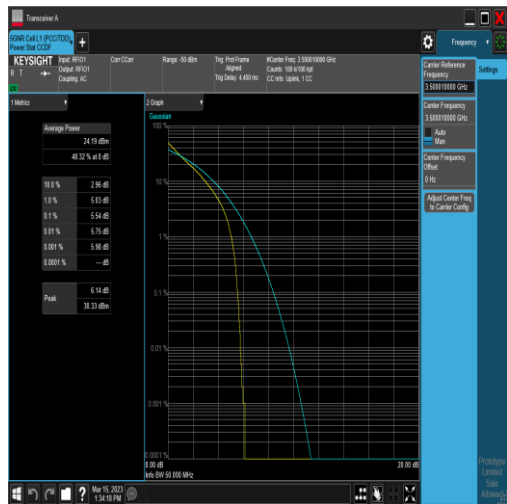
8A-3450-35 50								
DC_2A_n7 8A-3450-35 50	30	5+100	DFT-QPSK	M+H	Outer_Full	5.62	≤13	PASS
DC_2A_n7 8A-3450-35 50	30	5+100	CP-QPSK	M+H	Outer_Full	7.68	≤13	PASS

Test Graphs

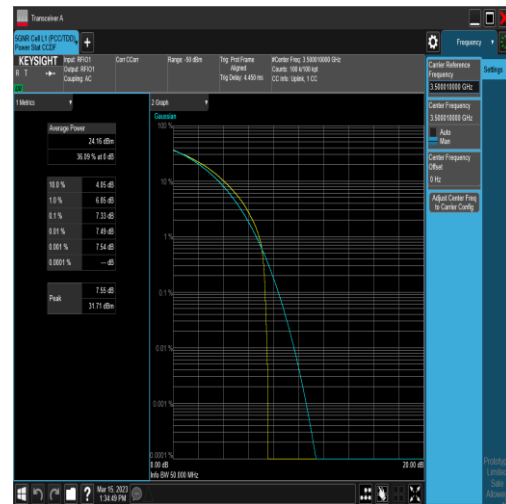




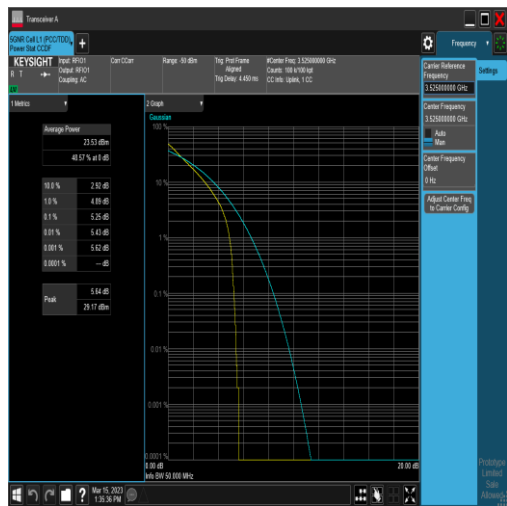




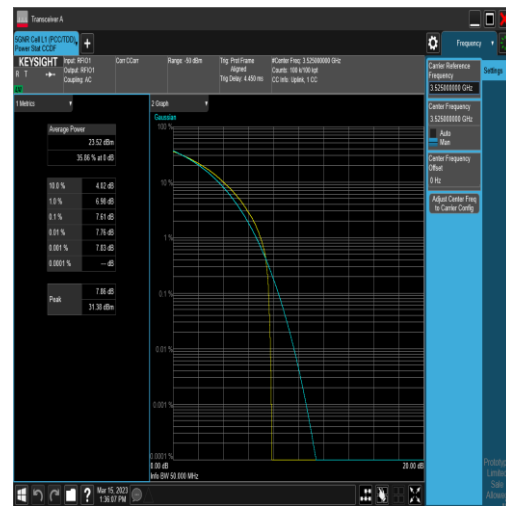
NTNV_DC_2A_n78A-3450-3550_PC3_30_5+50_
M+M_TID2_#1



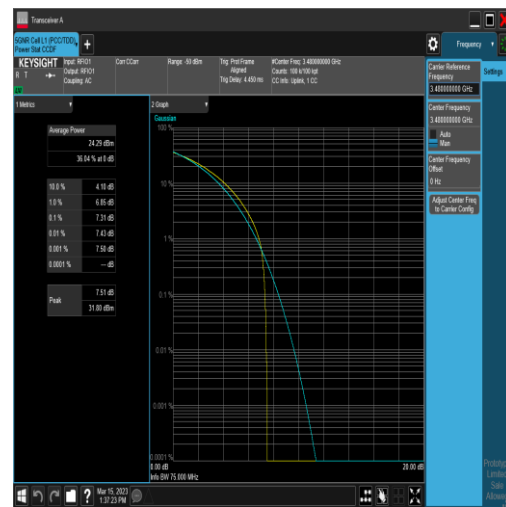
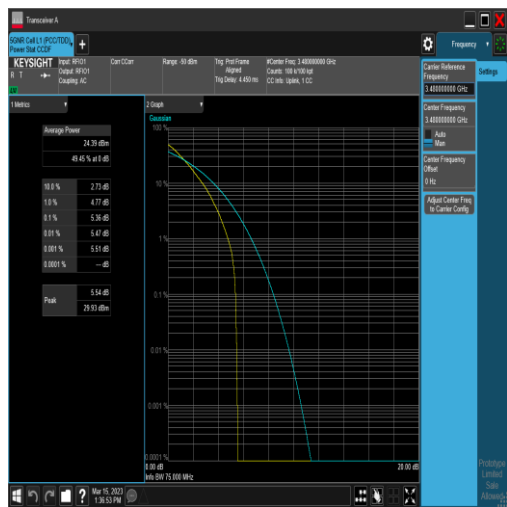
NTNV_DC_2A_n78A-3450-3550_PC3_30_5+50_
M+M_TID6_#1

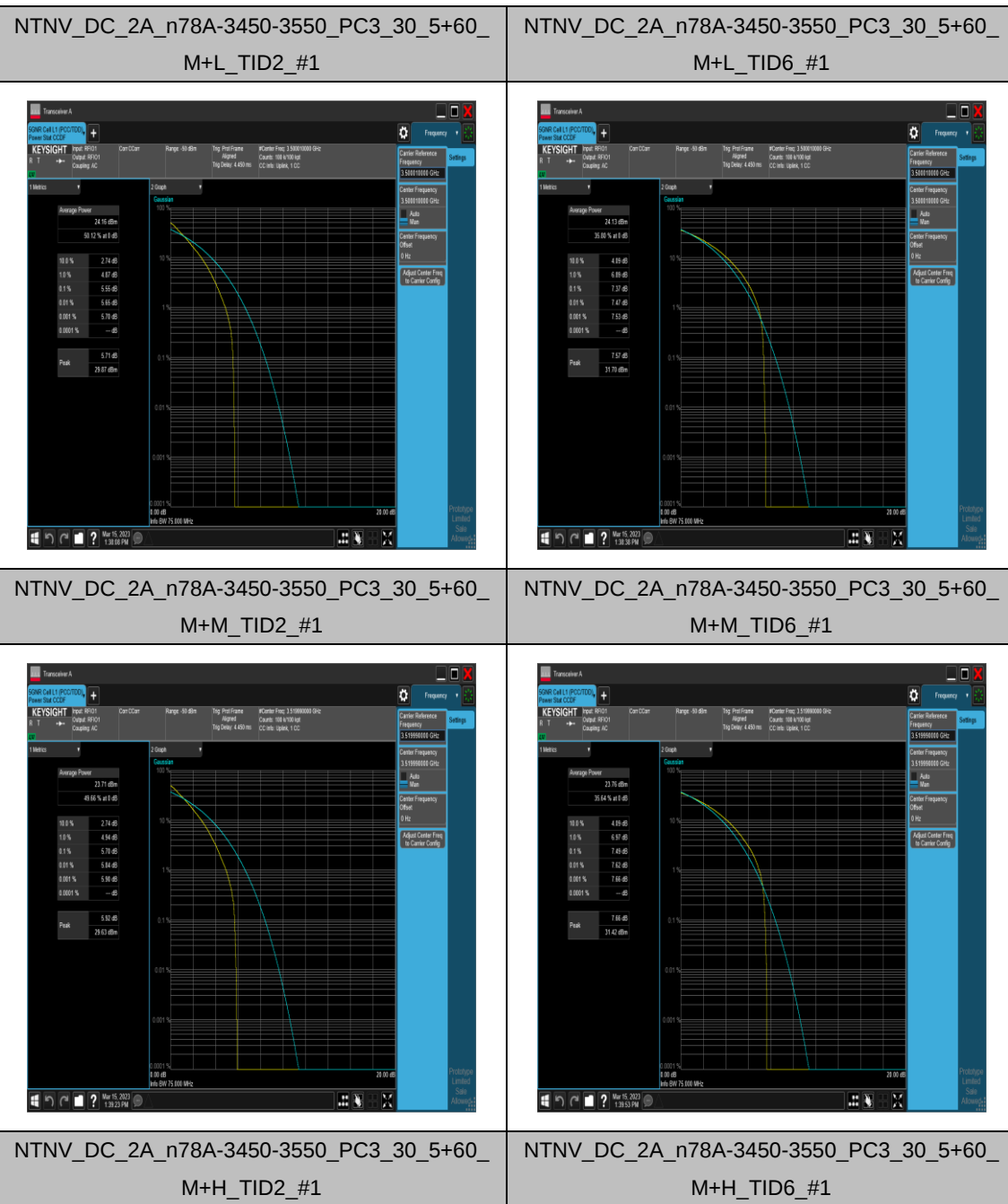


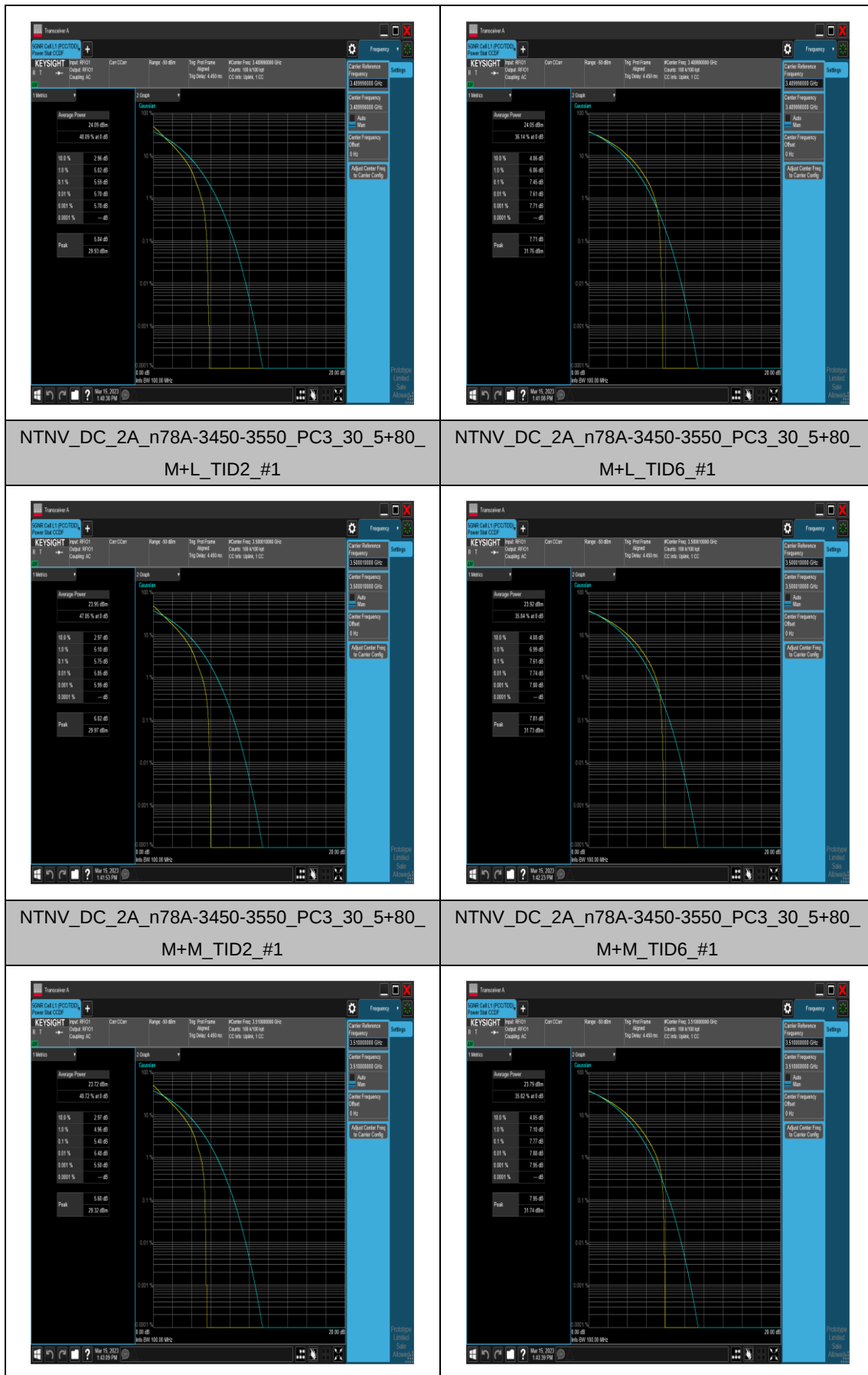
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M+H_TID2_#1

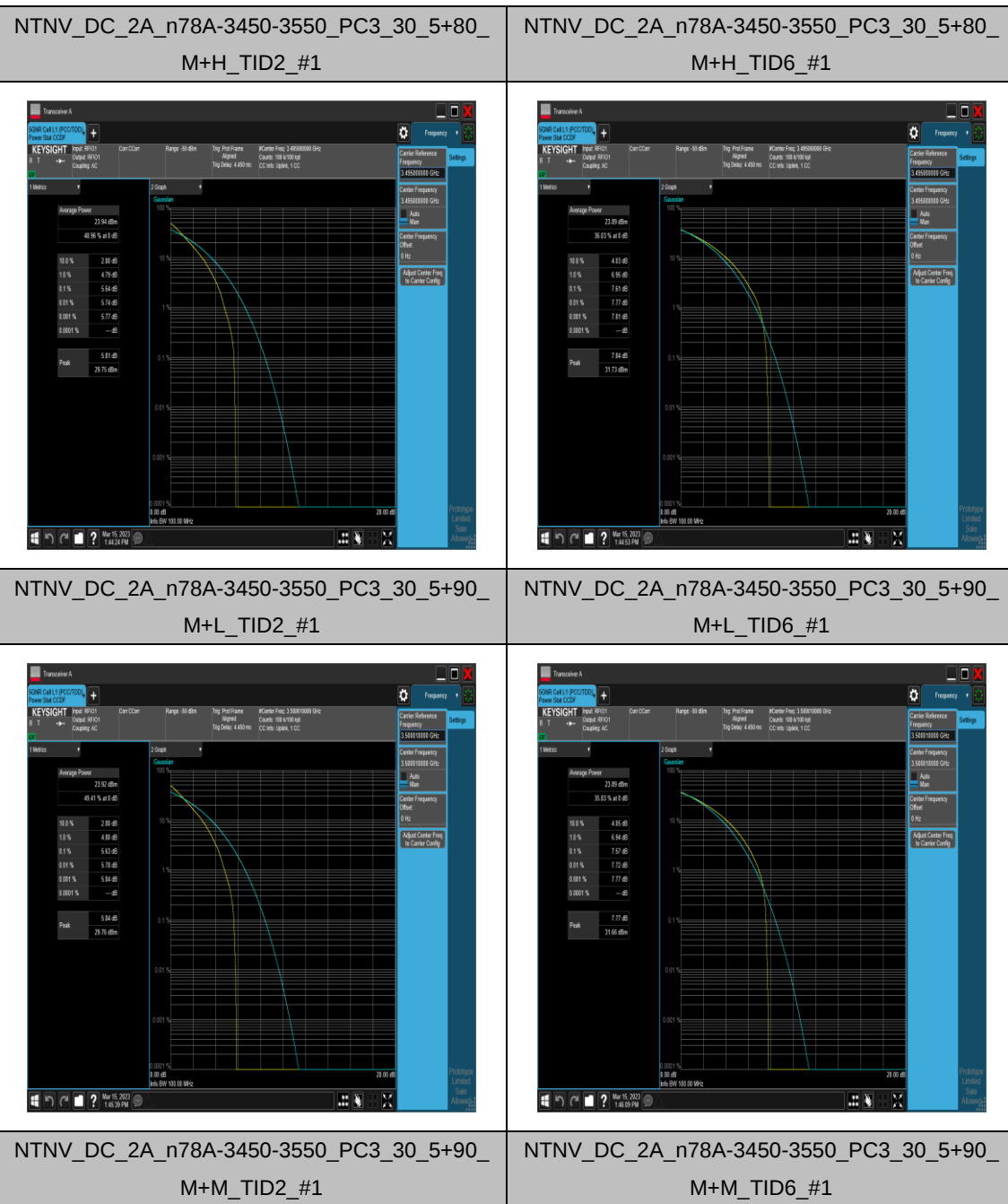


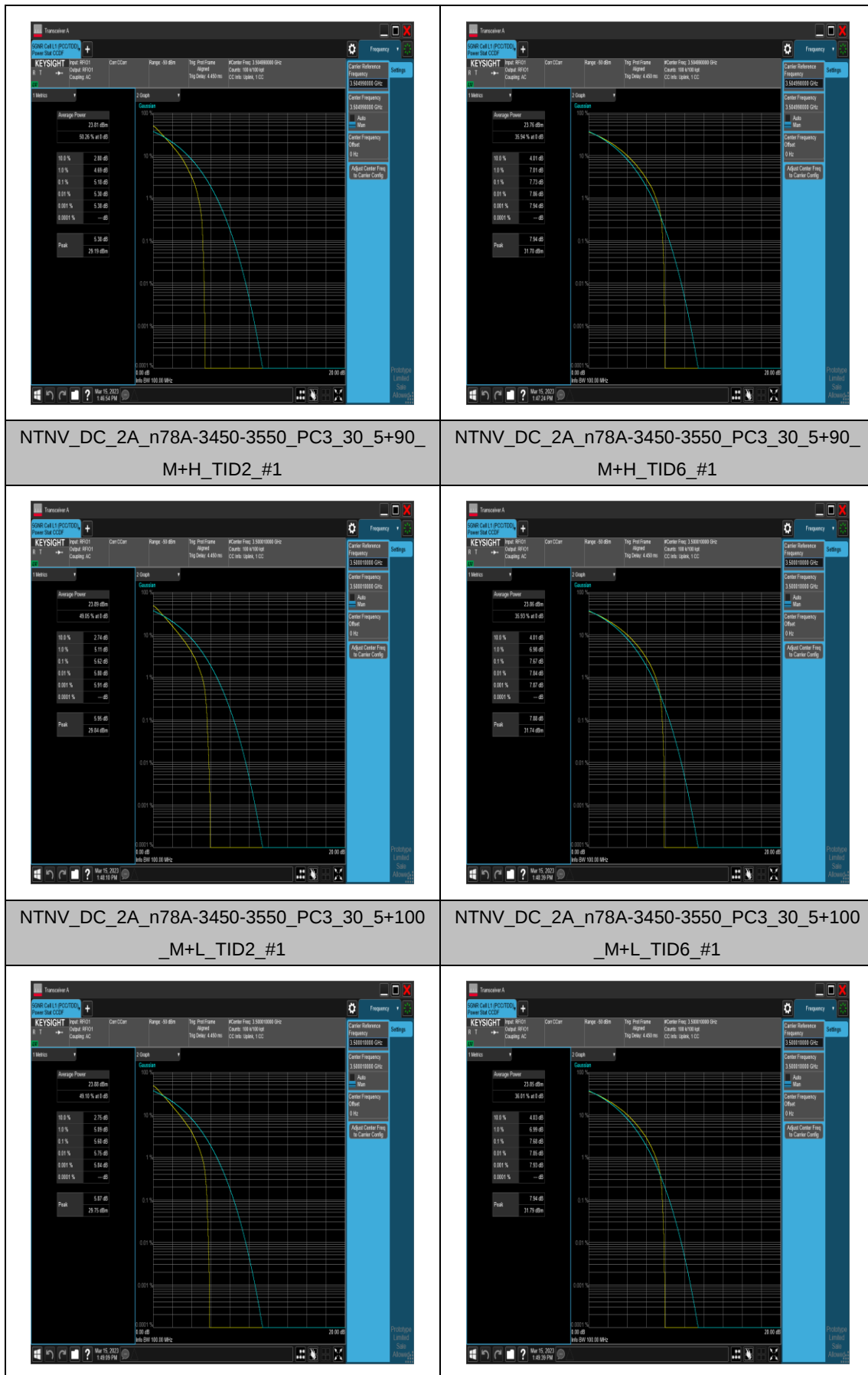
NTNV_DC_2A_n78A-3450-3550_PC3_30_5+50_
M+H_TID6_#1

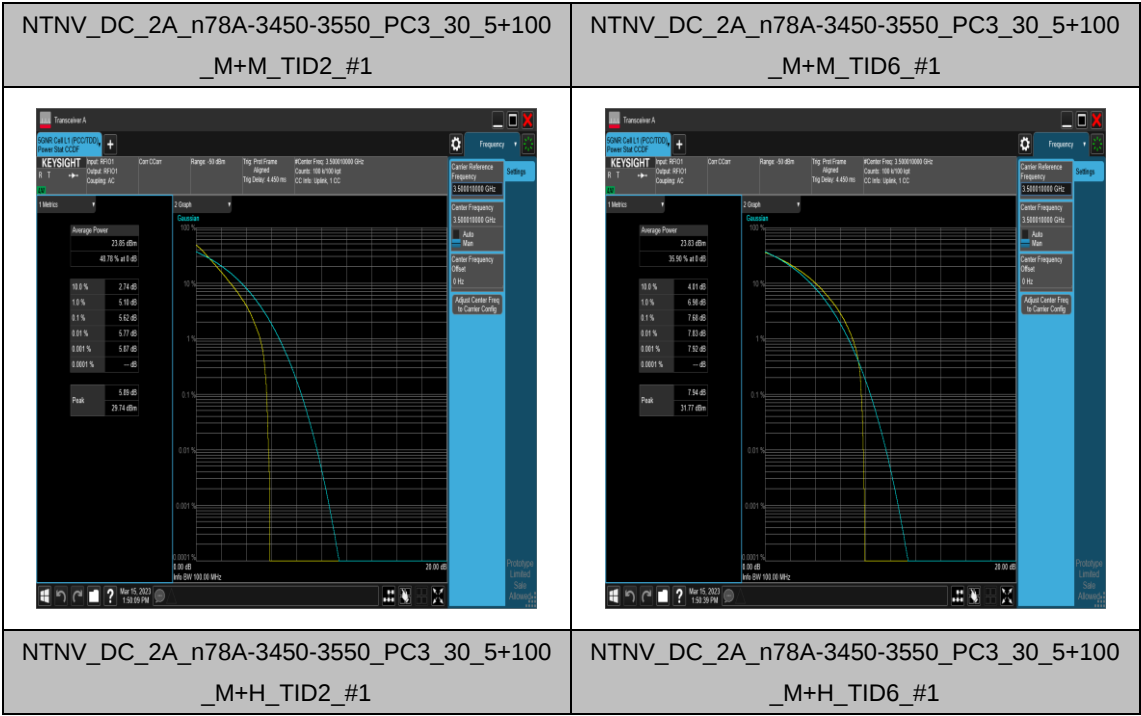












Appendix C: 26dB Bandwidth and Occupied Bandwidth for NSA

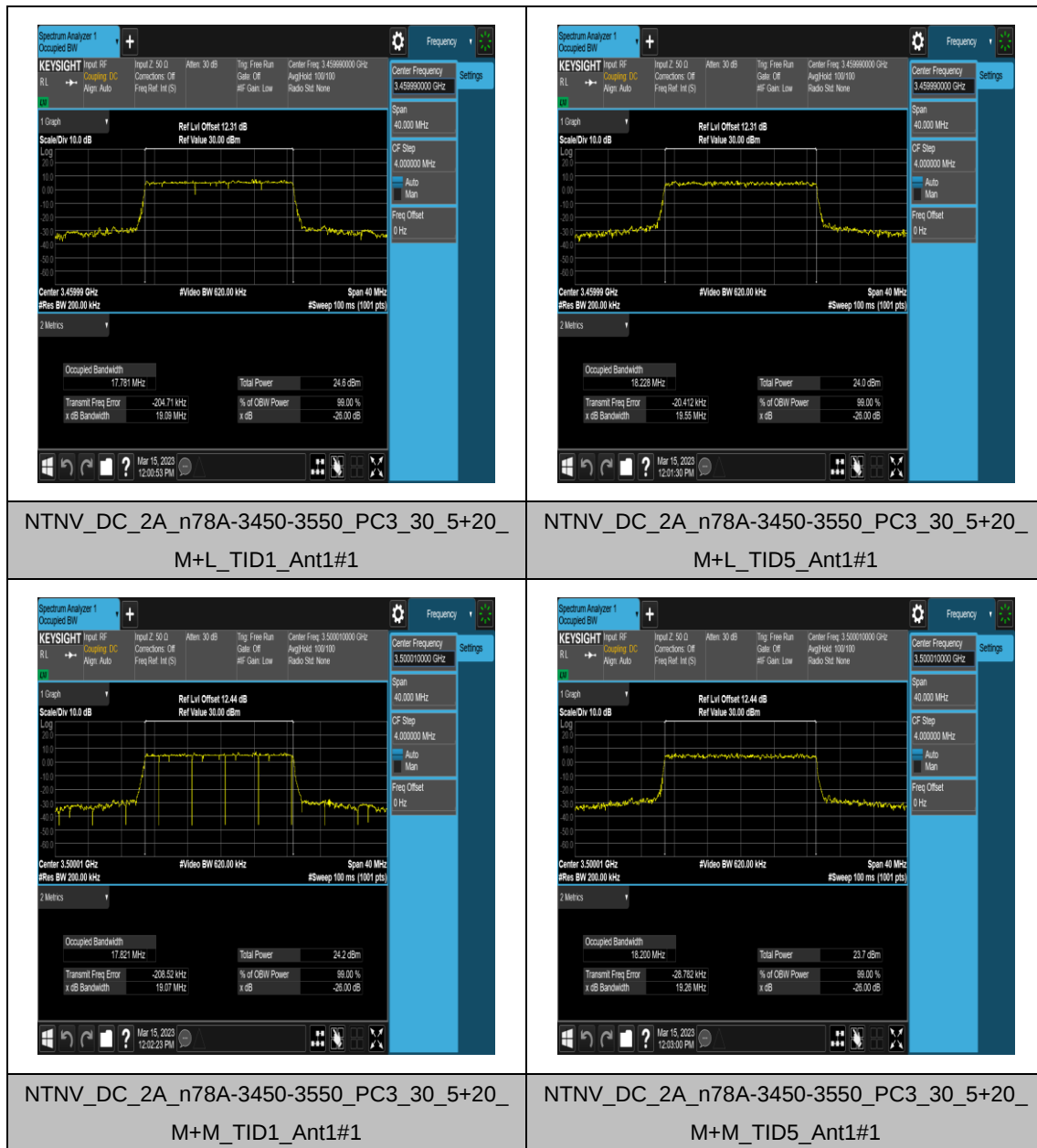
Test Result

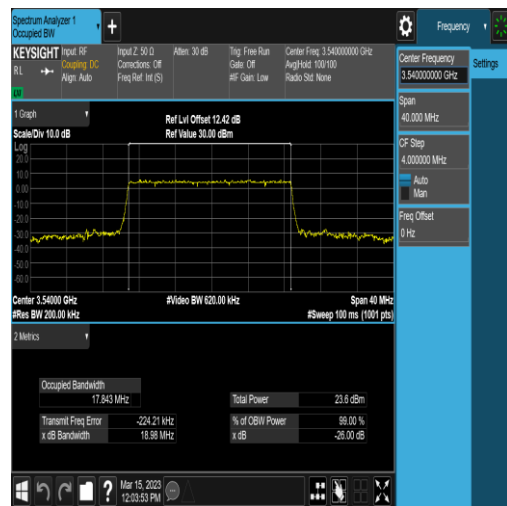
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_2A_n78A-3450-35 50	30	5+20	DFT-QPSK	M+L	Outer_Full	17.781	19.09	PASS
DC_2A_n78A-3450-35 50	30	5+20	CP-QPSK	M+L	Outer_Full	18.228	19.55	PASS
DC_2A_n78A-3450-35 50	30	5+20	DFT-QPSK	M+M	Outer_Full	17.821	19.07	PASS
DC_2A_n78A-3450-35 50	30	5+20	CP-QPSK	M+M	Outer_Full	18.200	19.26	PASS
DC_2A_n78A-3450-35 50	30	5+20	DFT-QPSK	M+H	Outer_Full	17.843	18.98	PASS
DC_2A_n78A-3450-35 50	30	5+20	CP-QPSK	M+H	Outer_Full	18.223	19.60	PASS
DC_2A_n78A-3450-35 50	30	5+40	DFT-QPSK	M+L	Outer_Full	35.693	37.36	PASS
DC_2A_n78A-3450-35 50	30	5+40	CP-QPSK	M+L	Outer_Full	37.820	39.49	PASS
DC_2A_n78A-3450-35 50	30	5+40	DFT-QPSK	M+M	Outer_Full	35.726	37.42	PASS
DC_2A_n78A-3450-35 50	30	5+40	CP-QPSK	M+M	Outer_Full	37.828	39.46	PASS
DC_2A_n78A-3450-35 50	30	5+40	DFT-QPSK	M+H	Outer_Full	35.664	37.35	PASS
DC_2A_n78A-3450-35 50	30	5+40	CP-QPSK	M+H	Outer_Full	37.934	39.56	PASS
DC_2A_n78A-3450-35 50	30	5+50	DFT-QPSK	M+L	Outer_Full	45.616	47.36	PASS
DC_2A_n78A-3450-35 50	30	5+50	CP-QPSK	M+L	Outer_Full	47.360	49.42	PASS
DC_2A_n78A-3450-35 50	30	5+50	DFT-QPSK	M+M	Outer_Full	45.657	47.37	PASS
DC_2A_n78A-3450-35 50	30	5+50	CP-QPSK	M+M	Outer_Full	47.374	49.50	PASS
DC_2A_n78A-3450-35 50	30	5+50	DFT-QPSK	M+H	Outer_Full	45.635	47.58	PASS

50								
DC_2A_n78A-3450-35 50	30	5+50	CP-QPSK	M+H	Outer_Full	47.459	49.46	PASS
DC_2A_n78A-3450-35 50	30	5+60	DFT-QPSK	M+L	Outer_Full	57.753	60.11	PASS
DC_2A_n78A-3450-35 50	30	5+60	CP-QPSK	M+L	Outer_Full	57.829	60.10	PASS
DC_2A_n78A-3450-35 50	30	5+60	DFT-QPSK	M+M	Outer_Full	57.703	60.16	PASS
DC_2A_n78A-3450-35 50	30	5+60	CP-QPSK	M+M	Outer_Full	57.814	59.78	PASS
DC_2A_n78A-3450-35 50	30	5+60	DFT-QPSK	M+H	Outer_Full	57.674	60.04	PASS
DC_2A_n78A-3450-35 50	30	5+60	CP-QPSK	M+H	Outer_Full	57.824	60.28	PASS
DC_2A_n78A-3450-35 50	30	5+80	DFT-QPSK	M+L	Outer_Full	76.933	79.75	PASS
DC_2A_n78A-3450-35 50	30	5+80	CP-QPSK	M+L	Outer_Full	77.263	80.08	PASS
DC_2A_n78A-3450-35 50	30	5+80	DFT-QPSK	M+M	Outer_Full	76.981	79.78	PASS
DC_2A_n78A-3450-35 50	30	5+80	CP-QPSK	M+M	Outer_Full	77.415	80.21	PASS
DC_2A_n78A-3450-35 50	30	5+80	DFT-QPSK	M+H	Outer_Full	77.017	79.79	PASS
DC_2A_n78A-3450-35 50	30	5+80	CP-QPSK	M+H	Outer_Full	77.475	80.10	PASS
DC_2A_n78A-3450-35 50	30	5+90	DFT-QPSK	M+L	Outer_Full	86.367	89.62	PASS
DC_2A_n78A-3450-35 50	30	5+90	CP-QPSK	M+L	Outer_Full	86.645	90.42	PASS
DC_2A_n78A-3450-35 50	30	5+90	DFT-QPSK	M+M	Outer_Full	86.376	89.68	PASS
DC_2A_n78A-3450-35 50	30	5+90	CP-QPSK	M+M	Outer_Full	87.363	90.29	PASS
DC_2A_n78A-3450-35 50	30	5+90	DFT-QPSK	M+H	Outer_Full	86.327	89.73	PASS
DC_2A_n78A-3450-35 50	30	5+90	CP-QPSK	M+H	Outer_Full	87.400	90.43	PASS
DC_2A_n78A-3450-35 50	30	5+100	DFT-QPSK	M+L	Outer_Full	96.079	99.68	PASS
DC_2A_n78A-3450-35	30	5+100	CP-QPSK	M+L	Outer_Full	97.219	100.8	PASS

50								
DC_2A_n78A-3450-35 50	30	5+100	DFT-QPSK	M+M	Outer_Full	96.037	99.51	PASS
DC_2A_n78A-3450-35 50	30	5+100	CP-QPSK	M+M	Outer_Full	97.167	100.8	PASS
DC_2A_n78A-3450-35 50	30	5+100	DFT-QPSK	M+H	Outer_Full	95.933	99.66	PASS
DC_2A_n78A-3450-35 50	30	5+100	CP-QPSK	M+H	Outer_Full	97.256	100.7	PASS

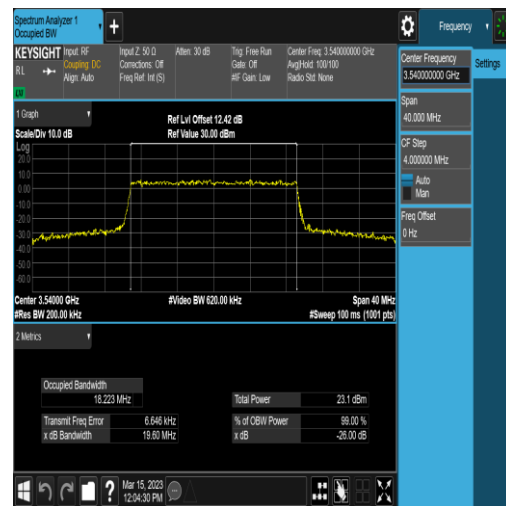
Test Graphs





NTNV_DC_2A_n78A-3450-3550_PC3_30_5+20_

M+H_TID1_Ant1#1



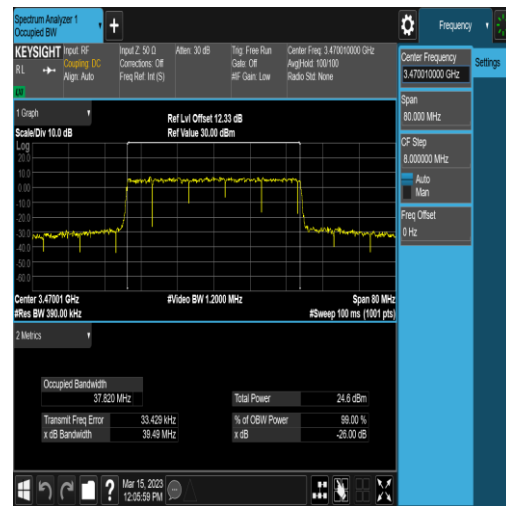
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M+H_TID5_Ant1#1



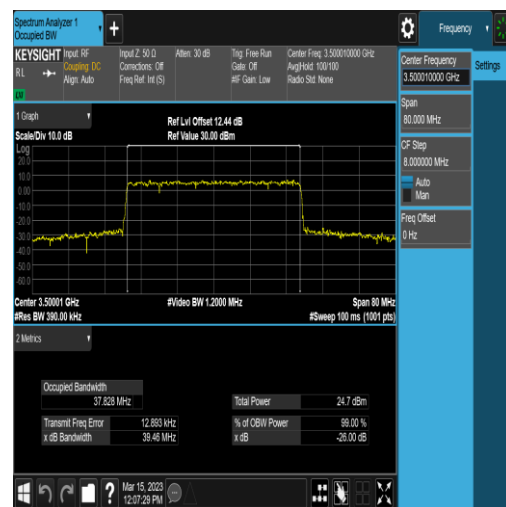
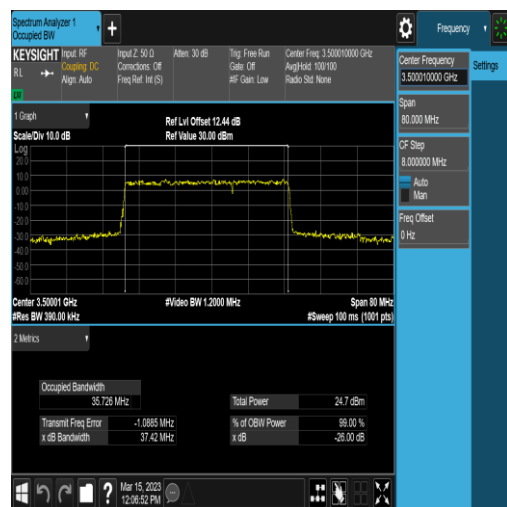
NTNV_DC_2A_n78A-3450-3550_PC3_30_5+40_

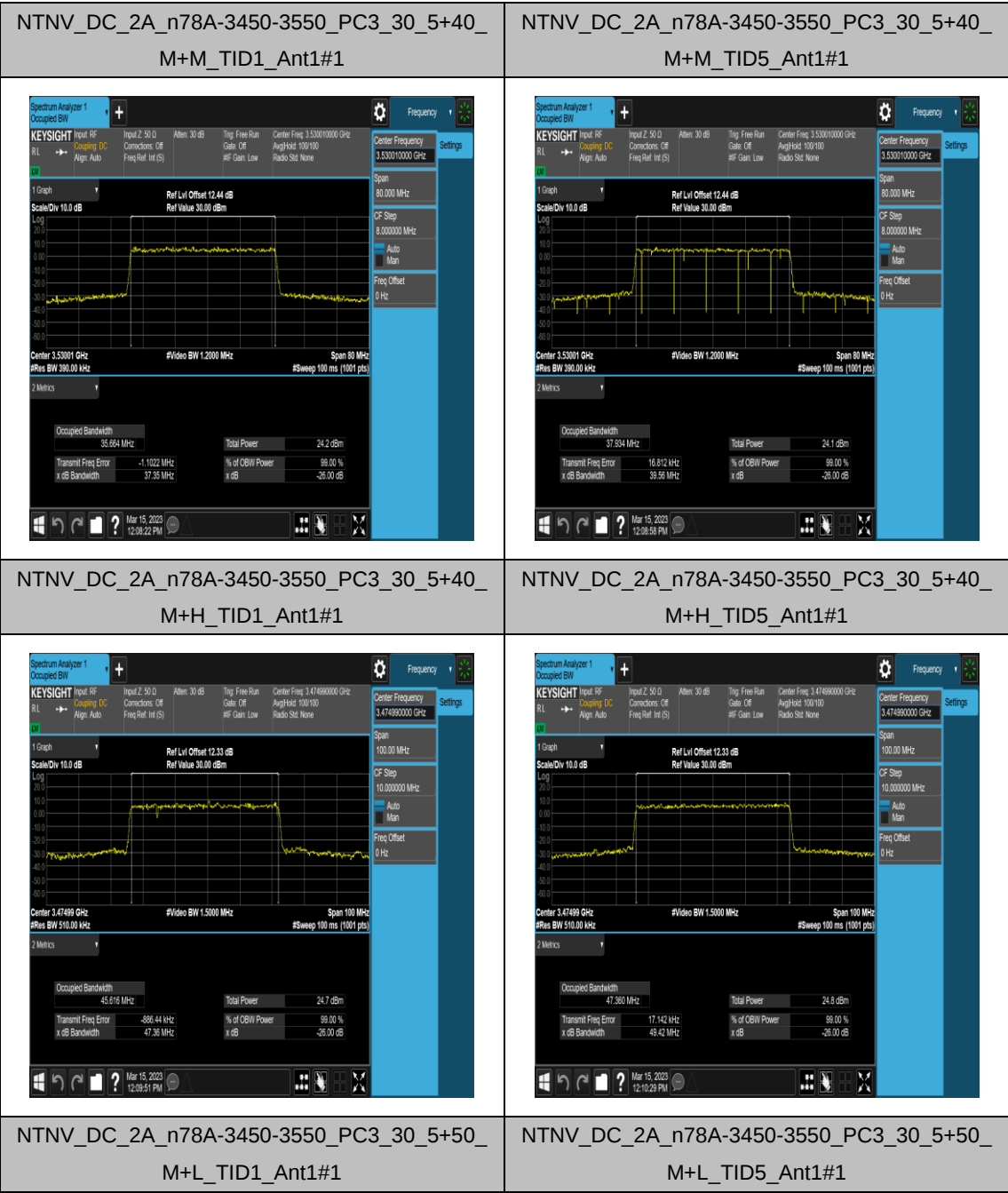
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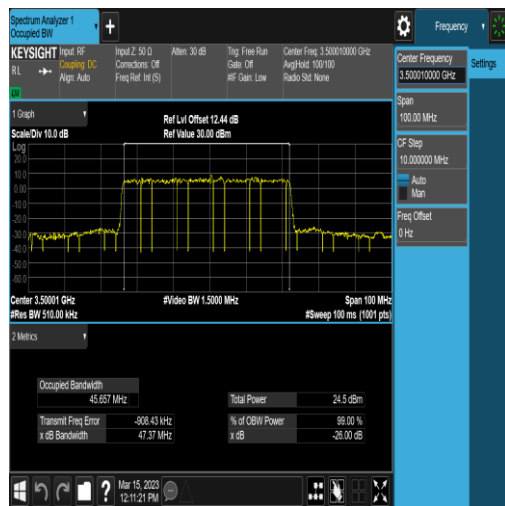


NTNV_DC_2A_n78A-3450-3550_PC3_30_5+40_

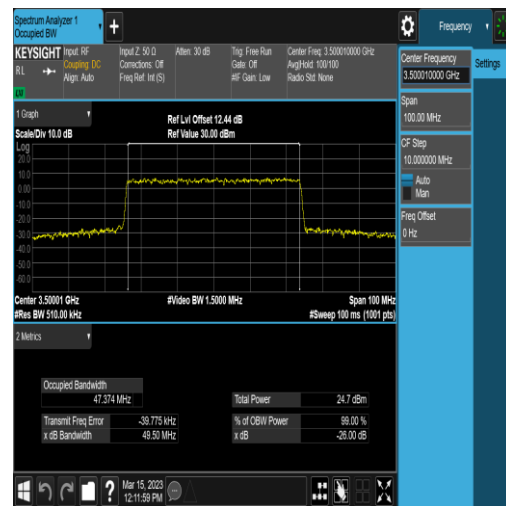
M+L_TID5_Ant1#1



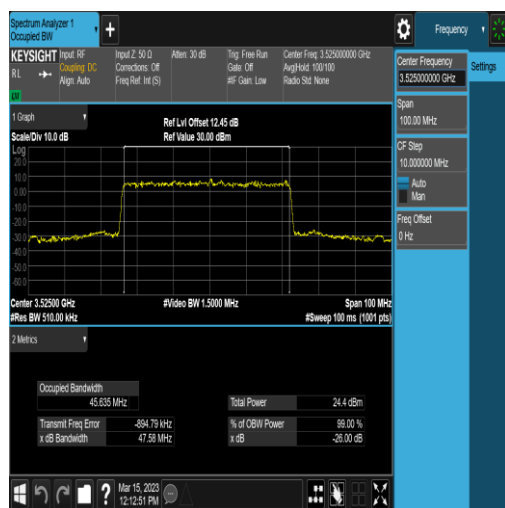




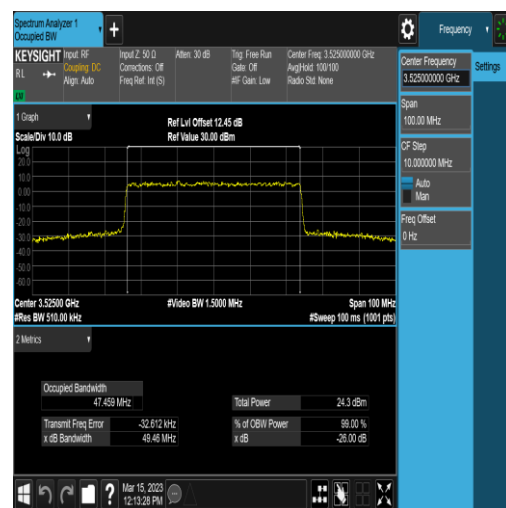
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M+M_TID1_Ant1#1



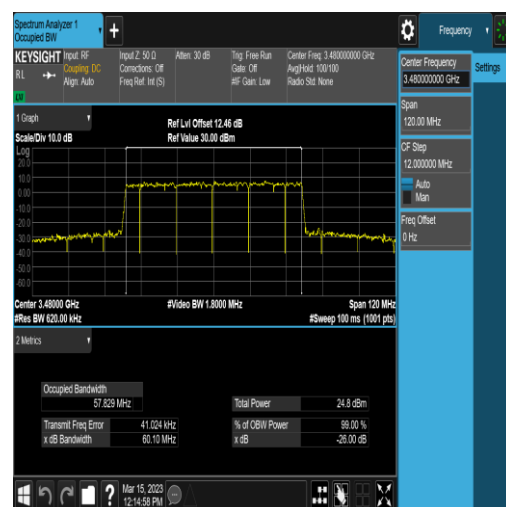
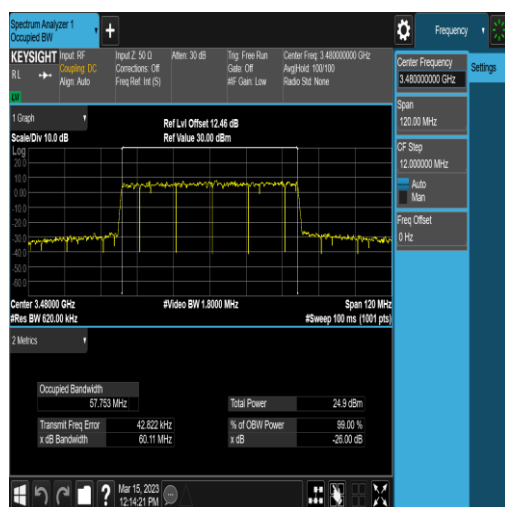
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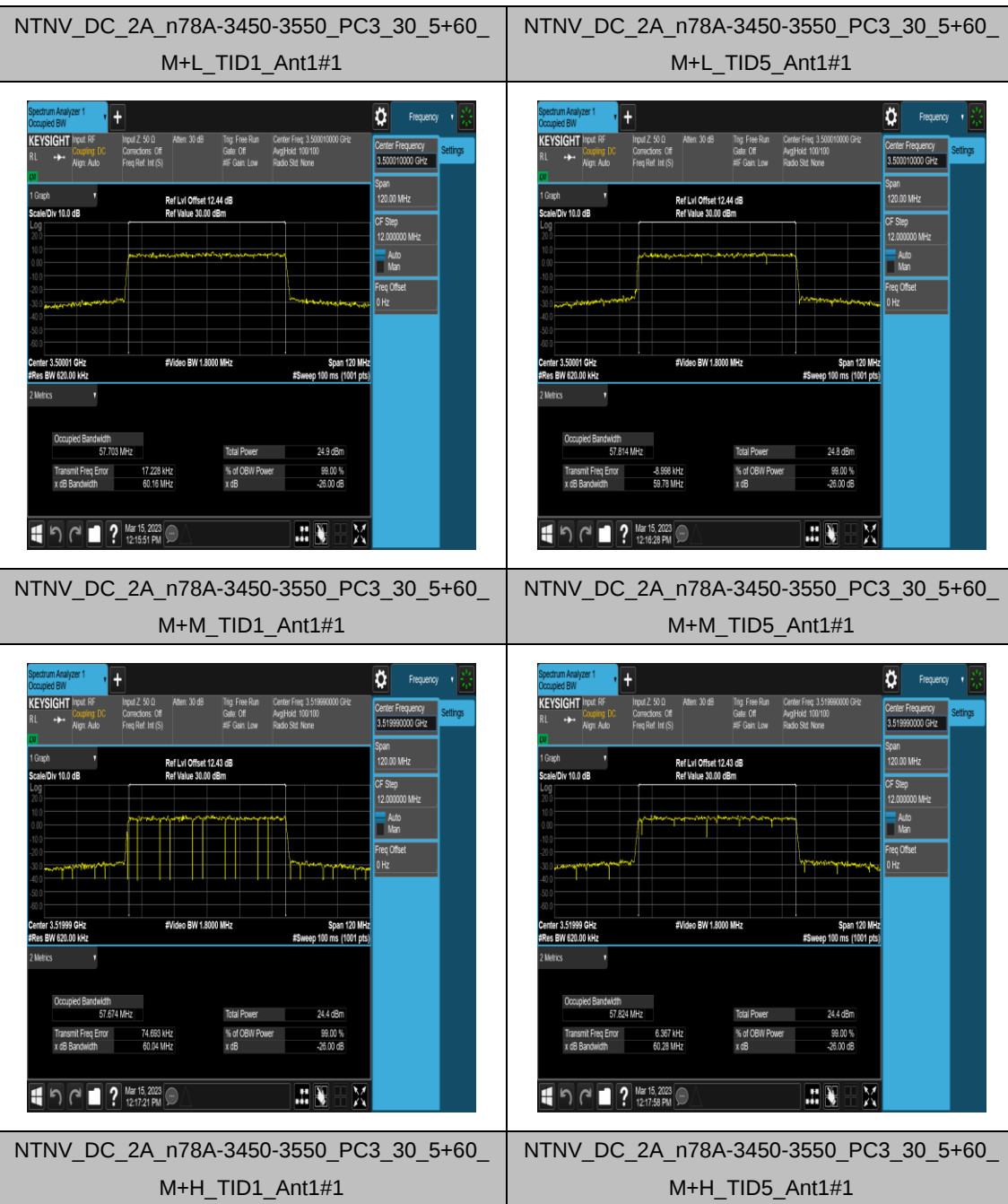


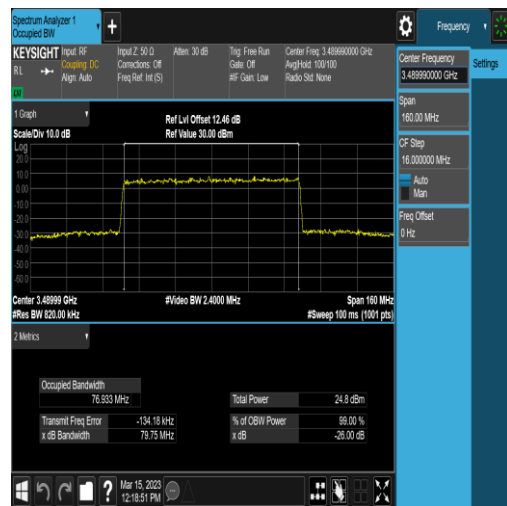
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M+H_TID1_Ant1#1



NTNV_DC_2A_n78A-3450-3550_PC3_30_5+50_
M+H_TID5_Ant1#1

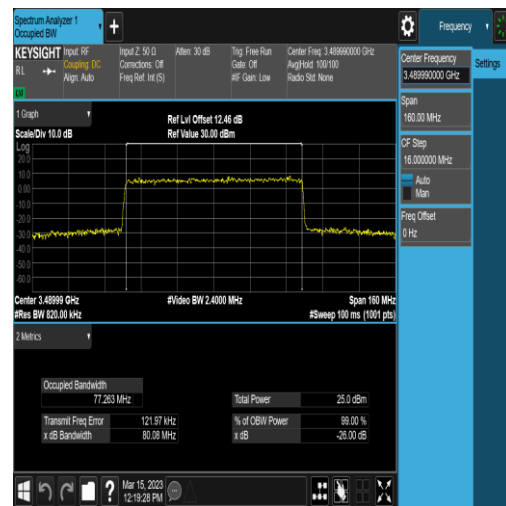






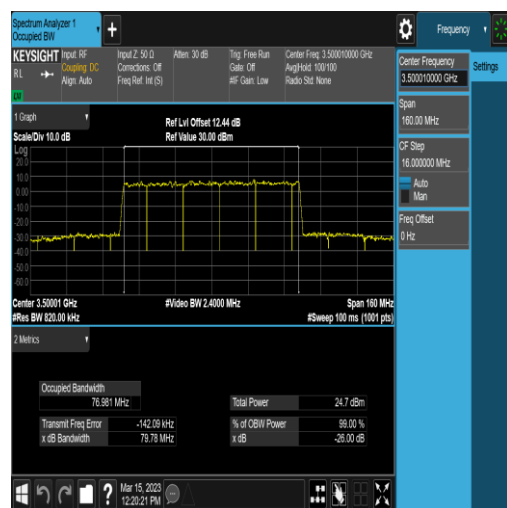
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M+L_TID1_Ant1#1



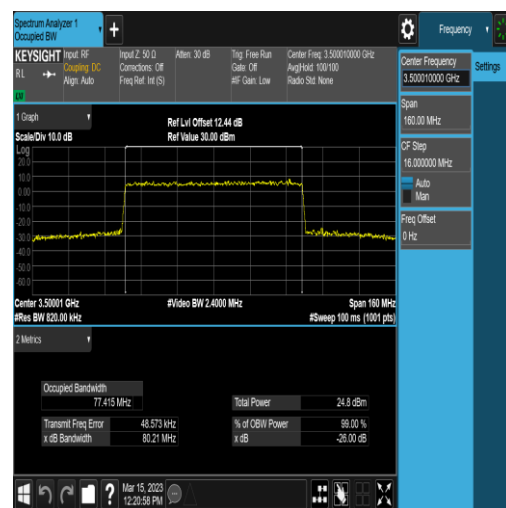
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M+L_TID5_Ant1#1



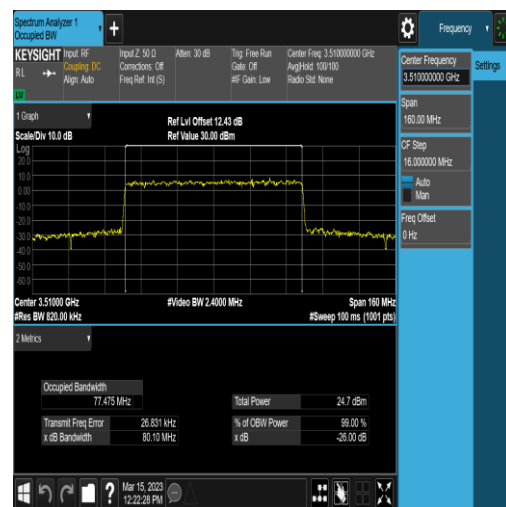
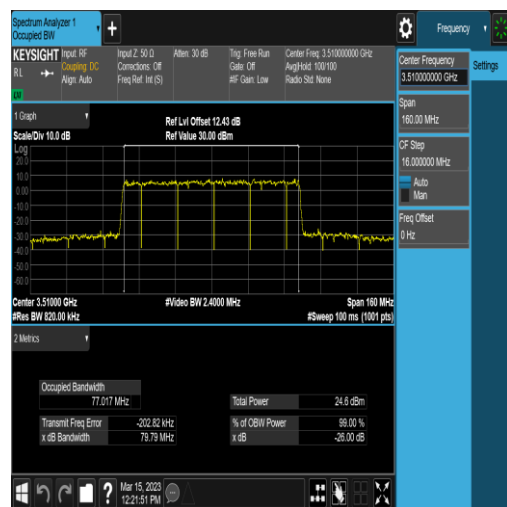
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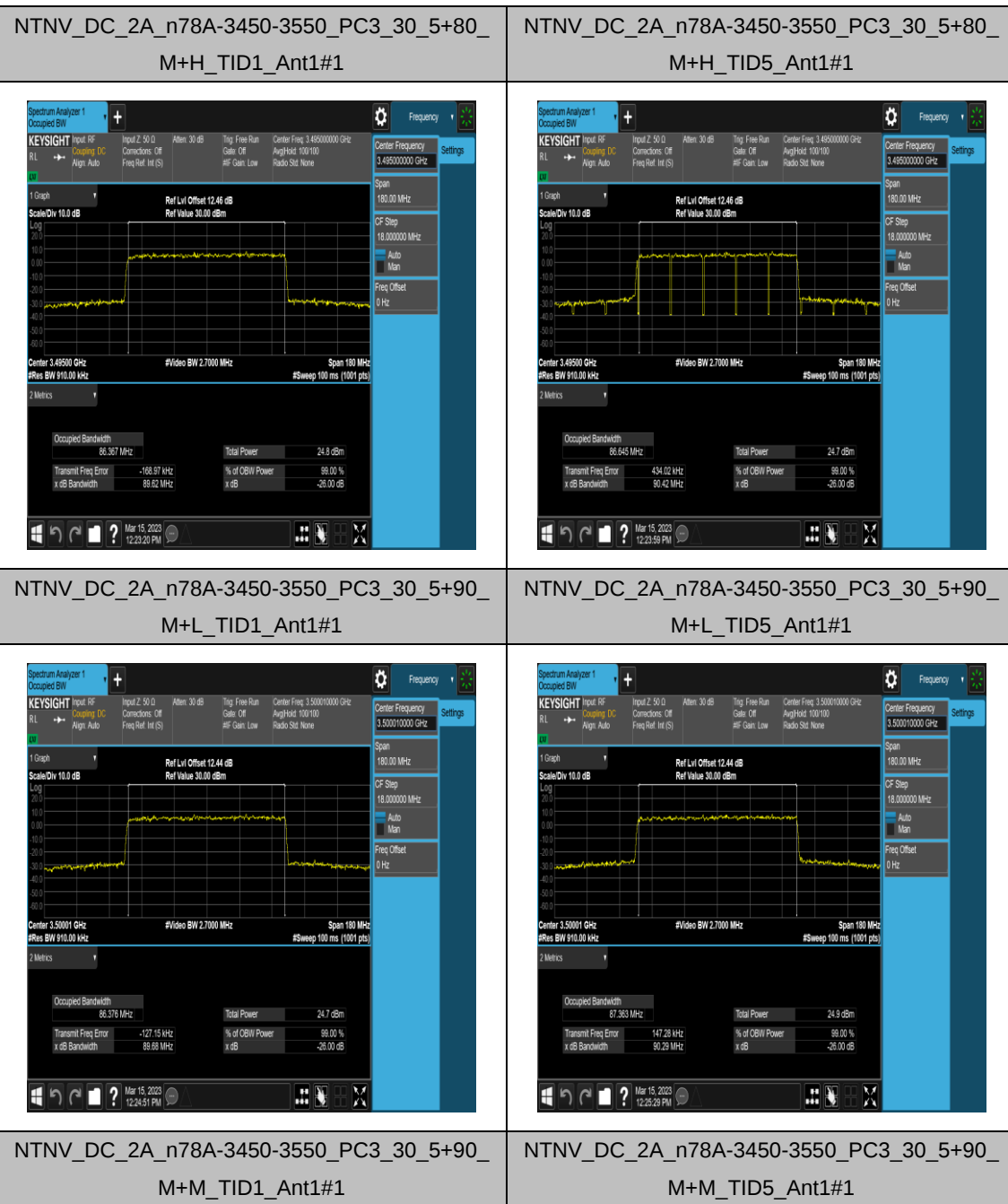
M+M_TID1_Ant1#1

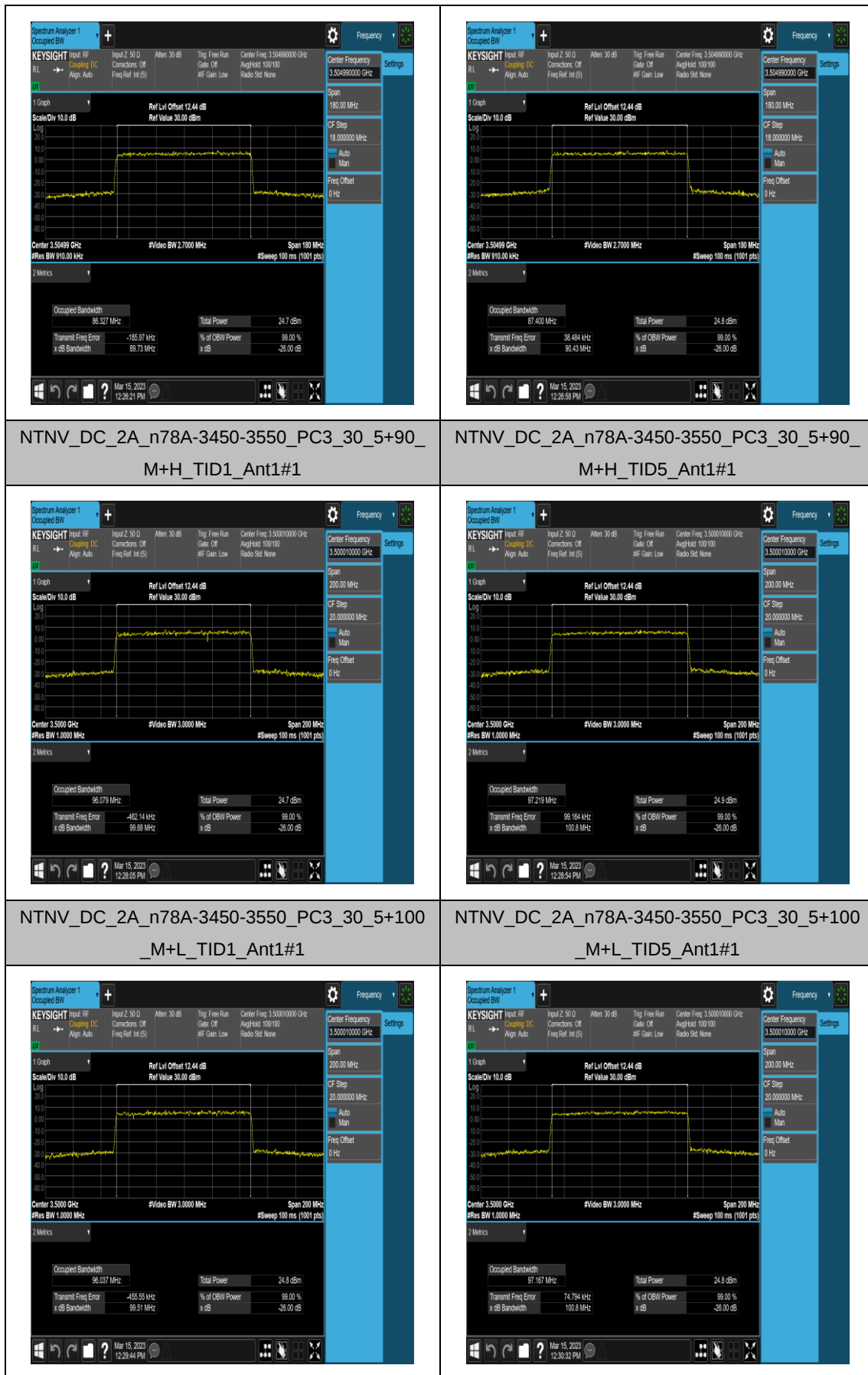


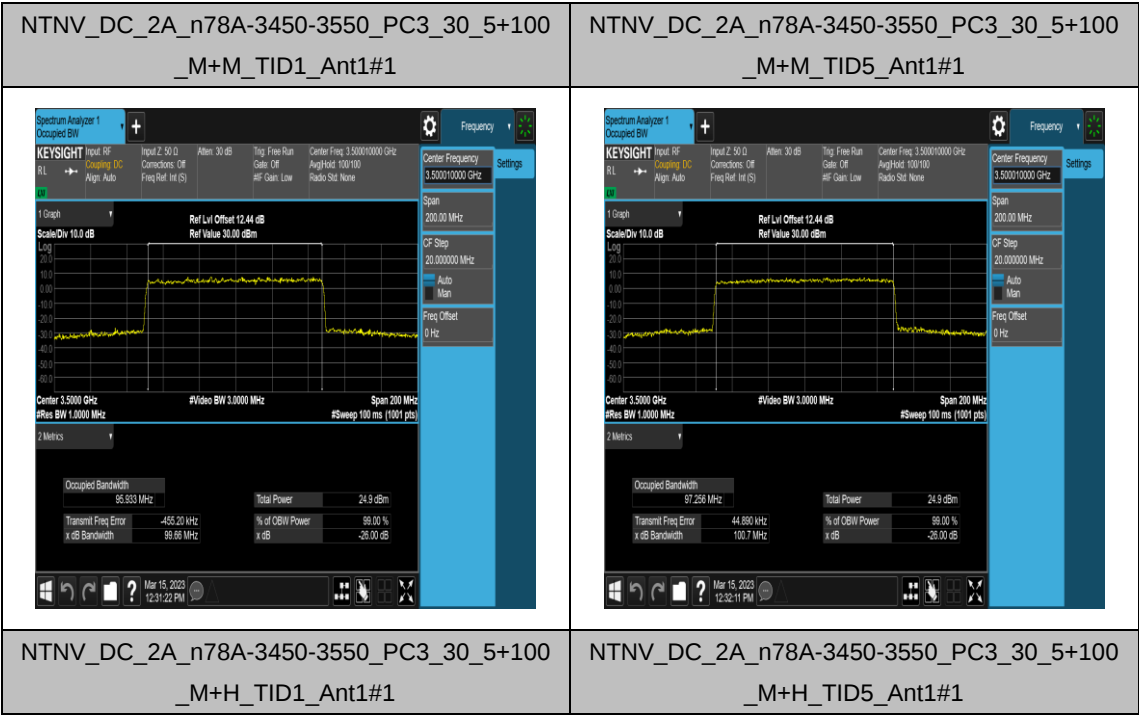
NTNV_DC_2A_n78A-3450-3550_PC3_30_5+80_

M+M_TID5_Ant1#1









Appendix D: Band Edge for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Verdict
DC_2A_n78A-3450-35 50	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+20	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+20	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+40	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+40	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+50	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+50	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+60	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+60	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+80	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-35	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS

50							
DC_2A_n78A-3450-35 50	30	5+80	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+90	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+90	CP-QPSK	M+H	Outer_Full	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+100	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_2A_n78A-3450-35 50	30	5+100	CP-QPSK	M+H	Outer_Full	see graph	PASS