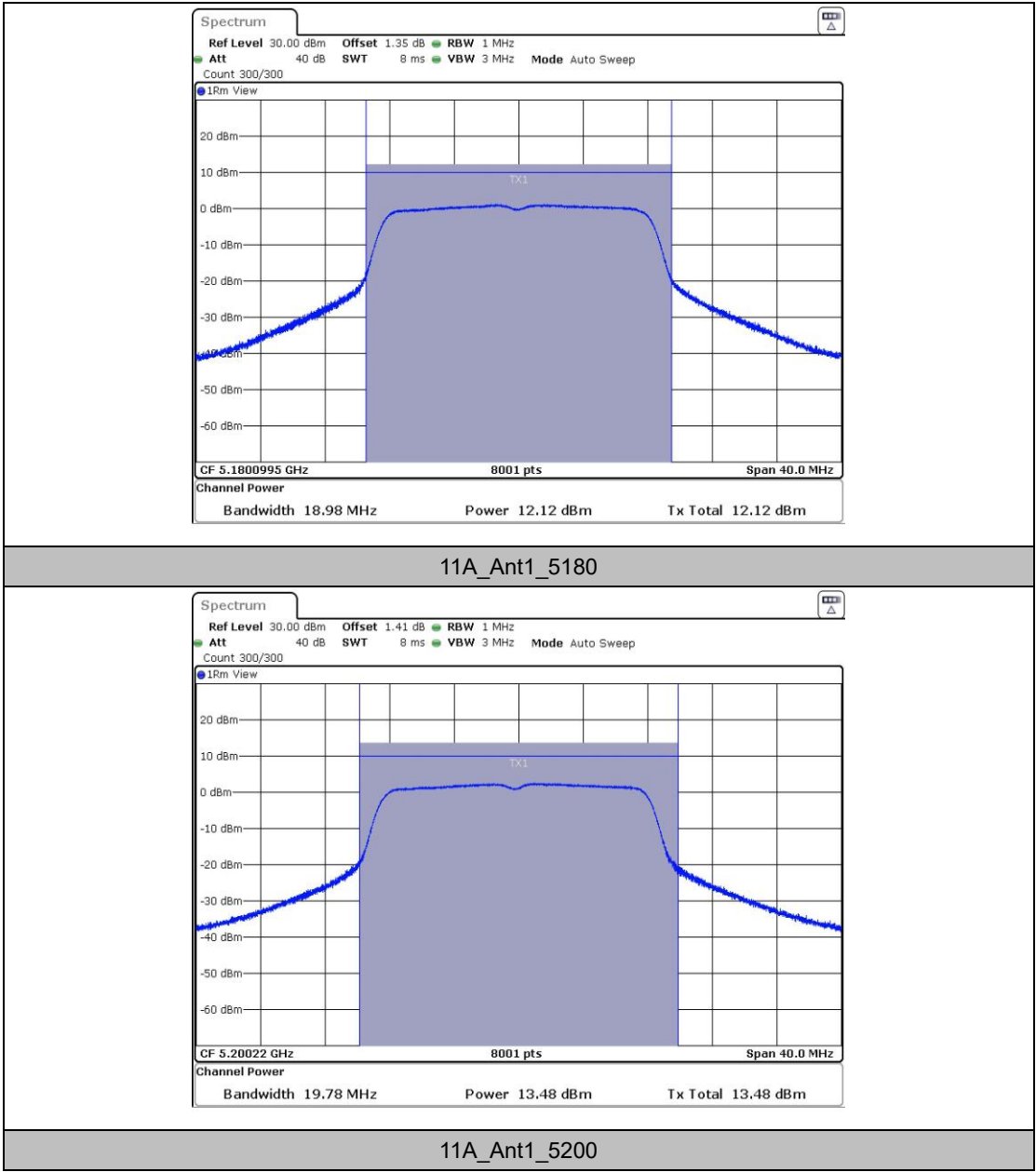
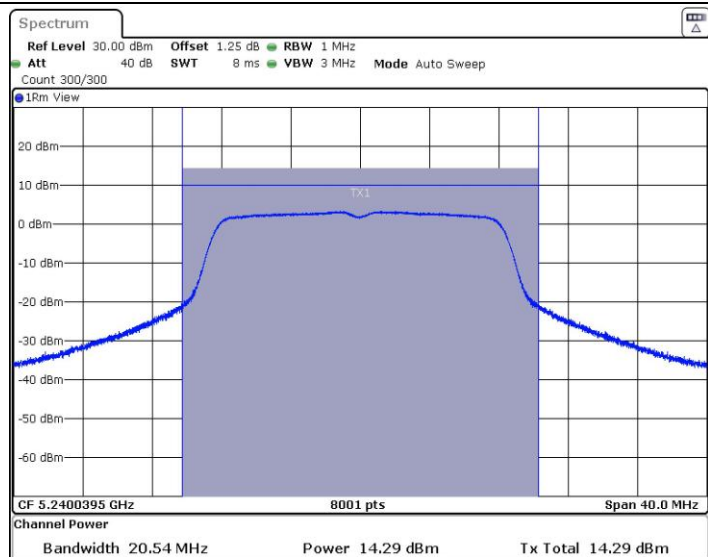
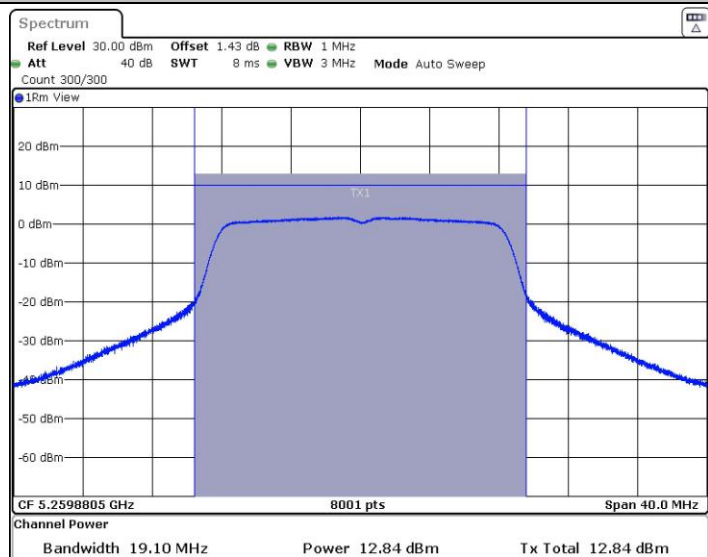


Test Graphs

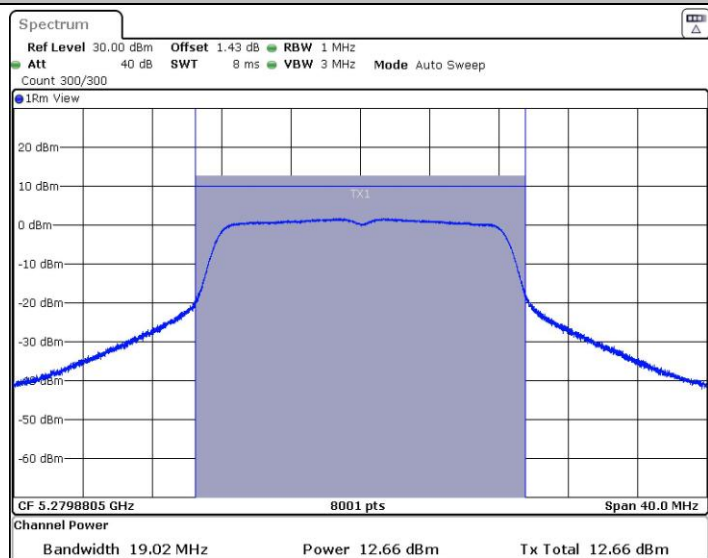




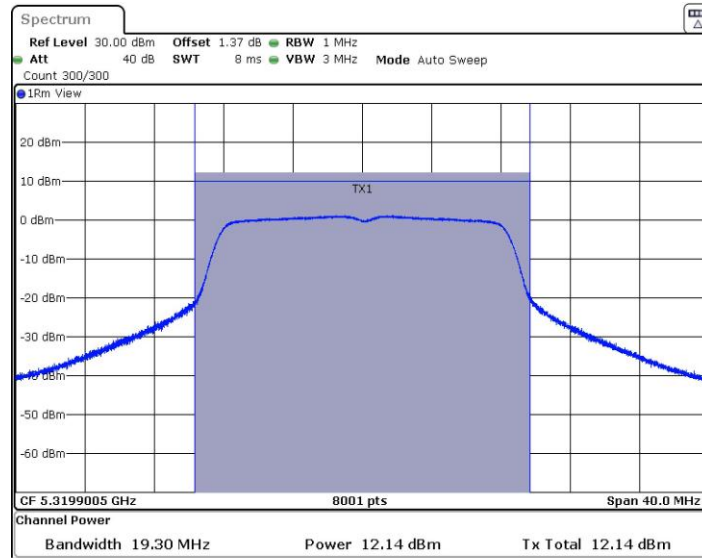
11A_Ant1_5240



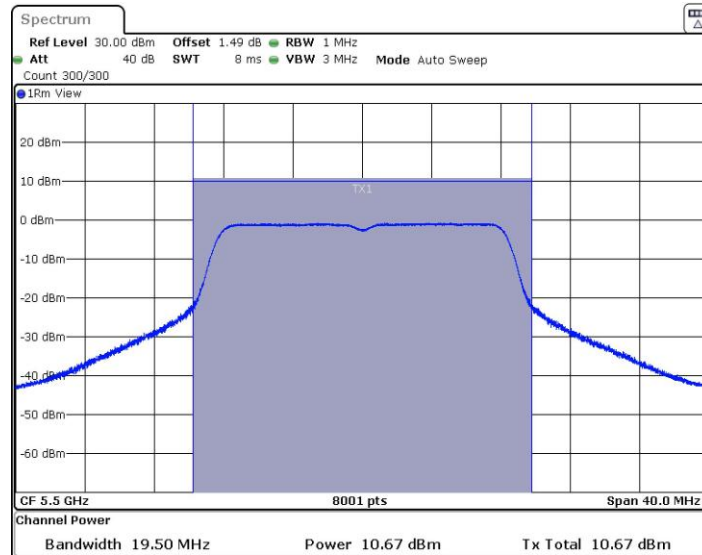
11A_Ant1_5260



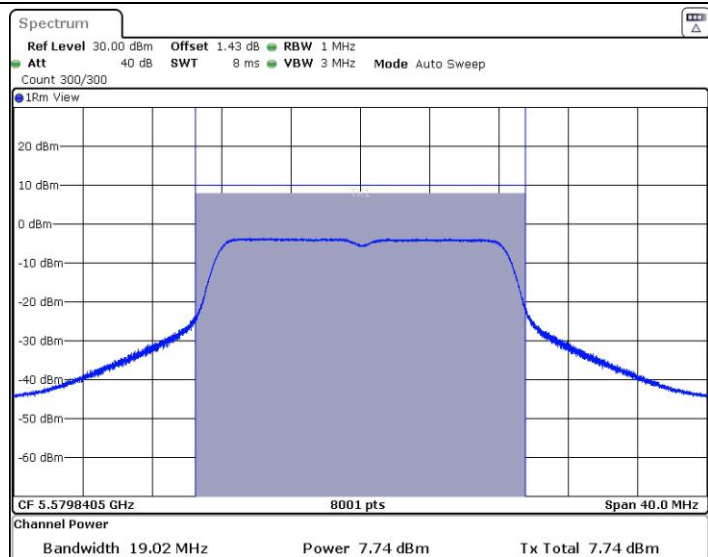
11A_Ant1_5280



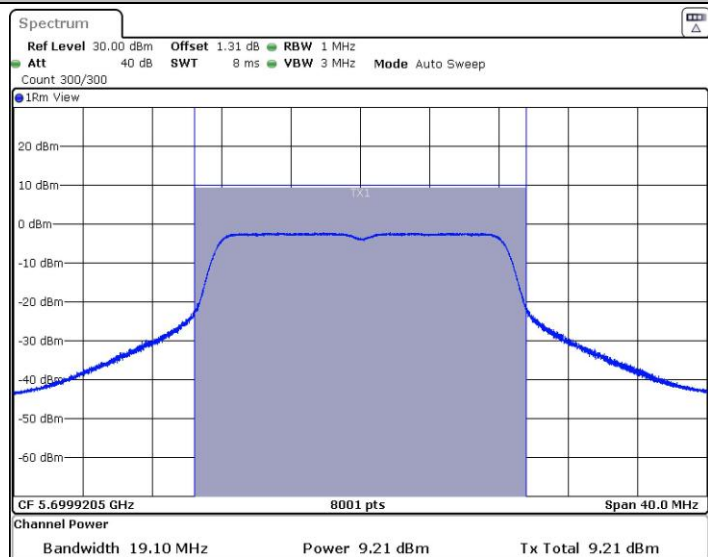
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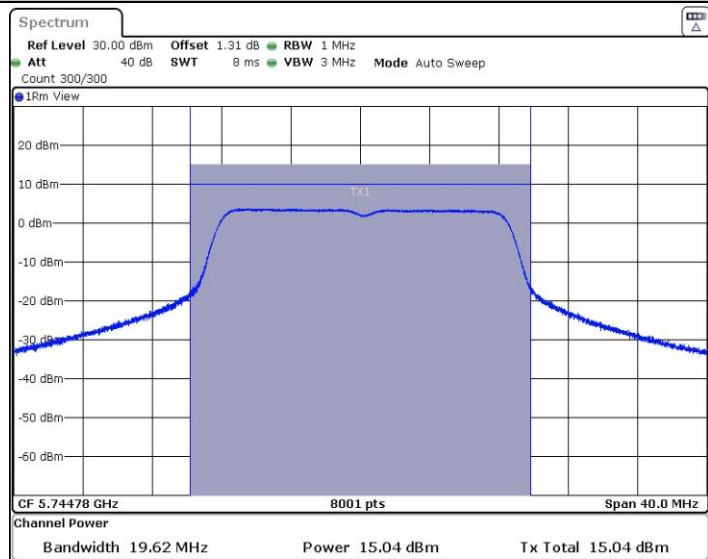
11A_Ant1_5500



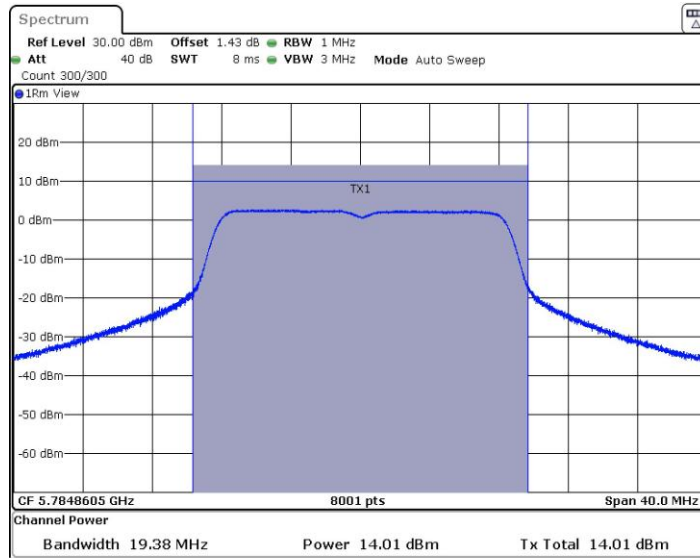
11A_Ant1_5580



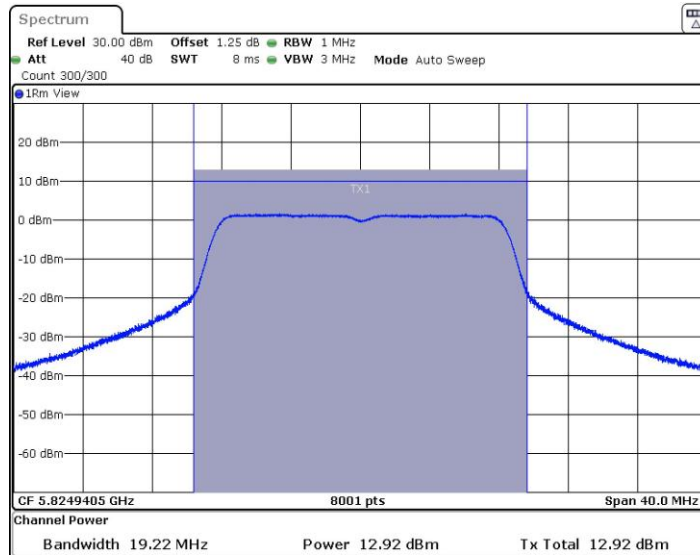
11A_Ant1_5700



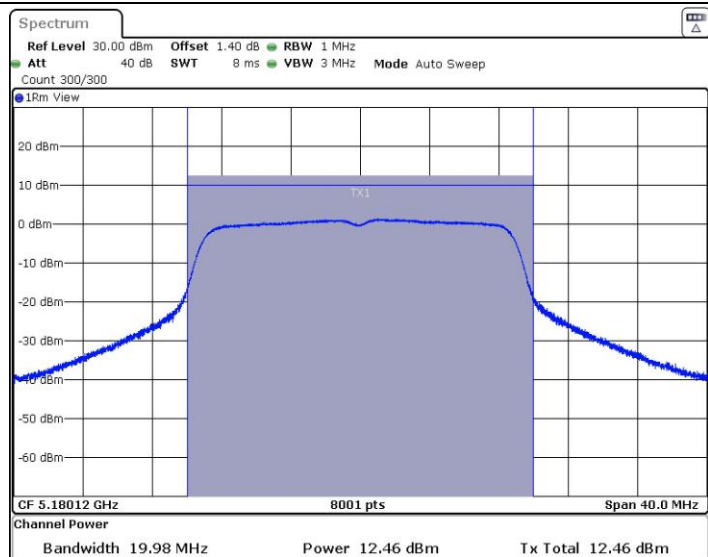
11A_Ant1_5745



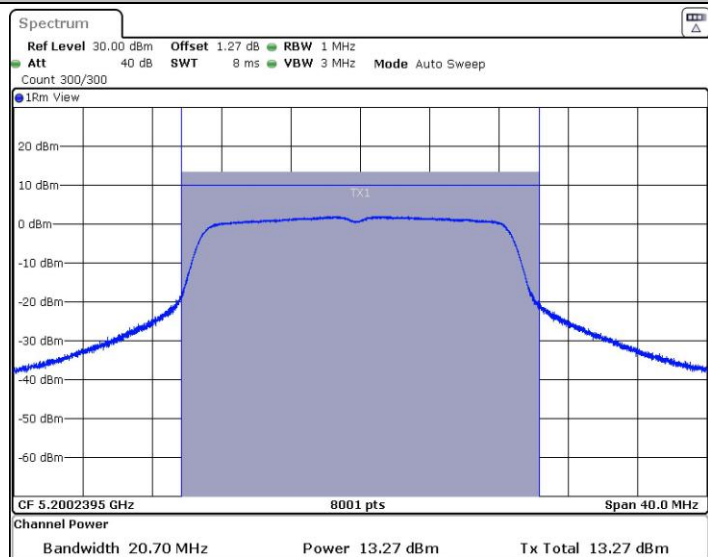
11A_Ant1_5785



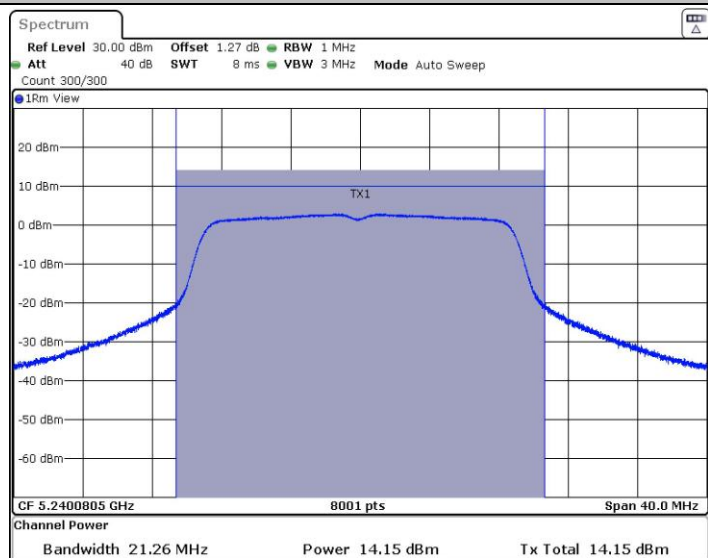
11A_Ant1_5825



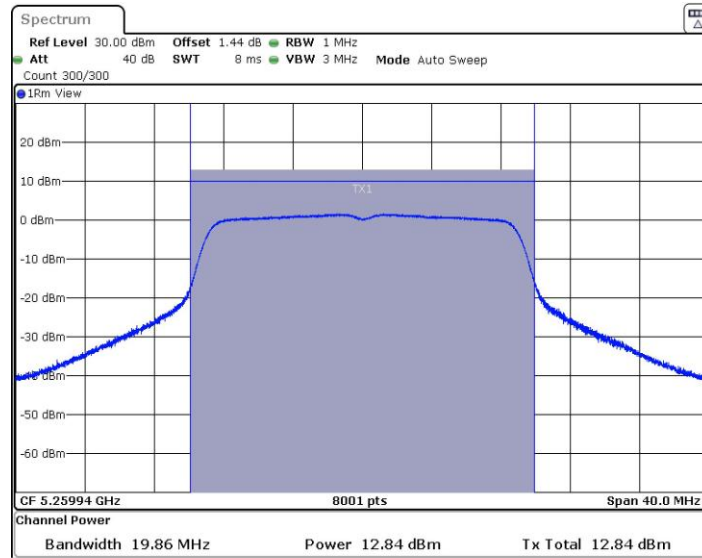
11N20SISO_Ant1_5180



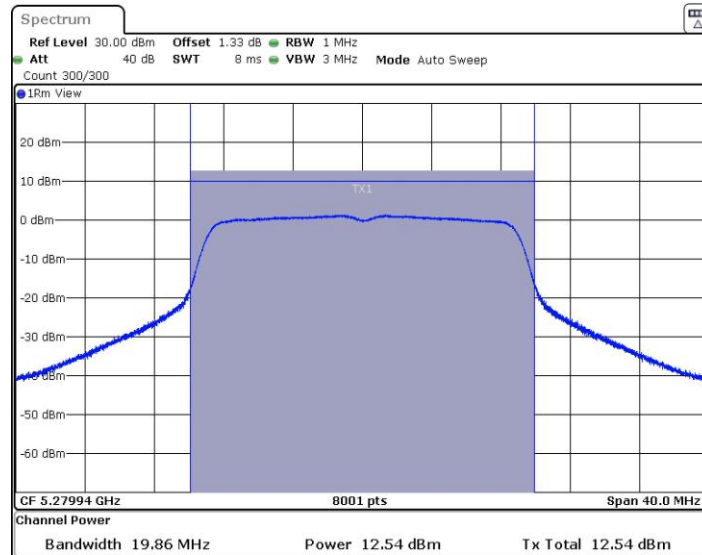
11N20SISO_Ant1_5200



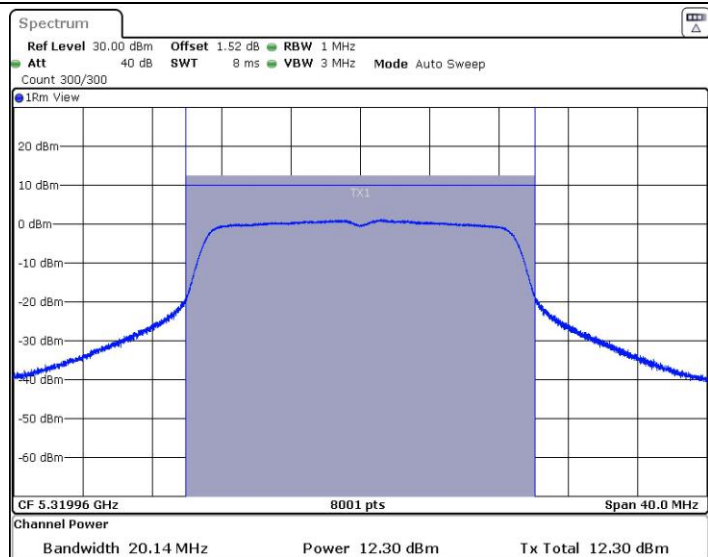
11N20SISO_Ant1_5240



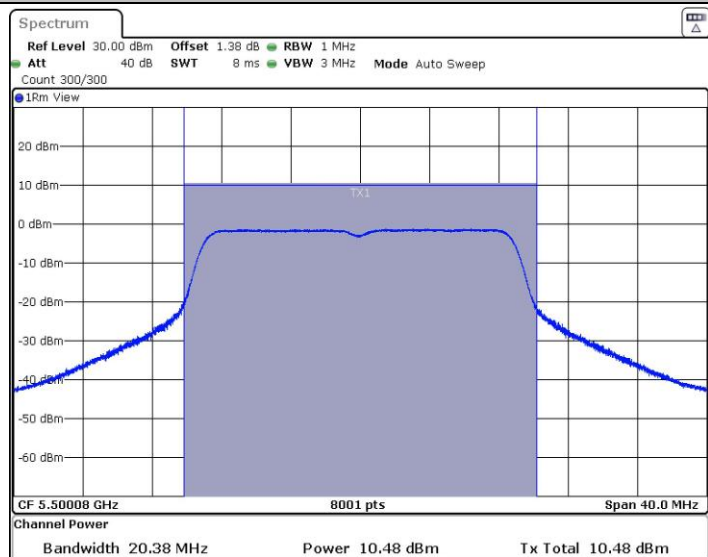
11N20SISO_Ant1_5260



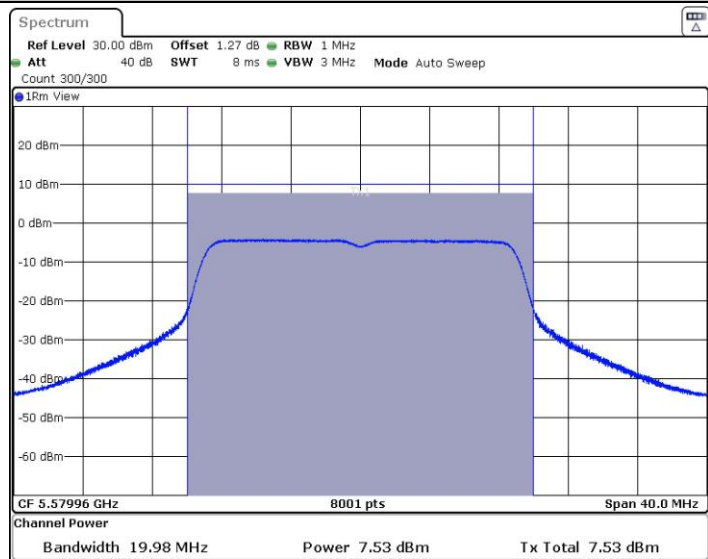
11N20SISO_Ant1_5280



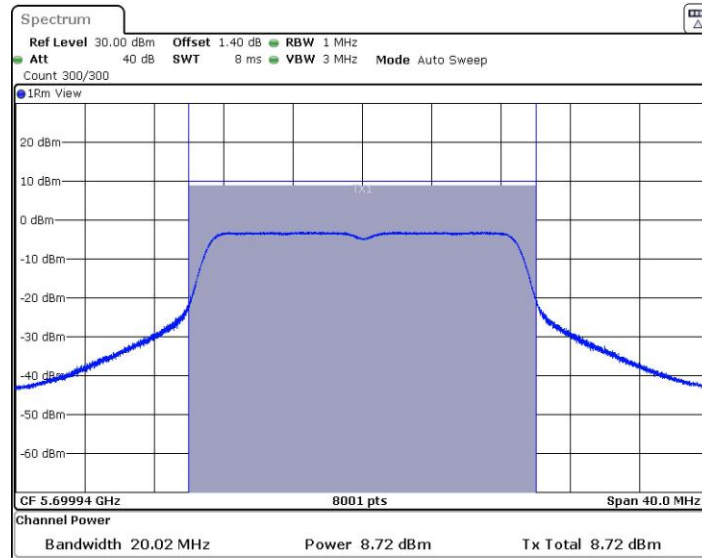
11N20SISO_Ant1_5320



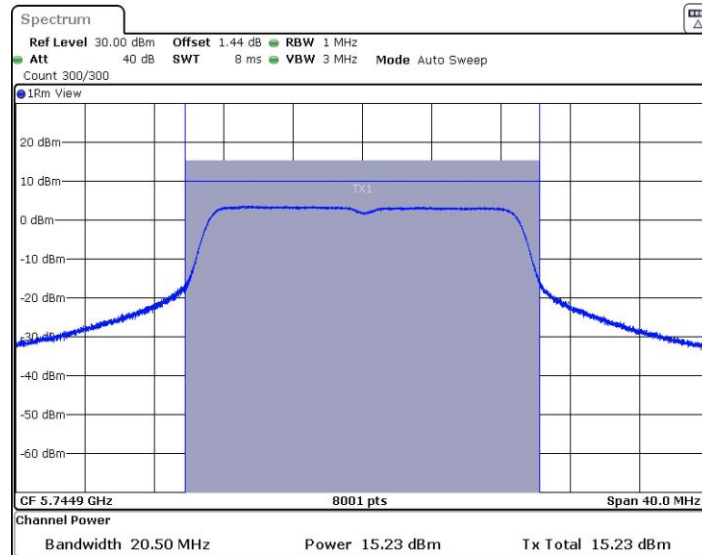
11N20SISO_Ant1_5500



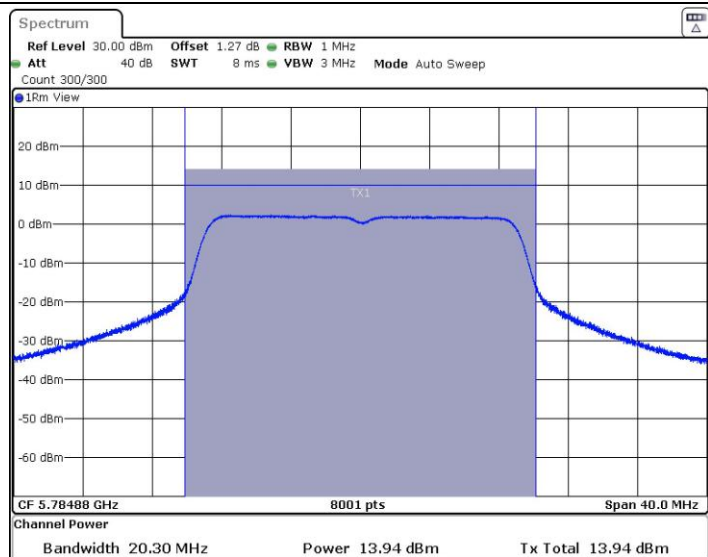
11N20SISO_Ant1_5580



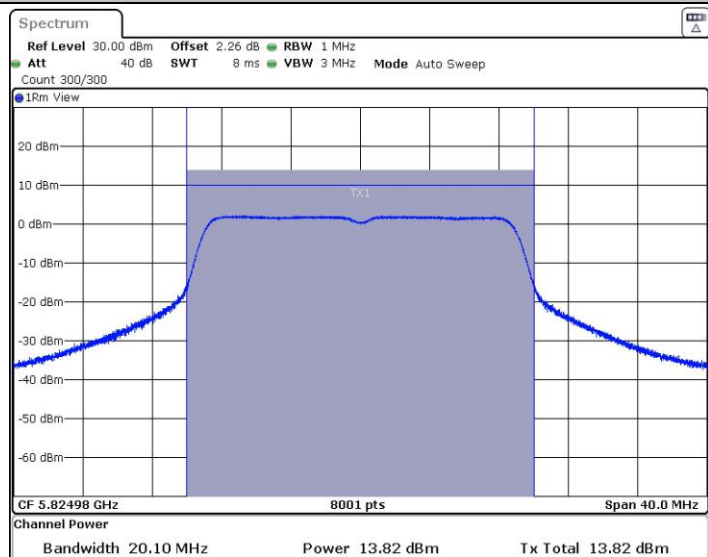
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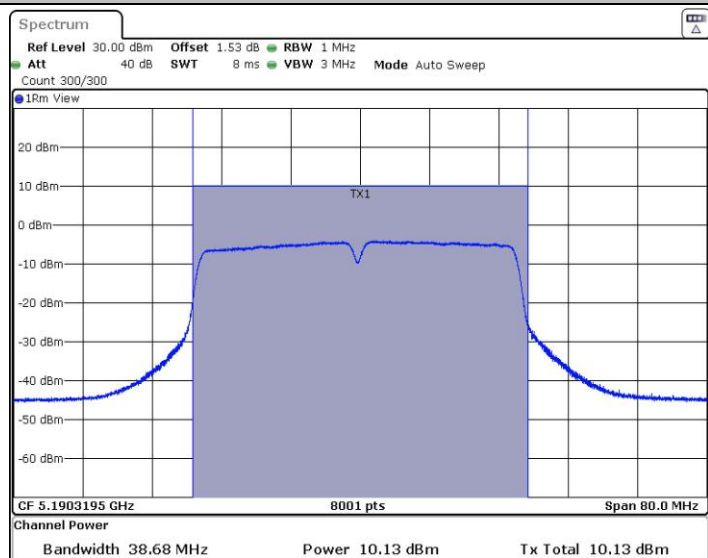
11N20SISO_Ant1_5745



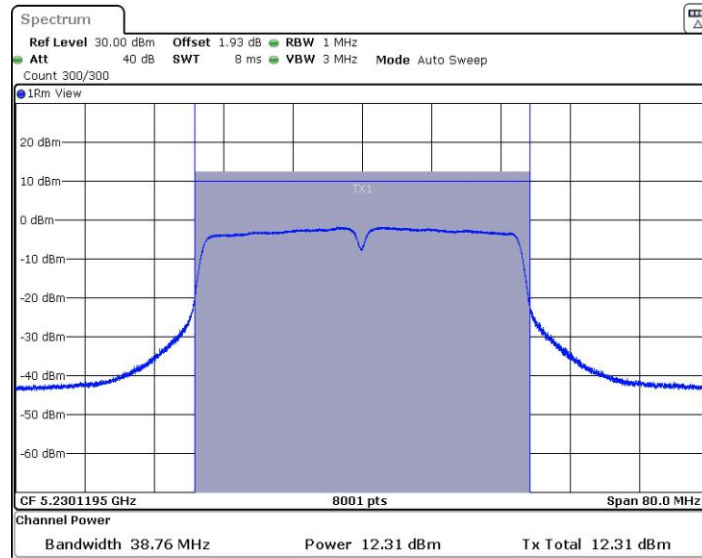
11N20SISO_Ant1_5785



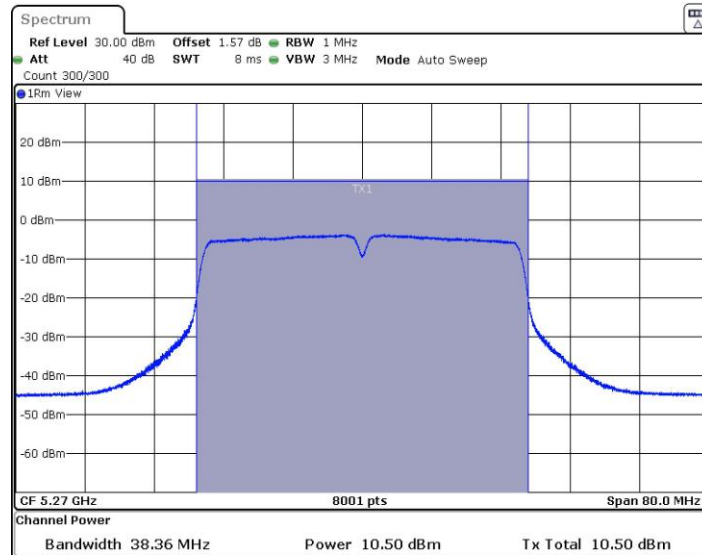
11N20SISO_Ant1_5825



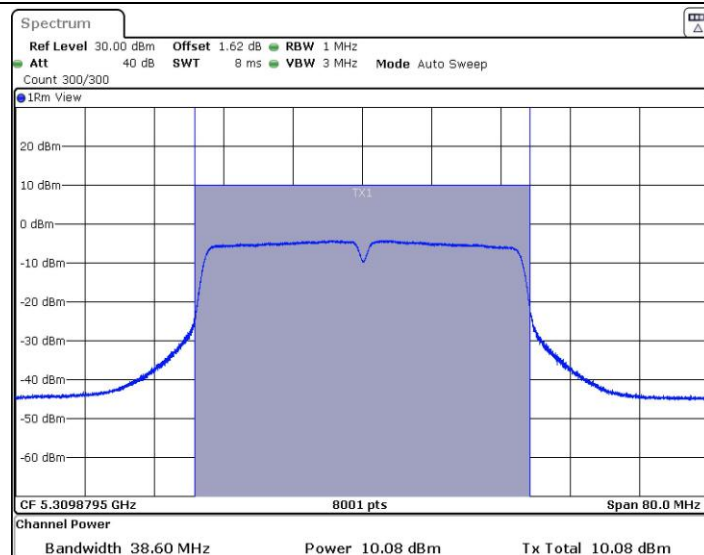
11N40SISO_Ant1_5190



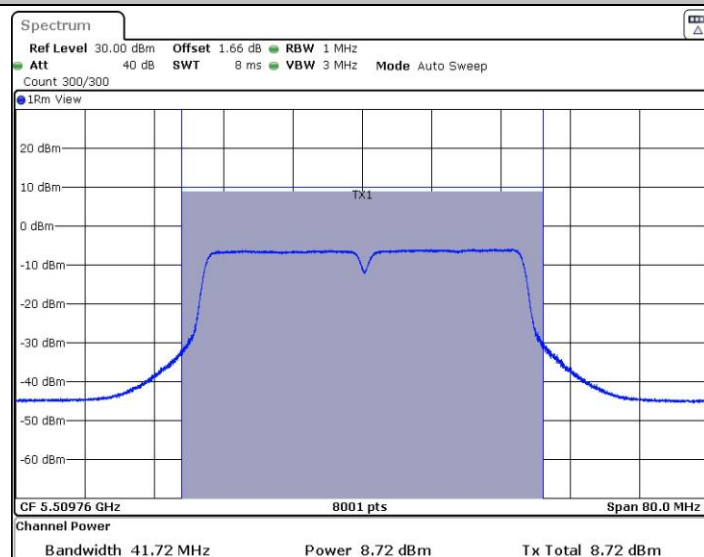
11N40SISO_Ant1_5230



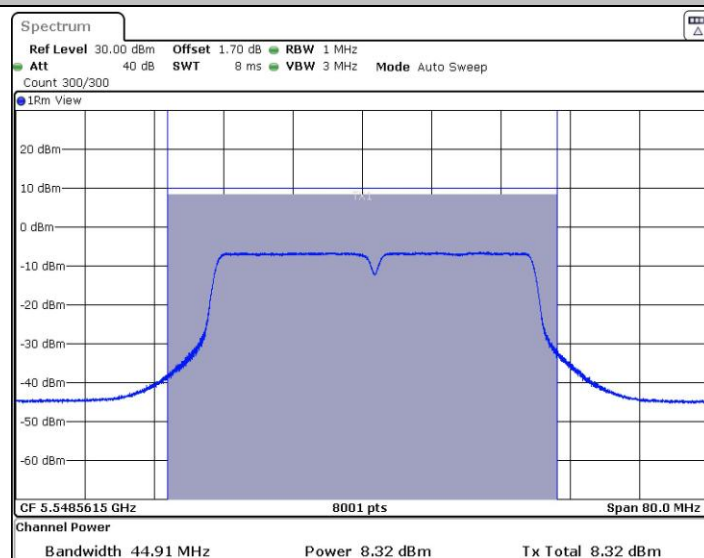
11N40SISO_Ant1_5270



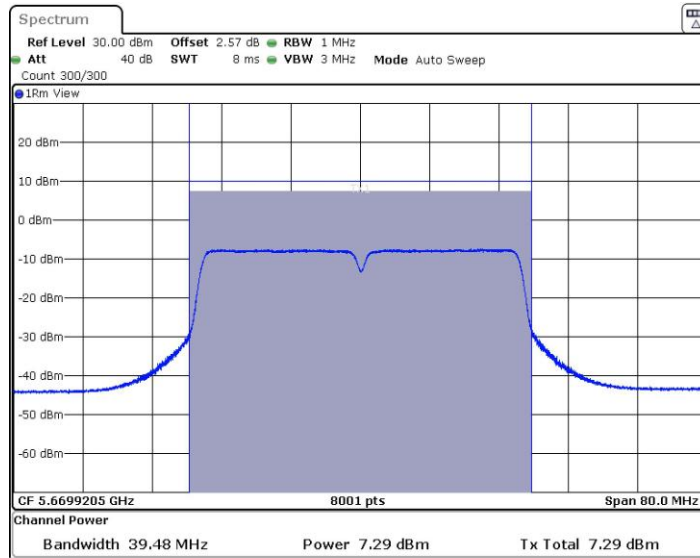
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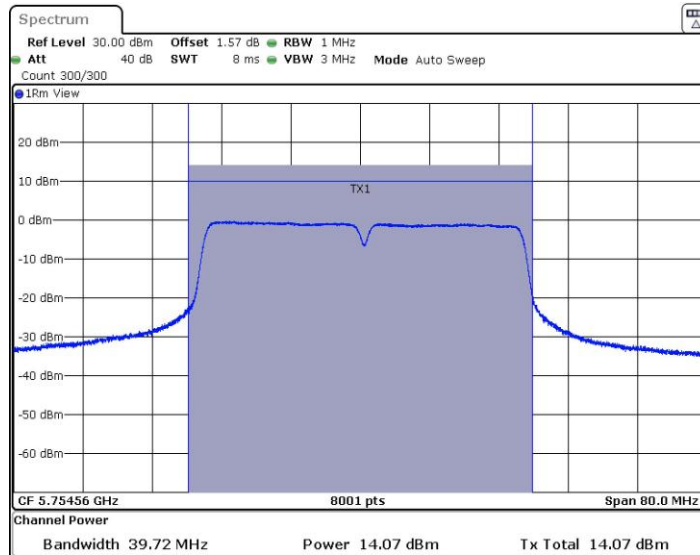
11N40SISO_Ant1_5510



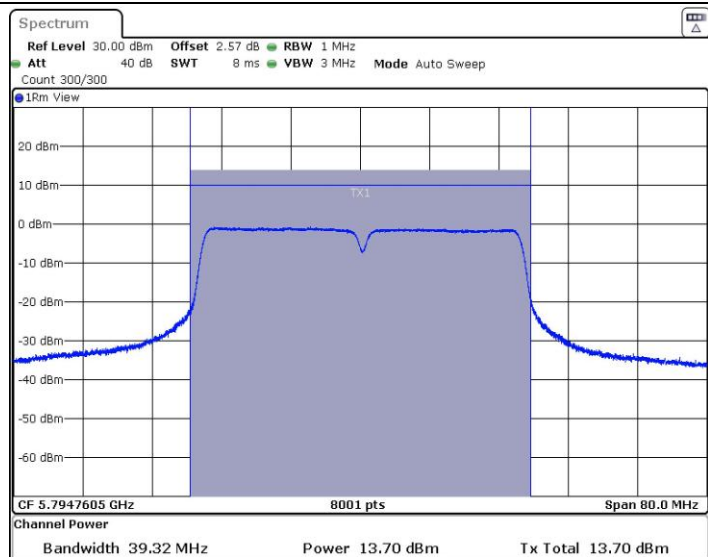
11N40SISO_Ant1_5550



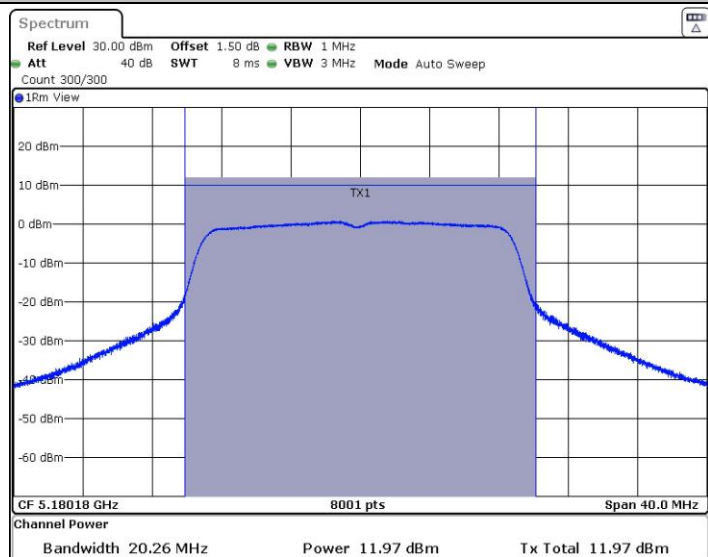
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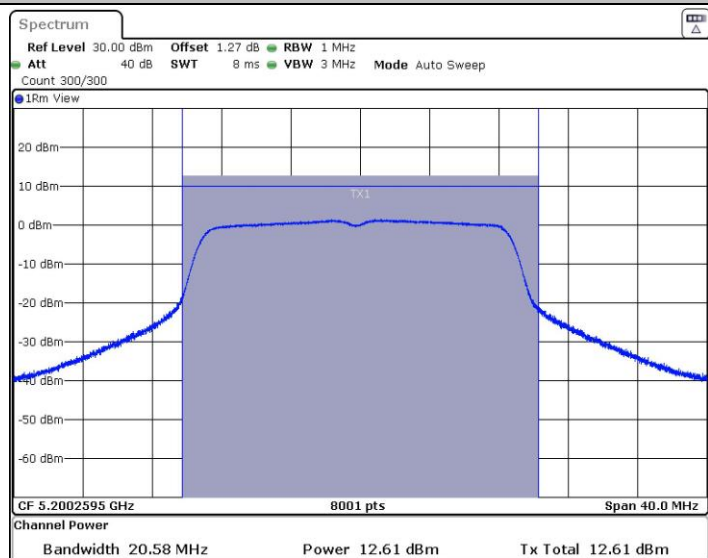
11N40SISO_Ant1_5755



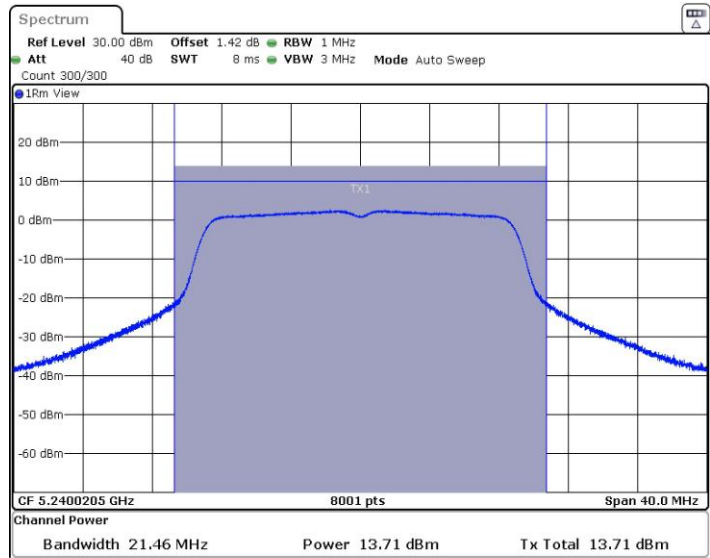
11N40SISO_Ant1_5795



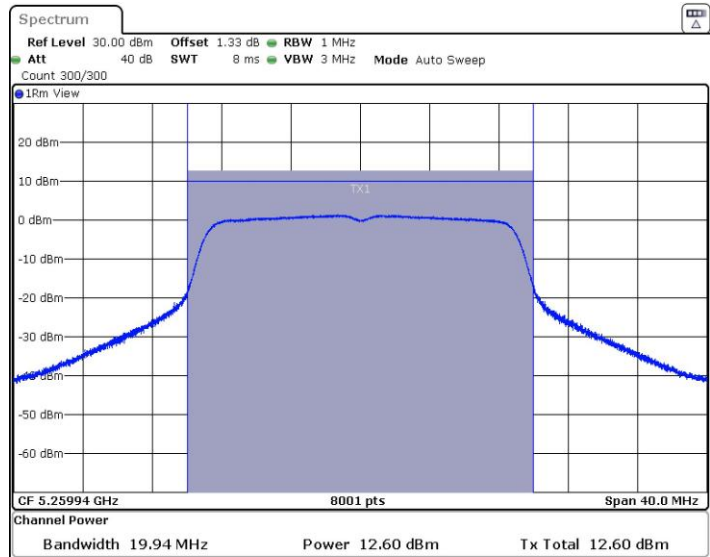
11AC20SISO_Ant1_5180



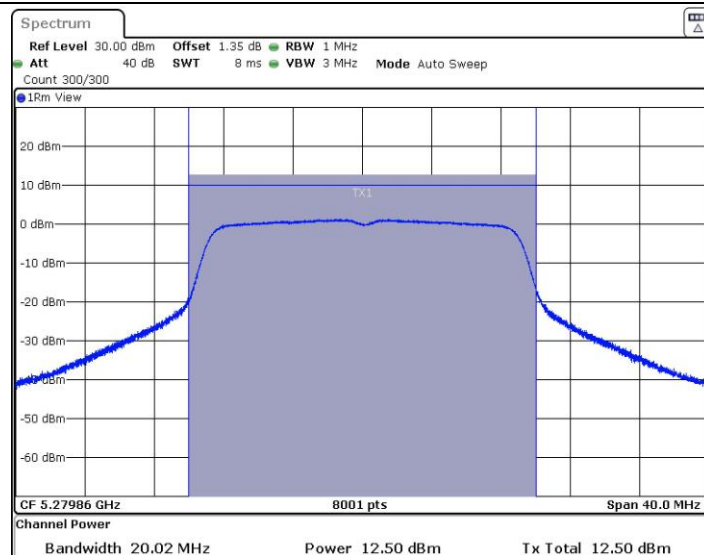
11AC20SISO_Ant1_5200



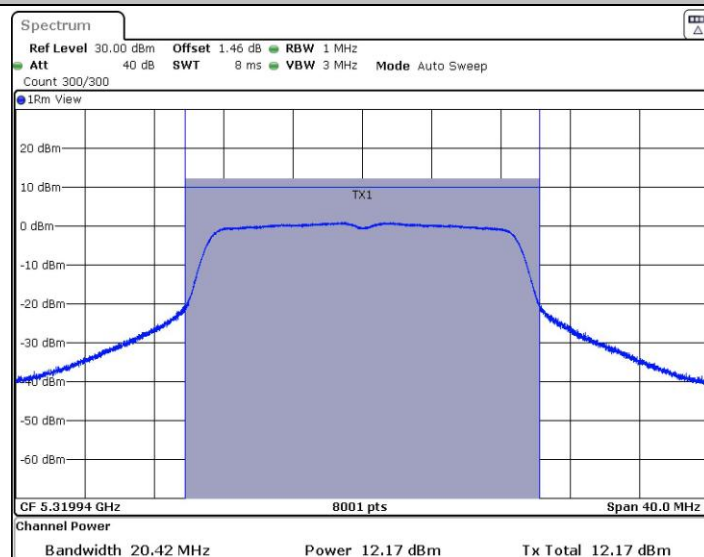
11AC20SISO_Ant1_5240



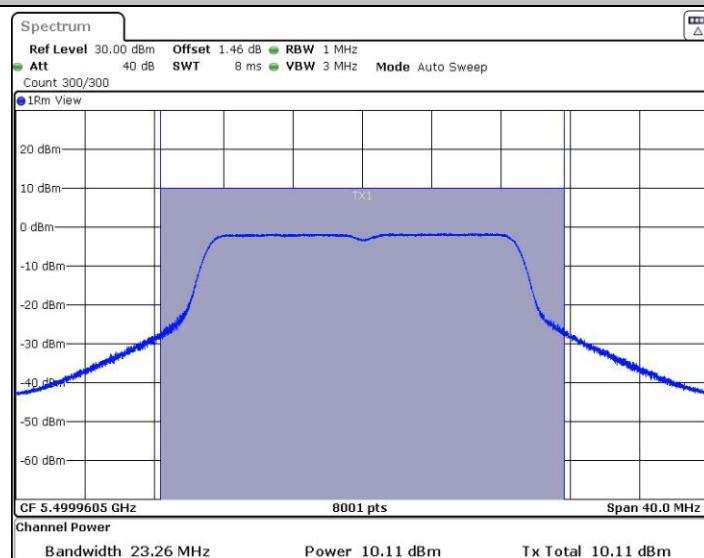
11AC20SISO_Ant1_5260



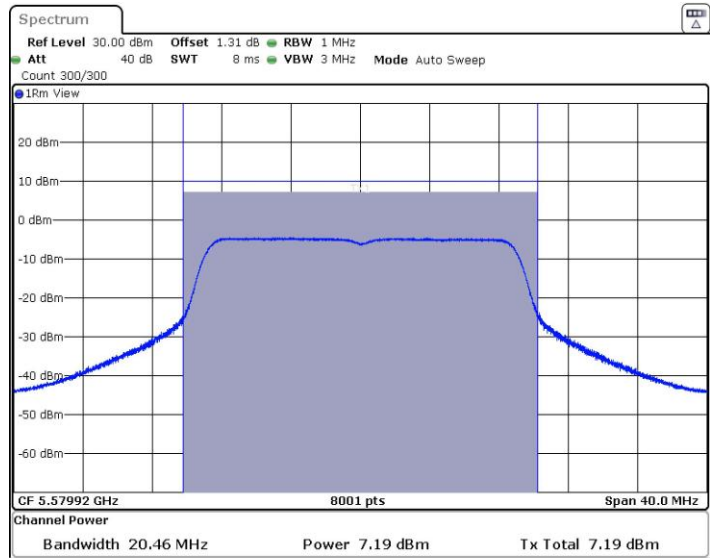
11AC20SISO_Ant1_5280



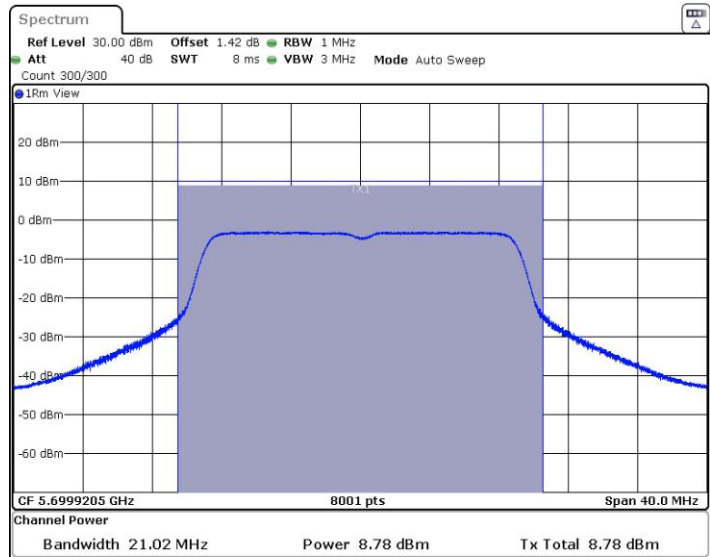
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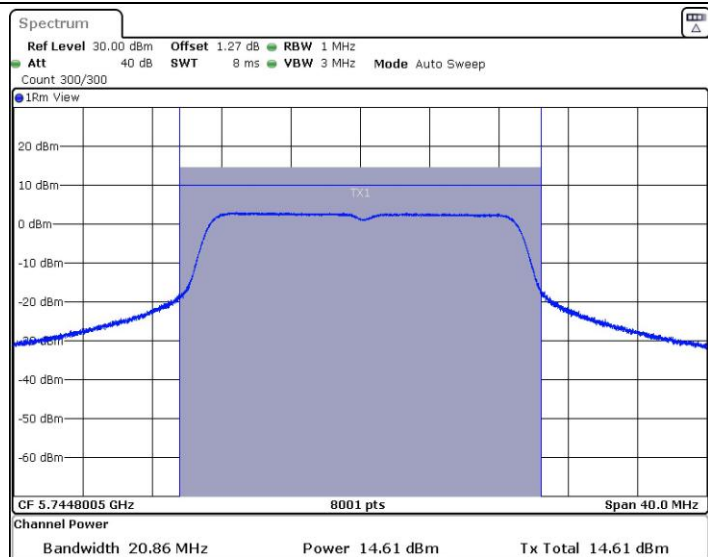
11AC20SISO_Ant1_5500



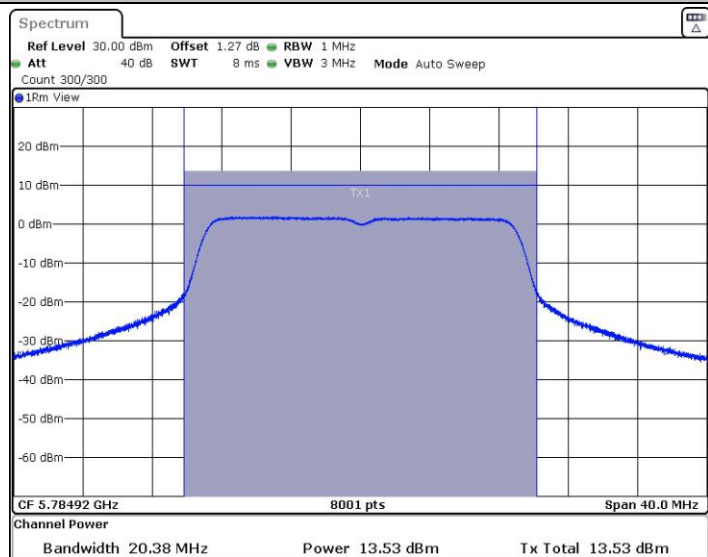
11AC20SISO_Ant1_5580



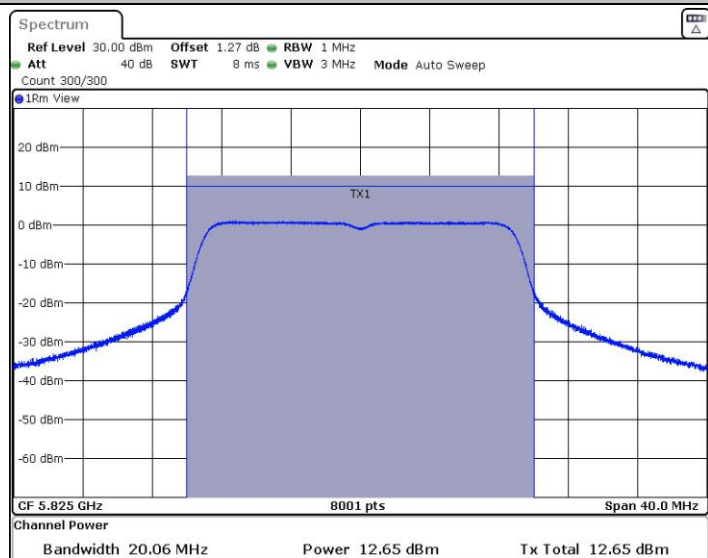
11AC20SISO_Ant1_5700



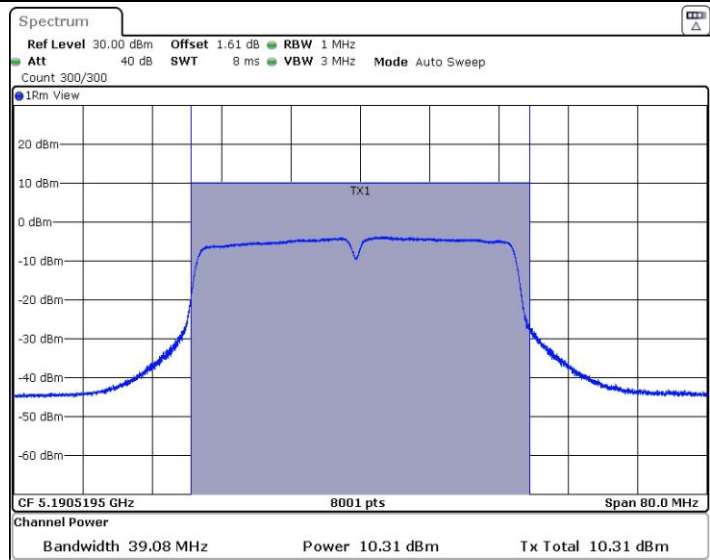
11AC20SISO_Ant1_5745



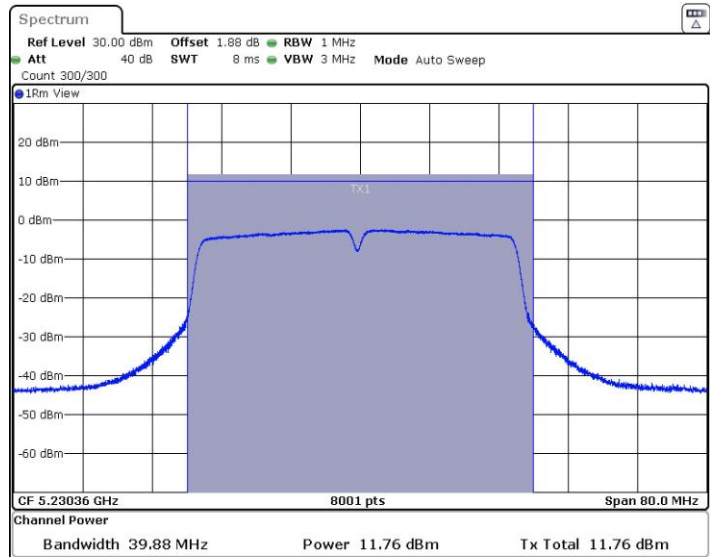
11AC20SISO_Ant1_5785



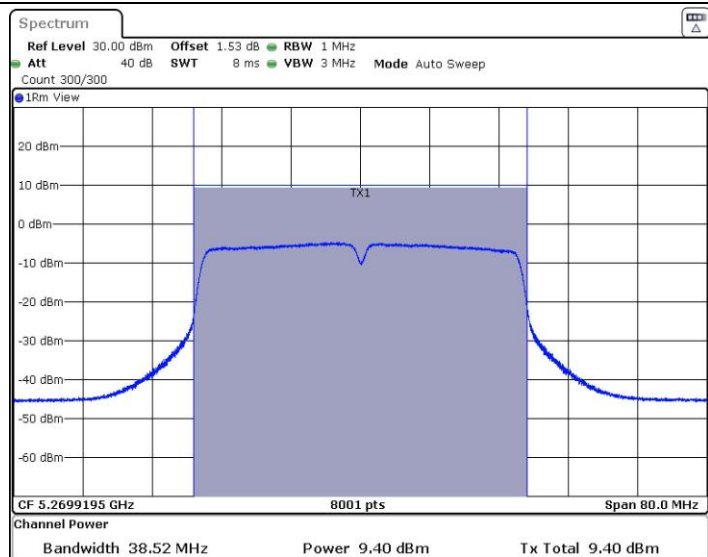
11AC20SISO_Ant1_5825



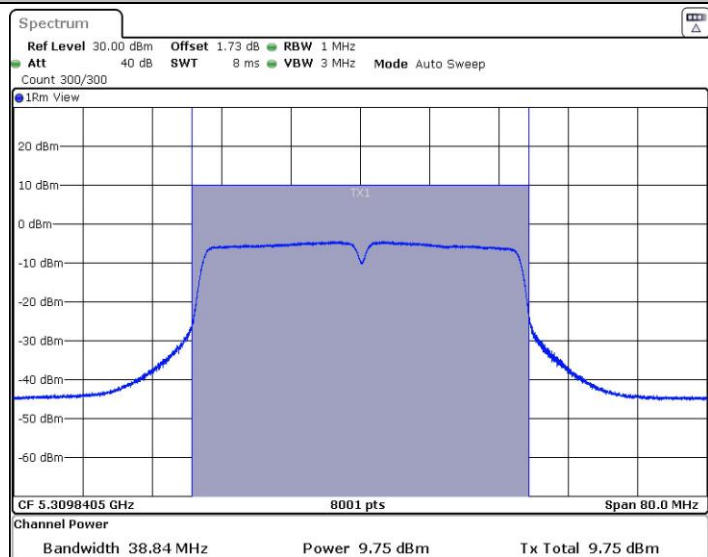
11AC40SISO_Ant1_5190



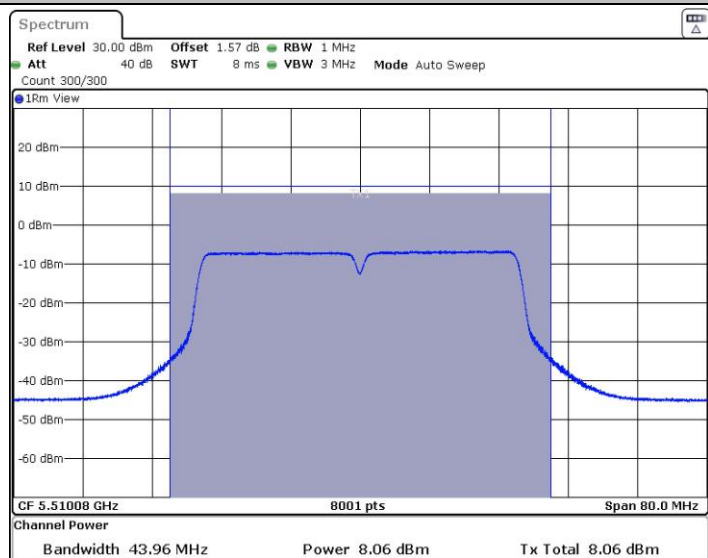
11AC40SISO_Ant1_5230



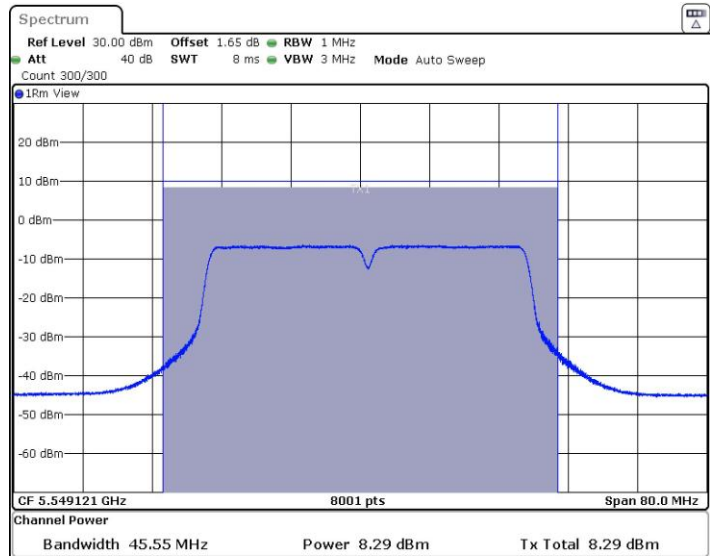
11AC40SISO_Ant1_5270



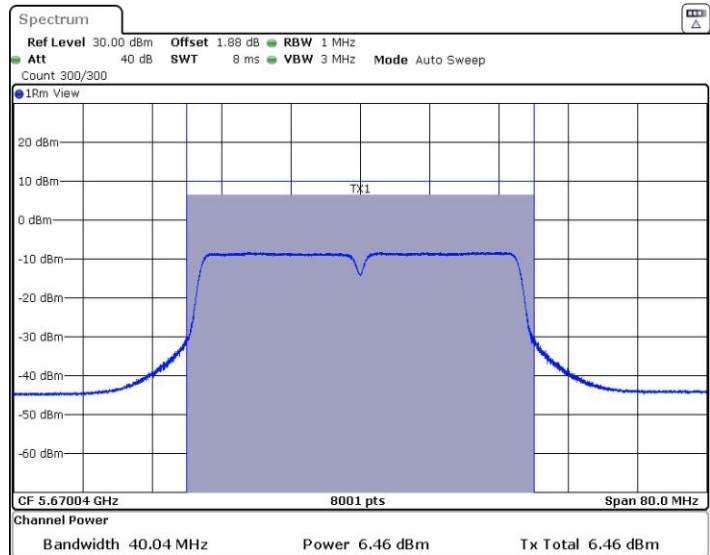
11AC40SISO_Ant1_5310



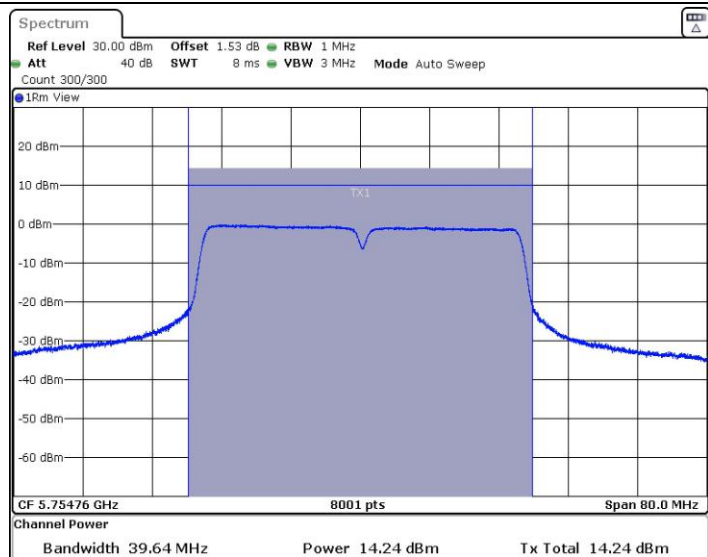
11AC40SISO_Ant1_5510



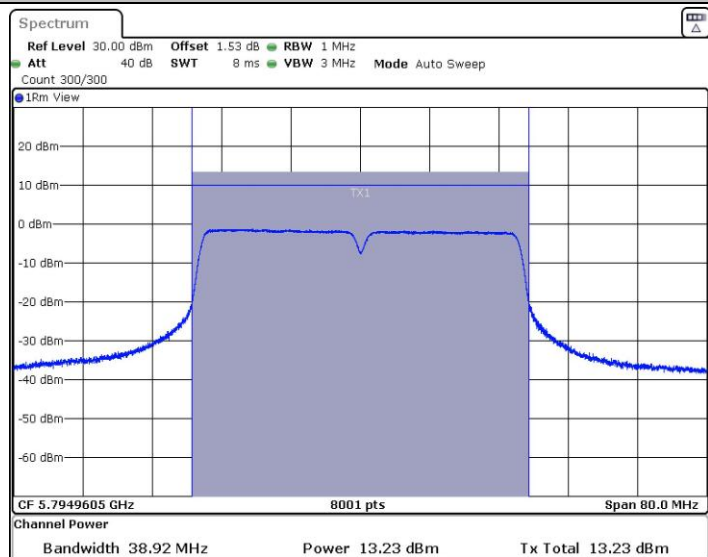
11AC40SISO_Ant1_5550



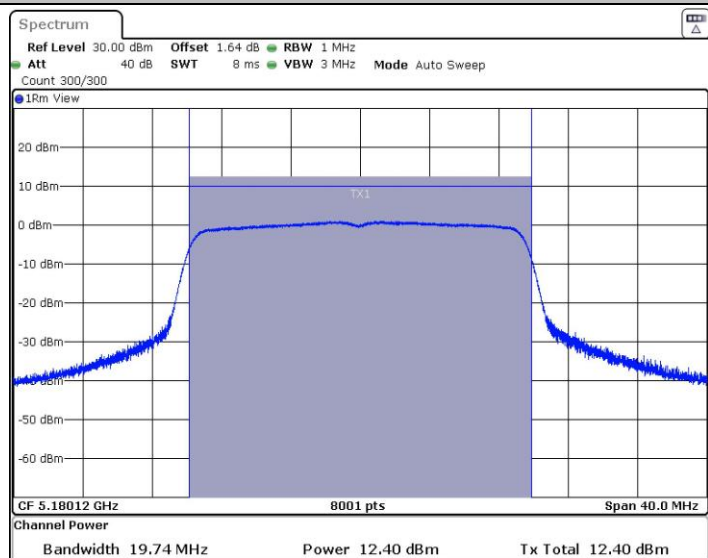
11AC40SISO_Ant1_5670



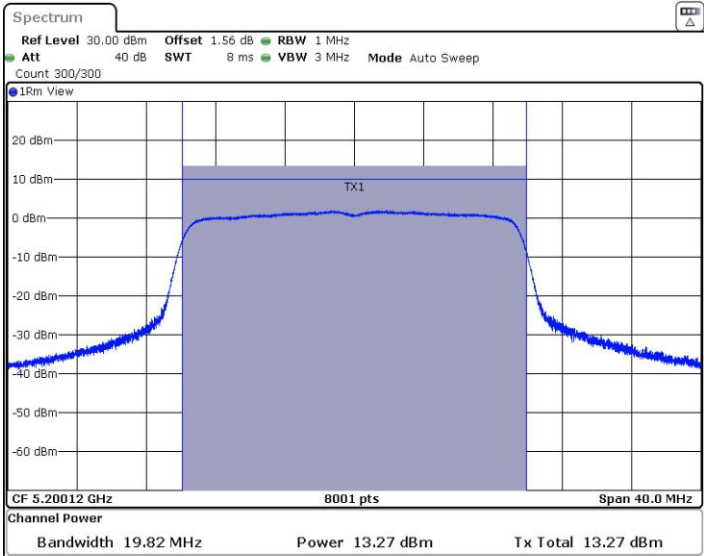
11AC40SISO_Ant1_5755



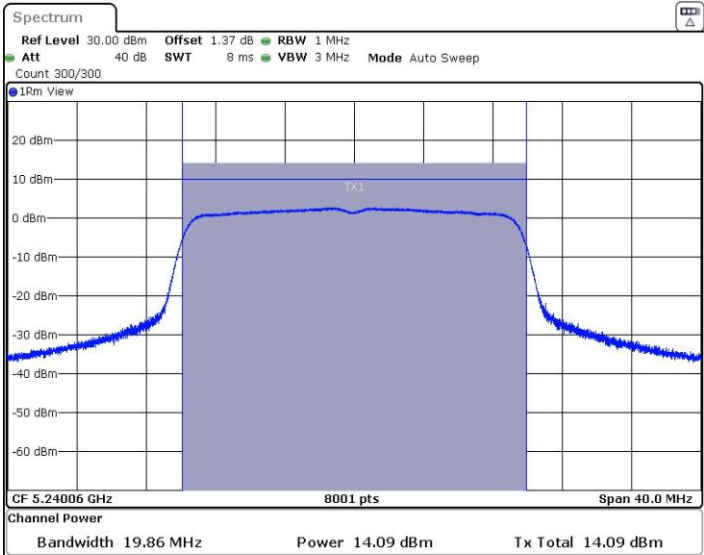
11AC40SISO_Ant1_5795



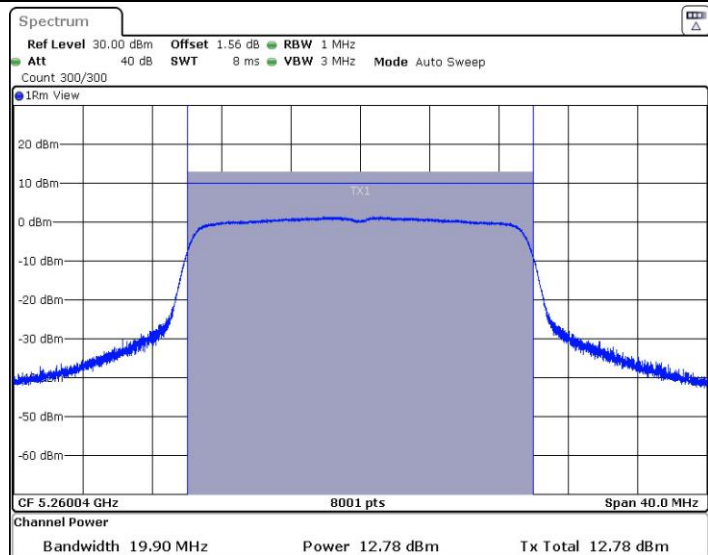
11AX20SISO_Ant1_5180



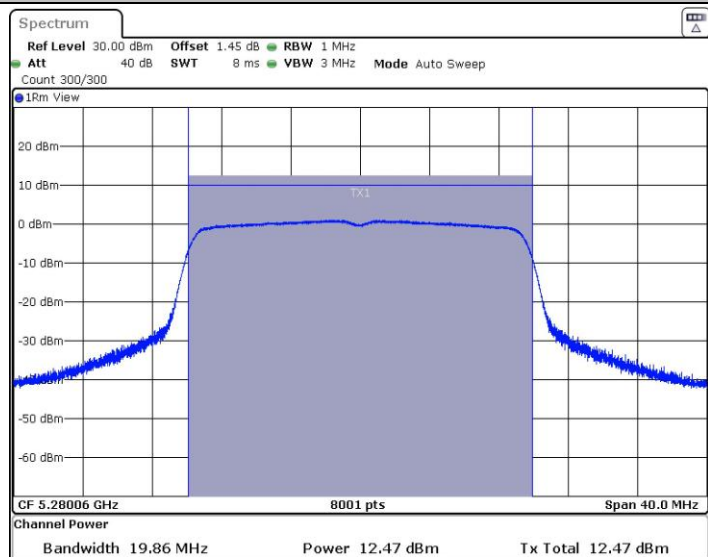
11AX20SISO_Ant1_5200



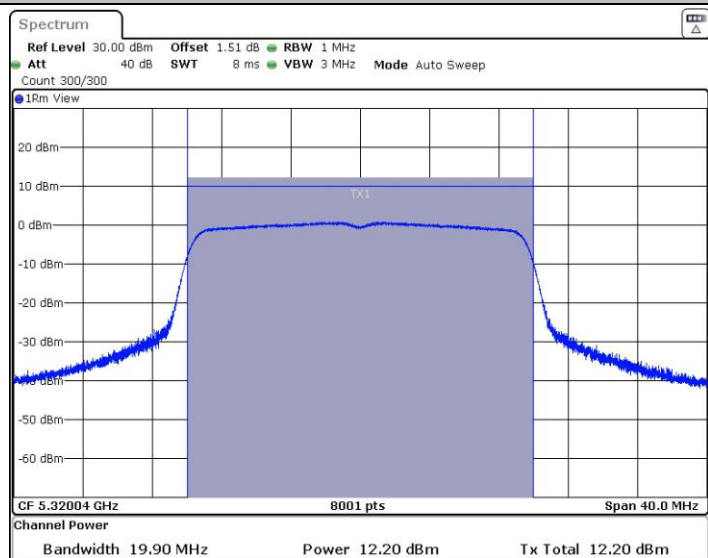
11AX20SISO_Ant1_5240



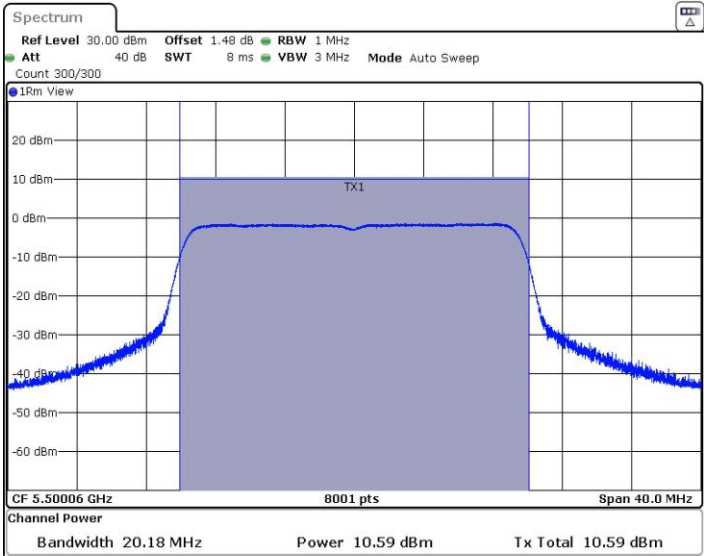
11AX20SISO_Ant1_5260



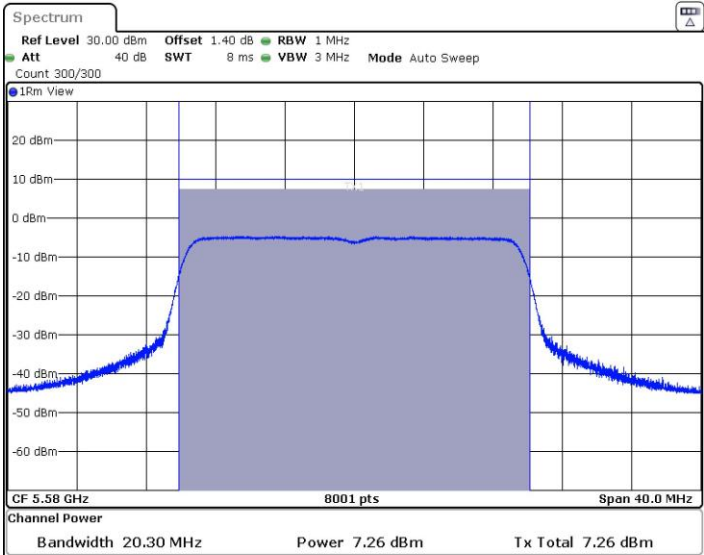
11AX20SISO_Ant1_5280



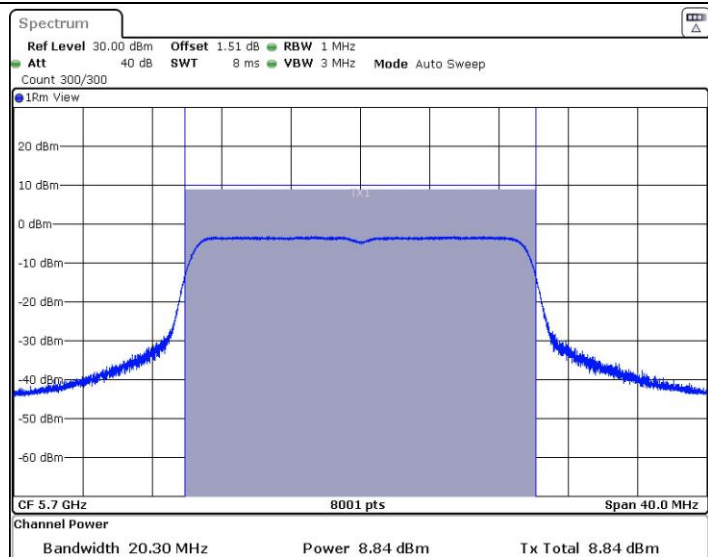
11AX20SISO_Ant1_5320



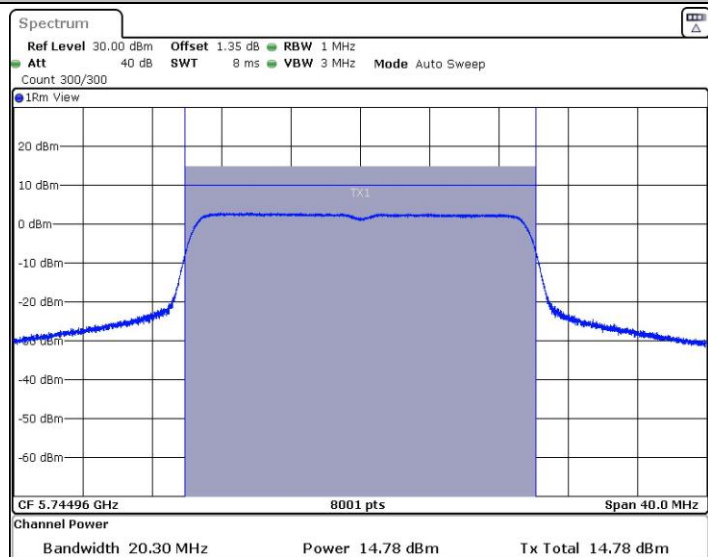
11AX20SISO_Ant1_5500



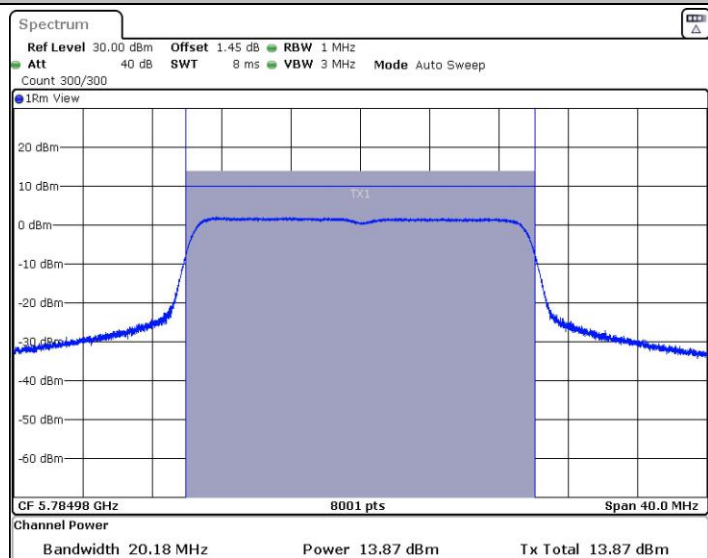
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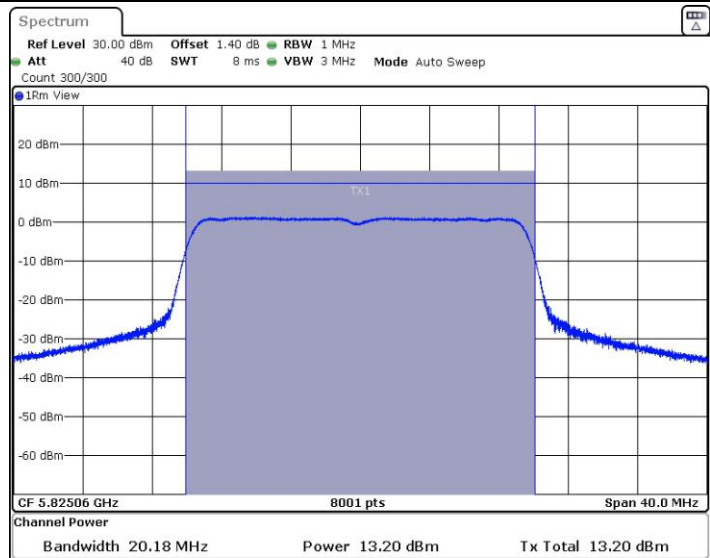
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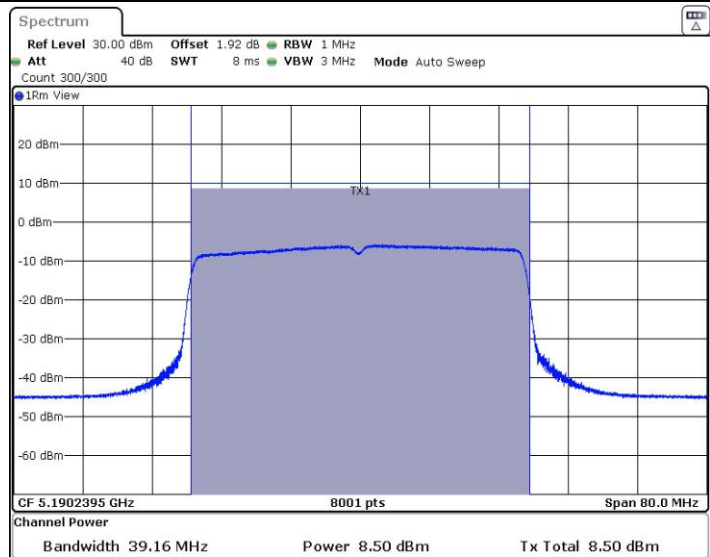
11AX20SISO_Ant1_5745



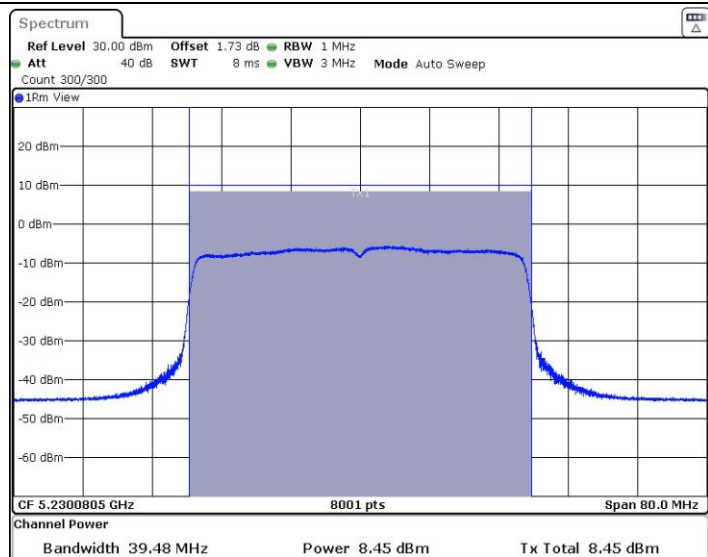
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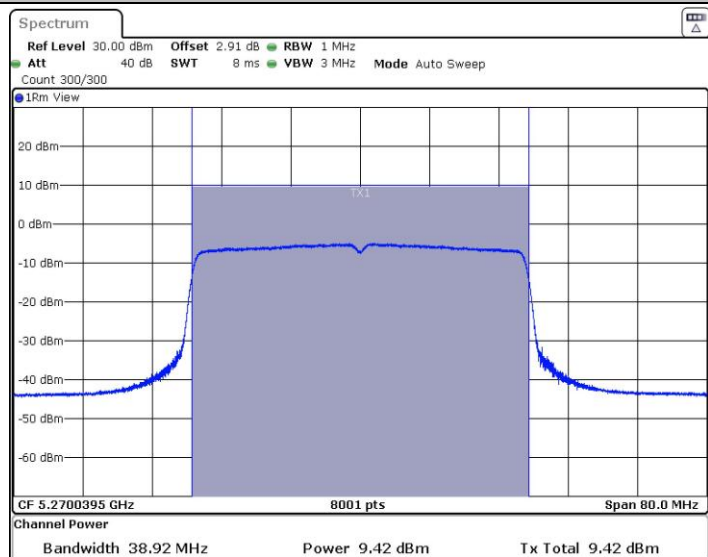
11AX20SISO_Ant1_5825



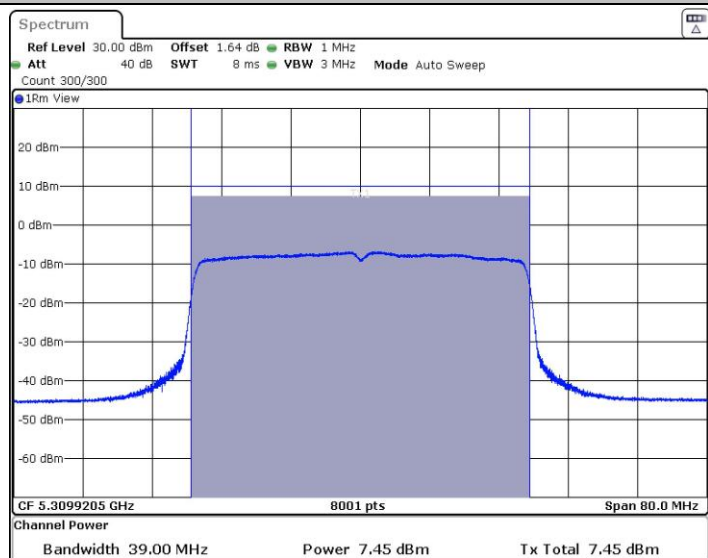
11AX40SISO_Ant1_5190



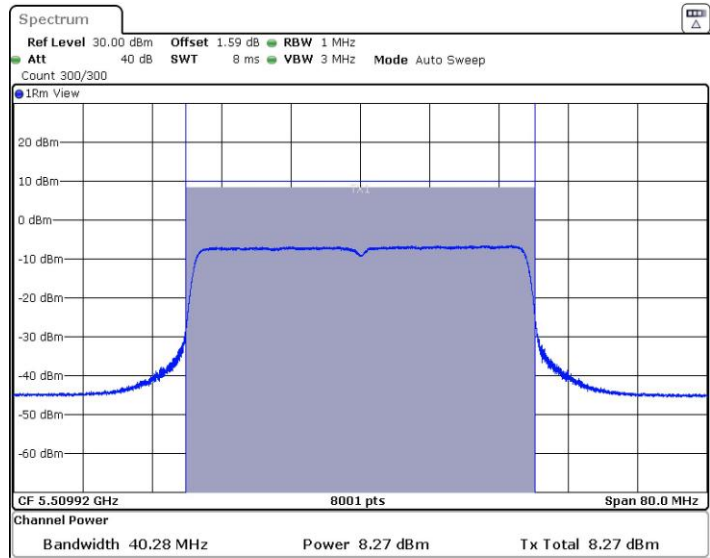
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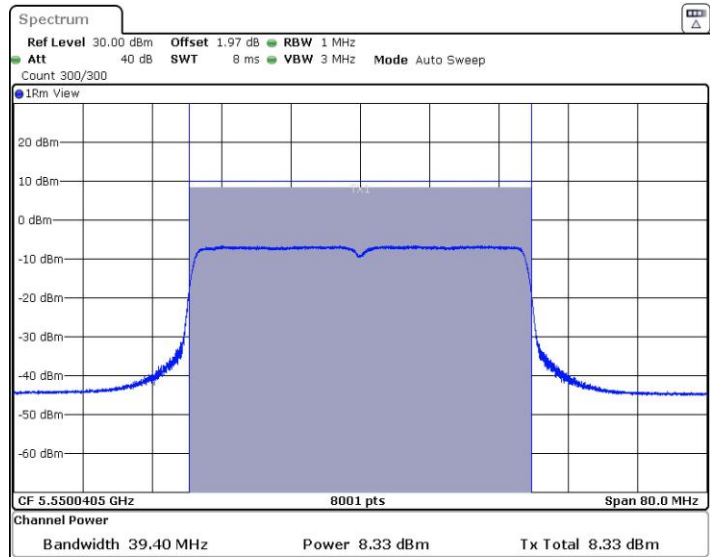
11AX40SISO_Ant1_5270



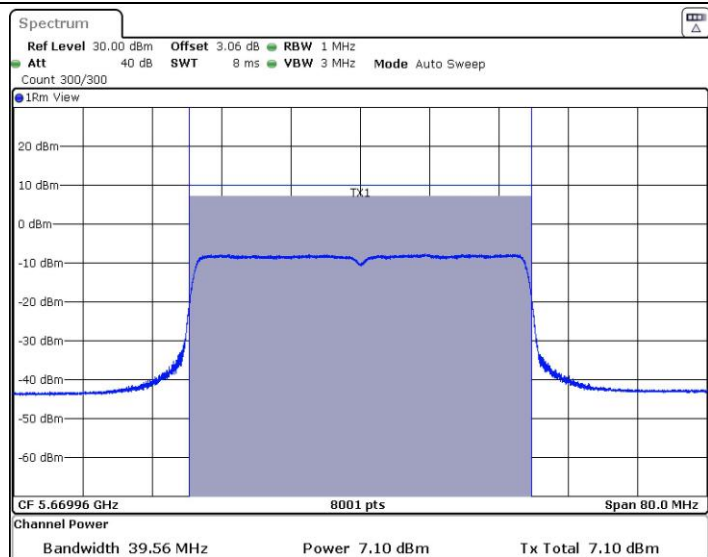
11AX40SISO_Ant1_5310



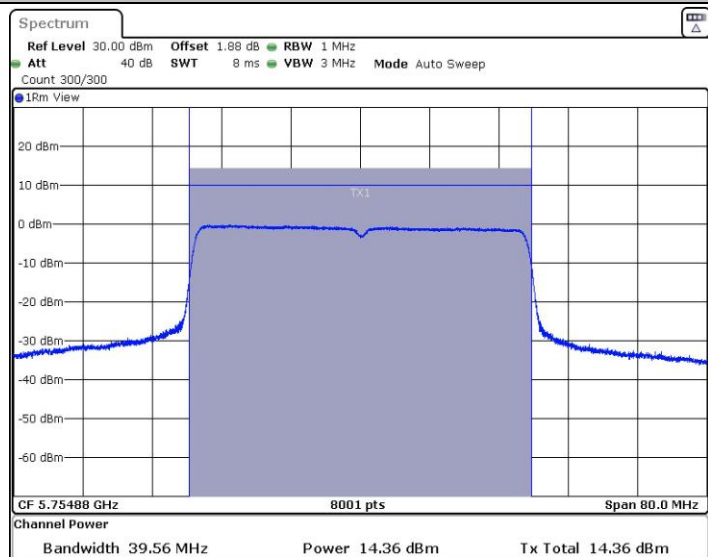
11AX40SISO_Ant1_5510



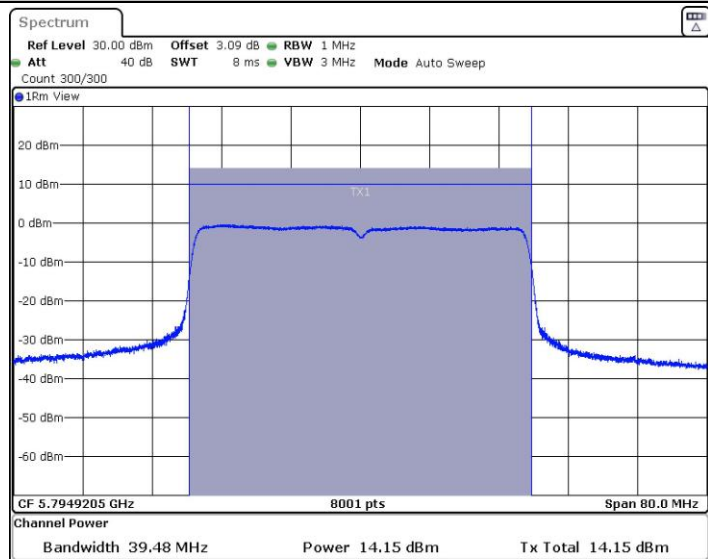
11AX40SISO_Ant1_5550



11AX40SISO_Ant1_5670



11AX40SISO_Ant1_5755



11AX40SISO_Ant1_5795

Appendix C: Maximum power spectral density

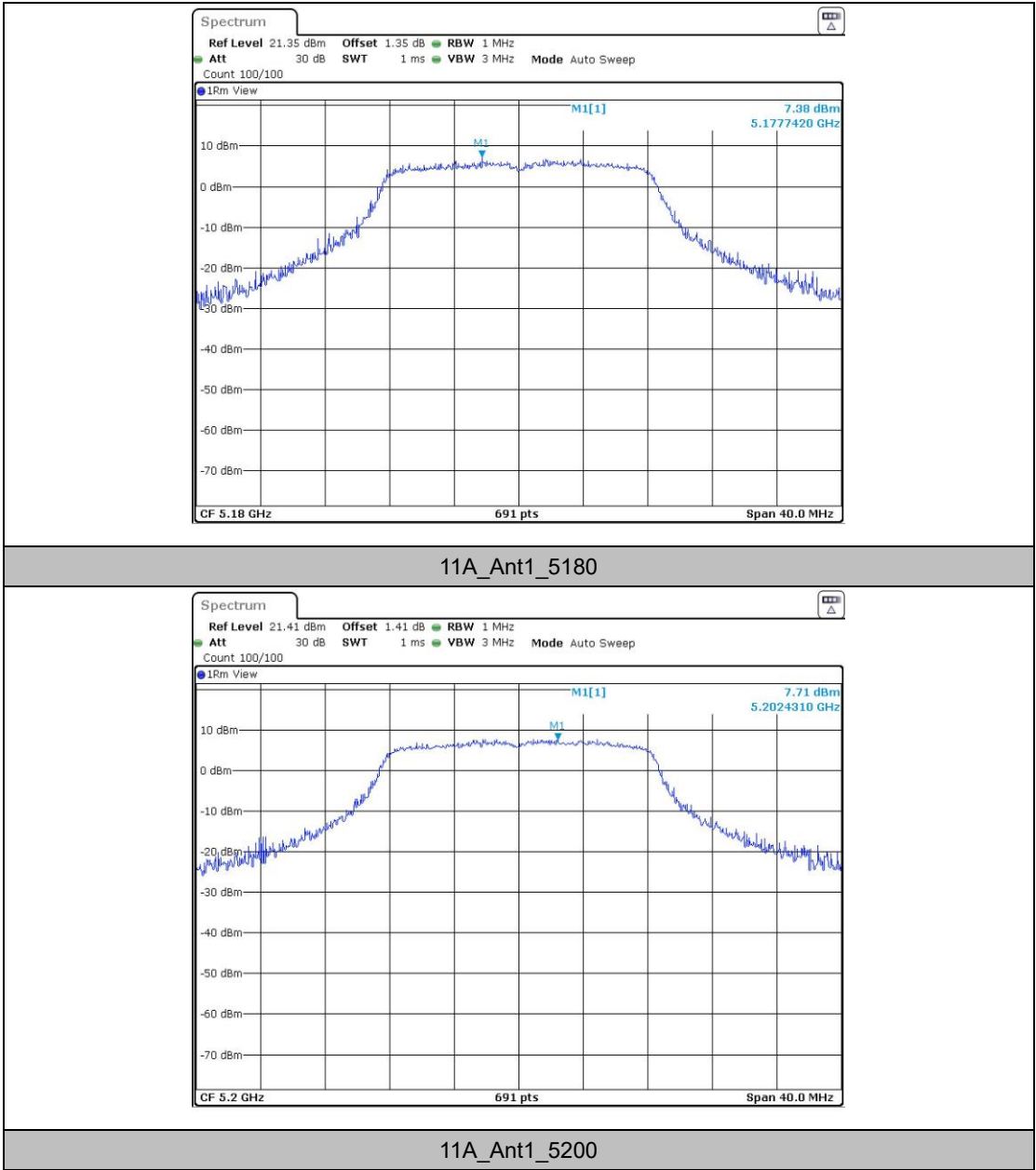
Test Result

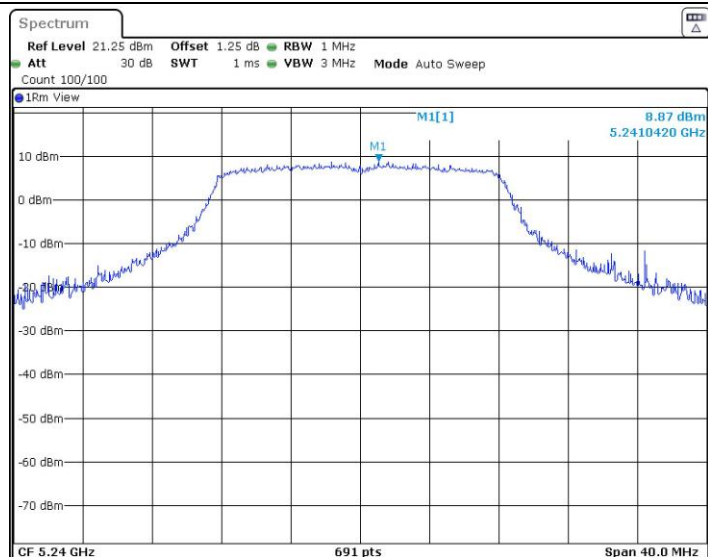
TestMode	Antenna	Channel	Result[dBm/MHz]	Limit[dBm/MHz]	Verdict
11A	Ant1	5180	7.38	<=11	PASS
		5200	7.71	<=11	PASS
		5240	8.87	<=11	PASS
		5260	7.35	<=11	PASS
		5280	7.24	<=11	PASS
		5320	6.48	<=11	PASS
		5500	5.01	<=11	PASS
		5580	2.04	<=11	PASS
		5700	3.91	<=11	PASS
		5745	8.14	<=30	PASS
		5785	6.58	<=30	PASS
		5825	5.79	<=30	PASS
11N20SISO	Ant1	5180	6.96	<=11	PASS
		5200	7.19	<=11	PASS
		5240	8.18	<=11	PASS
		5260	6.89	<=11	PASS
		5280	6.63	<=11	PASS
		5320	6.43	<=11	PASS
		5500	4.65	<=11	PASS
		5580	1.46	<=11	PASS
		5700	2.83	<=11	PASS
		5745	7.7	<=30	PASS
		5785	6.52	<=30	PASS
		5825	6.84	<=30	PASS
11N40SISO	Ant1	5190	1.56	<=11	PASS
		5230	3.6	<=11	PASS
		5270	2.24	<=11	PASS
		5310	1.43	<=11	PASS
		5510	0.28	<=11	PASS
		5550	-0.26	<=11	PASS
		5670	-1.04	<=11	PASS
		5755	4.76	<=30	PASS
		5795	4.68	<=30	PASS
11AC20SISO	Ant1	5180	6.22	<=11	PASS
		5200	6.67	<=11	PASS
		5240	8.47	<=11	PASS
		5260	6.99	<=11	PASS
		5280	6.81	<=11	PASS

		5320	6.71	<=11	PASS
		5500	4.46	<=11	PASS
		5580	1.34	<=11	PASS
		5700	2.76	<=11	PASS
		5745	7.41	<=30	PASS
		5785	6.08	<=30	PASS
		5825	5.5	<=30	PASS
11AC40SISO	Ant1	5190	1.57	<=11	PASS
		5230	3.43	<=11	PASS
		5270	1.57	<=11	PASS
		5310	1.11	<=11	PASS
		5510	-0.22	<=11	PASS
		5550	-0.24	<=11	PASS
		5670	-1.37	<=11	PASS
		5755	4.01	<=30	PASS
		5795	3.06	<=30	PASS
11AX20SISO	Ant1	5180	9.18	<=11	PASS
		5200	9.66	<=11	PASS
		5240	8.67	<=11	PASS
		5260	9.85	<=11	PASS
		5280	9.55	<=11	PASS
		5320	9.84	<=11	PASS
		5500	7.47	<=11	PASS
		5580	4.1	<=11	PASS
		5700	5.19	<=11	PASS
		5745	10.32	<=30	PASS
		5785	9.23	<=30	PASS
		5825	8.51	<=30	PASS
11AX40SISO	Ant1	5190	1.95	<=11	PASS
		5230	3.56	<=11	PASS
		5270	3.87	<=11	PASS
		5310	1.19	<=11	PASS
		5510	2.28	<=11	PASS
		5550	2.77	<=11	PASS
		5670	2.69	<=11	PASS
		5755	6.56	<=30	PASS
		5795	6.1	<=30	PASS

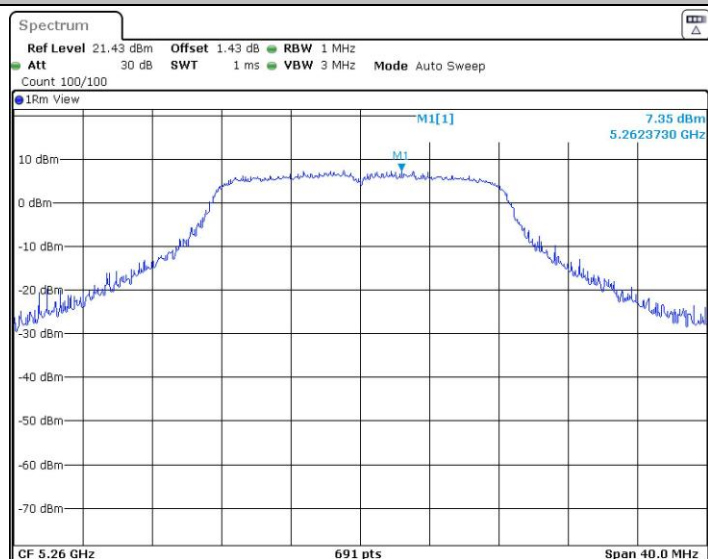
Note: The Duty Cycle Factor is compensated in the graph.

Test Graphs

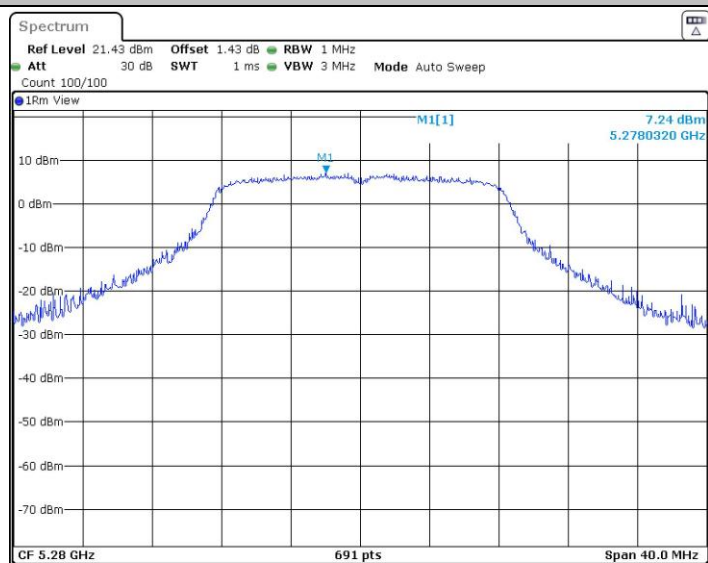




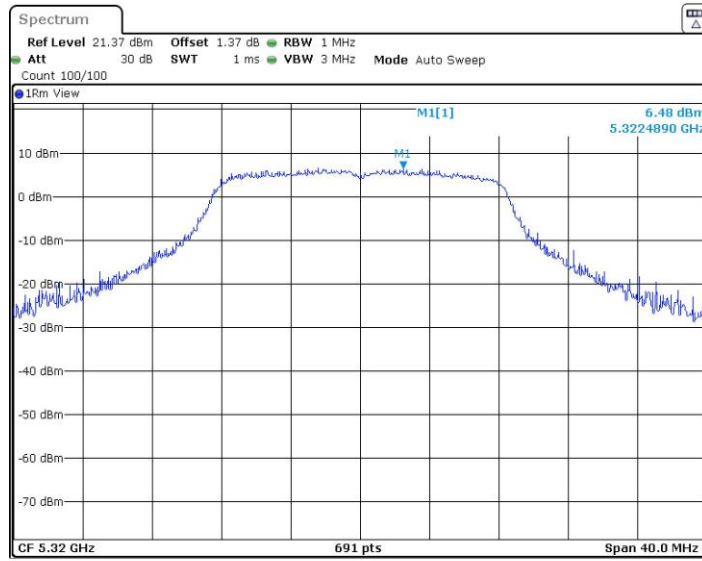
11A_Ant1_5240



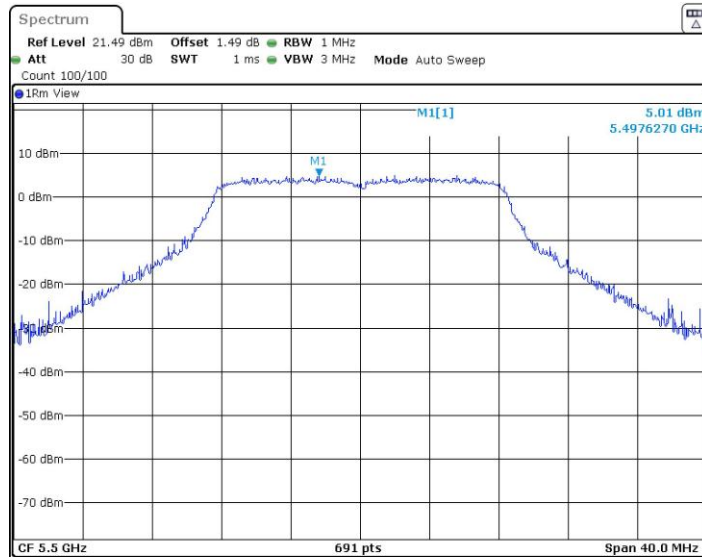
11A_Ant1_5260



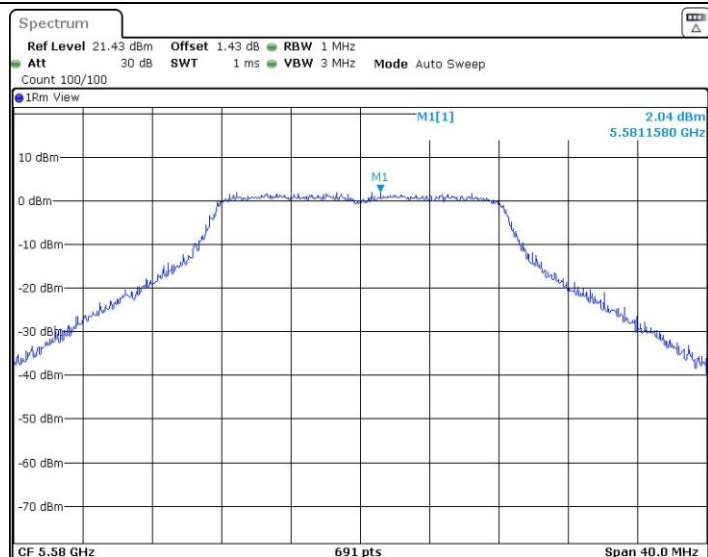
11A_Ant1_5280



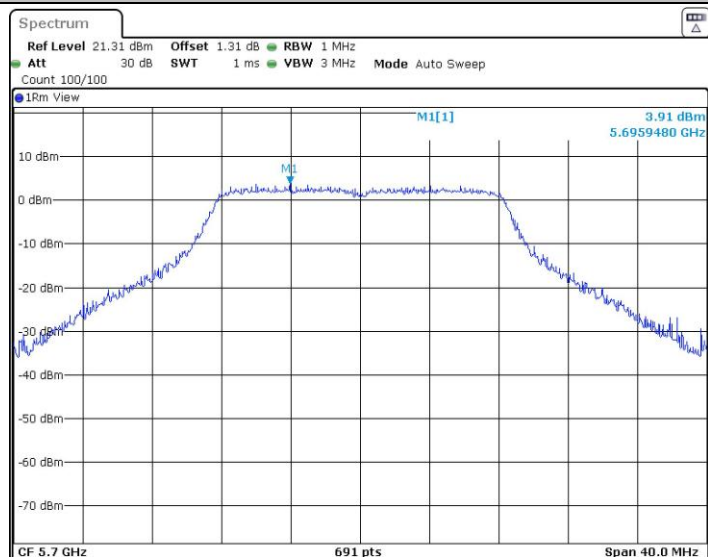
11A_Ant1_5320



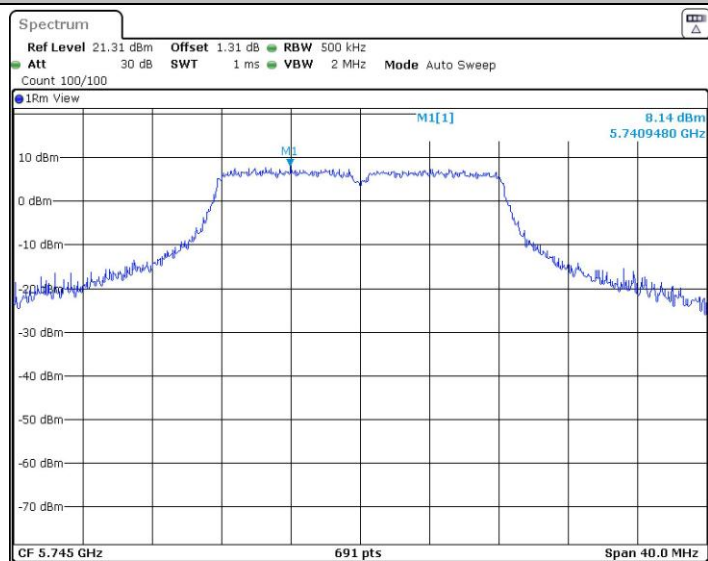
11A_Ant1_5500



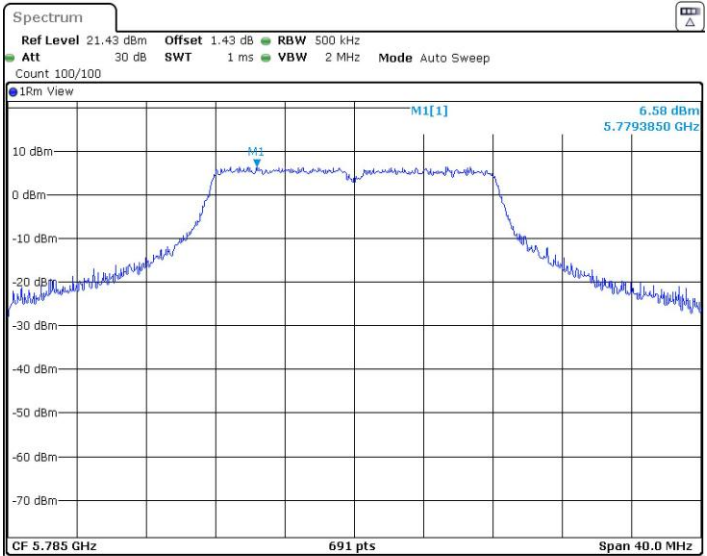
11A_Ant1_5580



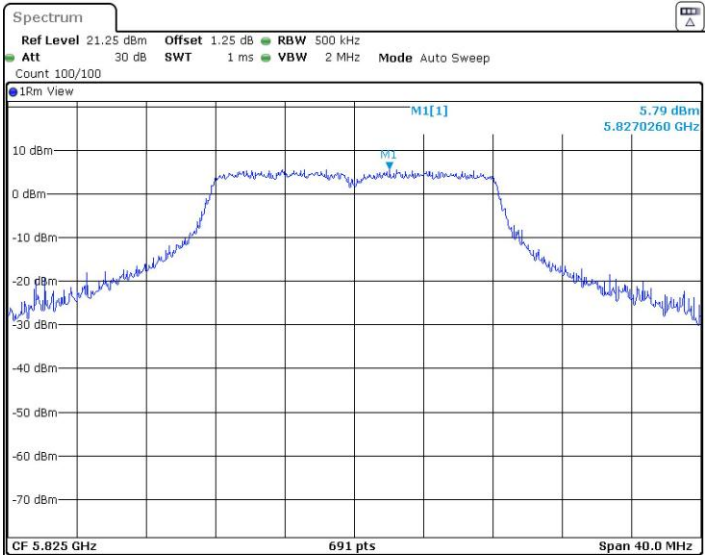
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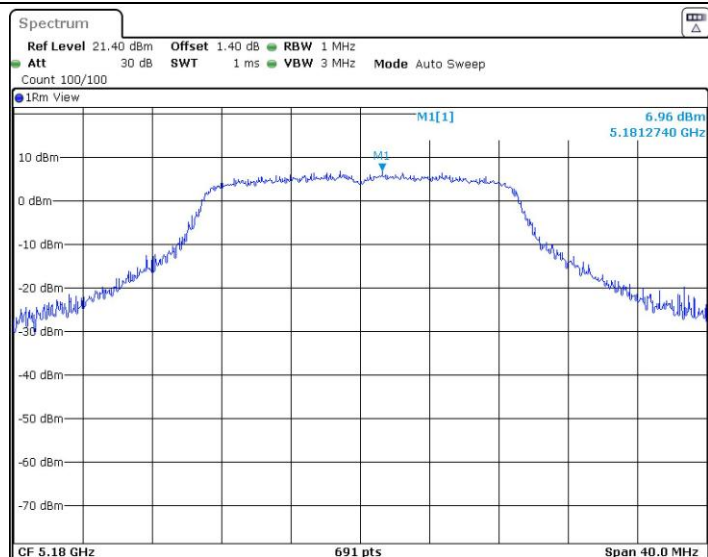
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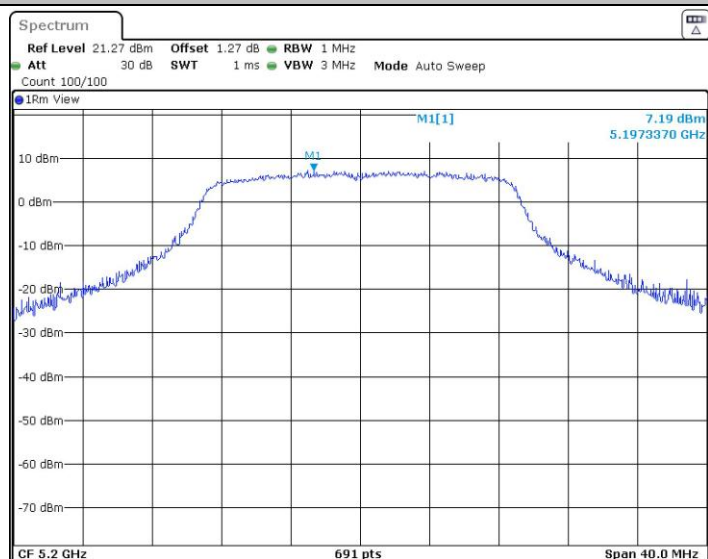
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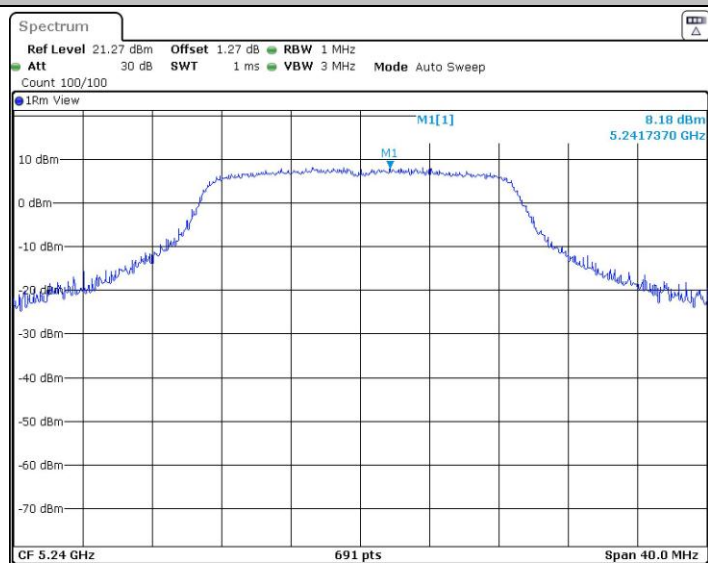
11A_Ant1_5825



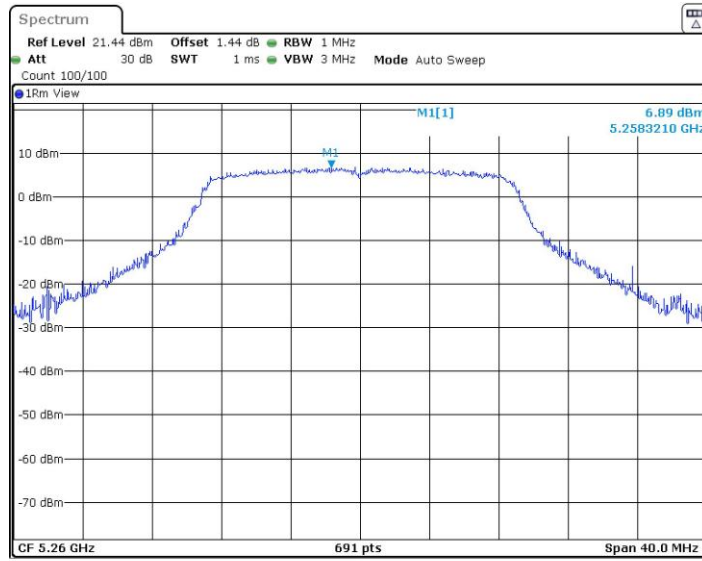
11N20SISO_Ant1_5180



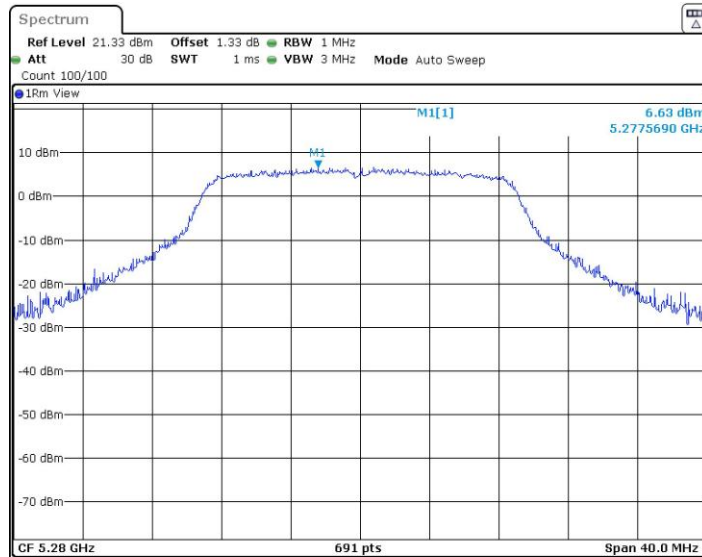
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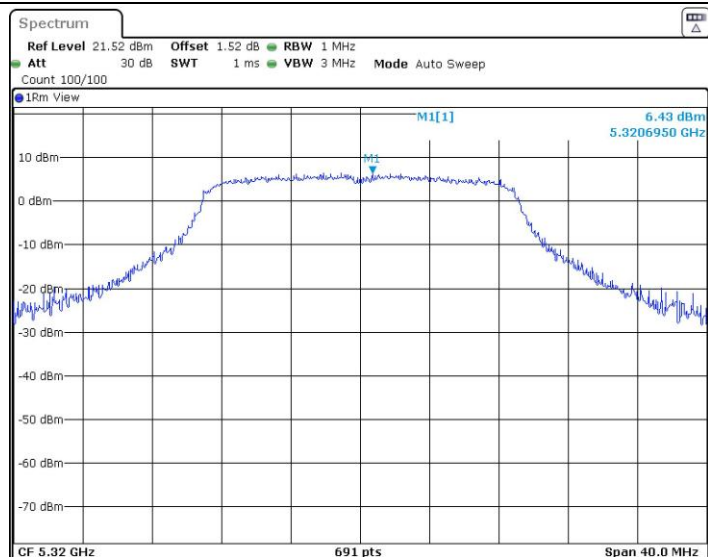
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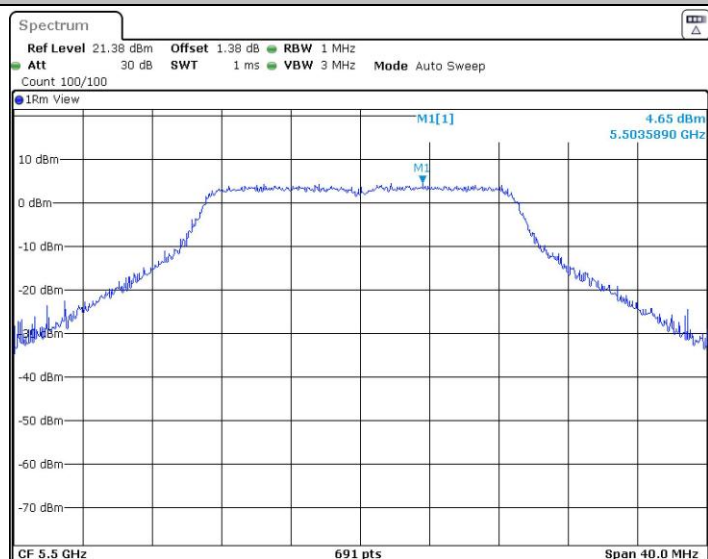
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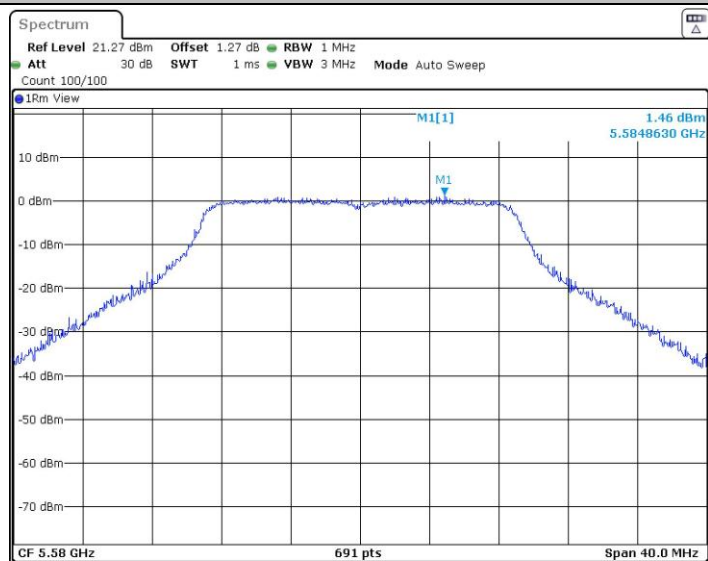
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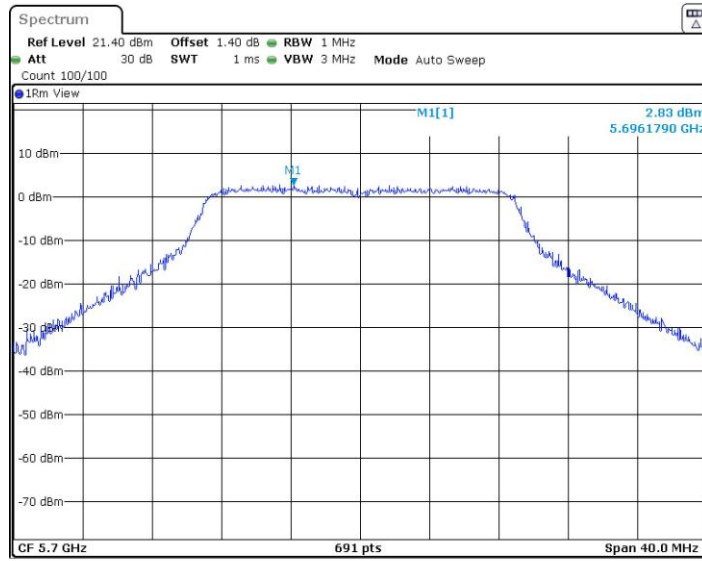
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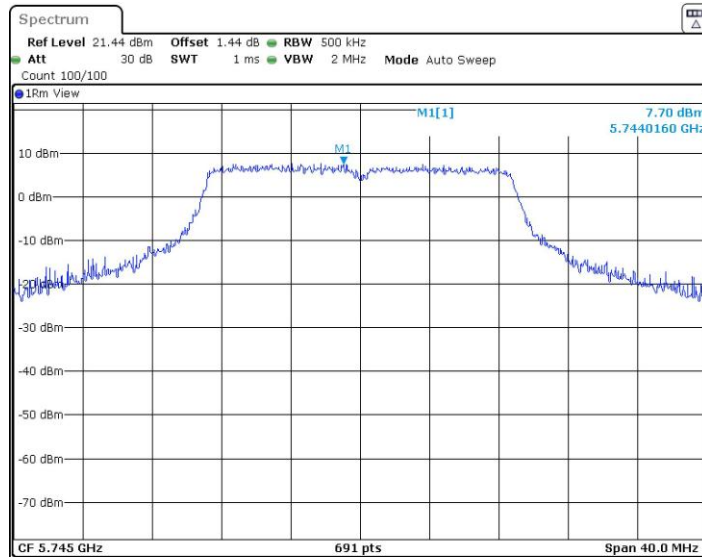
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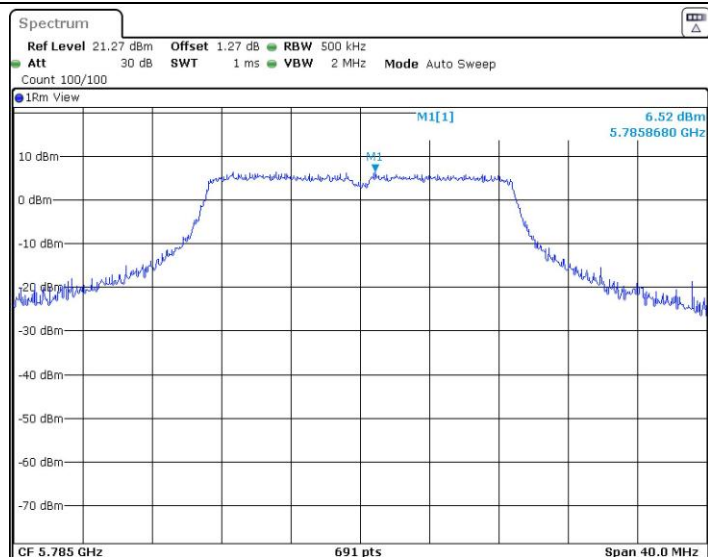
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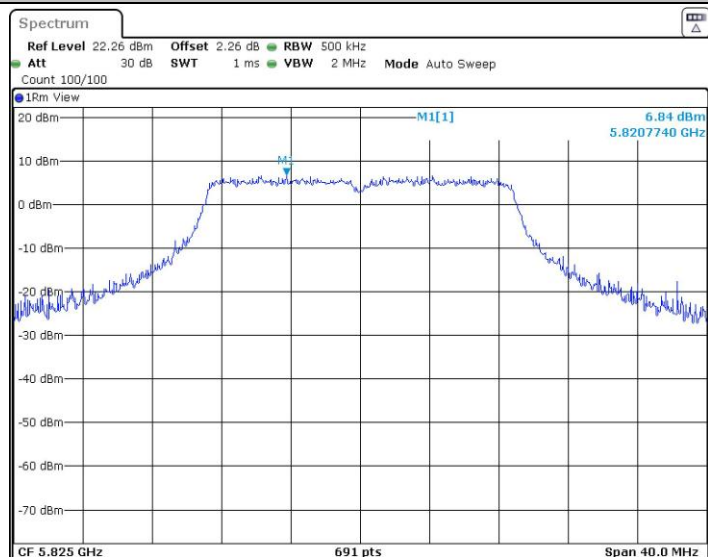
11N20SISO_Ant1_5700



11N20SISO_Ant1_5745



11N20SISO_Ant1_5785



11N20SISO_Ant1_5825

