

Appendix B.4: Maximum conducted output power

Test Result Channel Power

Test Mode	Antenna	Frequency[M Hz]	Channel Power [dBm]	Duty Cycle [%]	DC Factor [dBm]	Result [dBm]	Limit [dBm]	Verdict
11A	Ant2	5180	13.26	95.86	0.18	13.44	≤23.98	PASS
		5200	14.17	95.89	0.18	14.35	≤23.98	PASS
		5240	9.17	95.89	0.18	9.35	≤23.98	PASS
11N20SISO	Ant2	5180	12.76	95.59	0.20	12.96	≤23.98	PASS
		5200	13.59	96.32	0.16	13.75	≤23.98	PASS
		5240	11.29	95.59	0.20	11.49	≤23.98	PASS
11N40SISO	Ant2	5190	12.60	91.55	0.38	12.98	≤23.98	PASS
		5230	9.91	91.55	0.38	10.29	≤23.98	PASS
11AC20SISO	Ant2	5180	13.76	93.24	0.30	14.06	≤23.98	PASS
		5200	13.71	91.89	0.37	14.08	≤23.98	PASS
		5240	9.09	91.89	0.37	9.46	≤23.98	PASS
11AC40SISO	Ant2	5190	10.98	48.61	3.13	14.11	≤23.98	PASS
		5230	10.17	85.37	0.69	10.86	≤23.98	PASS
11AC80SISO	Ant2	5210	10.86	32.73	4.85	15.71	≤23.98	PASS
11AX20SISO	Ant2	5180	13.02	90.00	0.46	13.48	≤23.98	PASS
		5200	13.49	90.00	0.46	13.95	≤23.98	PASS
		5240	10.40	90.00	0.46	10.86	≤23.98	PASS
11AX40SISO	Ant2	5190	12.68	83.33	0.79	13.47	≤23.98	PASS
		5230	9.27	83.33	0.79	10.06	≤23.98	PASS
11AX80SISO	Ant2	5210	8.25	31.48	5.02	13.27	≤23.98	PASS

Note: The Duty Cycle Factor is compensated in the graph.

Appendix B.5: Maximum power spectral density

Test Result

TestMode	Antenna	Frequency[MHz]	Result [dBm/MHz]	Limit[dBm/MHz]	Verdict
11A	Ant2	5180	2.61	≤11.00	PASS
		5200	3.73	≤11.00	PASS
		5240	-1.38	≤11.00	PASS
11N20SISO	Ant2	5180	1.88	≤11.00	PASS
		5200	2.71	≤11.00	PASS
		5240	0.47	≤11.00	PASS
11N40SISO	Ant2	5190	-0.12	≤11.00	PASS
		5230	-2.75	≤11.00	PASS
11AC20SISO	Ant2	5180	3.03	≤11.00	PASS
		5200	3	≤11.00	PASS
		5240	-1.6	≤11.00	PASS
11AC40SISO	Ant2	5190	5.96	≤11.00	PASS
		5230	-2.16	≤11.00	PASS
11AC80SISO	Ant2	5210	-0.01	≤11.00	PASS
11AX20SISO	Ant2	5180	1.86	≤11.00	PASS
		5200	2.34	≤11.00	PASS
		5240	-0.64	≤11.00	PASS
11AX40SISO	Ant2	5190	-0.1	≤11.00	PASS
		5230	-3.35	≤11.00	PASS
11AX80SISO	Ant2	5210	-2.7	≤11.00	PASS

Note: 1.The Result and Limit Unit is dBm/500 kHz in the band 5.725–5.85 GHz.

2.The Duty Cycle Factor and RBW Factor is compensated in the graph.

Test Graphs

11A_Ant2_5180



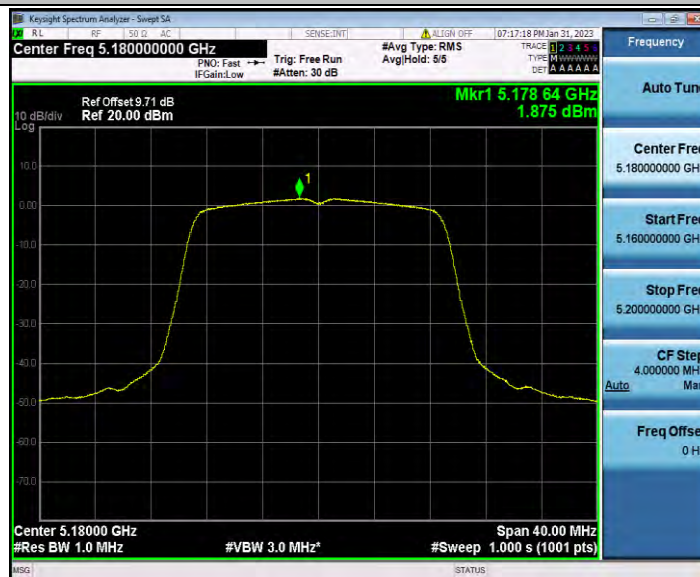
11A_Ant2_5200



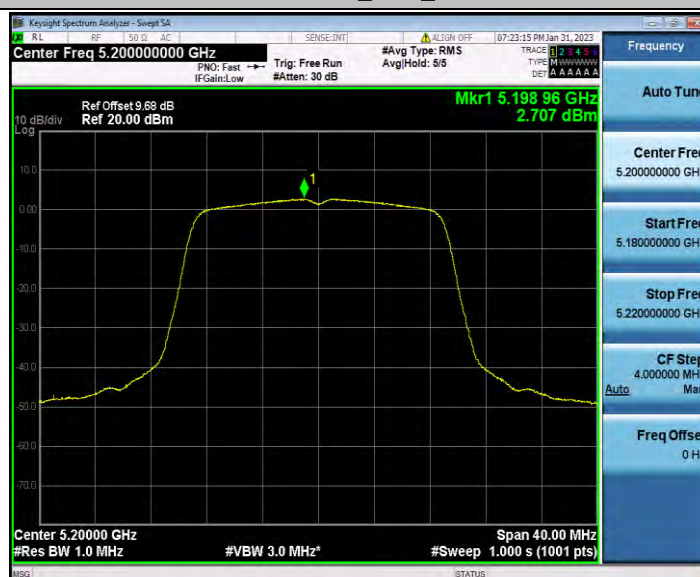
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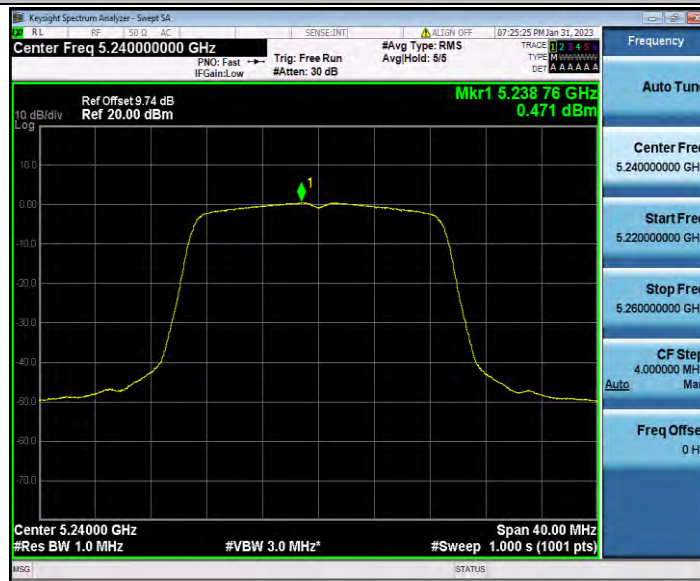
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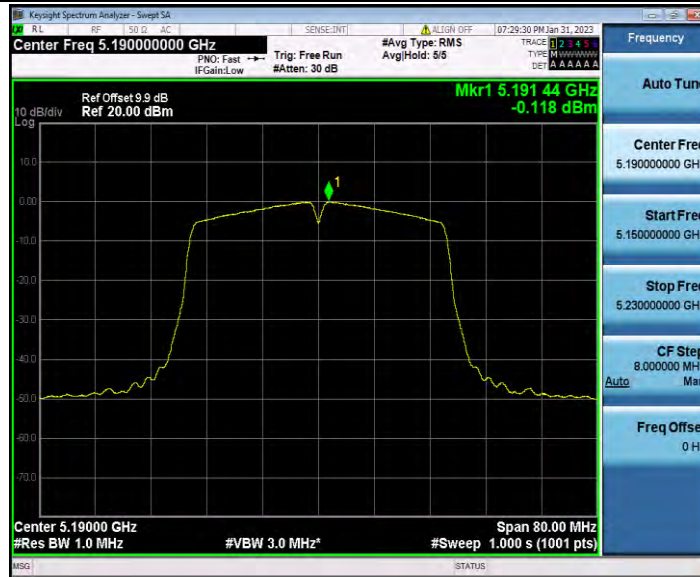
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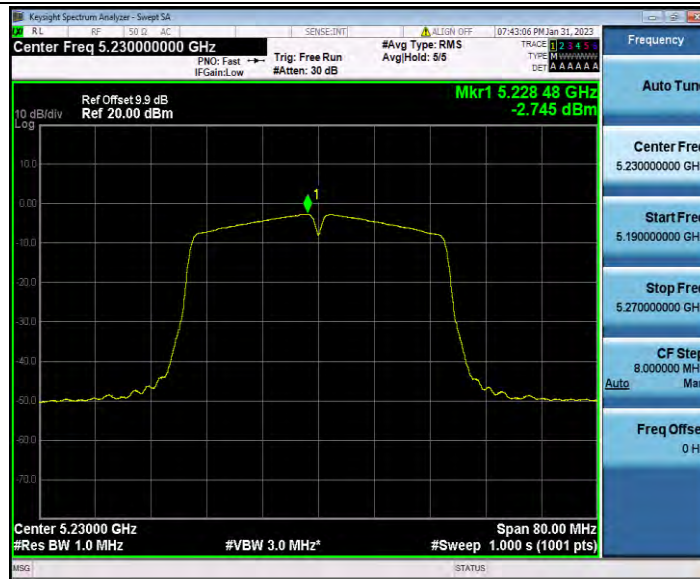
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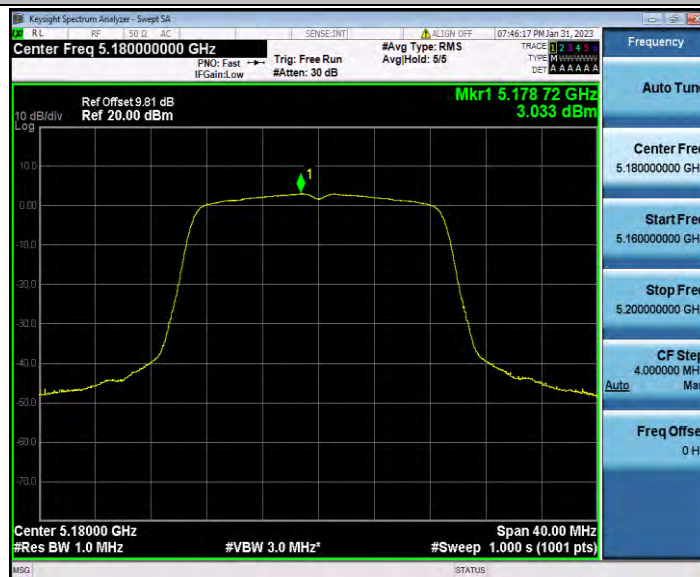
11N40SISO_Ant2_5190



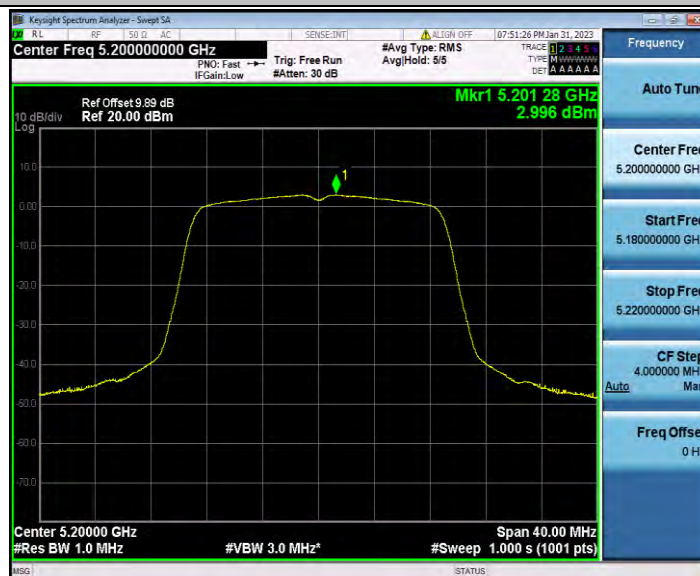
11N40SISO_Ant2_5230



11AC20SISO_Ant2_5180



11AC20SISO_Ant2_5200



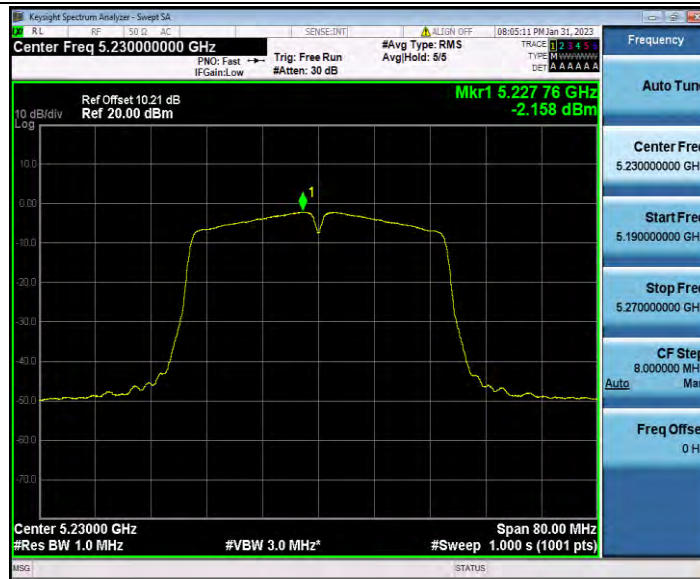
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11AC40SISO_Ant2_5190



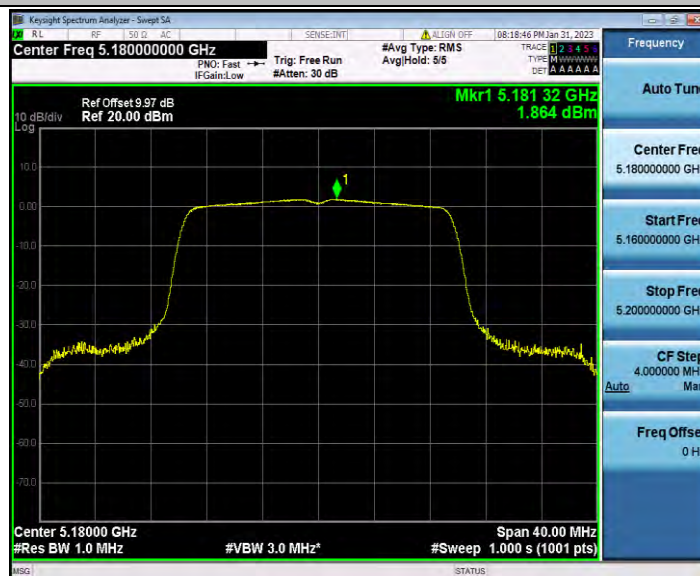
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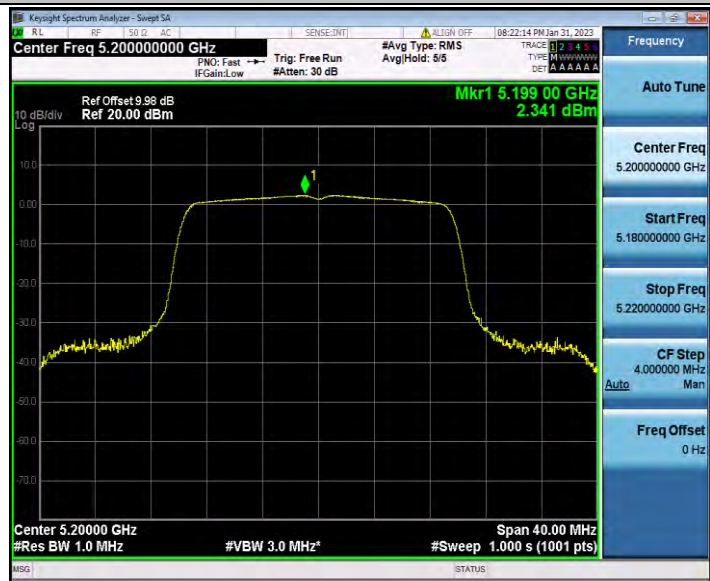
11AC80SISO_Ant2_5210



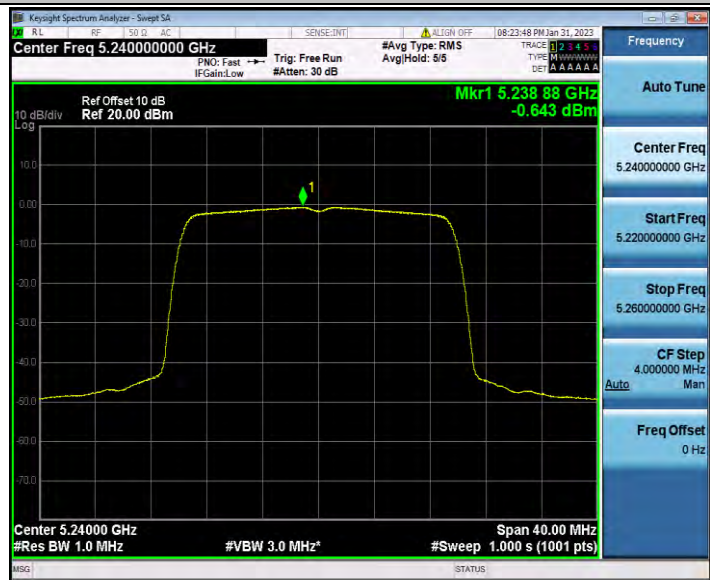
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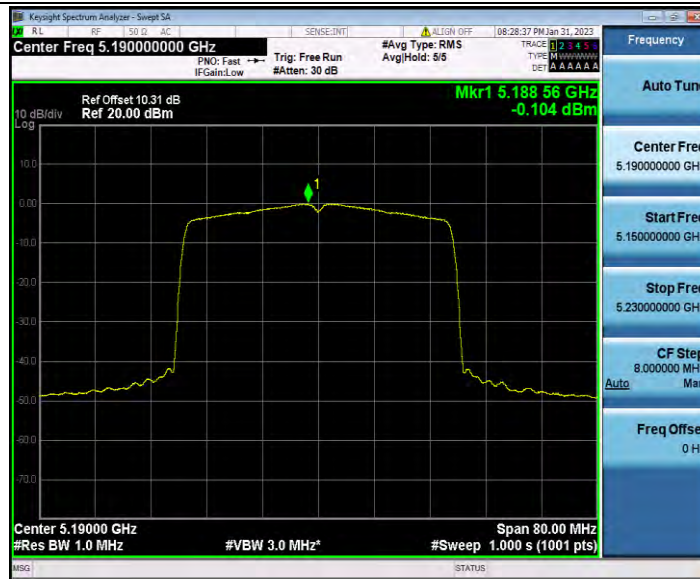
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11AX20SISO_Ant2_5240



11AX40SISO_Ant2_5190



11AX40ISO_Ant2_5230



11AX80ISO_Ant2_5210



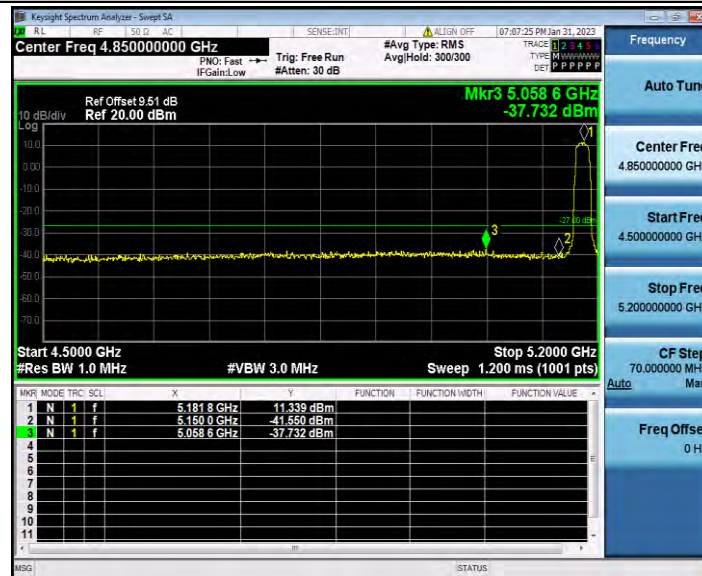
Appendix B.6: Band edge measurements

Test Result B1

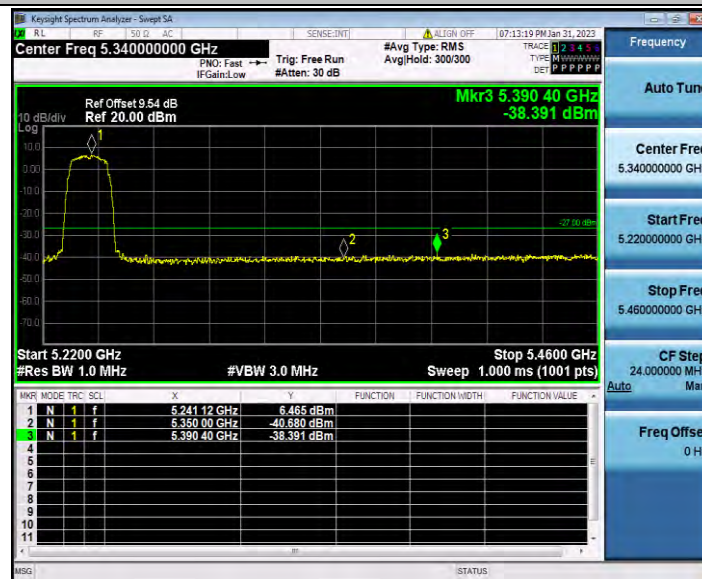
TestMode	Antenna	ChName	Frequency[MHz]	Result[dBm]	Limit[dBm]	Verdict
11A	Ant2	Low	5180	-37.73	≤-27	PASS
		High	5240	-38.39	≤-27	PASS
11N20SISO	Ant2	Low	5180	-37.36	≤-27	PASS
		High	5240	-38.47	≤-27	PASS
11N40SISO	Ant2	Low	5190	-37.17	≤-27	PASS
		High	5230	-38.24	≤-27	PASS
11AC20SISO	Ant2	Low	5180	-37.58	≤-27	PASS
		High	5240	-36.17	≤-27	PASS
11AC40SISO	Ant2	Low	5190	-35.26	≤-27	PASS
		High	5230	-38.08	≤-27	PASS
11AC80SISO	Ant2	Low	5210	-37.74	≤-27	PASS
		High	5210	-37.57	≤-27	PASS
11AX20SISO	Ant2	Low	5180	-37.03	≤-27	PASS
		High	5240	-38.18	≤-27	PASS
11AX40SISO	Ant2	Low	5190	-34.15	≤-27	PASS
		High	5230	-37.66	≤-27	PASS
11AX80SISO	Ant2	Low	5210	-33.16	≤-27	PASS
		High	5210	-36.71	≤-27	PASS

Test Graphs B1

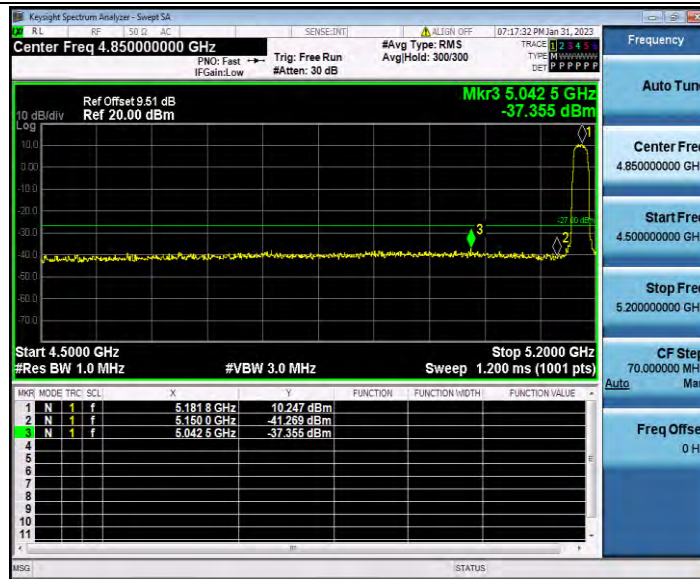
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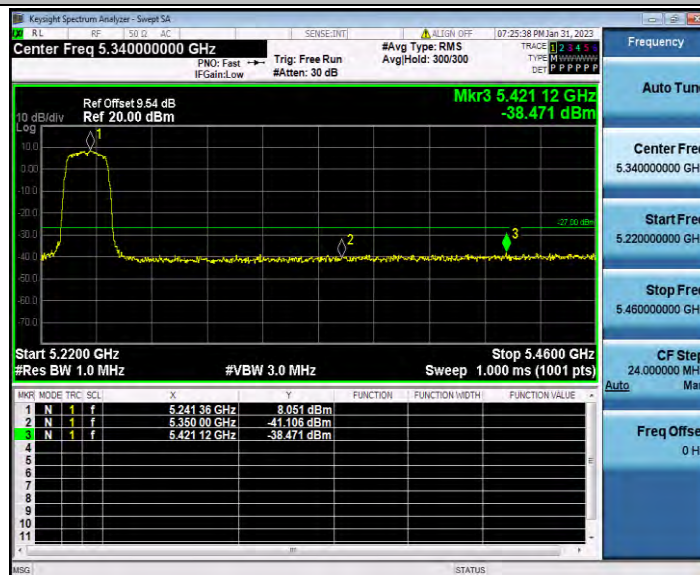
11A_Ant2_High_5240



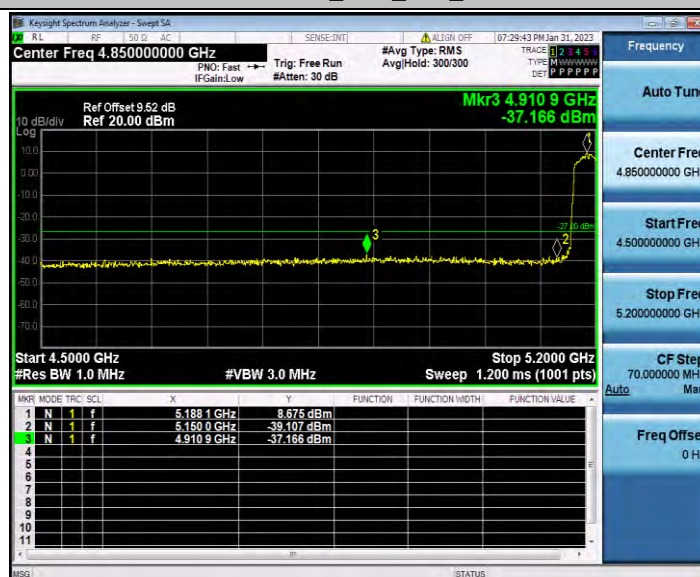
11N20SISO_Ant2_Low_5180



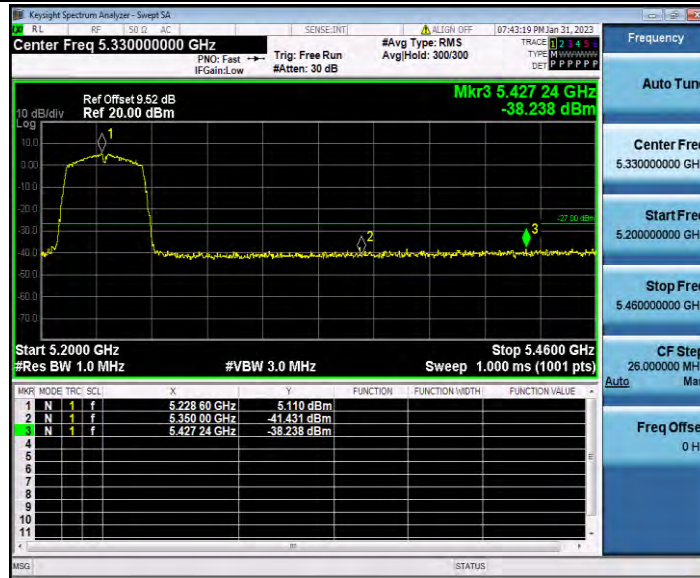
11N20SISO_Ant2_High_5240



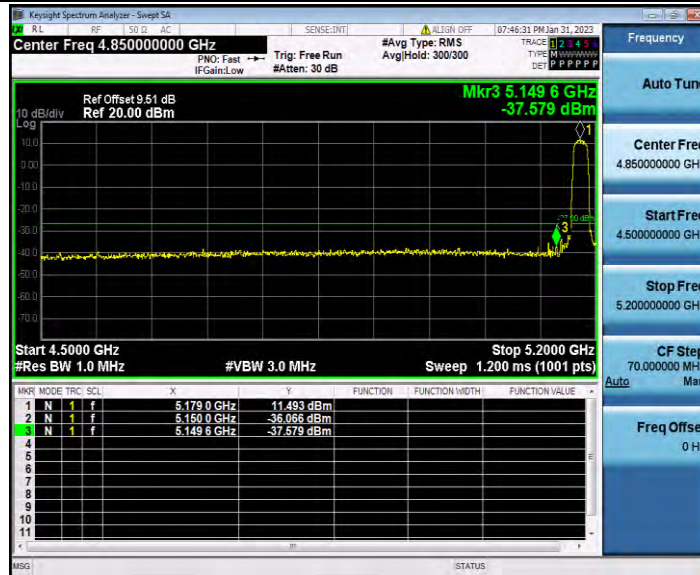
11N40SISO_Ant2_Low_5190



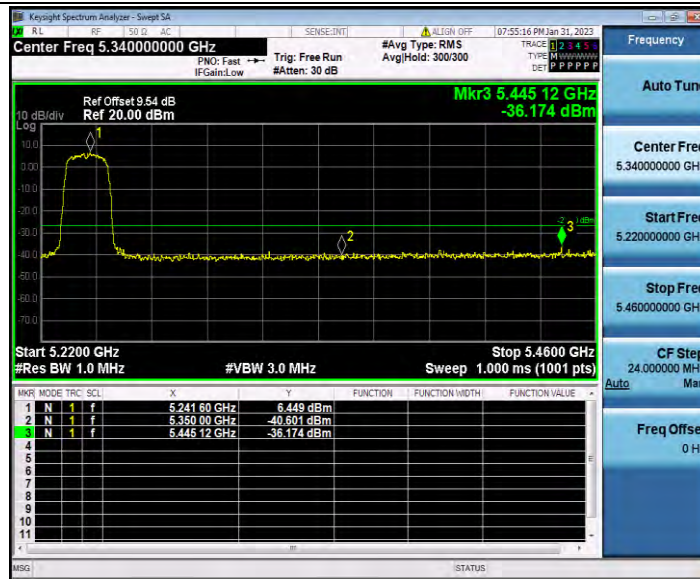
11N40SISO_Ant2_High_5230



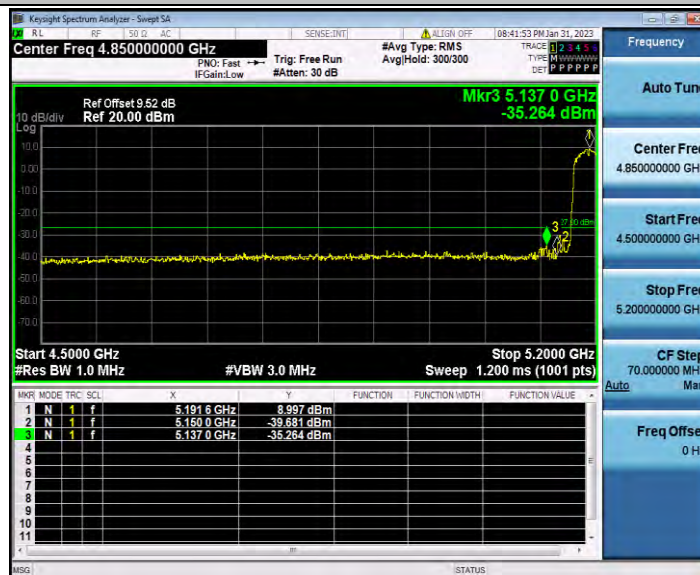
11AC20SISO_Ant2_Low_5180



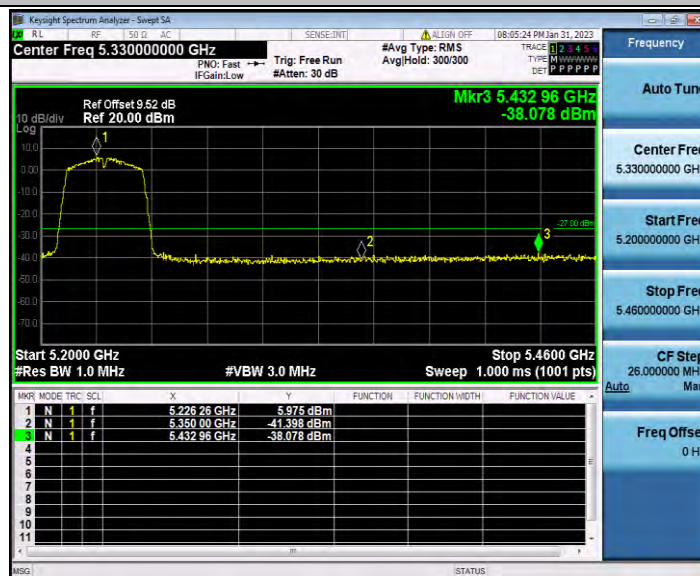
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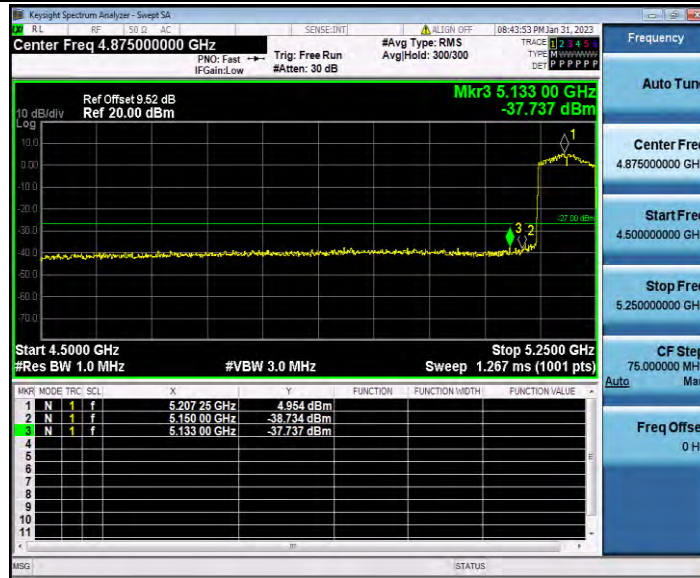
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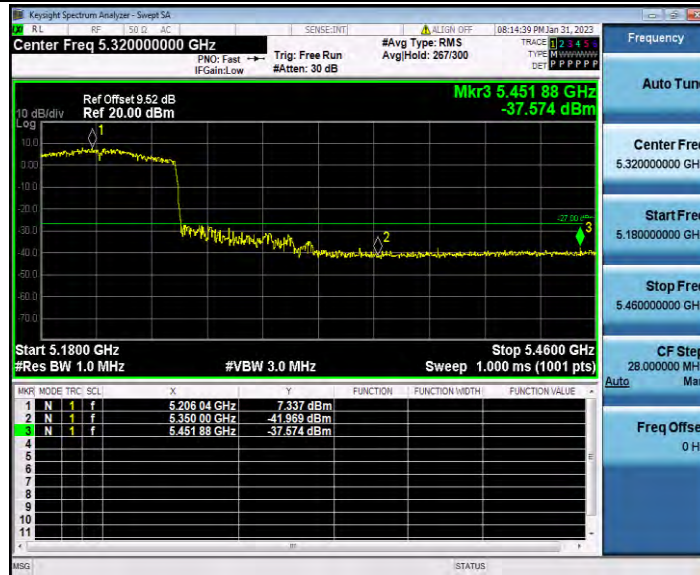
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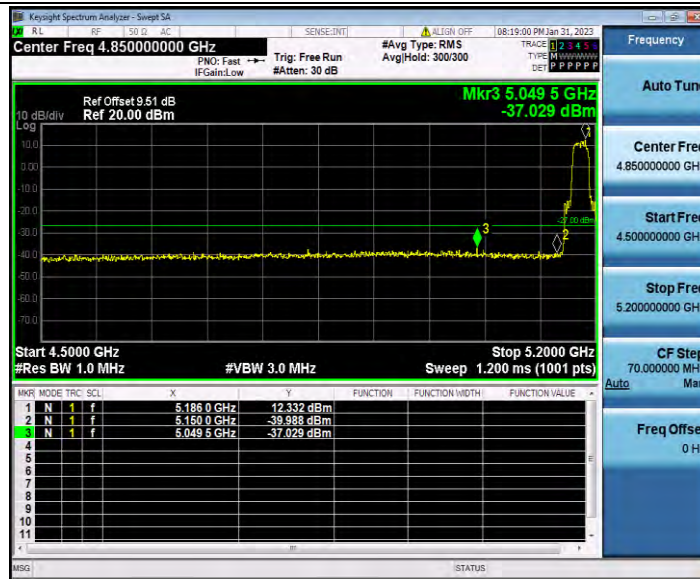
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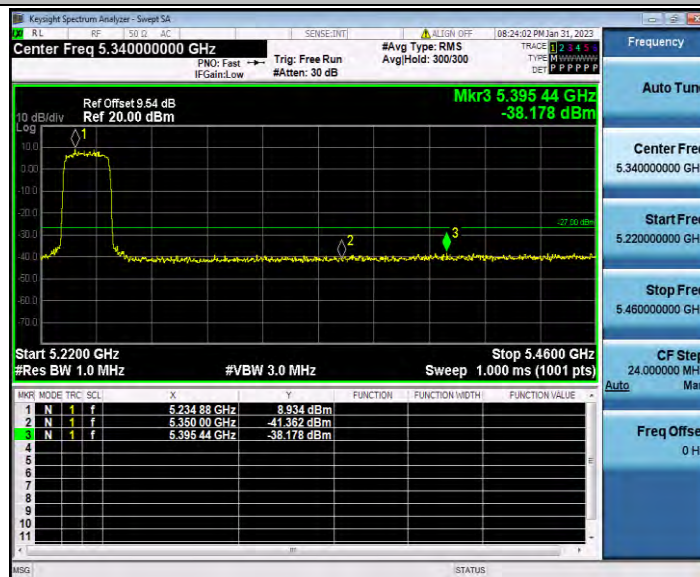
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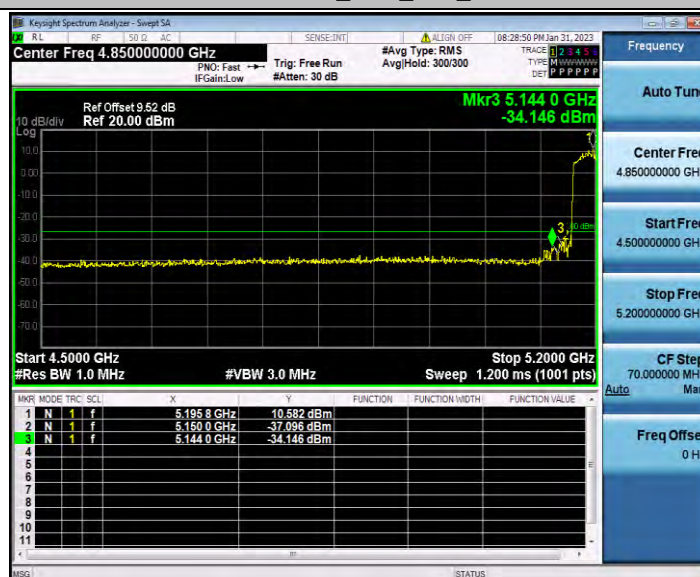
11AX20SISO_Ant2_Low_5180



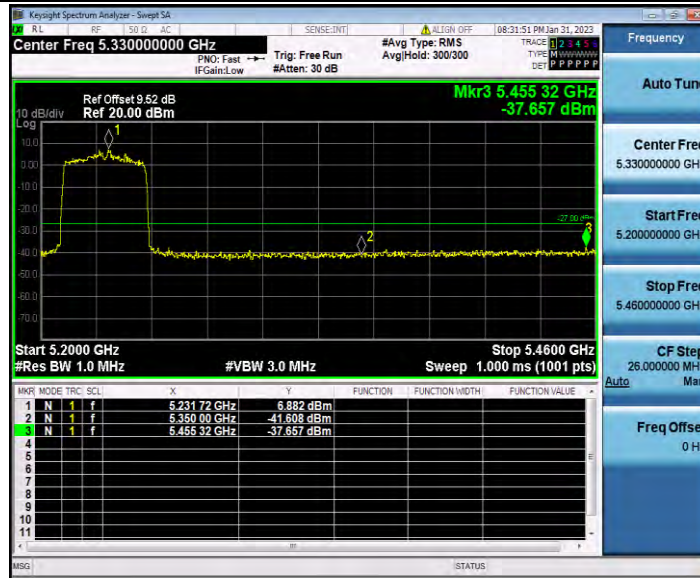
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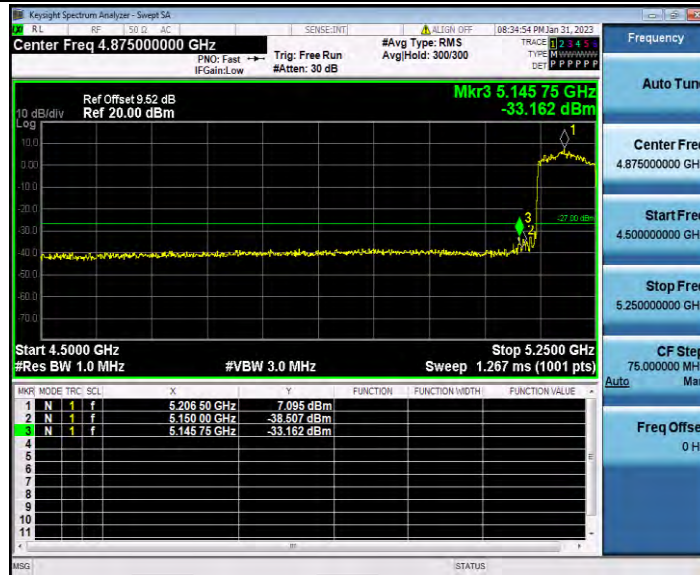
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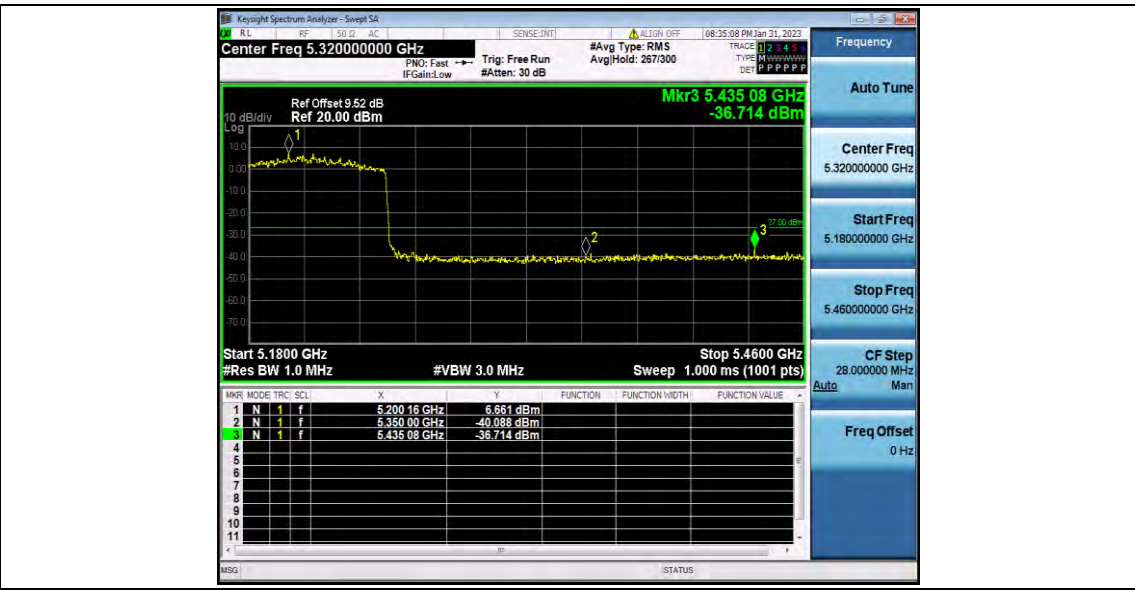
11AX40SISO_Ant2_High_5230



11AX80SISO_Ant2_Low_5210



11AX80SISO_Ant2_High_5210



Appendix B.7: Conducted Spurious Emission

Test Result

TestMode	Antenna	Frequency[MHz]	FreqRange [MHz]	Max. Fre [MHz]	Max. Level [dBm]	Limit [dBm]	Verdict
11A	Ant2	5180	30~5140	5061.65	-46.3	≤-27	PASS
			5360~40000	24271.92	-37.92	≤-27	PASS
		5200	30~5140	2676.13	-46.91	≤-27	PASS
			5360~40000	24330.35	-38.45	≤-27	PASS
		5240	30~5140	1755.99	-45.6	≤-27	PASS
			5360~40000	23497.93	-38.34	≤-27	PASS
11N20SISO	Ant2	5180	30~5140	1763.31	-42.56	≤-27	PASS
			5360~40000	24929.05	-38.24	≤-27	PASS
		5200	30~5140	5038.48	-45.96	≤-27	PASS
			5360~40000	26618.41	-37.8	≤-27	PASS
		5240	30~5140	4975.12	-45.73	≤-27	PASS
			5360~40000	24255.33	-38.39	≤-27	PASS
11N40SISO	Ant2	5190	30~5140	5127.4	-42.05	≤-27	PASS
			5360~40000	23614.06	-37.86	≤-27	PASS
		5230	30~5140	2479.9	-46.95	≤-27	PASS
			5360~40000	24217.82	-36.8	≤-27	PASS
11AC20SISO	Ant2	5180	30~5140	5120.75	-46.66	≤-27	PASS
			5360~40000	26784.32	-37.9	≤-27	PASS
		5200	30~5140	5127.91	-46.4	≤-27	PASS
			5360~40000	26908.39	-37.84	≤-27	PASS
		5240	30~5140	5011.23	-46.94	≤-27	PASS
			5360~40000	24356.31	-38.3	≤-27	PASS
11AC40SISO	Ant2	5190	30~5140	5137.62	-28.83	≤-27	PASS
			5360~40000	25230.57	-38.22	≤-27	PASS
		5230	30~5140	3285.92	-46.86	≤-27	PASS
			5360~40000	26903.34	-37.41	≤-27	PASS
11AC80SISO	Ant2	5210	30~5140	5136.08	-30.71	≤-27	PASS
			5360~40000	25209.65	-37.8	≤-27	PASS
11AX20SISO	Ant2	5180	30~5140	4167.23	-45.97	≤-27	PASS
			5360~40000	24249.56	-37.92	≤-27	PASS
		5200	30~5140	4988.57	-46.27	≤-27	PASS
			5360~40000	26062.27	-37.97	≤-27	PASS
		5240	30~5140	2413.47	-44.93	≤-27	PASS
			5360~40000	23445.99	-38.28	≤-27	PASS
11AX40SISO	Ant2	5190	30~5140	5134.04	-38.03	≤-27	PASS
			5360~40000	26025.48	-38.51	≤-27	PASS

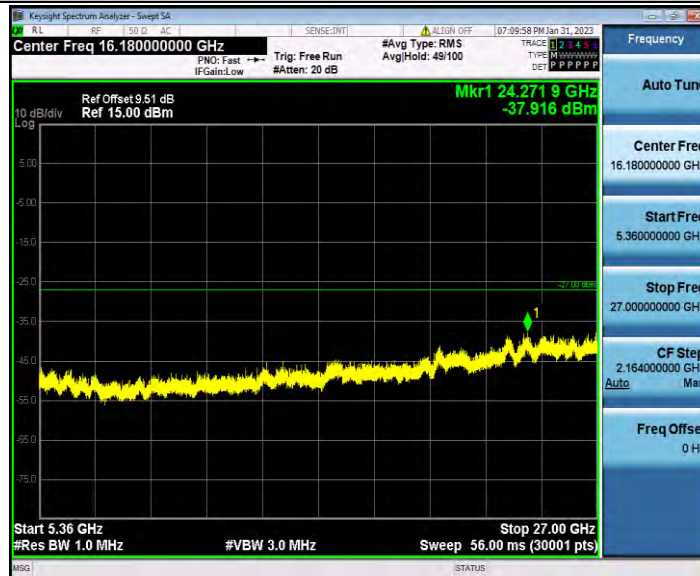
		5230	30~5140	4950.08	-47.05	≤-27	PASS
			5360~40000	24817.25	-37.89	≤-27	PASS
11AX80SISO	Ant2	5210	30~5140	5138.98	-40.87	≤-27	PASS
			5360~40000	26047.12	-36.82	≤-27	PASS

Test Graphs

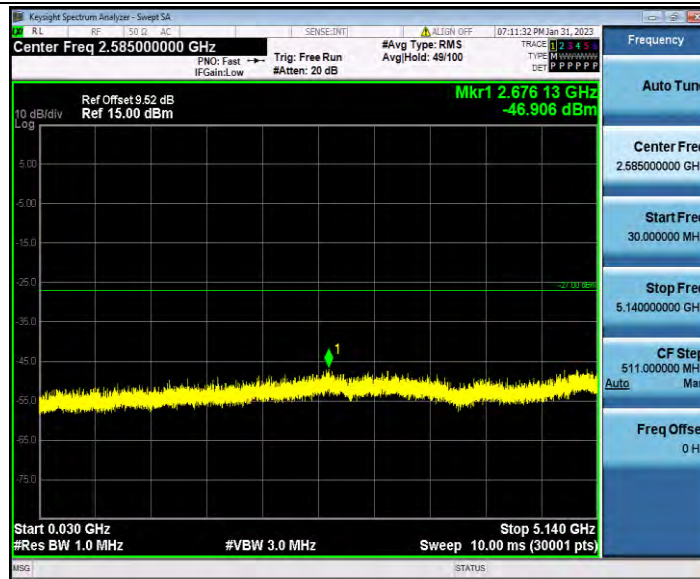
11A_Ant2_5180_30~5140



11A_Ant2_5180_5360~40000



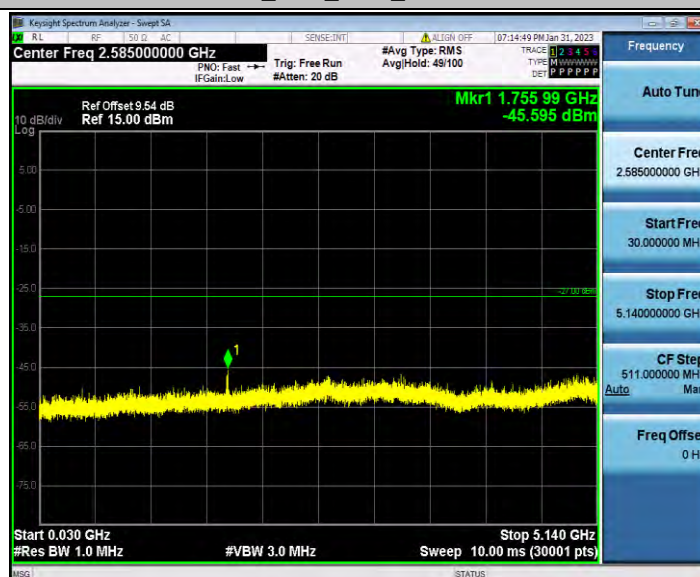
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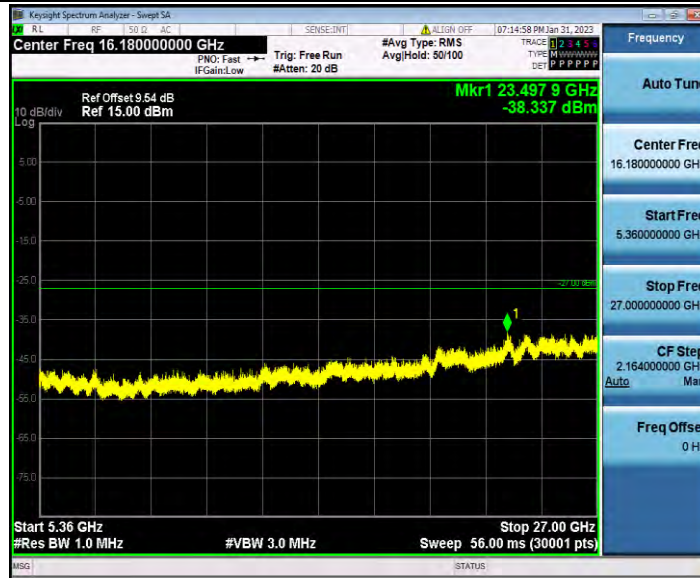
11A_Ant2_5200_5360~40000



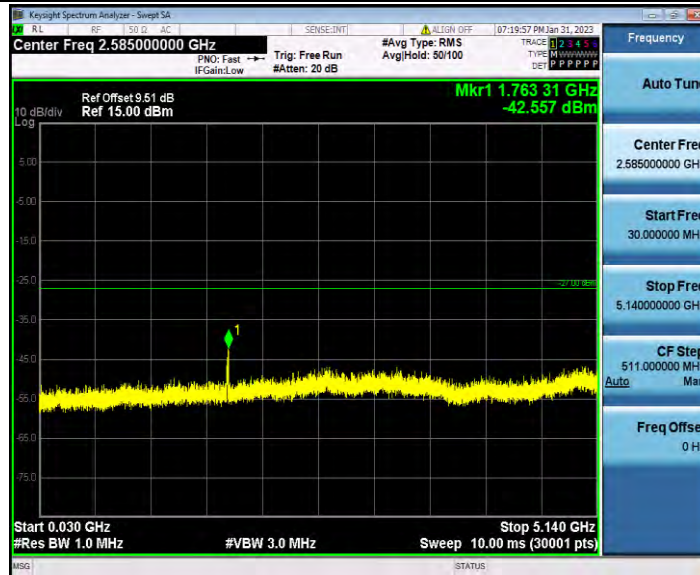
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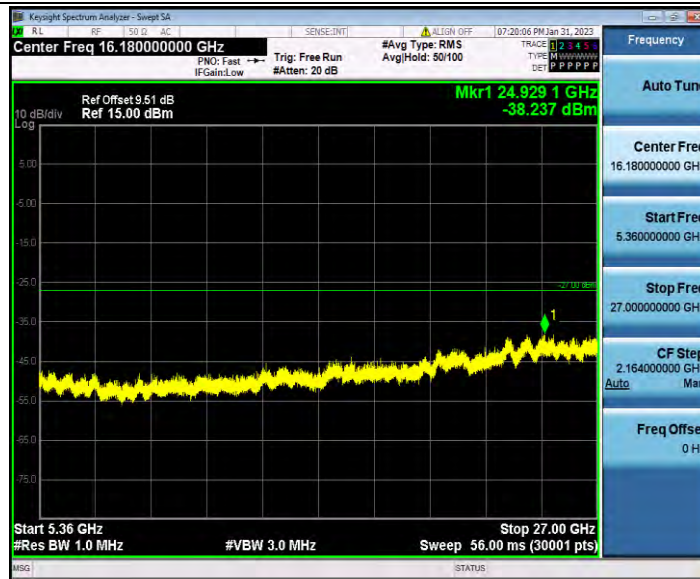
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11N20SISO_Ant2_5180_30~5140



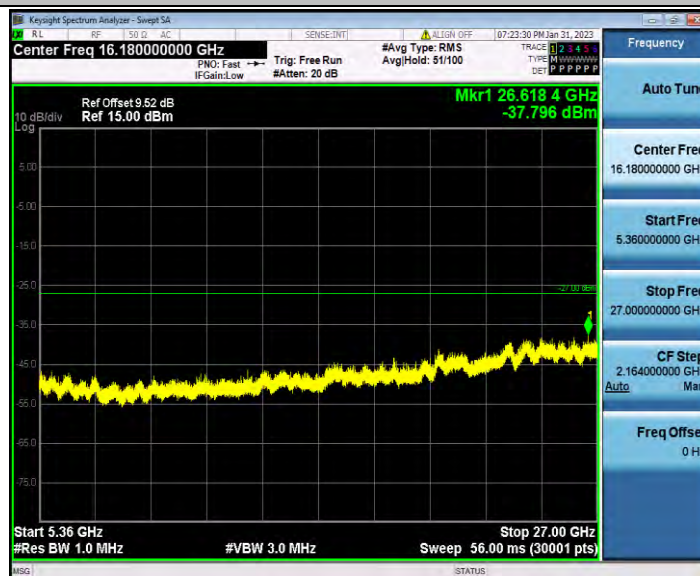
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11N20SISO_Ant2_5200_30~5140



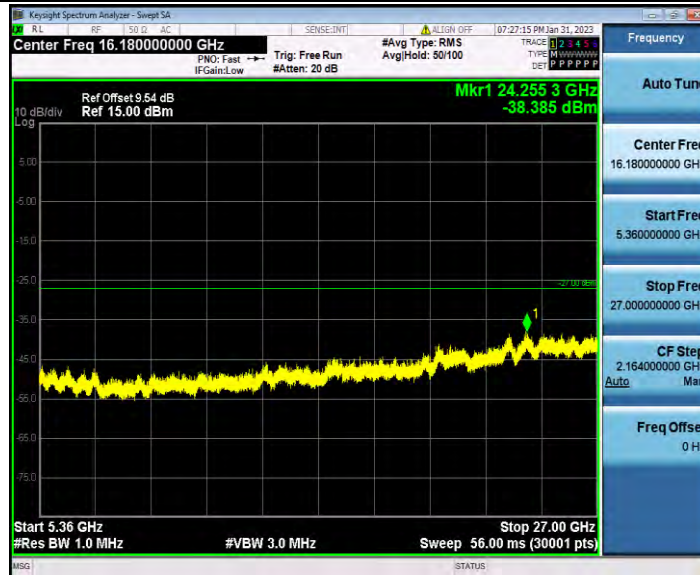
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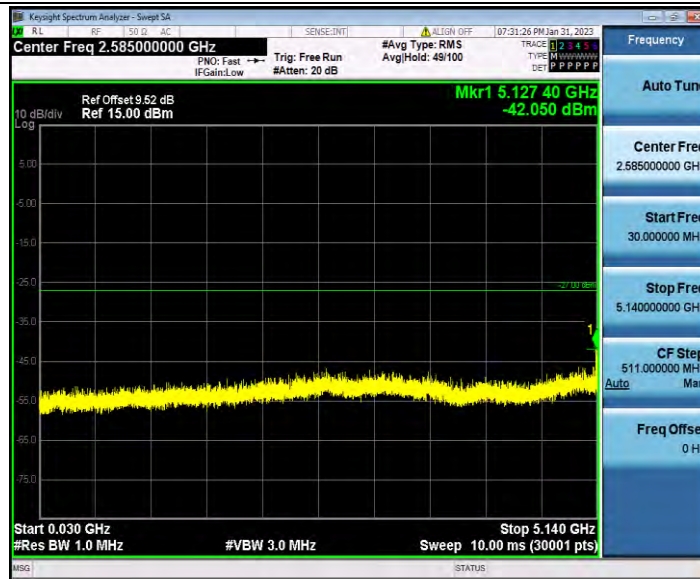
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11N20SISO_Ant2_5240_5360~40000



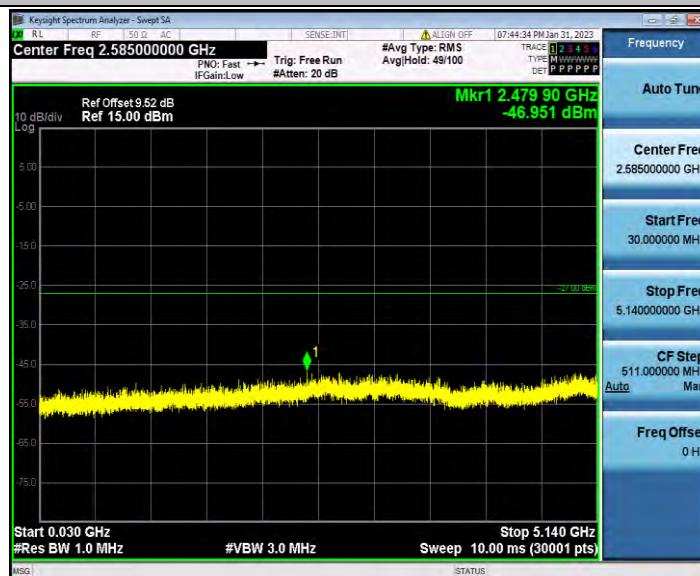
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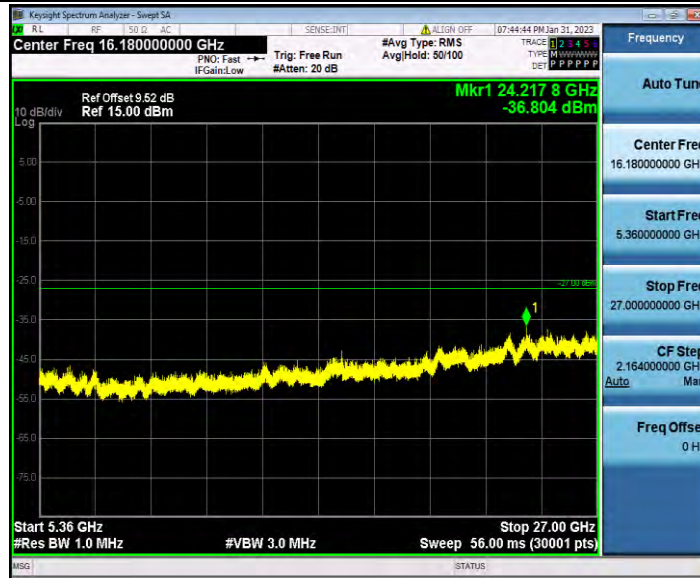
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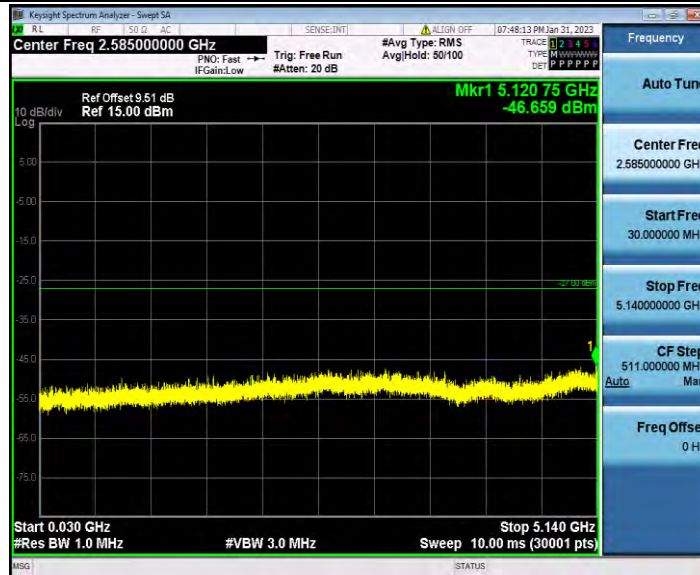
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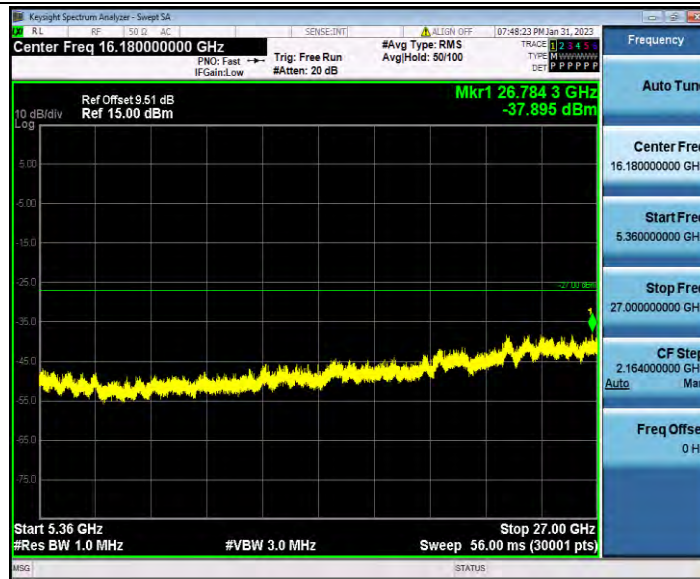
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11AC20SISO_Ant2_5180_30~5140



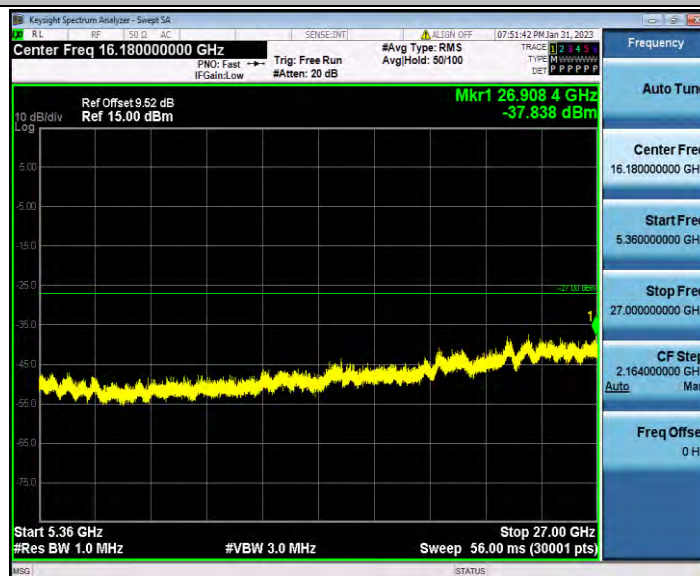
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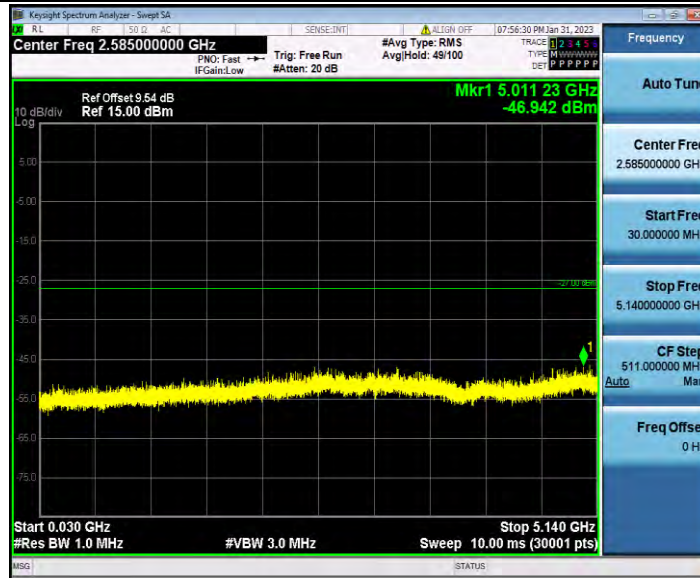
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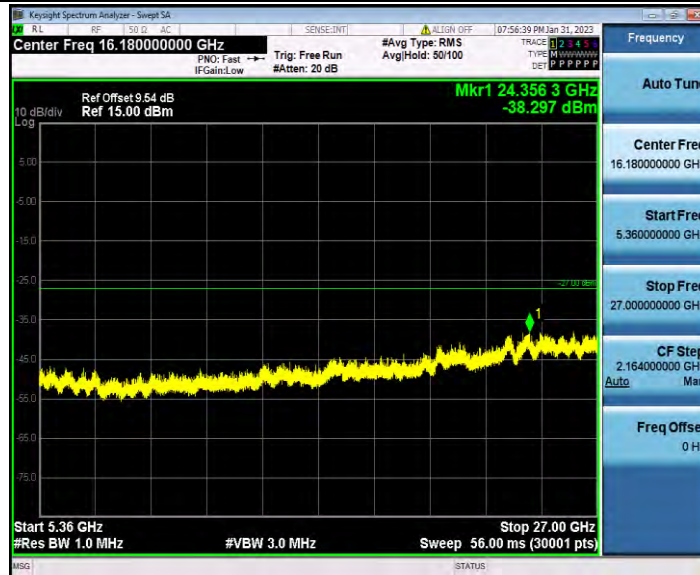
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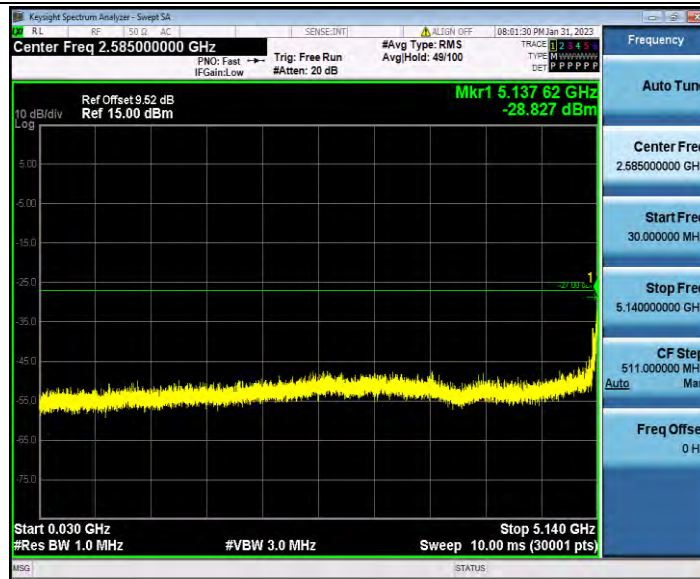
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11AC20SISO_Ant2_5240_5360~40000



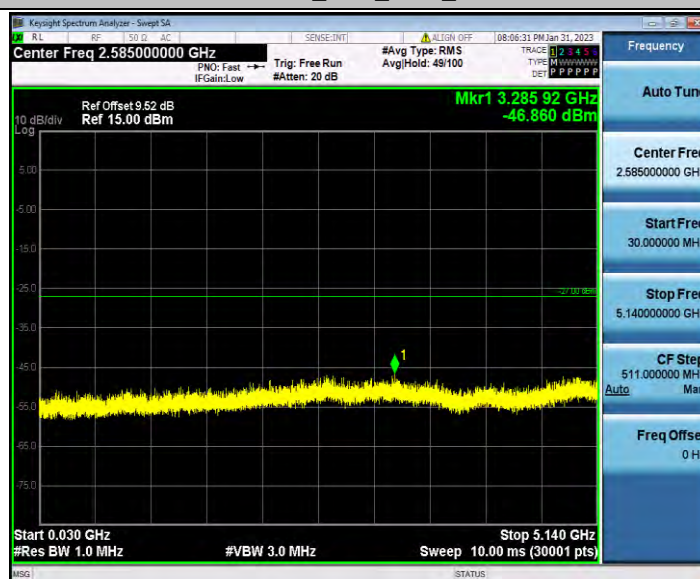
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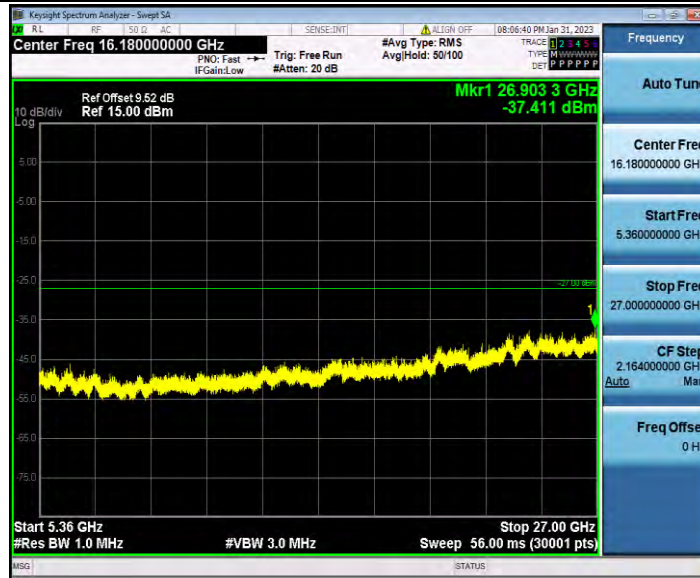
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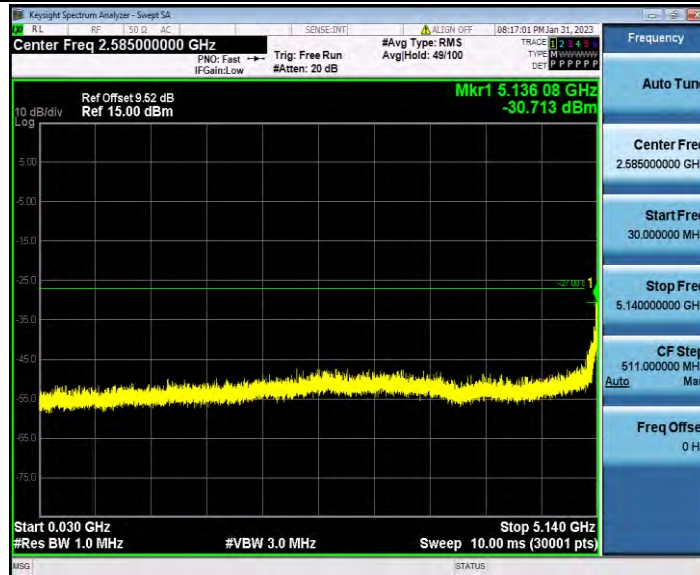
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11AC40SISO_Ant2_5230_5360~40000



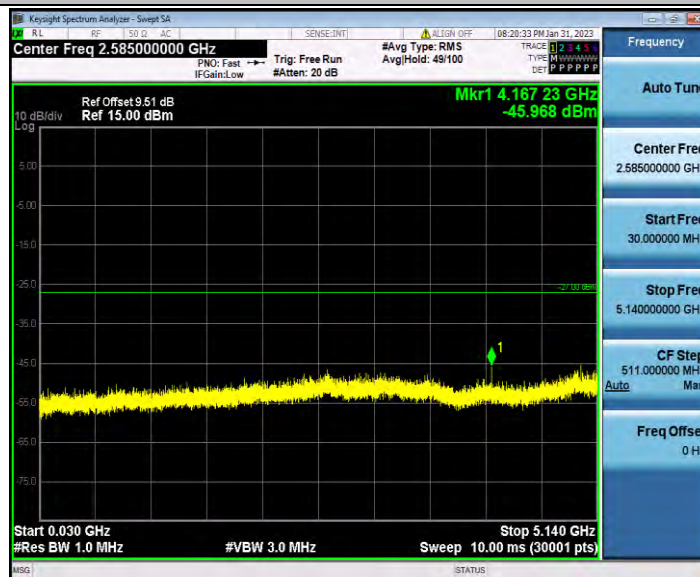
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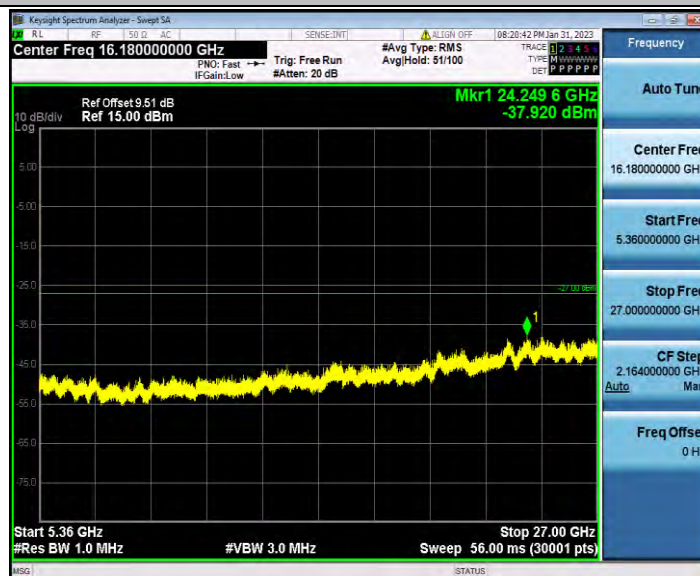
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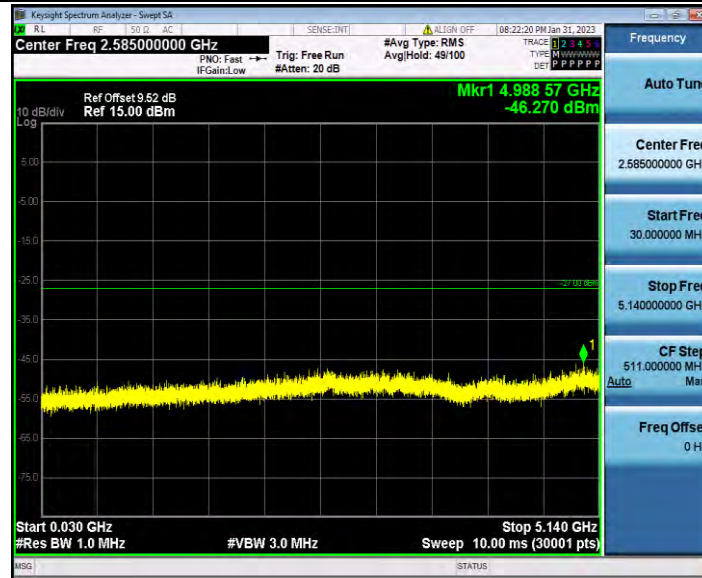
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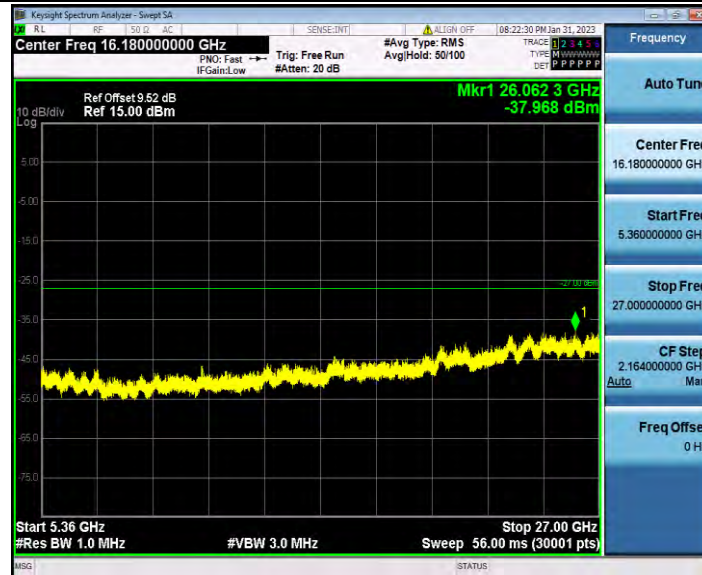
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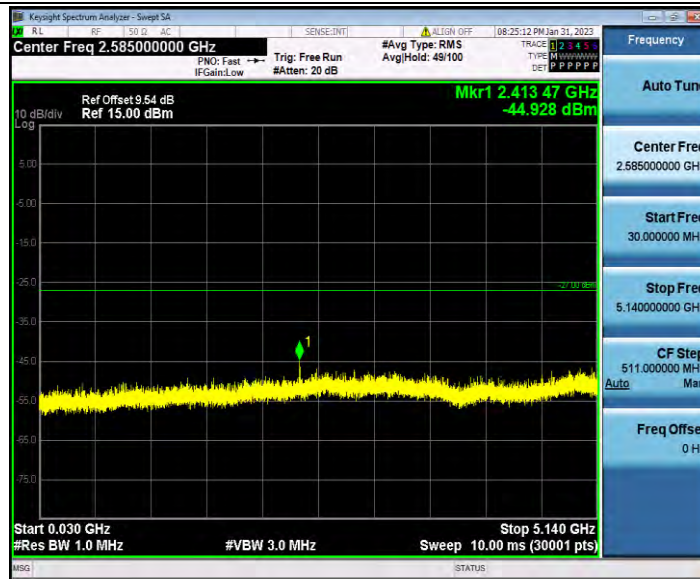
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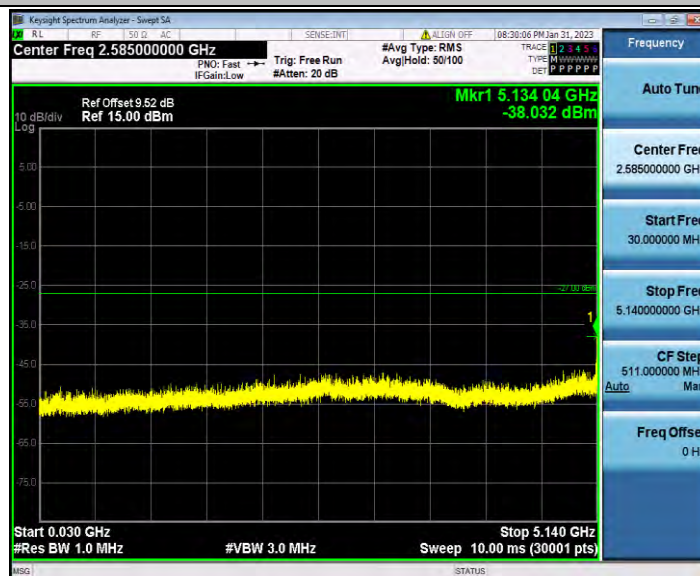
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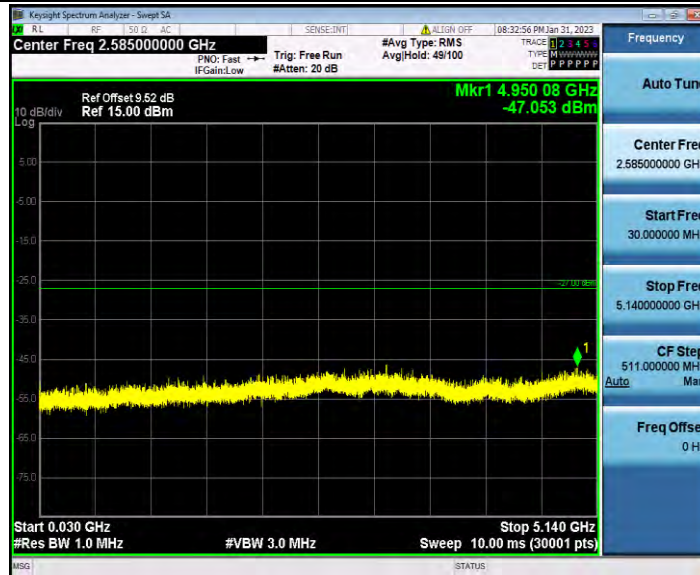
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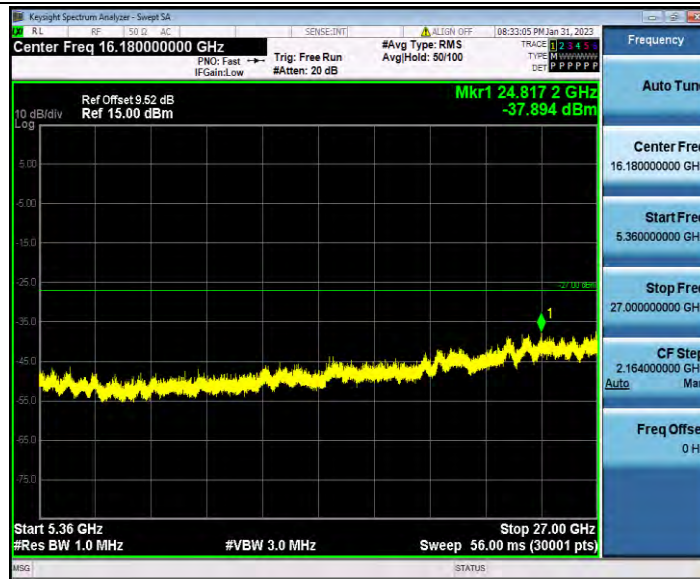
11AX40SISO_Ant2_5190_5360~40000



11AX40SISO_Ant2_5230_30~5140



11AX40SISO_Ant2_5230_5360~40000



11AX80SISO_Ant2_5210_30~5140



11AX80SISO_Ant2_5210_5360~40000

