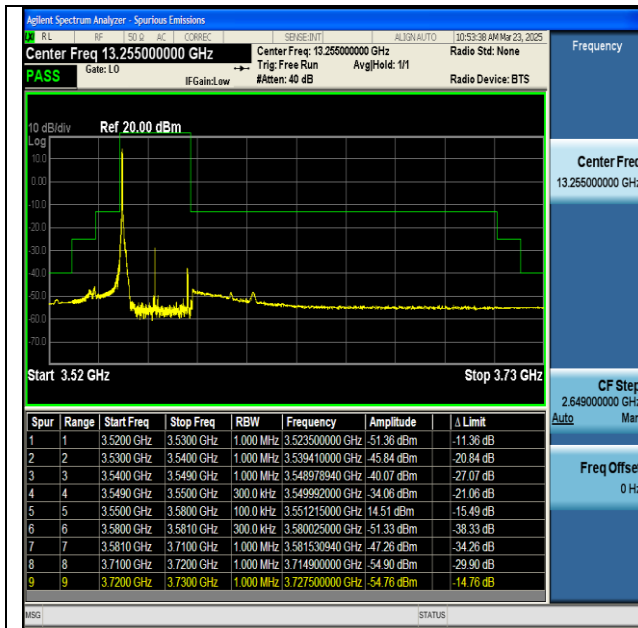
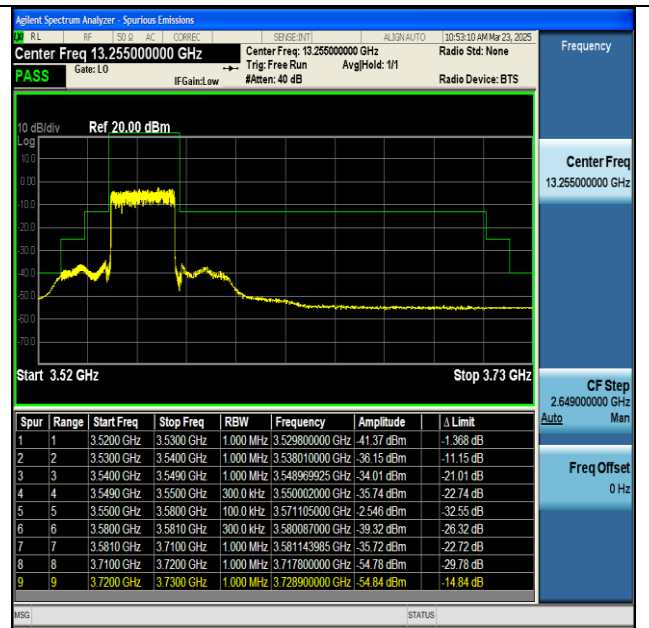
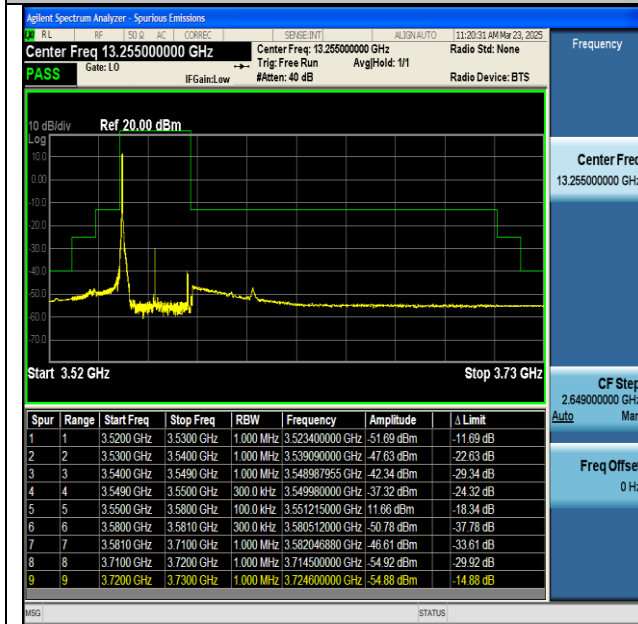




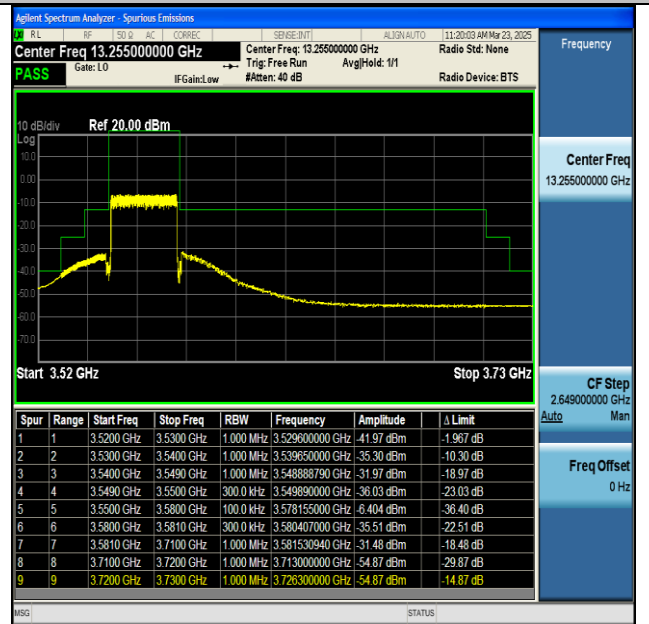
NTNV-n48-PC3-30-30-L-DFT-BPSK-Edge_1RB_Left-PASS



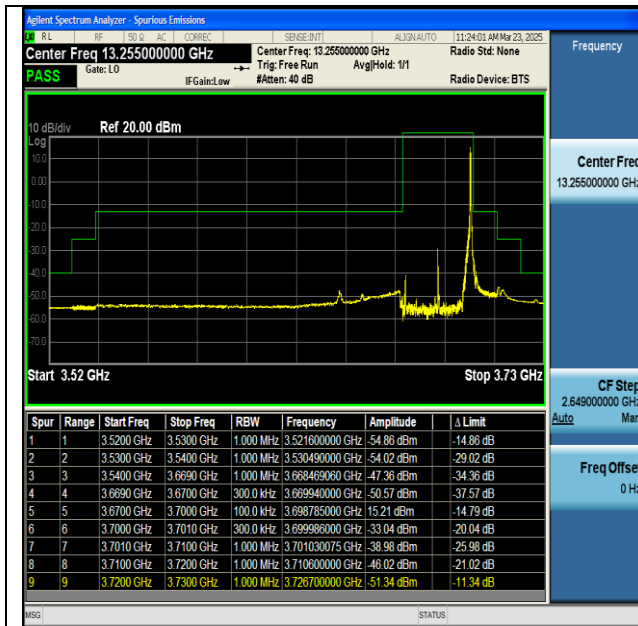
NTNV-n48-PC3-30-30-L-DFT-BPSK-Outer_Full-PASS



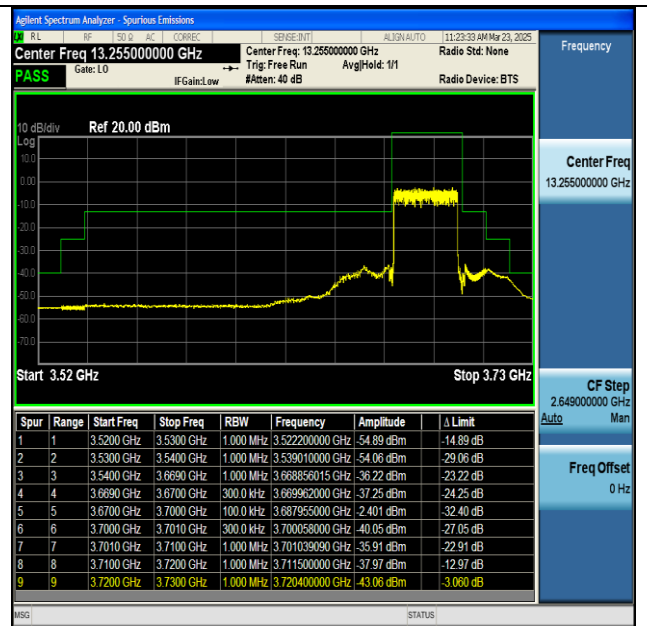
NTNV-n48-PC3-30-30-L-CP-QPSK-Edge_1RB_Left-PASS



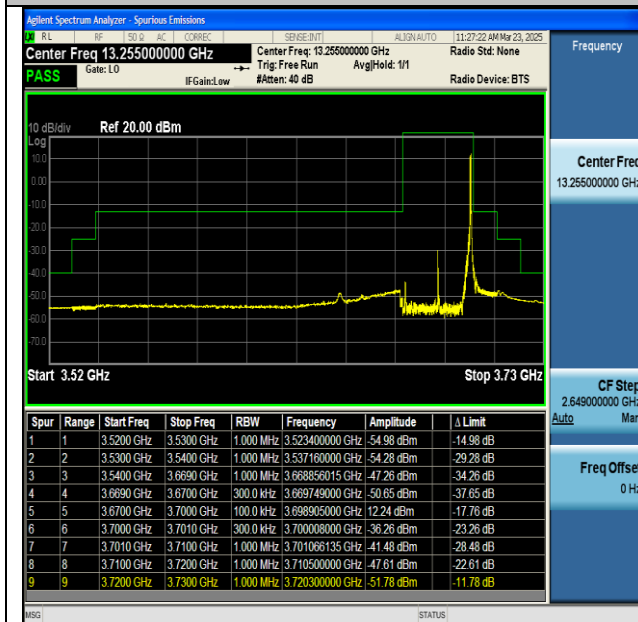
NTNV-n48-PC3-30-30-L-CP-QPSK-Outer_Full-PASS



NTNV-n48-PC3-30-30-H-DFT-BPSK-Edge_1RB_Right-PASS



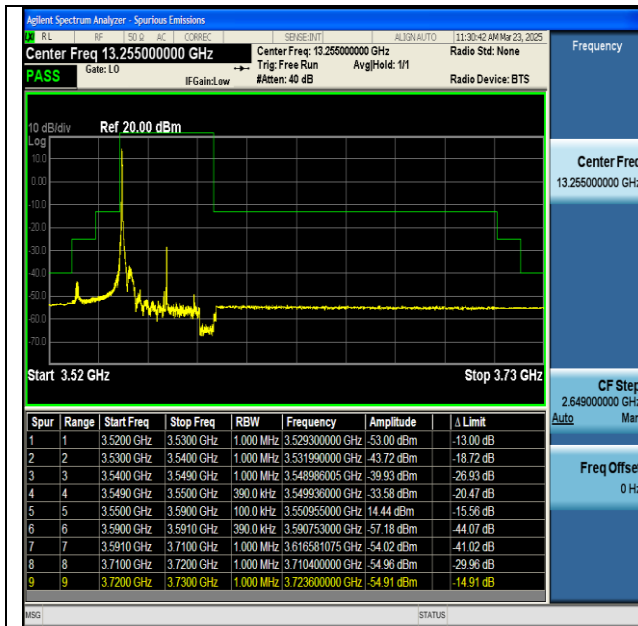
NTNV-n48-PC3-30-30-H-DFT-BPSK-Outer_Full-PASS



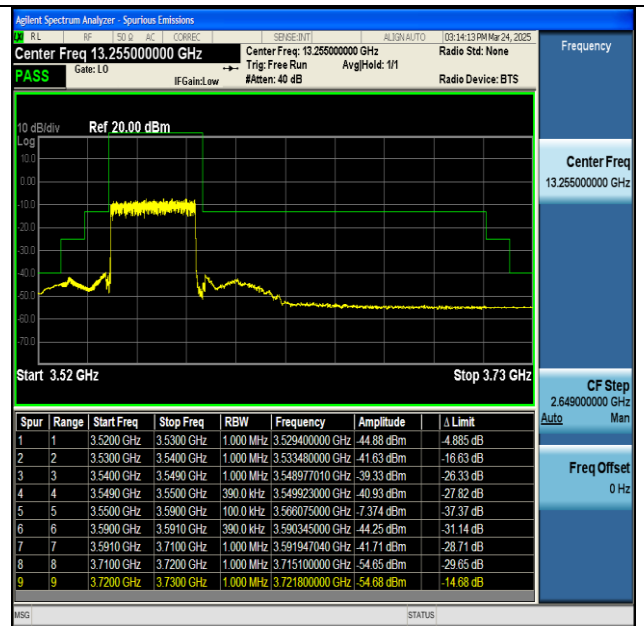
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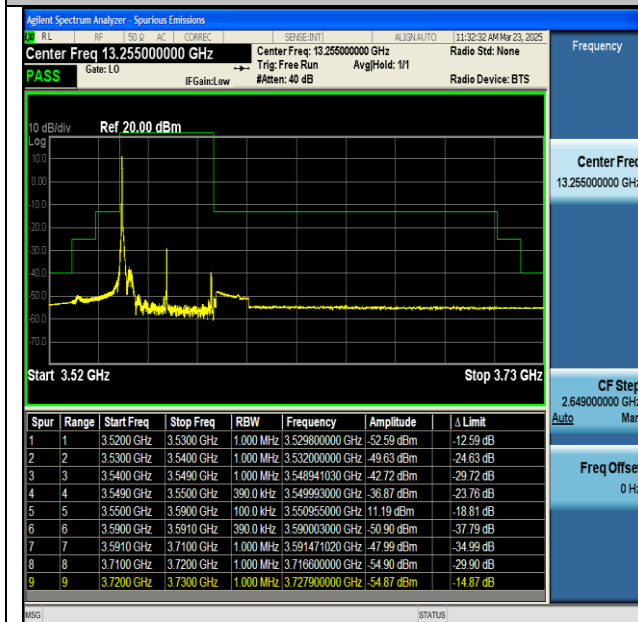
NTNV-n48-PC3-30-30-H-CP-QPSK-Outer_Full-PASS



NTNV-n48-PC3-30-40-L-DFT-BPSK-Edge_1RB_Left-PASS



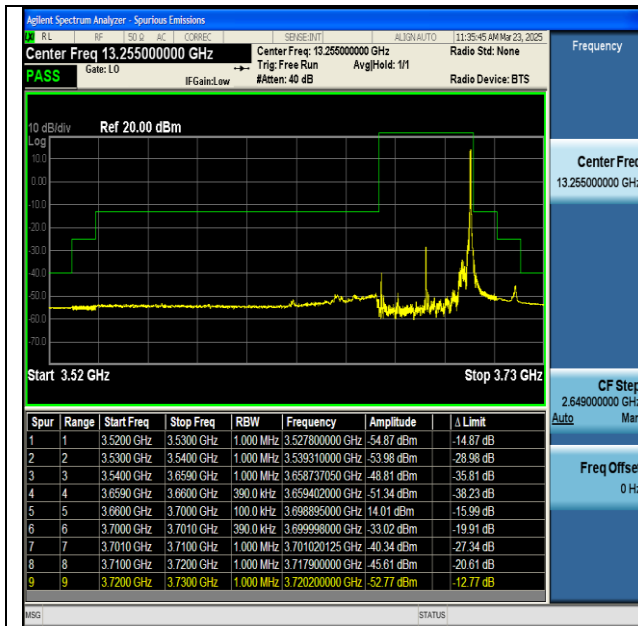
NTNV-n48-PC3-30-40-L-DFT-BPSK-Outer_Full-PASS



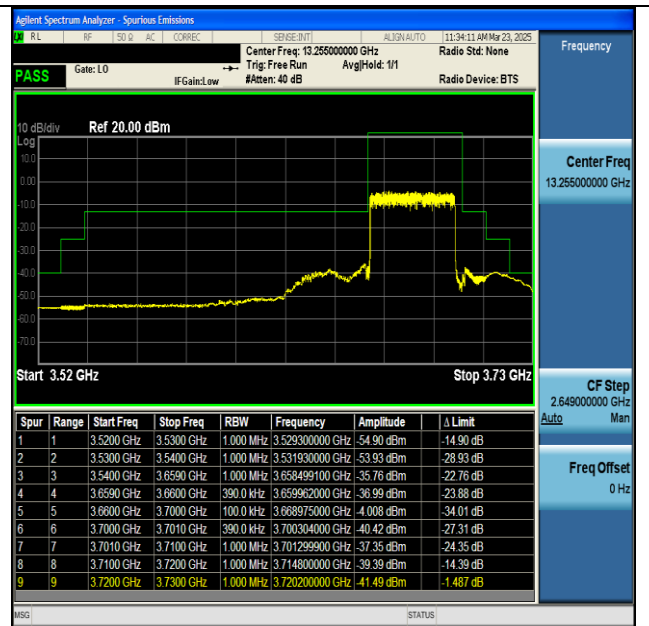
NTNV-n48-PC3-30-40-L-CP-QPSK-Edge_1RB_Left-PASS



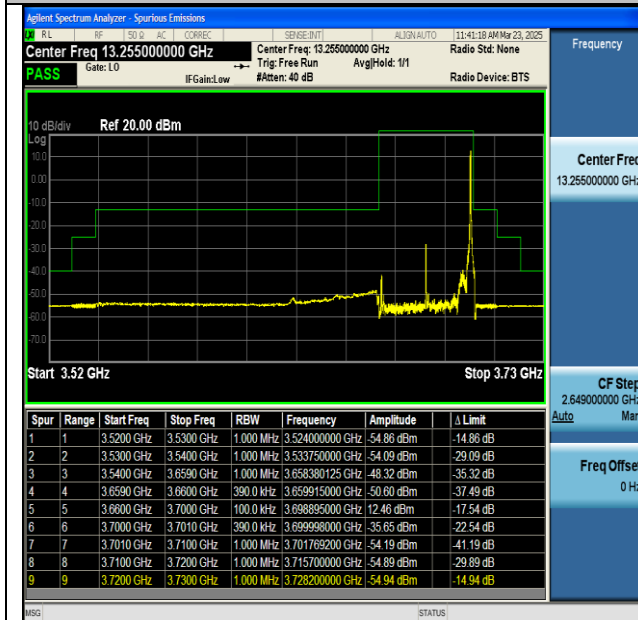
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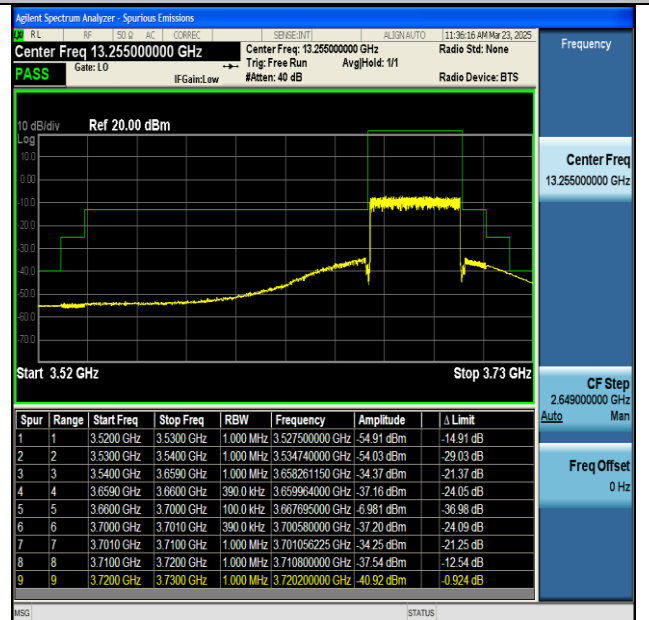
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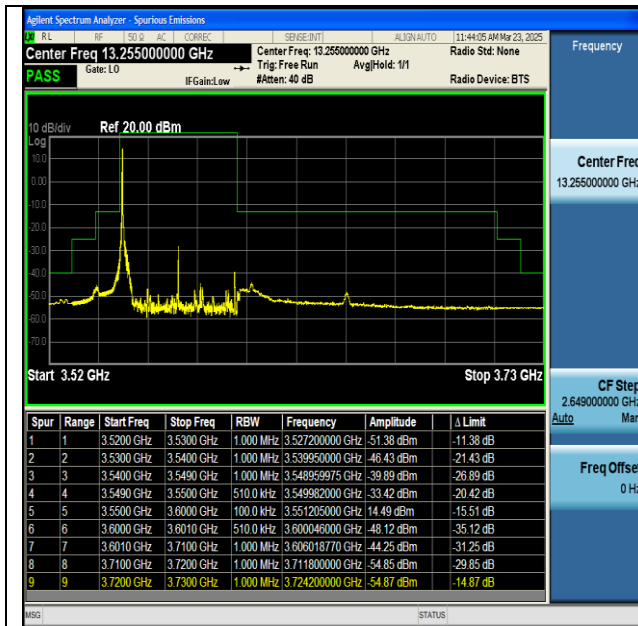
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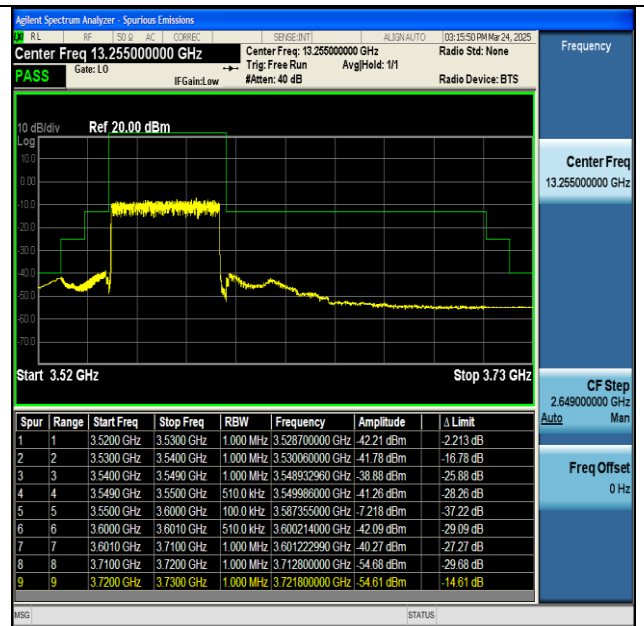
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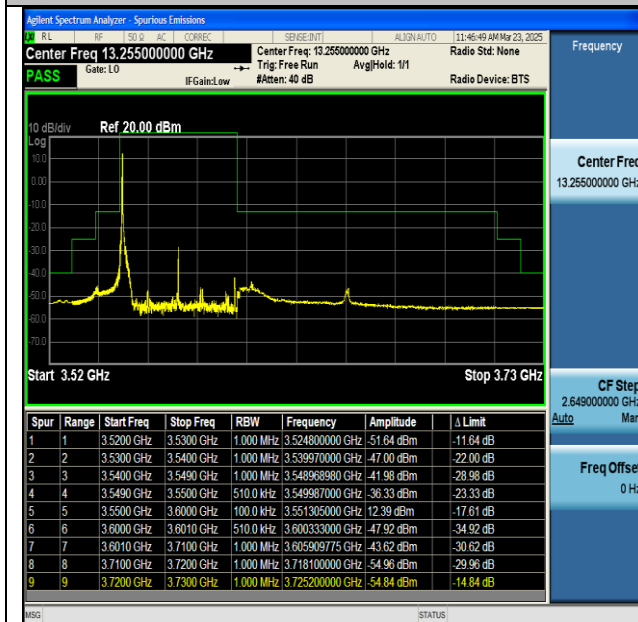
NTNV-n48-PC3-30-40-H-CP-QPSK-Outer_Full-PASS



NTNV-n48-PC3-30-50-L-DFT-BPSK-Edge_1RB_Left-PASS



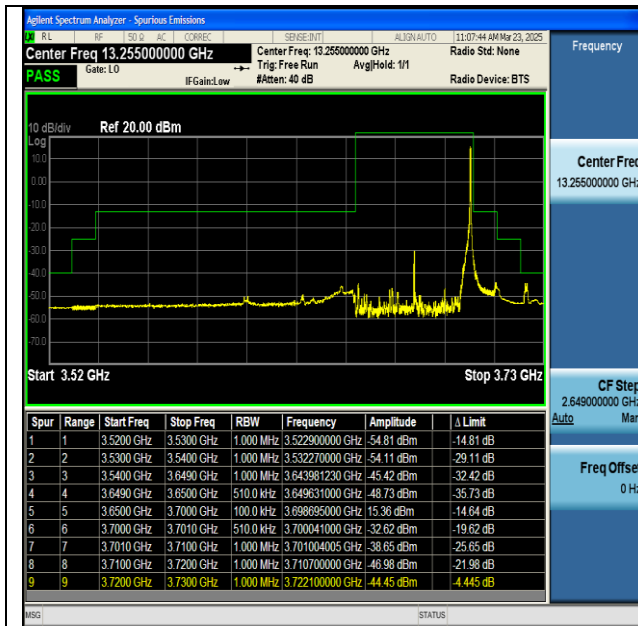
NTNV-n48-PC3-30-50-L-DFT-BPSK-Outer_Full-PASS



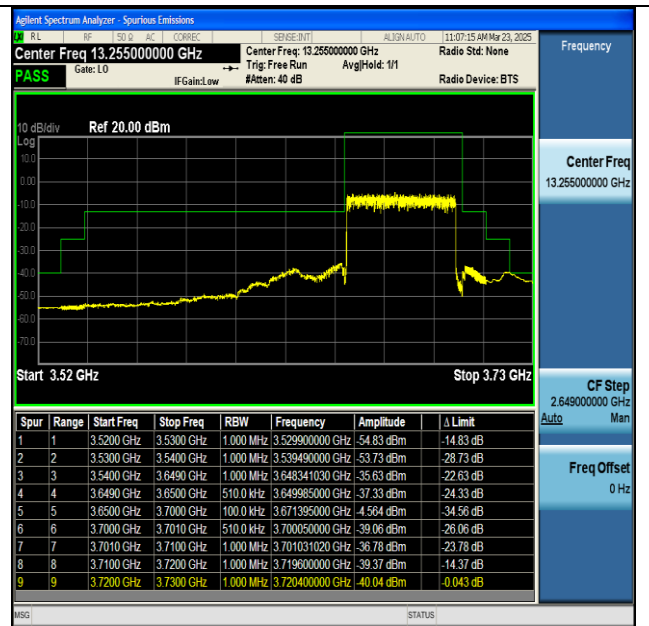
NTNV-n48-PC3-30-50-L-CP-QPSK-Edge_1RB_Left-PASS



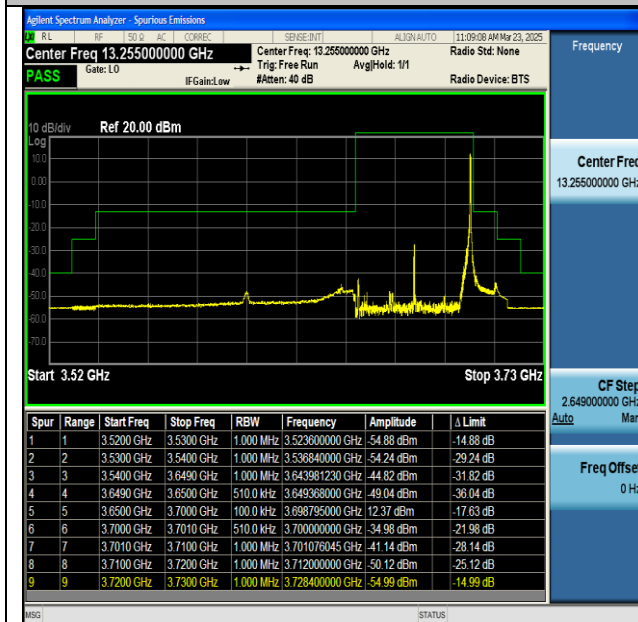
NTNV-n48-PC3-30-50-L-CP-QPSK-Outer_Full-PASS



NTNV-n48-PC3-30-50-H-DFT-BPSK-Edge_1RB_Right-PASS



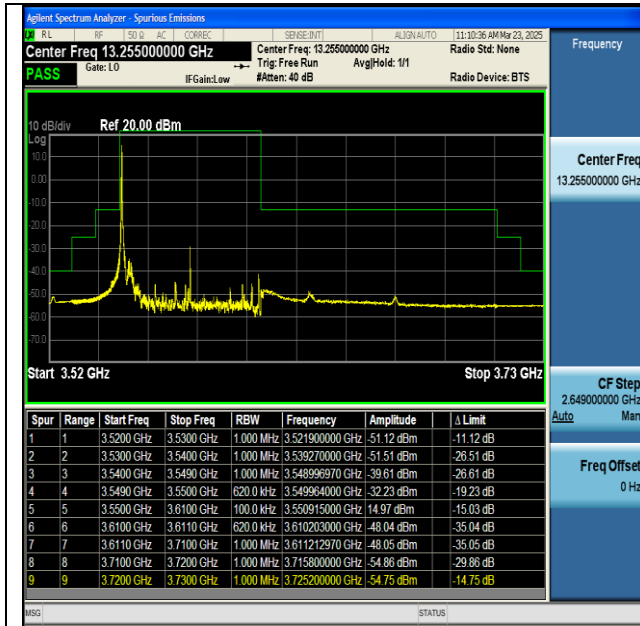
NTNV-n48-PC3-30-50-H-DFT-BPSK-Outer_Full-PASS



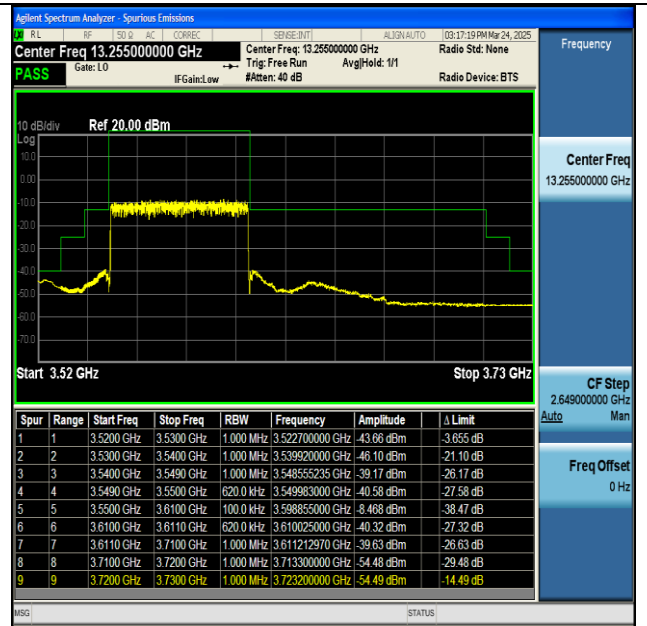
NTNV-n48-PC3-30-50-H-CP-QPSK-Edge_1RB_Right-PASS



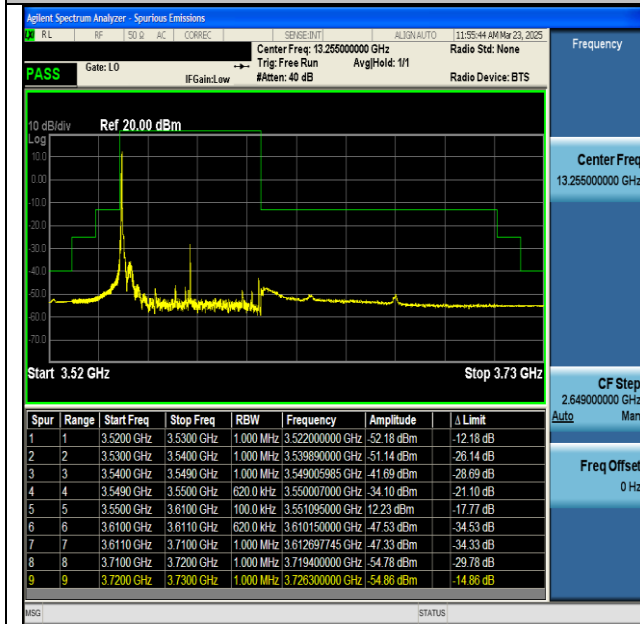
NTNV-n48-PC3-30-50-H-CP-QPSK-Outer_Full-PASS



NTNV-n48-PC3-30-60-L-DFT-BPSK-Edge_1RB_Left-PASS



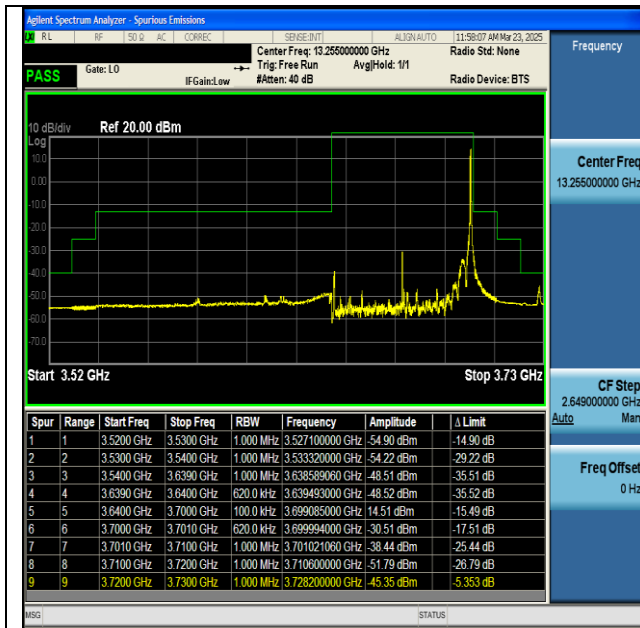
NTNV-n48-PC3-30-60-L-DFT-BPSK-Outer_Full-PASS



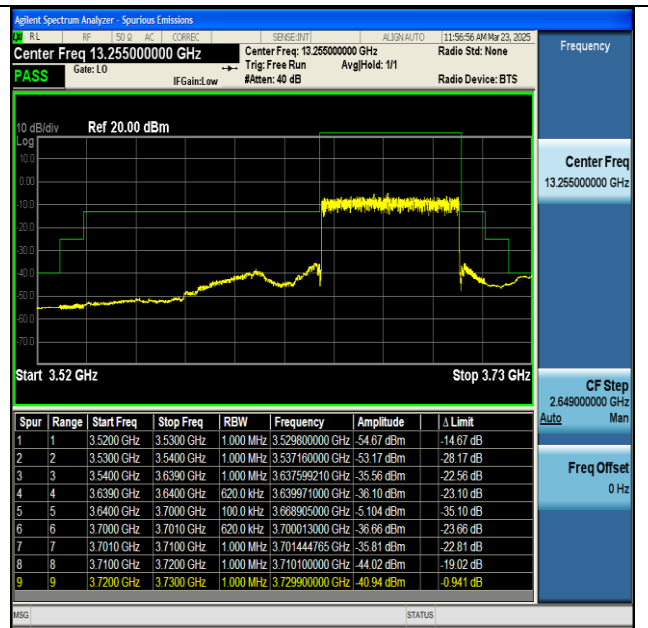
NTNV-n48-PC3-30-60-L-CP-QPSK-Edge_1RB_Left-PASS



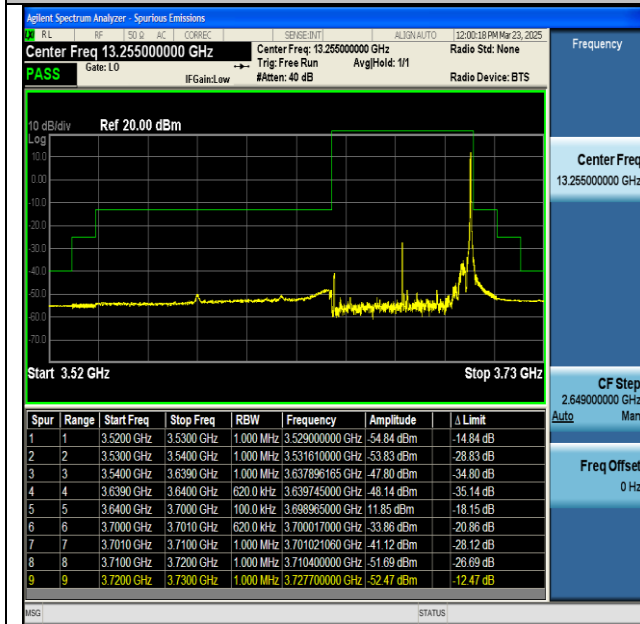
NTNV-n48-PC3-30-60-L-CP-QPSK-Outer_Full-PASS



NTNV-n48-PC3-30-60-H-DFT-BPSK-Edge_1RB_Right-PASS



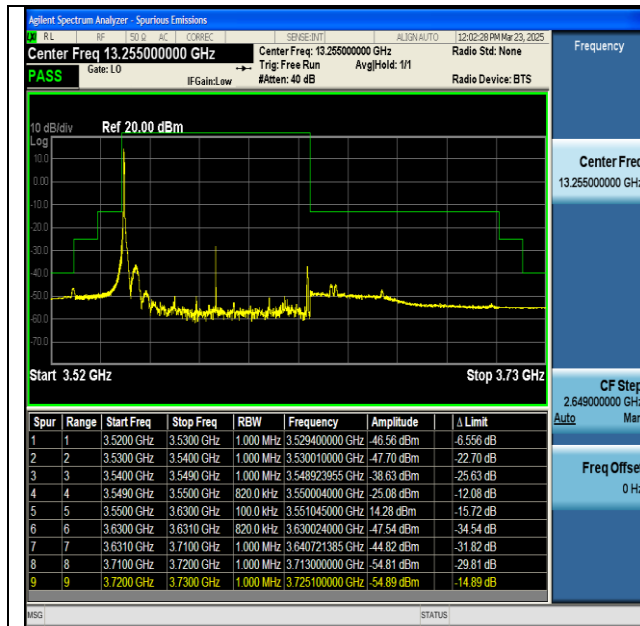
NTNV-n48-PC3-30-60-H-DFT-BPSK-Outer_Full-PASS



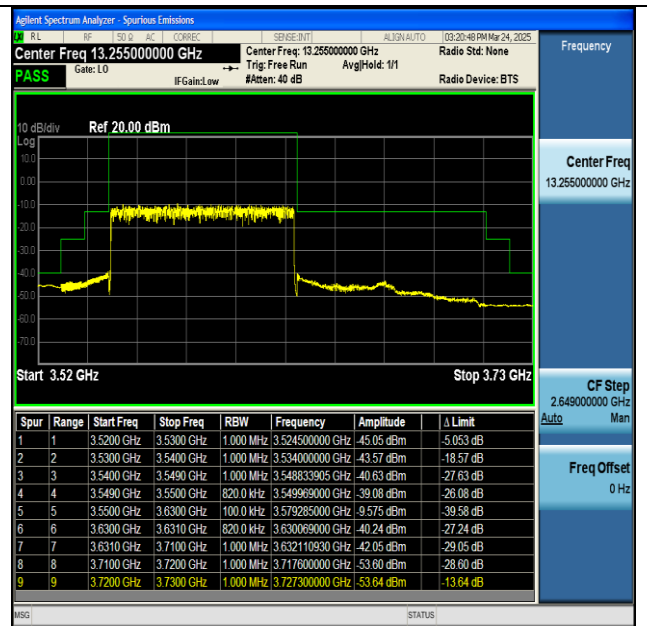
NTNV-n48-PC3-30-60-H-CP-QPSK-Edge_1RB_Right-PASS



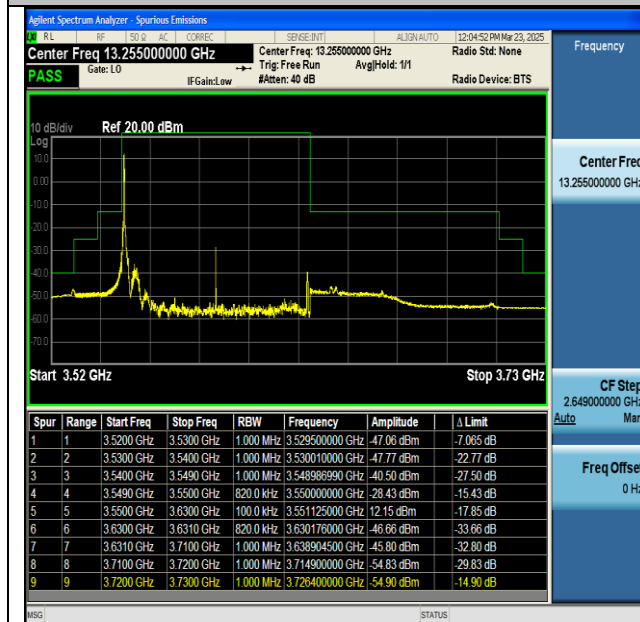
NTNV-n48-PC3-30-60-H-CP-QPSK-Outer_Full-PASS



NTNV-n48-PC3-30-80-L-DFT-BPSK-Edge_1RB_Left-PASS



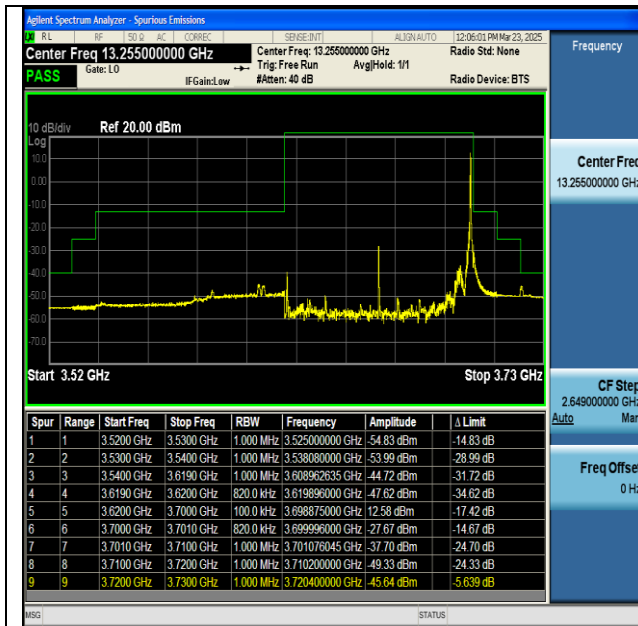
NTNV-n48-PC3-30-80-L-DFT-BPSK-Outer_Full-PASS



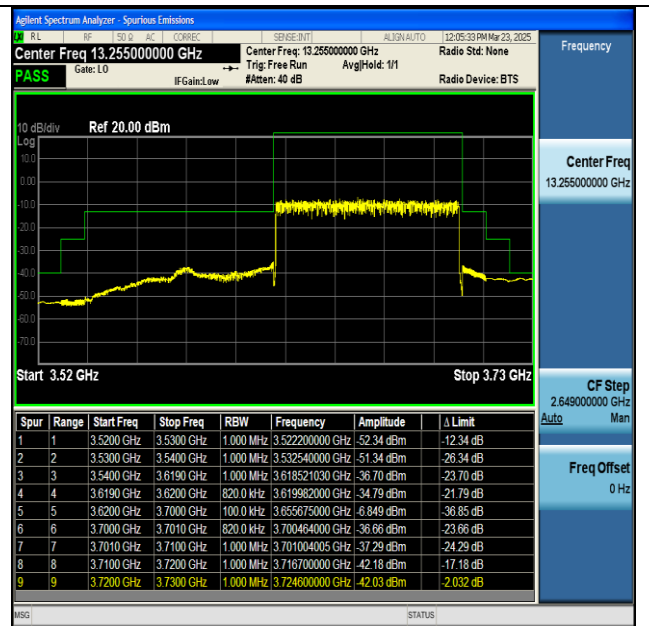
NTNV-n48-PC3-30-80-L-CP-QPSK-Edge_1RB_Left-PASS



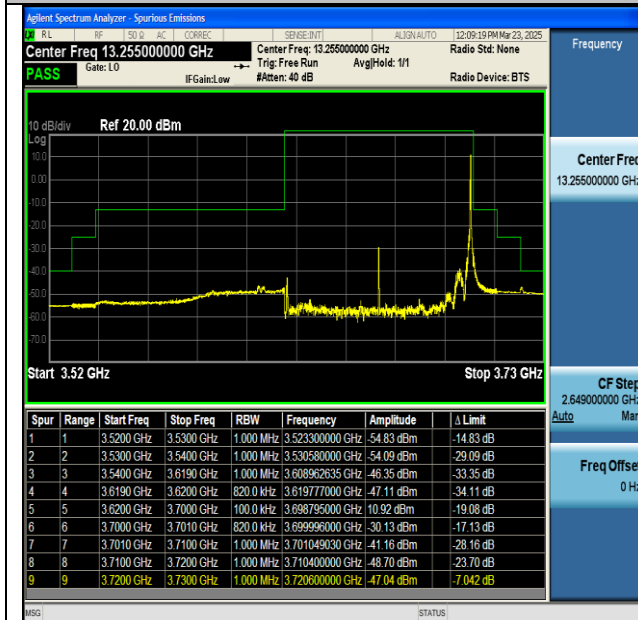
NTNV-n48-PC3-30-80-L-CP-QPSK-Outer_Full-PASS



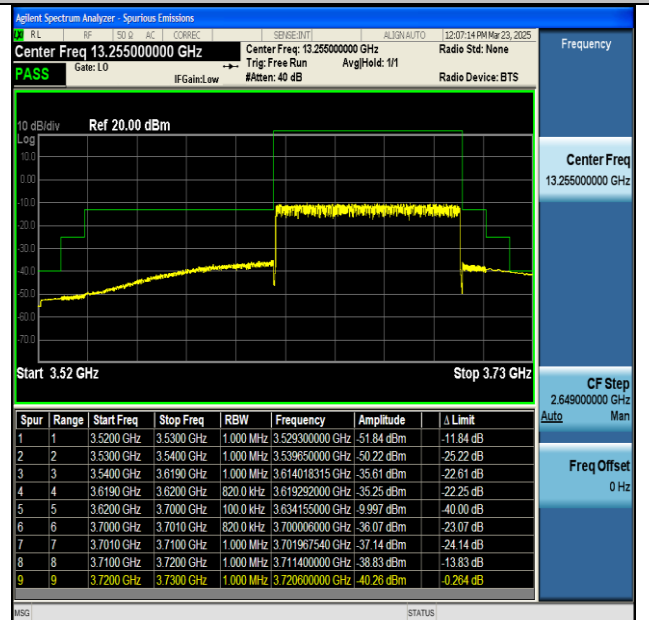
NTNV-n48-PC3-30-80-H-DFT-BPSK-Edge_1RB_Right-PASS



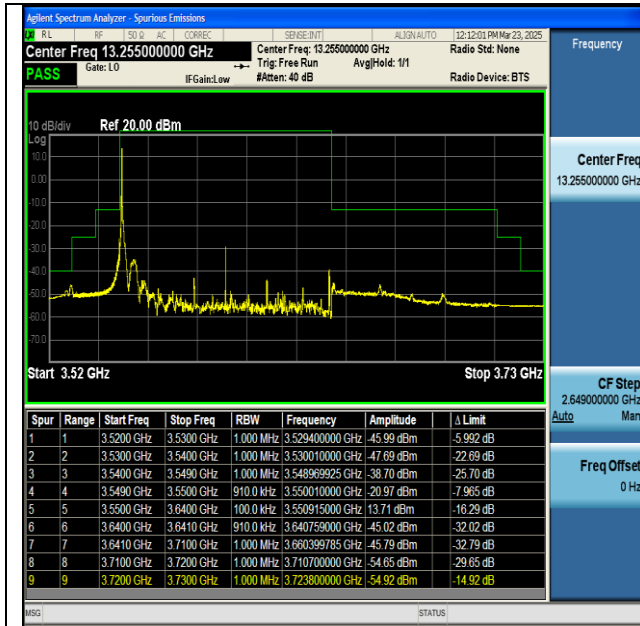
NTNV-n48-PC3-30-80-H-DFT-BPSK-Outer_Full-PASS



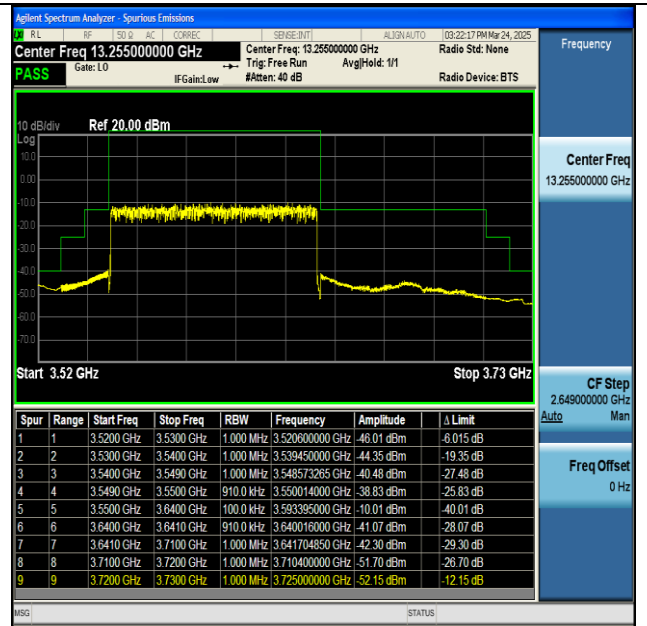
NTNV-n48-PC3-30-80-H-CP-QPSK-Edge_1RB_Right-PASS



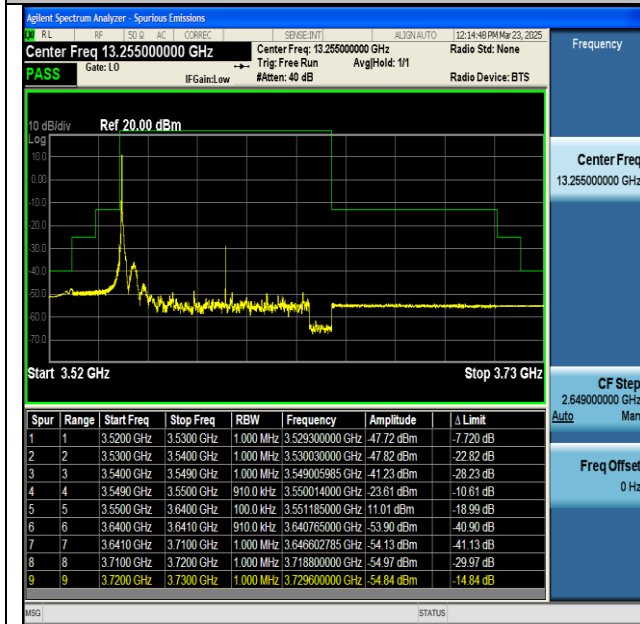
NTNV-n48-PC3-30-80-H-CP-QPSK-Outer_Full-PASS



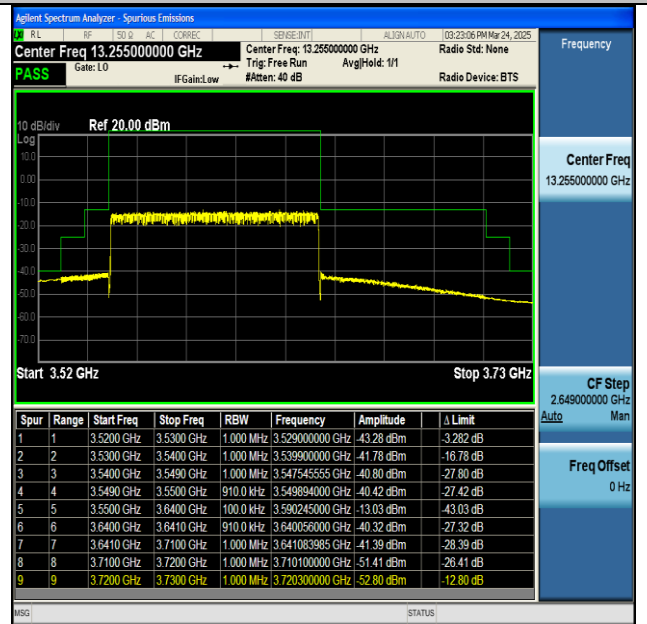
NTNV-n48-PC3-30-90-L-DFT-BPSK-Edge_1RB_Left-PASS



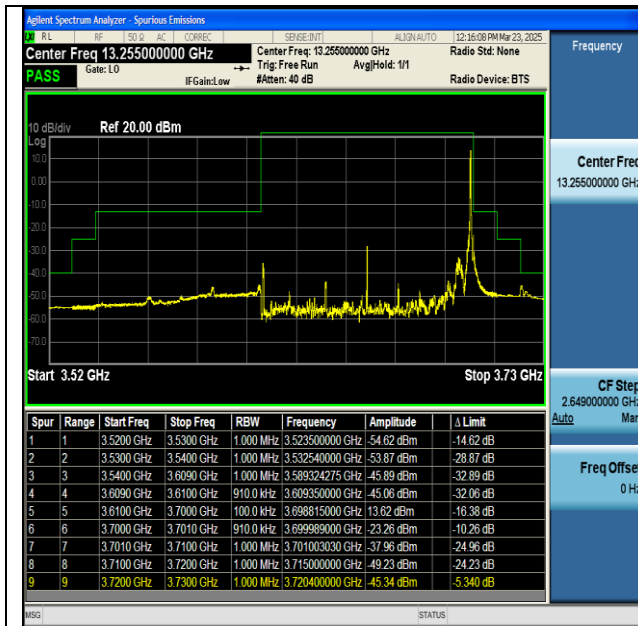
NTNV-n48-PC3-30-90-L-DFT-BPSK-Outer_Full-PASS



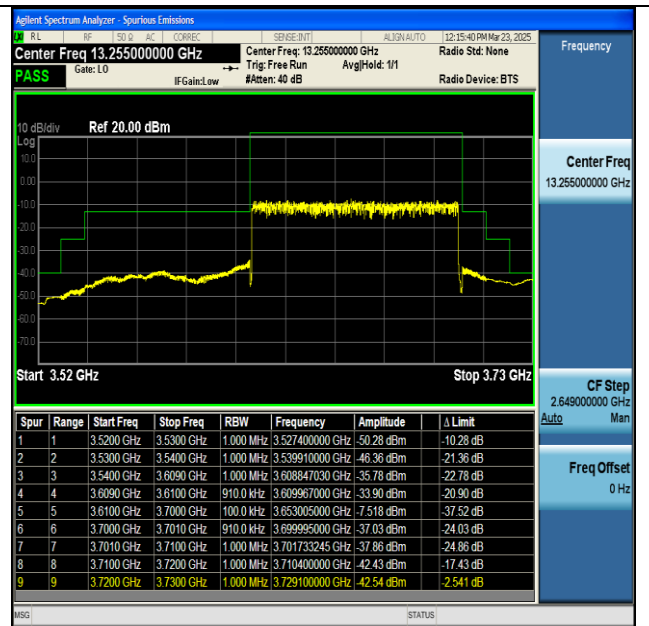
NTNV-n48-PC3-30-90-L-CP-QPSK-Edge_1RB_Left-PASS



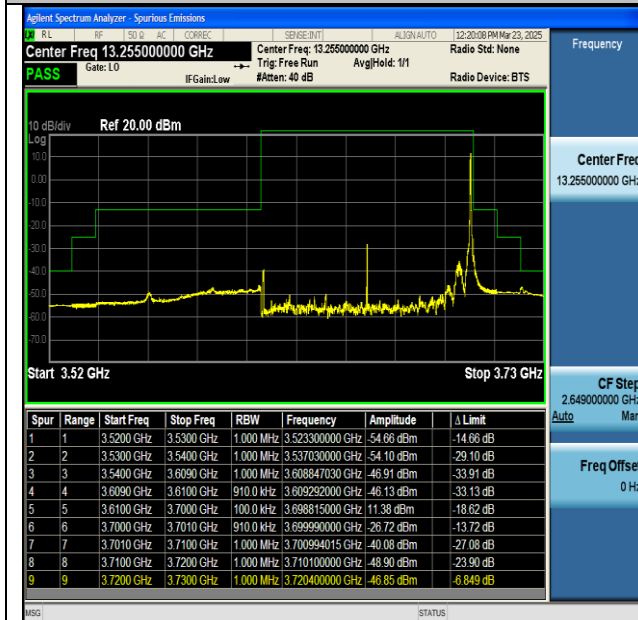
NTNV-n48-PC3-30-90-L-CP-QPSK-Outer_Full-PASS



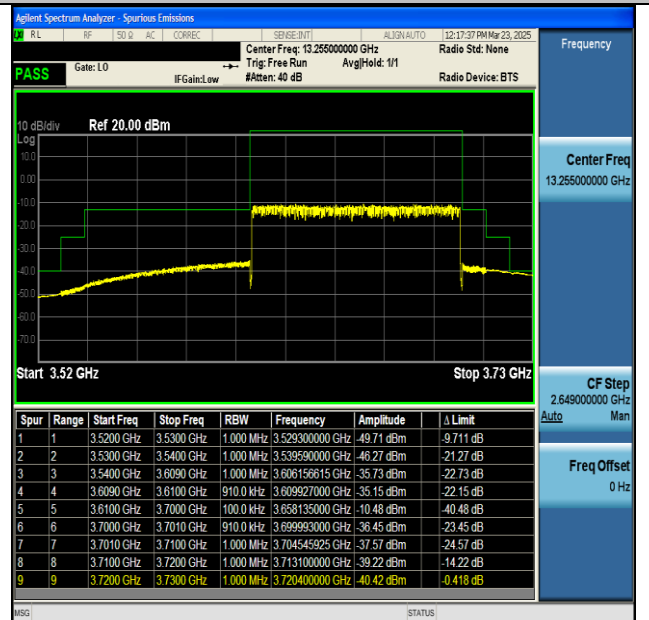
NTNV-n48-PC3-30-90-H-DFT-BPSK-Edge_1RB_Right-PASS



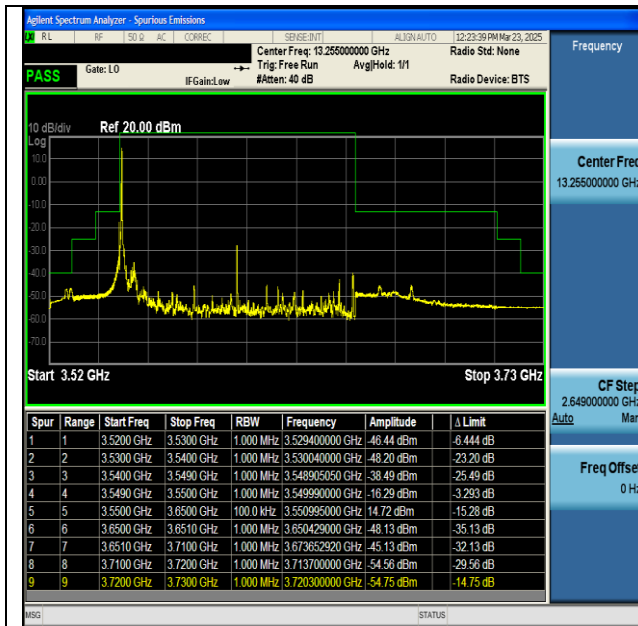
NTNV-n48-PC3-30-90-H-DFT-BPSK-Outer_Full-PASS



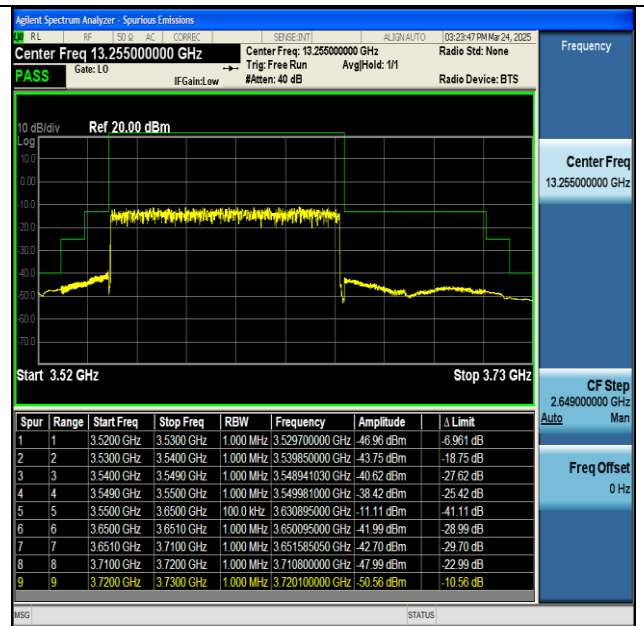
NTNV-n48-PC3-30-90-H-CP-QPSK-Edge_1RB_Right-PASS



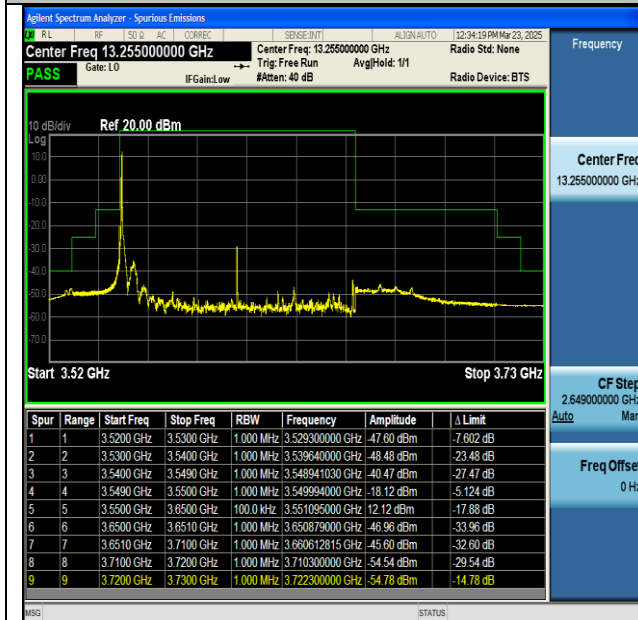
NTNV-n48-PC3-30-90-H-CP-QPSK-Outer_Full-PASS



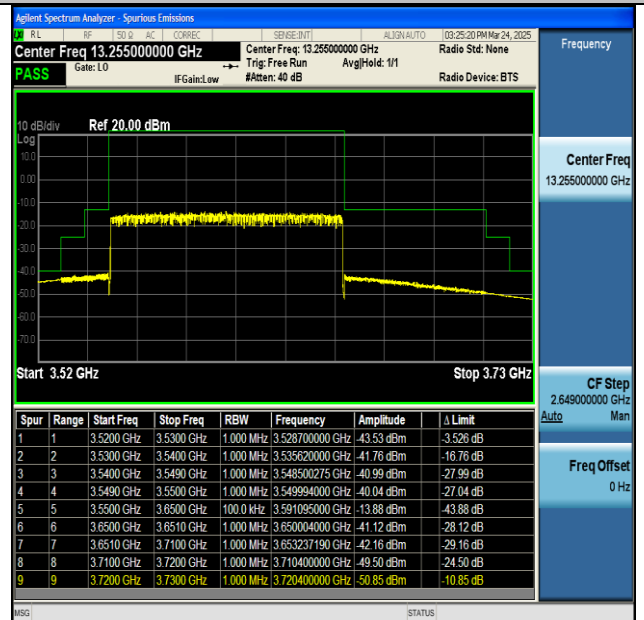
NTNV-n48-PC3-30-100-L-DFT-BPSK-Edge_1RB_Left-PASS



NTNV-n48-PC3-30-100-L-DFT-BPSK-Outer_Full-PASS



NTNV-n48-PC3-30-100-L-CP-QPSK-Edge_1RB_Left-PASS



NTNV-n48-PC3-30-100-L-CP-QPSK-Outer_Full-PASS