

	1	2	3	4	5	6	7	8
A								
B								
C								
D								
E								
F								
	1	2	3	4	5	6	7	8

V1.1 Change logs:

- 1. Change the size of MH5 to D3.81mm for the bracket.
- 2. Change the C10 to a smaller size capacitor - UCL1V221MNL1GS
- 3. Change the L2 to a smaller size inductor - NS10155T6R8NNA.
- 4. Change CN2 to a vertical connector.
- 5. Change CN17 to a SMD connector.
- 6. Correct 3.3V Power rail on LTE Page.
- 7. Change CN9, CN10 to smaller size connector - B4B-PH-K-S(LF)(SN)
- 8. Change CN13, CN14 to smaller size connector - B2B-PH-K-S(LF)(SN)
- 9. Removed Testing points from TP40 to TP51, merge the signals into CN17 and change CN17 to 20 PINs connector - 0878325922
- 10. Change CN18 to smaller size connector - B7B-PH-K-S(LF)(SN)
- 11. Swap signals on U6 and CN17 for better PCB routing

V1.2 Change logs:

- 1. Change Mounting holes to Non-plated and remove the GND
- 2. Swapp PIN1 and PIN2 on CN1
- 3. Change R2 to R3 in design note of U1

V2 Change logs:

- 1. Change CN13 to Molex 5055680271
- 2. Correct SCI5 and SCI8 naming.
- 3. Change U6 to R7FA6M3AH3CFP#AA0

V2.1 Change logs:

- 1. Change F1 to 0685T2000-01
- 2. Change U8 to ESP32-WROOM-32E(8MB)
- 3. DNP U9
- 4. Change the connector to match serial port (follow ESP-PROG pinout)
- 5. Remove Jtag test points
- 6. Add CLI passthrough Tx/Rx test points
- 7. Add SCI2 tx and rx test points

V2.6 Change logs:

- 1. Changed title bock
- 2. Updated Silk screen

V3.0 Change logs:

- 1. The programming ports are on bottom
- 2. Battery connector is flipped 180

V3.2 Change logs:

- 1. Changes solder mask bridge and Layer Stack as per Fabricator's requirement
- 2. Update pannel placement and Gerber file

V3.3 Change logs:

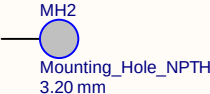
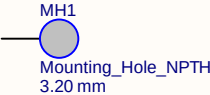
- 1. IR sensor current limiting resistor reduced
- 2. IR sensor collector resistor reduced
- 3. Cell sensor current limiting resistor reduced
- 4. Cell sensor collector resistor reduced

Notes (unless otherwise specified):

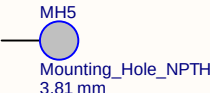
- 1. All resistors are 1%.
- 2. Board operating temperature is 0°C to 50°C.
- 3. Capacitors and resistors are 0603 unless noted (termination resistors and BGA capacitors are 0402).
- 4. Text in *italic* placed on a wire doesn't correspond to net name. It just helps to identify rapidly MCU IO related to this wire.
- 5. 10 mil PCB Layout clearance for Critical circuit section.

Medesto-PCBA3.0-V3.0

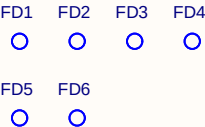
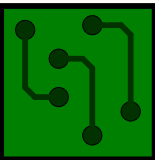
- 1. Title
- 2. Block Diagram
- 3. Power Management
- 4. Battery Charger
- 5. MCU
- 6. Stepper Motor Driver
- 7. Solenoid Driver
- 8. Sensor
- 9.UserInterface
- 10. SD Card
- 11. WiFi
- 12. NFC
- 13. Audio
- 14. LTE CAT M1
- 15. Extension Connector



Four holes (MH1 - 4) for #3 machine screws and washers



PCB1
Medesto-PCBA3.0-V3.3



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Title:
Medesto-PCBA3.0-V3.3

Size:
ANSI A

Perigon #:
LB100-3000-OA - PCBA 3.0

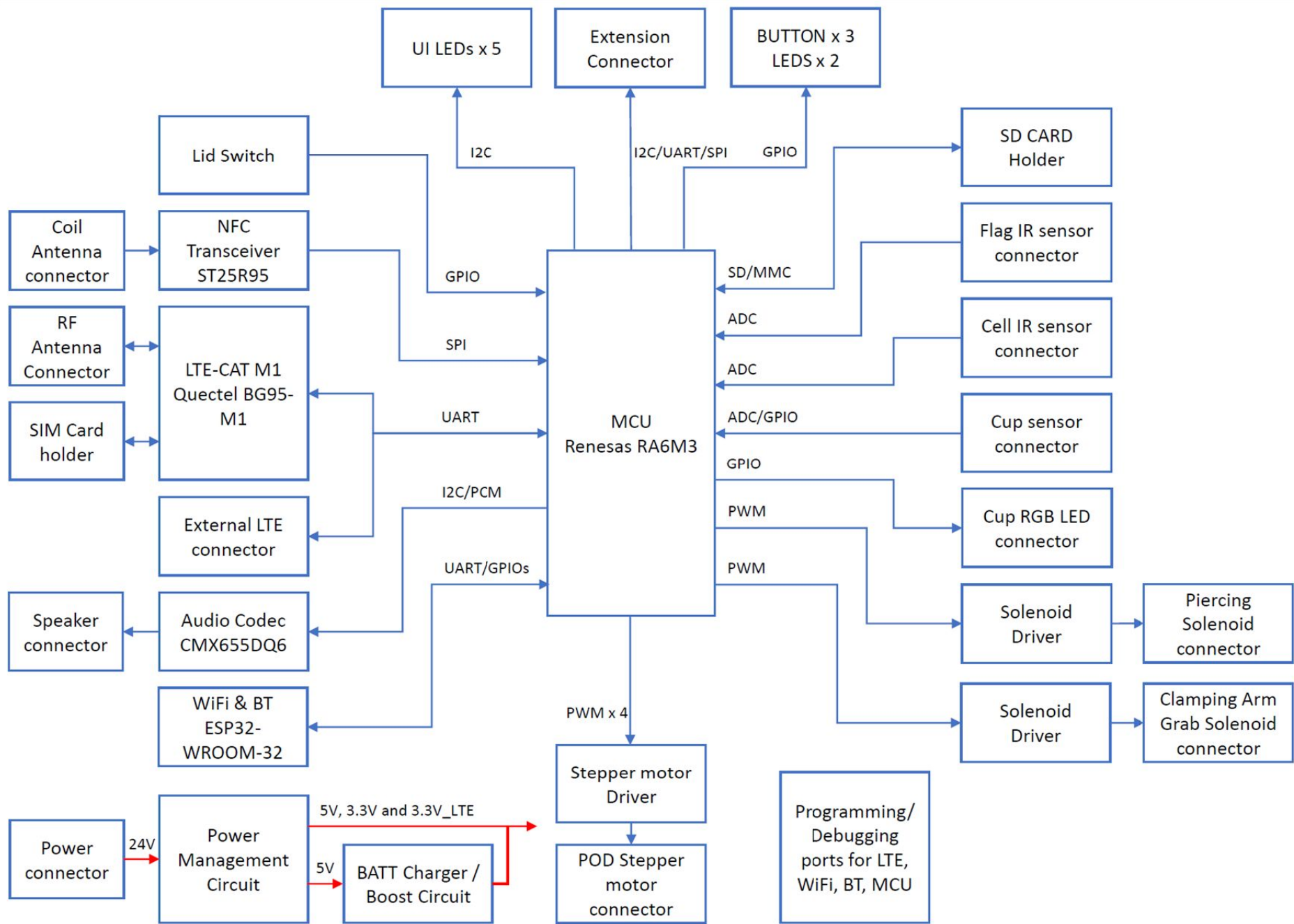
Project:
G3.5 Dispenser

Date:
6/21/202212:19:35 PM

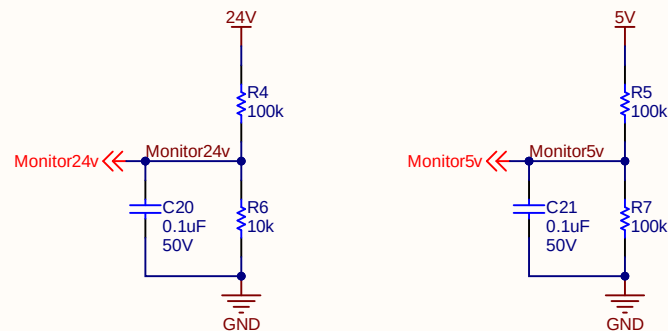
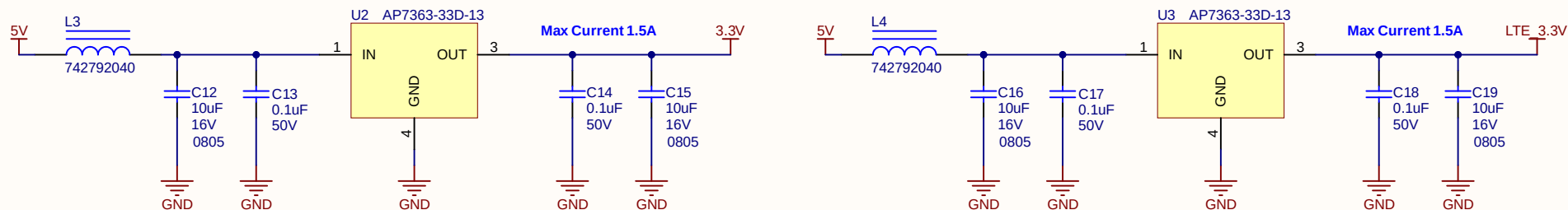
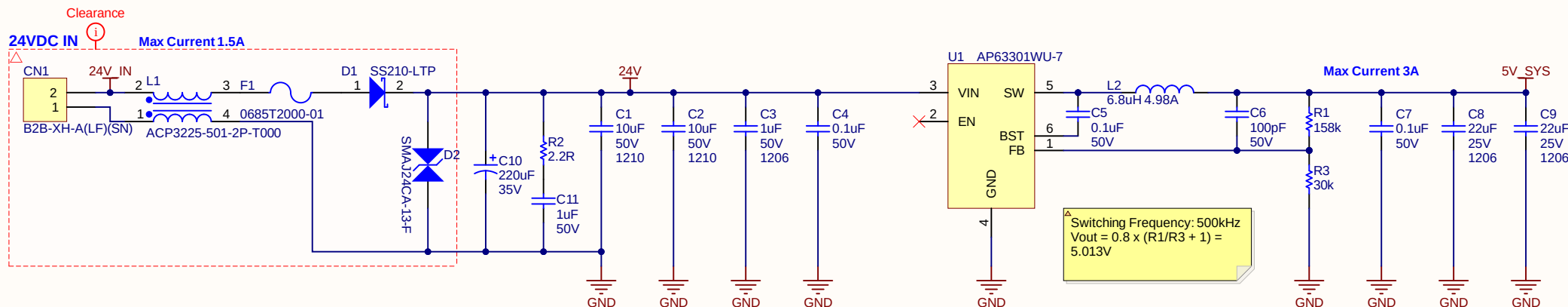
Page:
1 of 15

Designed By:
AU , JW

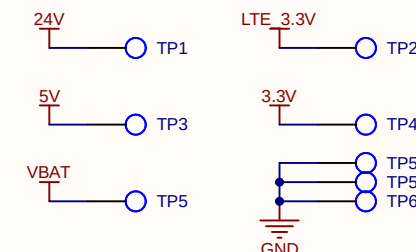
Approved By:
JG



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Title: Medesto-PCBA3.0-V3.3			
Size: ANSI A	Perigon #: LB100-3000-OA - PCBA 3.0	Project: G3.5 Dispenser	
Date: 6/21/2022 12:19:35 PM	Page: 2 of 15	Designed By: AU , JW	Approved By: JG



Jig Test Points



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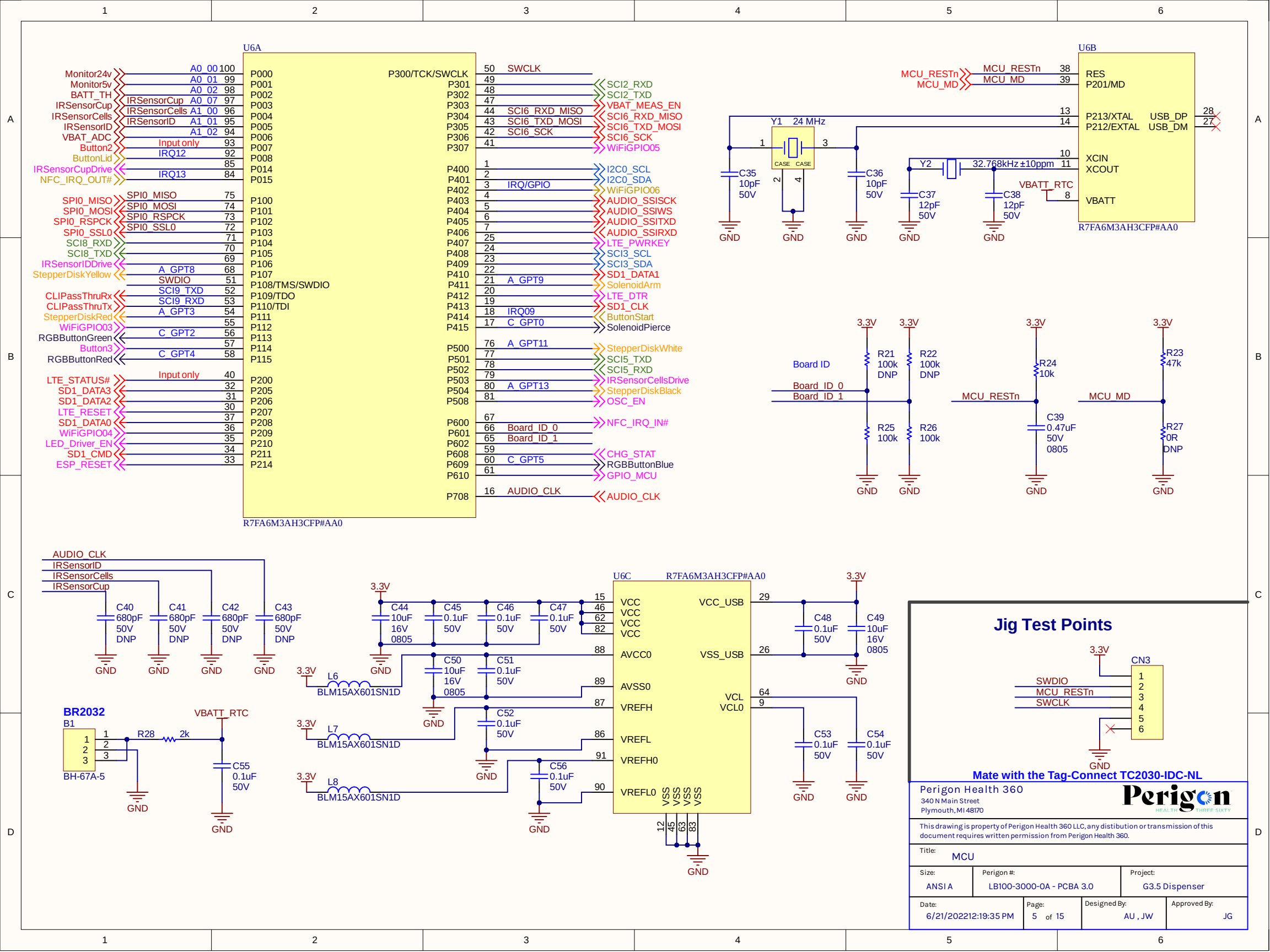
Perigon
 REAL TIME PROTECT SIXTY

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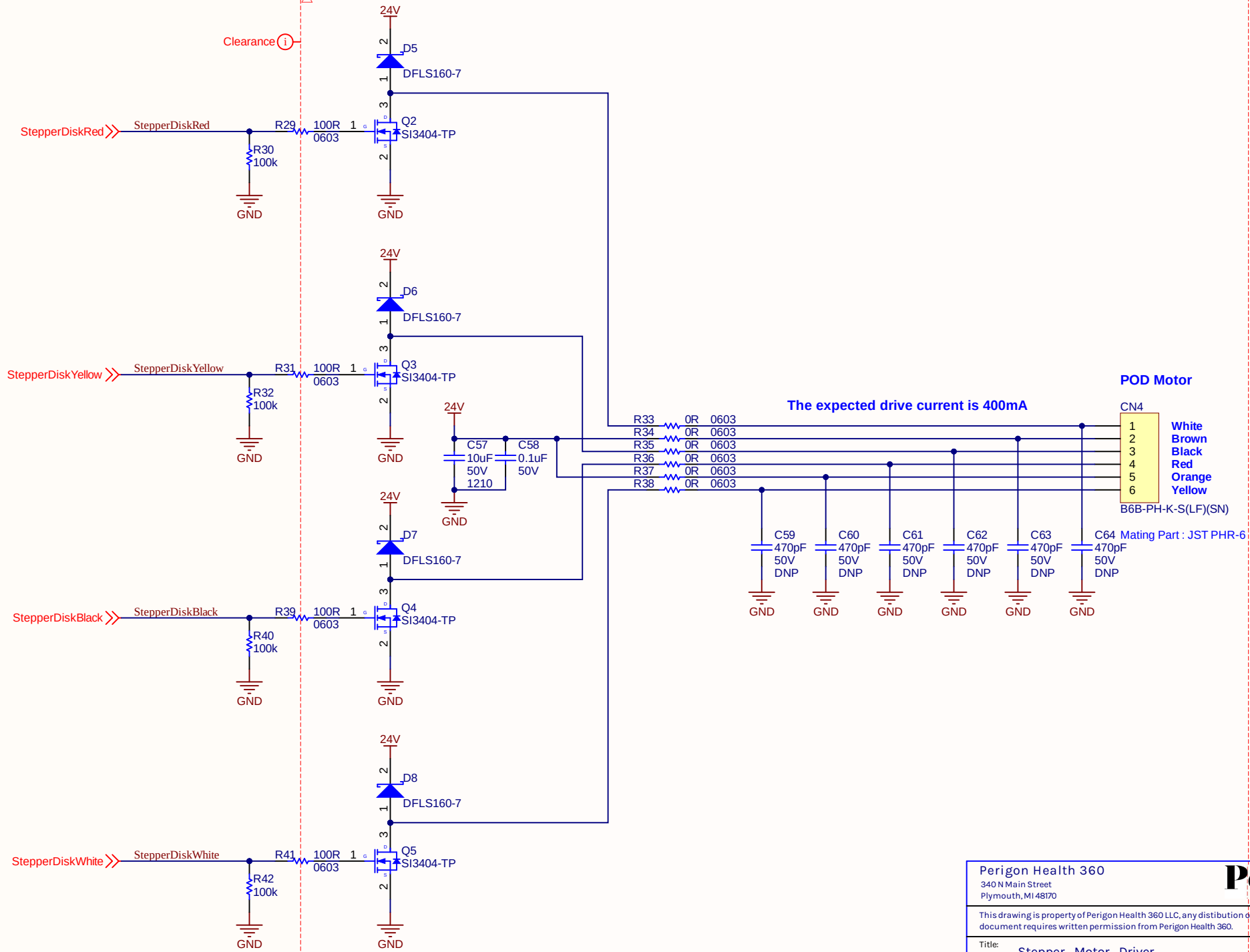
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Size: ANSI A	Perigon #: LB100-3000-OA - PCBA 3.0	Project: G3.5 Dispenser
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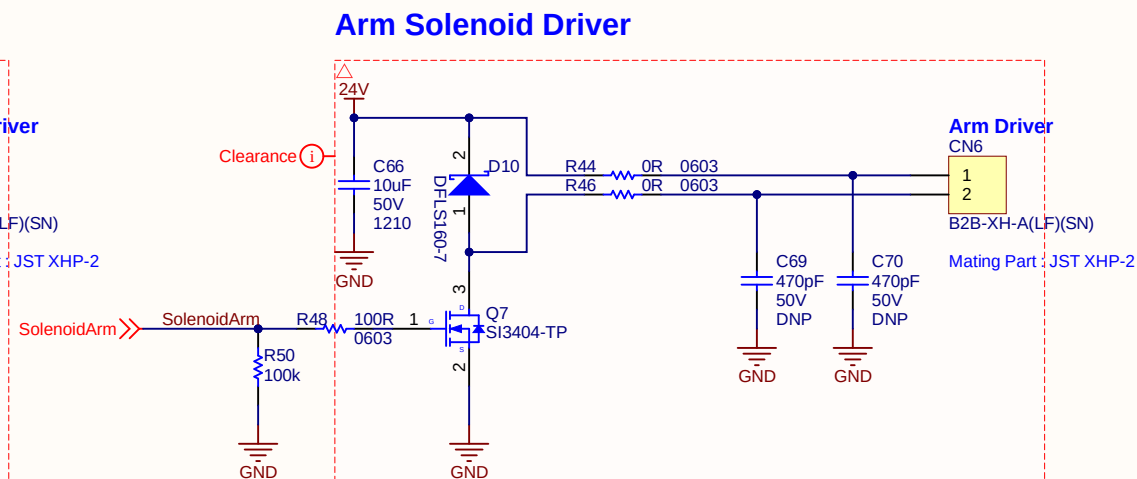
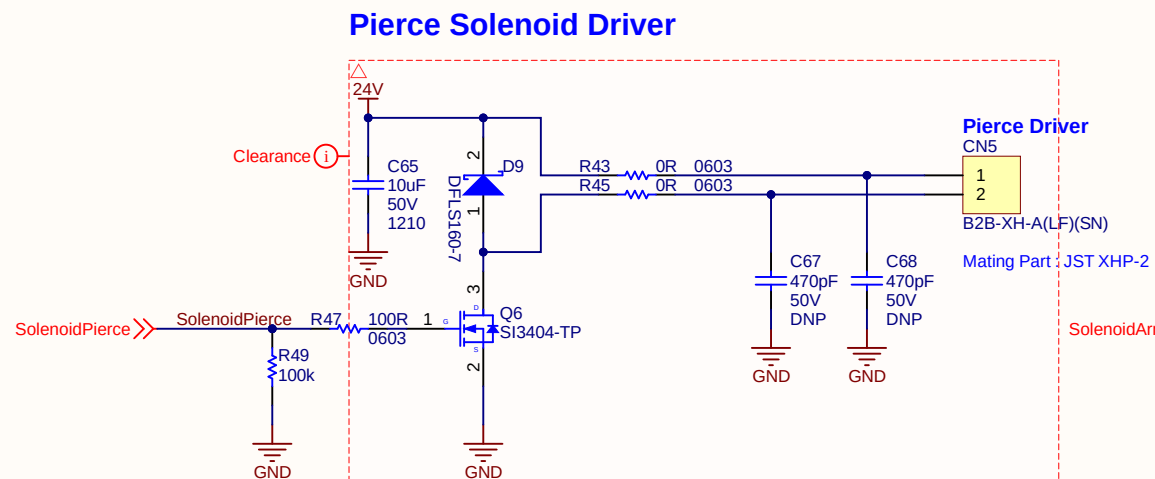
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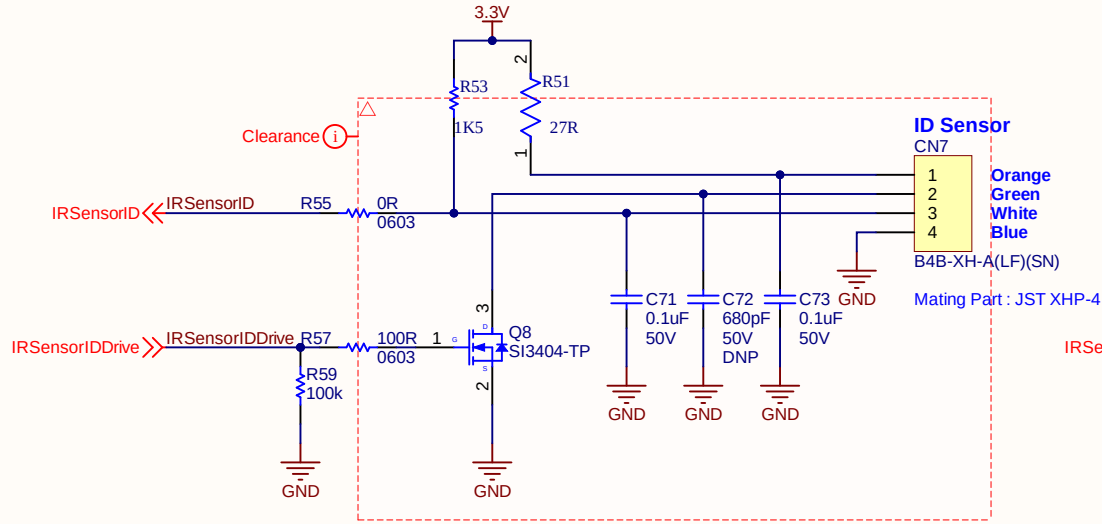
Disk Stepper Motor Driver



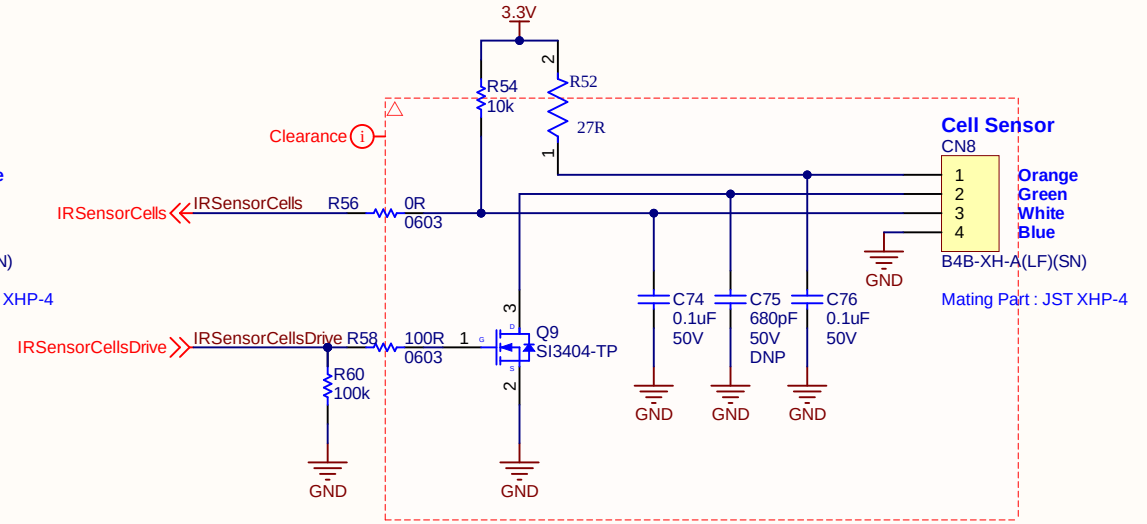
Perigon Health 360 340 N Main Street Plymouth, MI 48170			
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Title: Stepper_Motor_Driver			
Size: ANSI A	Perigon #: LB100-3000-0A - PCBA 3.0	Project: G3.5 Dispenser	
Date: 6/21/2022 12:19:36 PM	Page: 6 of 15	Designed By: AU , JW	Approved By: JG



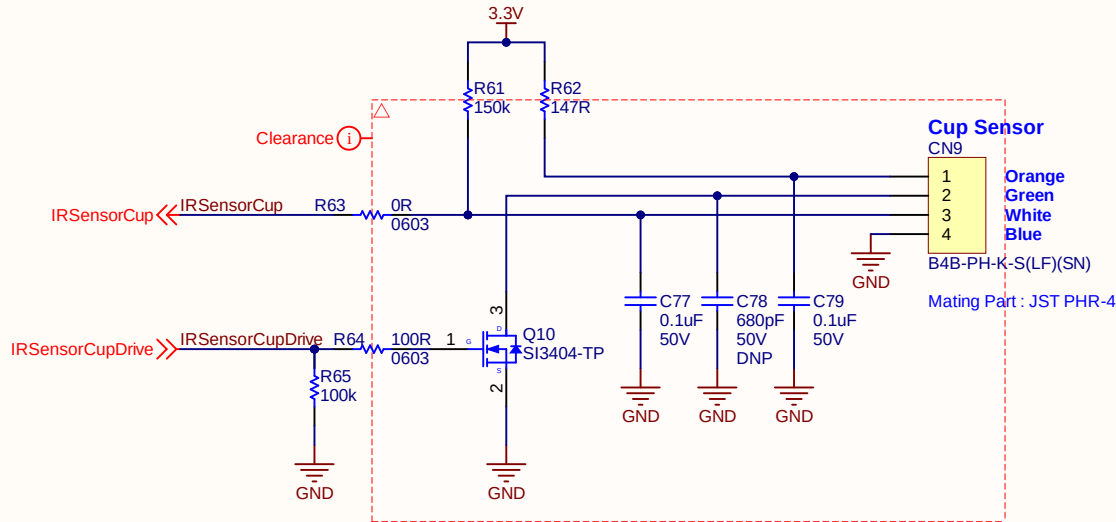
ID IR Sensor



Cells IR Sensor



Cup IR Sensor



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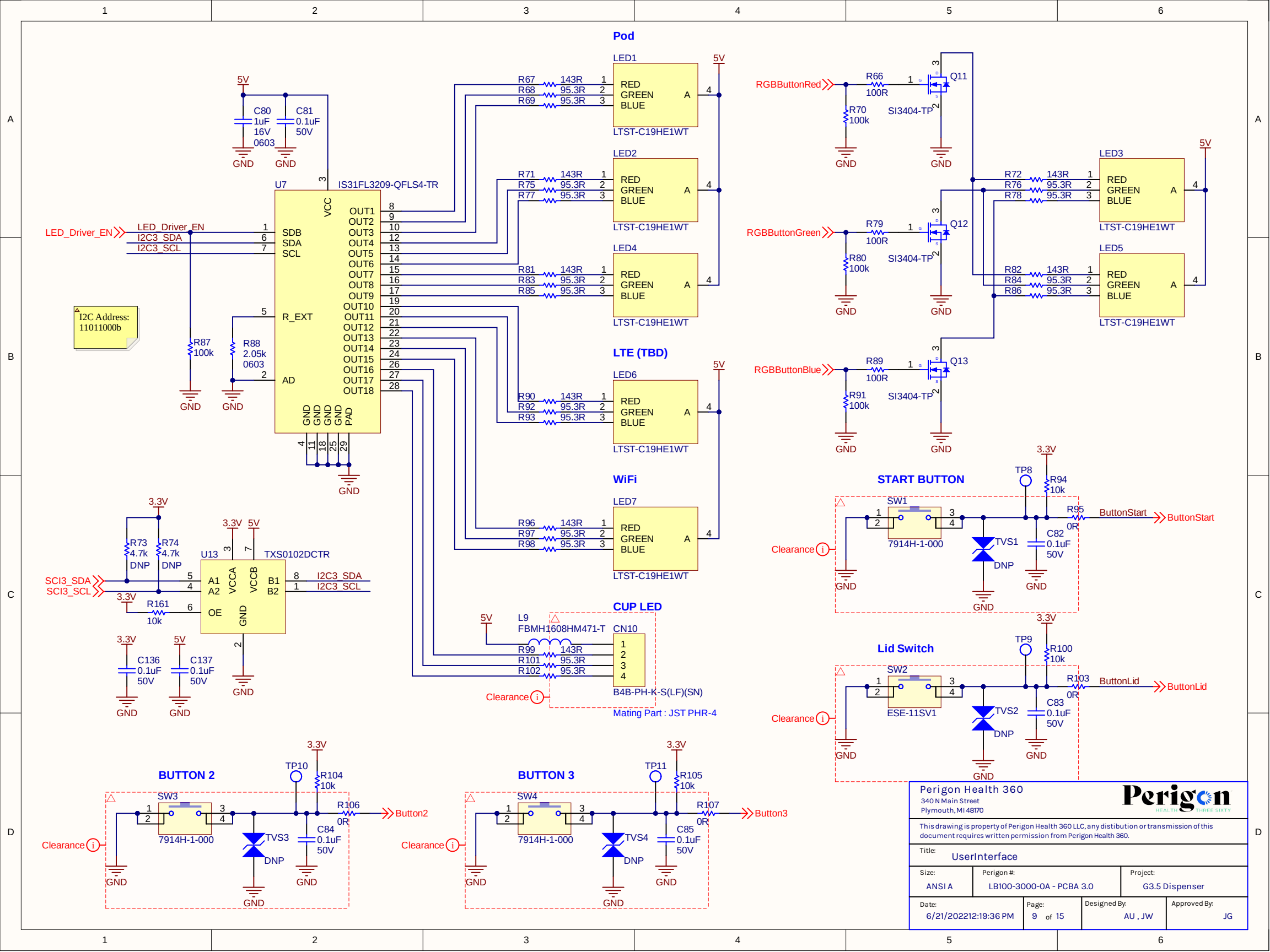


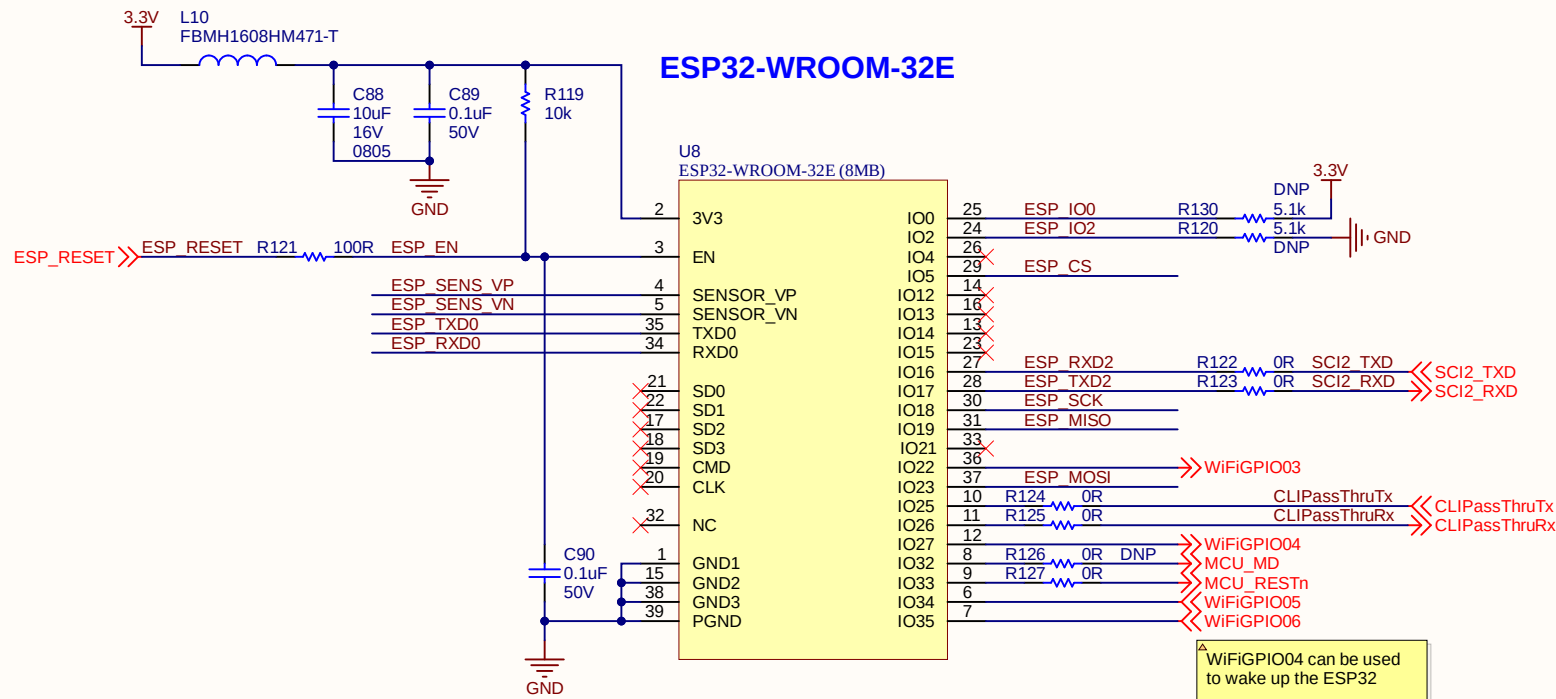
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Title: Sensor

Size: ANSI A	Perigon #: LB100-3000-OA - PCBA 3.0	Project: G3.5 Dispenser
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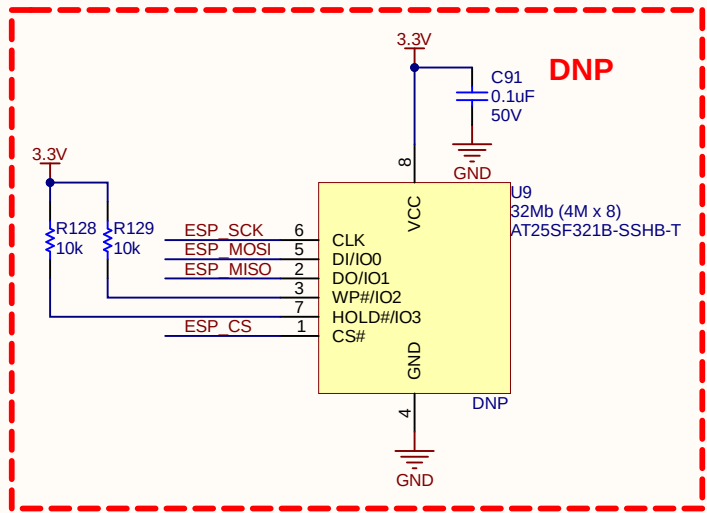
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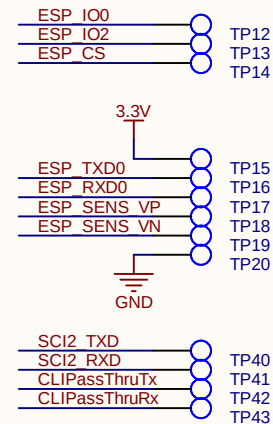


Wi-Fi GPIO04 can be used to wake up the ESP32

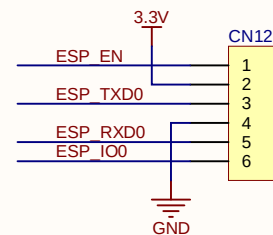
Wi-Fi GPIO06 can be used to wake up MCU if needed



Debug Test Points



Jig Test Points



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REAL TIME HEALTH MONITORING

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Title: Wi-Fi

Size: ANSI A

Perigon #: LB100-3000-OA - PCBA 3.0

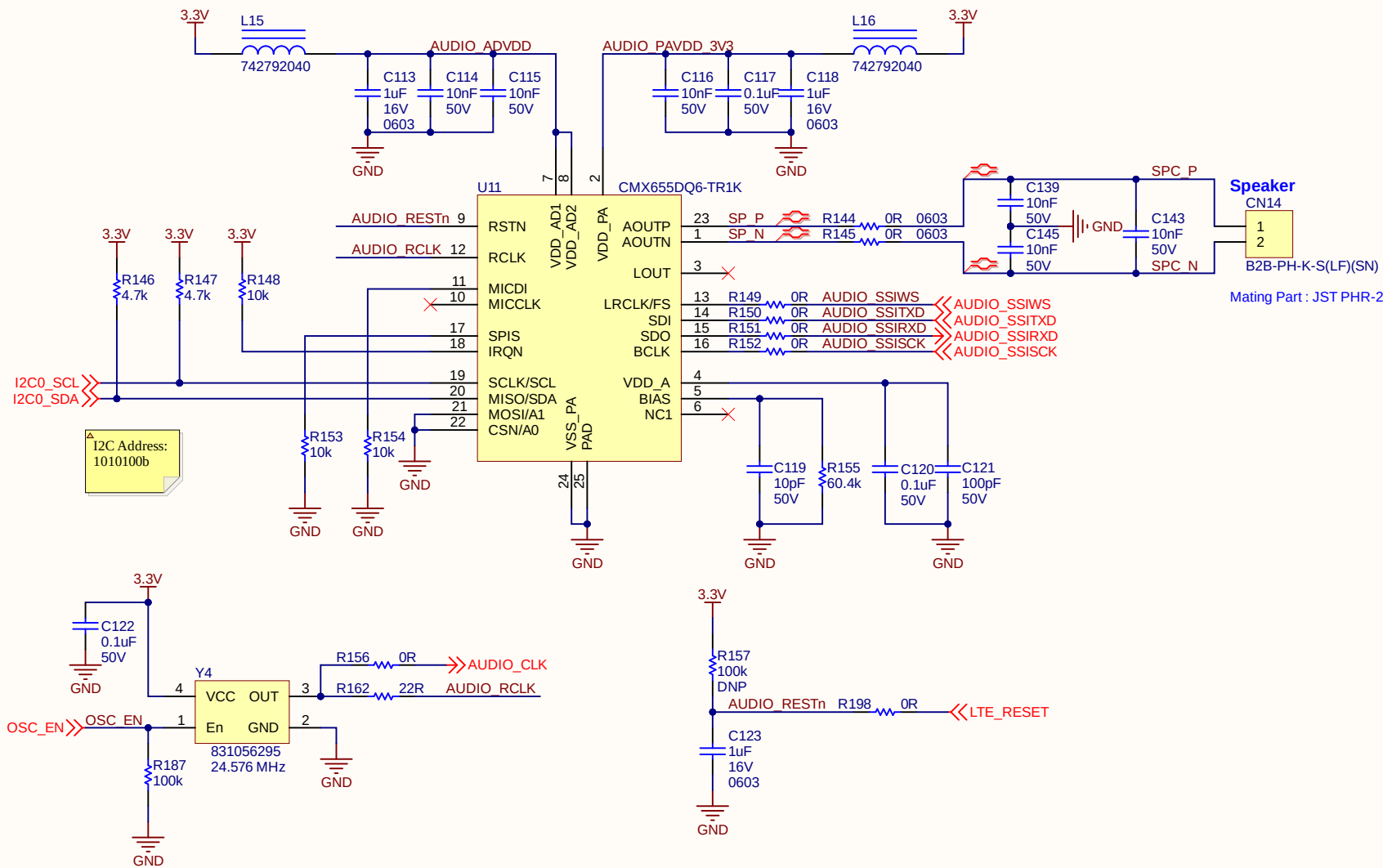
Project: G3.5 Dispenser

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Page: 11 of 15

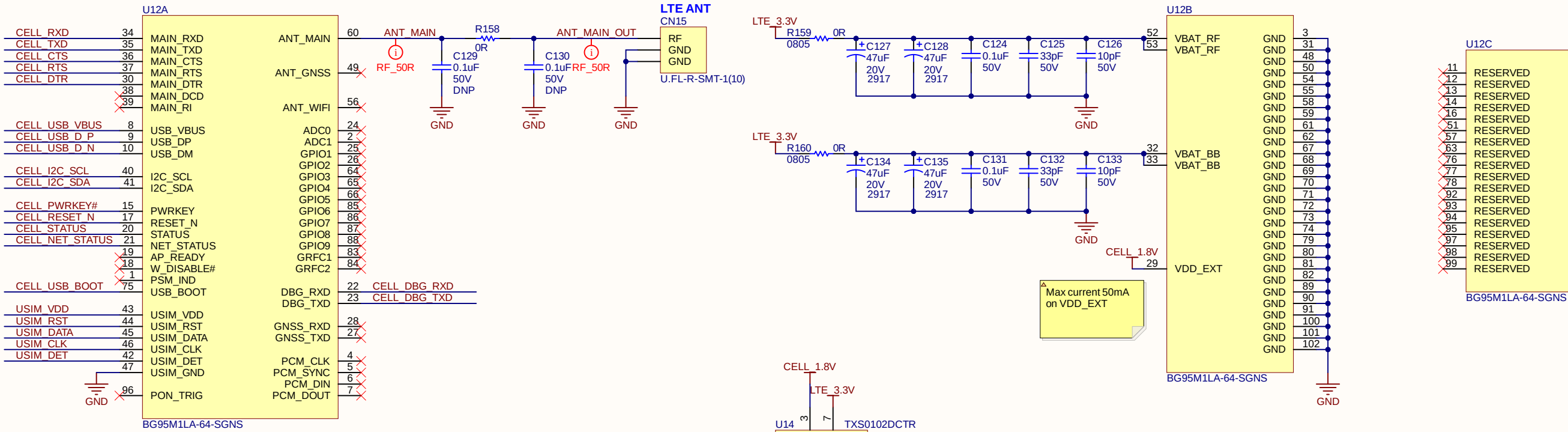
Designed By: AU, JW

Approved By: JG

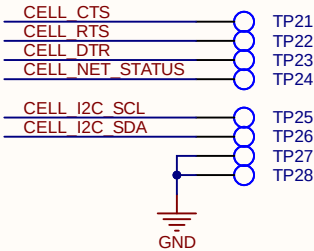


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Title: Audio			
Size: ANSI A	Perigon #: LB100-3000-OA - PCBA 3.0		Project: G3.5 Dispenser
Date: 6/21/202212:19:36 PM	Page: 13 of 15	Designed By: AU , JW	Approved By: JG

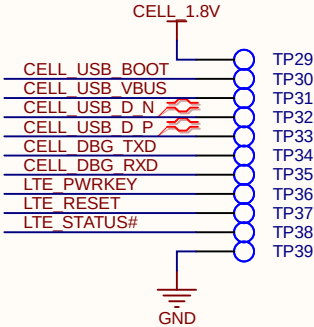
LTE Cat-M Modem



Debug Test Points

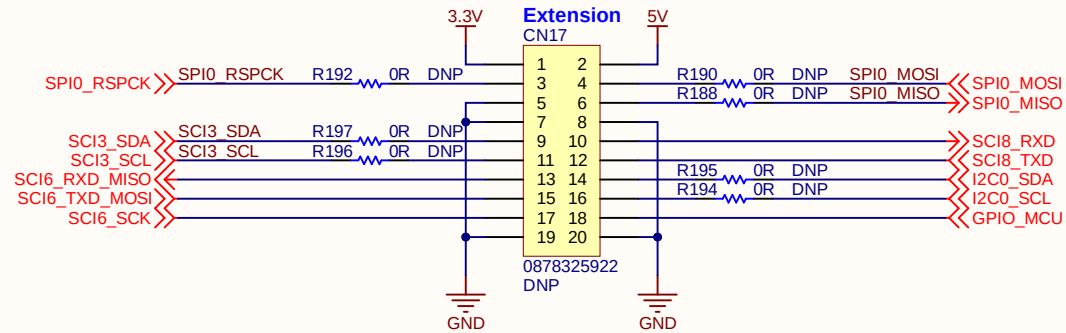


Jig Test Points

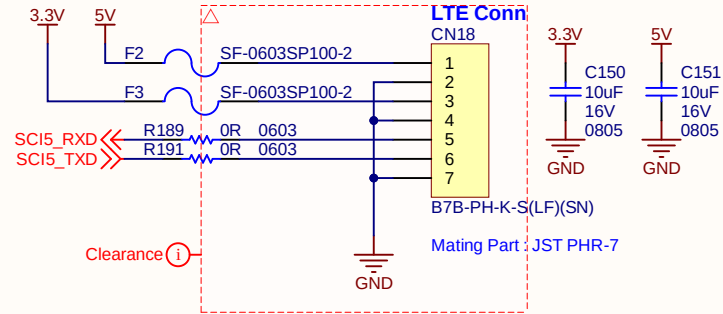


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Perigon HEALTH THREE SIXTY			
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Title: LTE_CAT_M1			
Size: ANSI A	Perigon #: LB100-3000-0A - PCBA 3.0	Project: G3.5 Dispenser	
Date: 6/21/2022 12:19:36 PM	Page: 14 of 15	Designed By: AU, JW	Approved By: JG

Extension Conn - DNP



External LTE Module Conn



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Title: Extension_Connector				
Size: ANSI A	Perigon #: LB100-3000-OA - PCBA 3.0		Project: G3.5 Dispenser	
Date: 6/21/2022 12:19:36 PM	Page: 15 of 15	Designed By: AU , JW		Approved By: JG