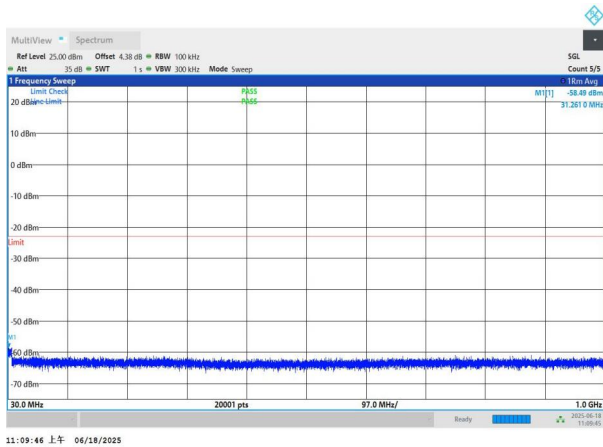
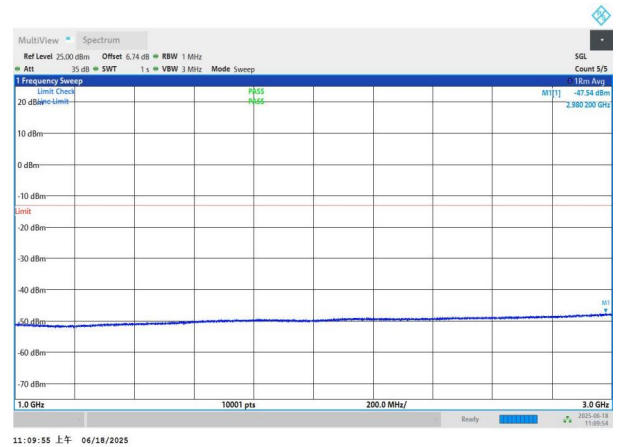
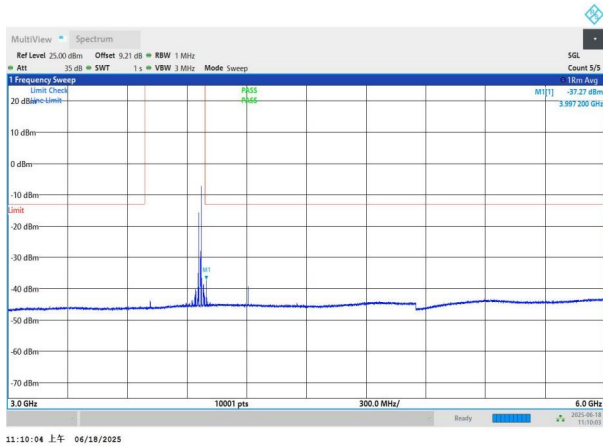




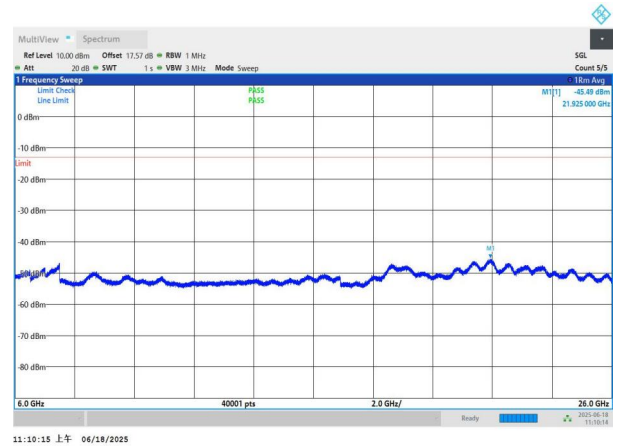
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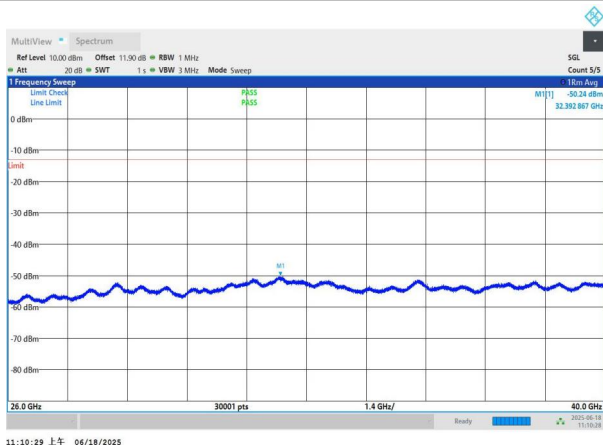
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1000-3000-PASS



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-3000-6000-PASS



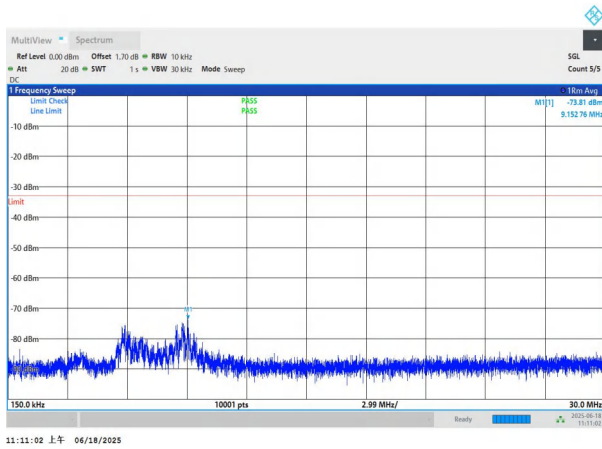
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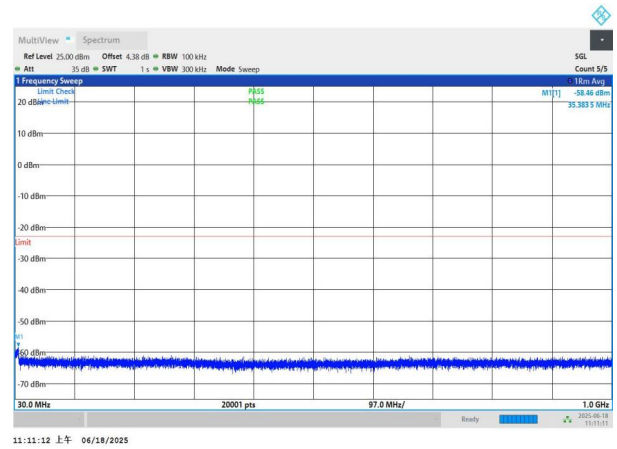
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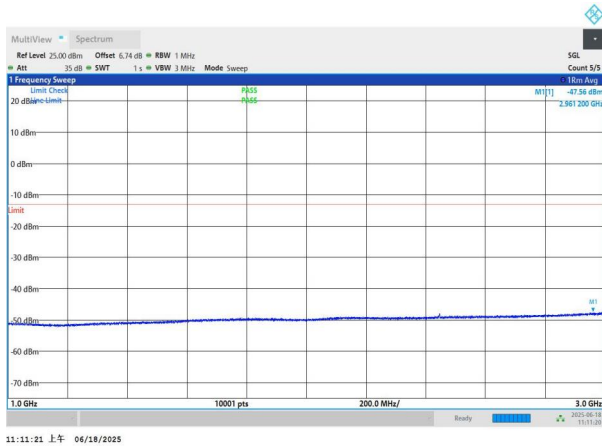
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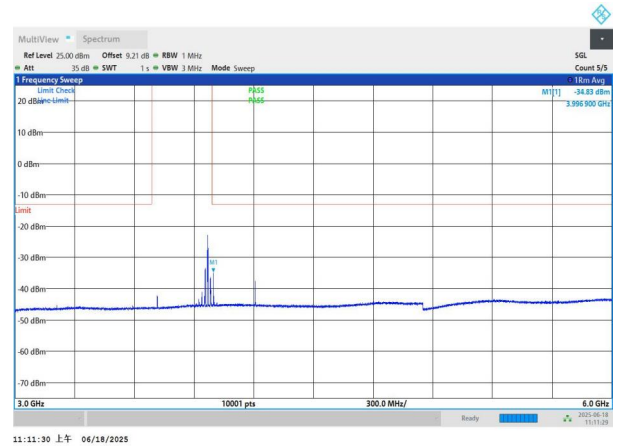
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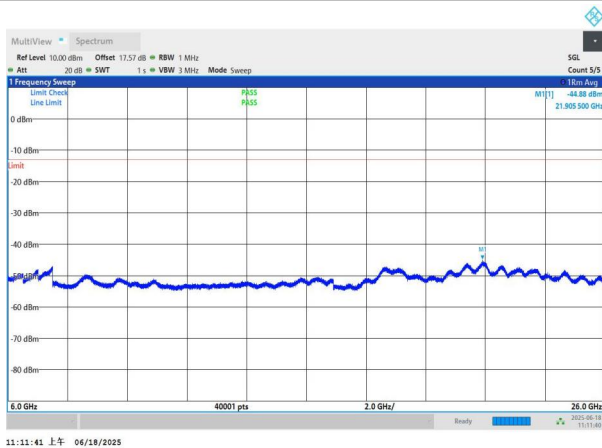
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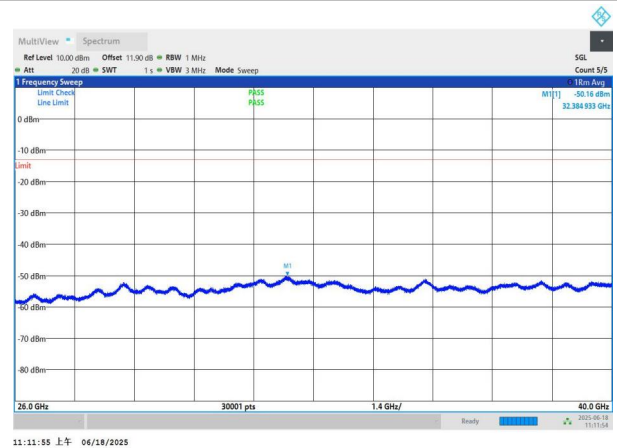
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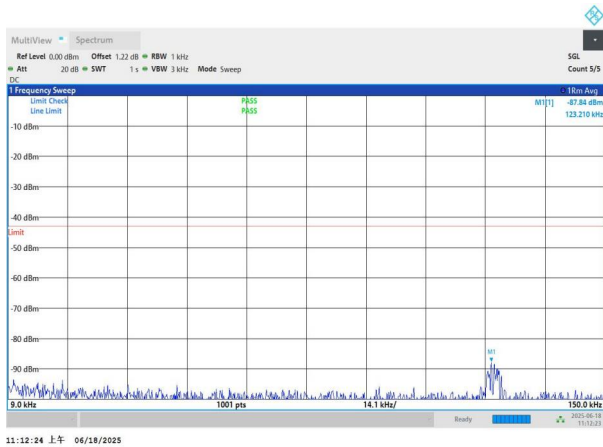
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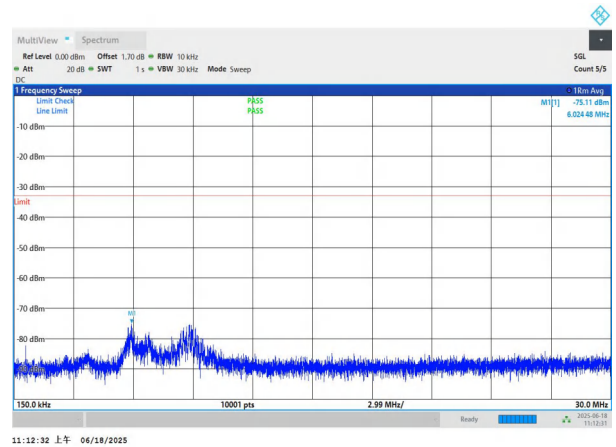
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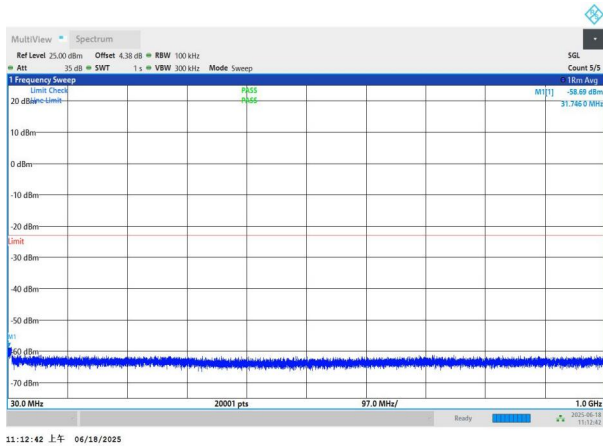
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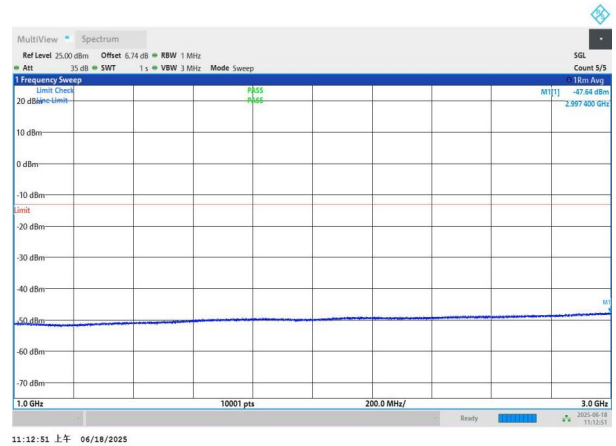
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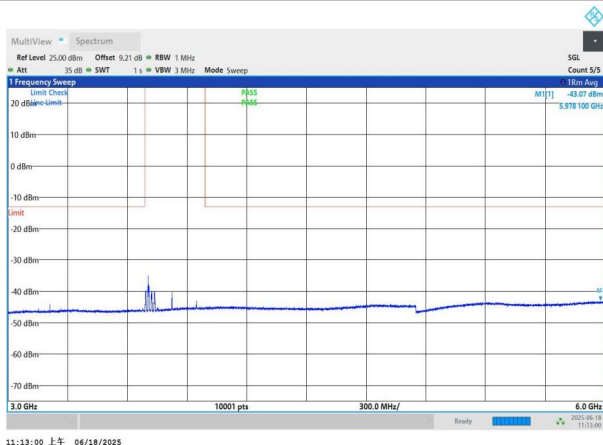
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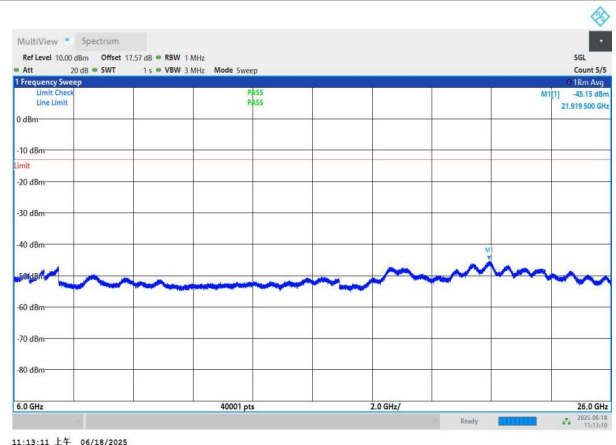
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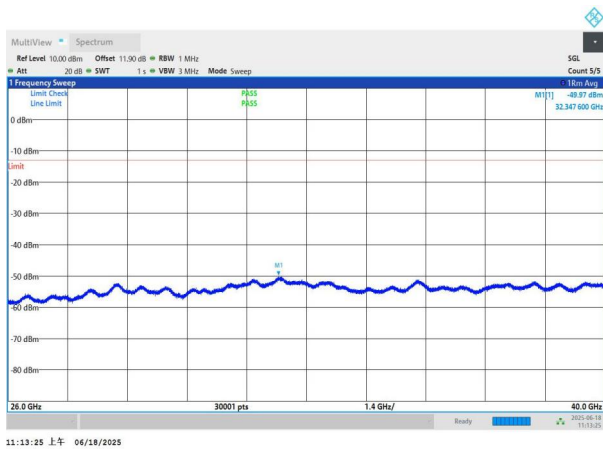
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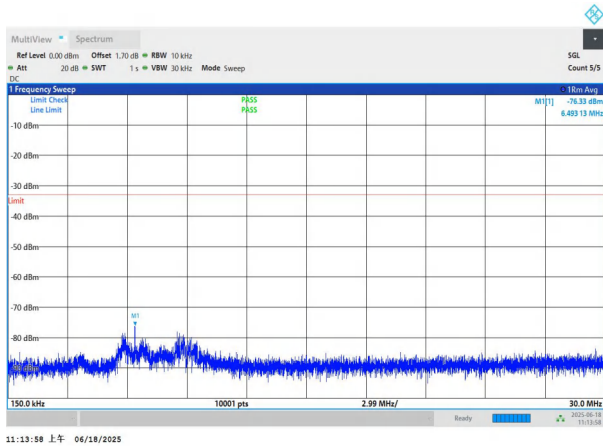
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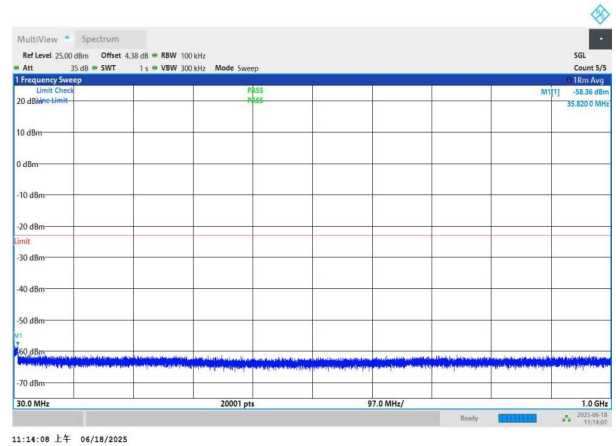
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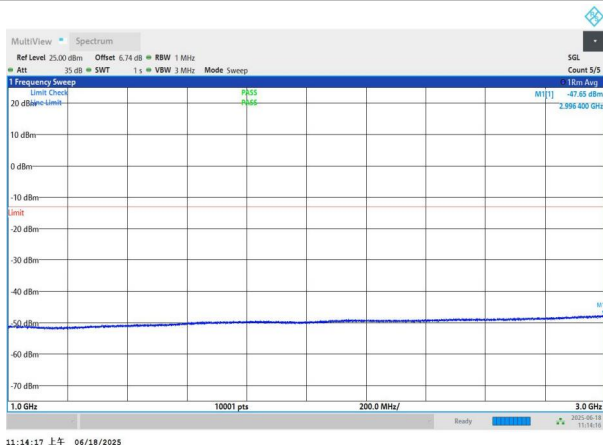
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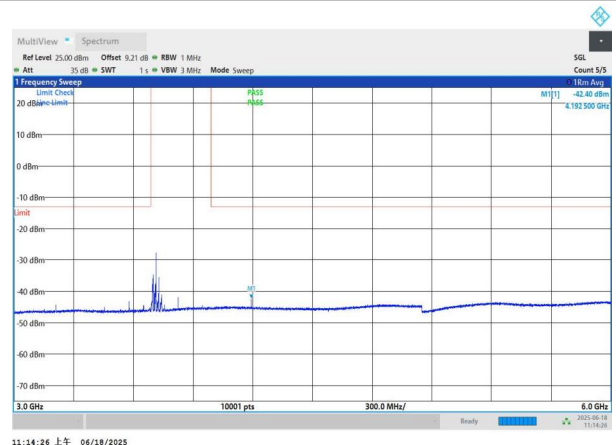
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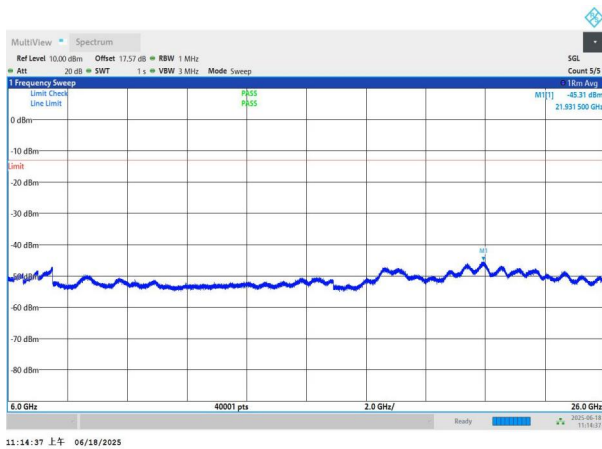
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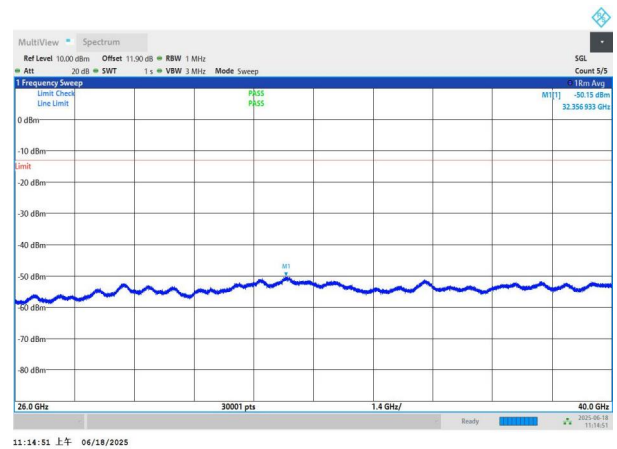
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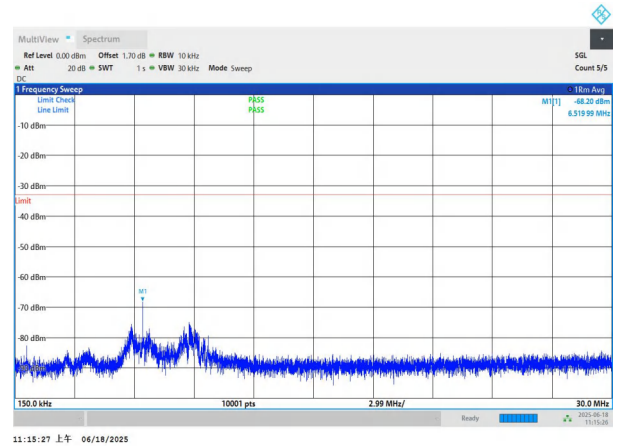
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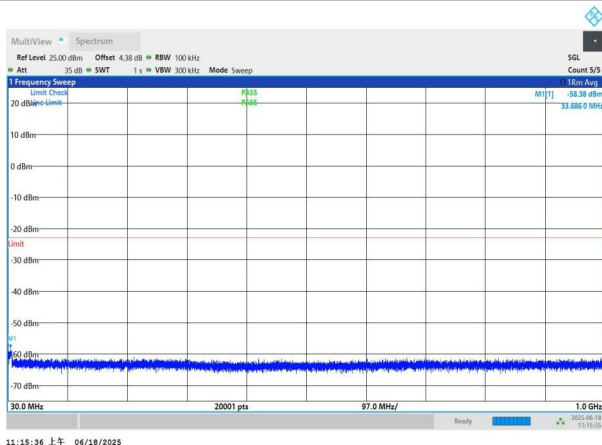
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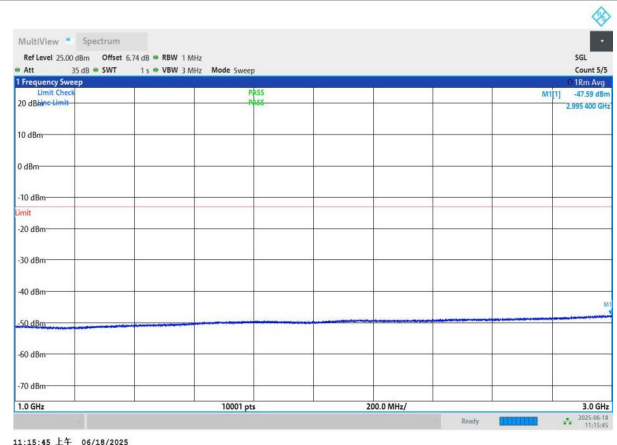
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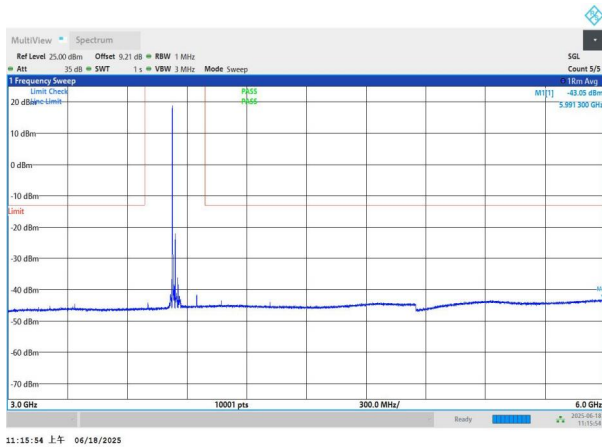
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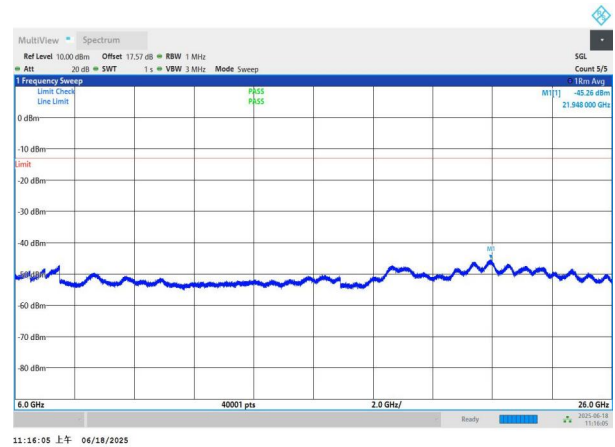
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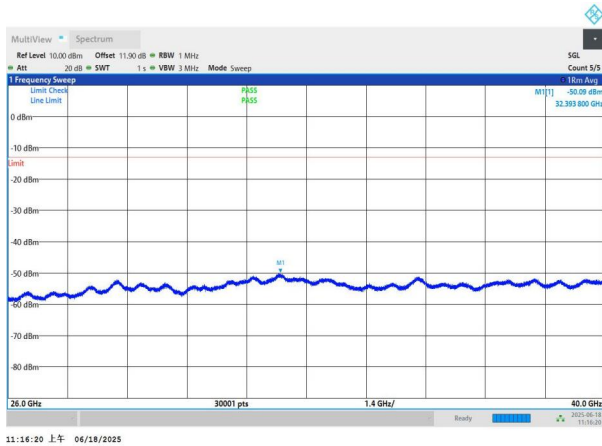
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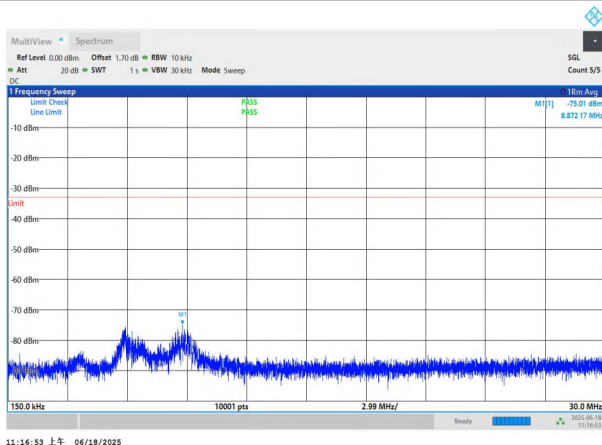
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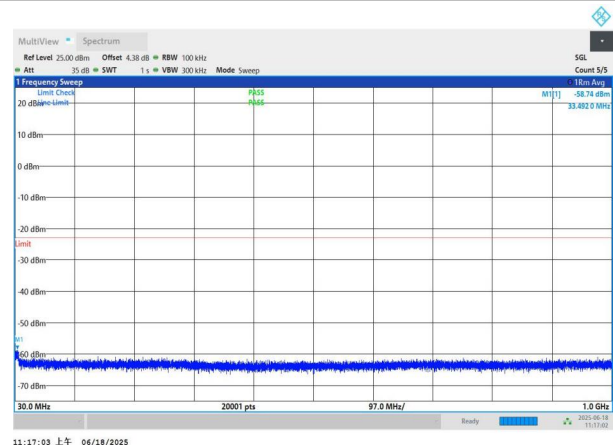
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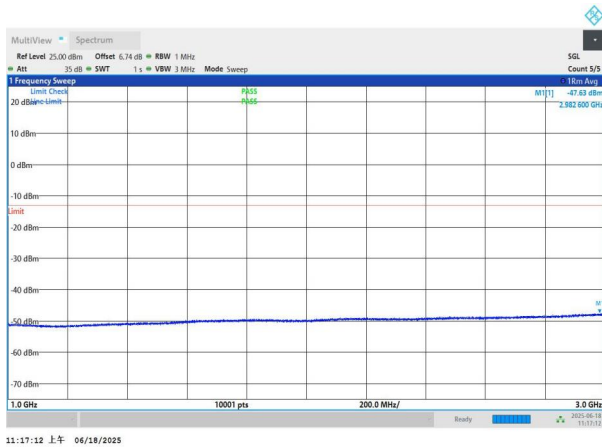
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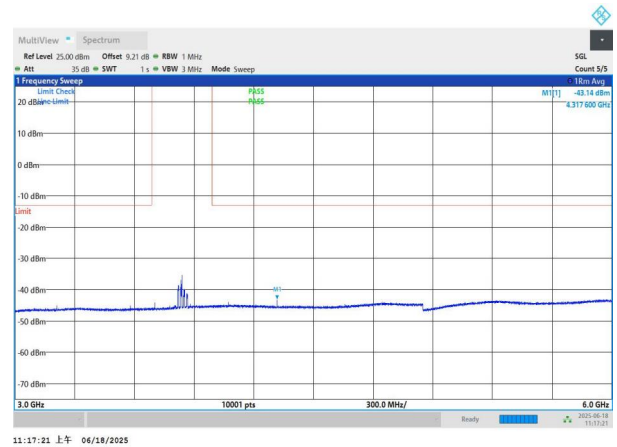
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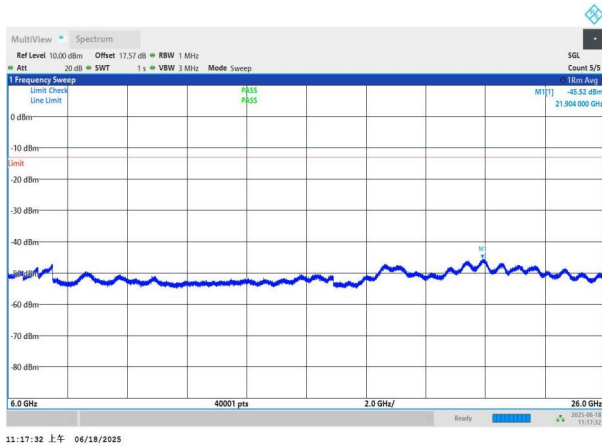
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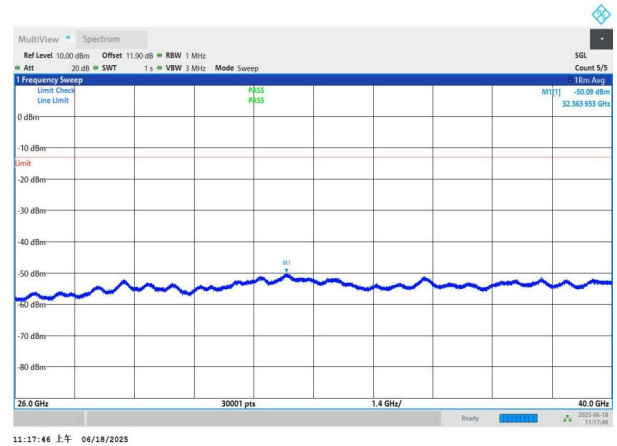
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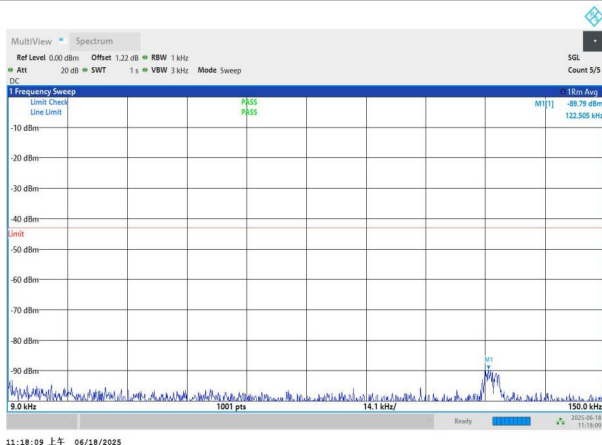
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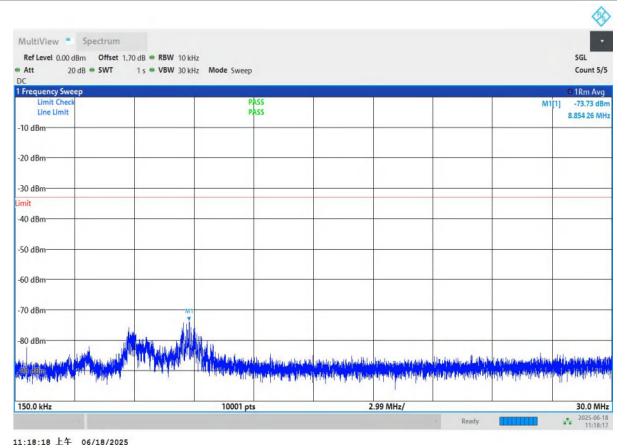
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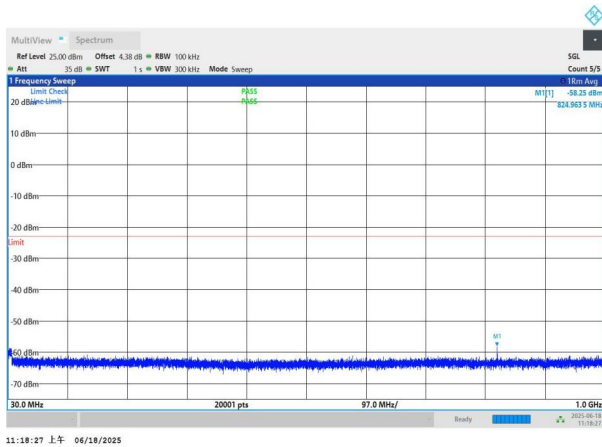
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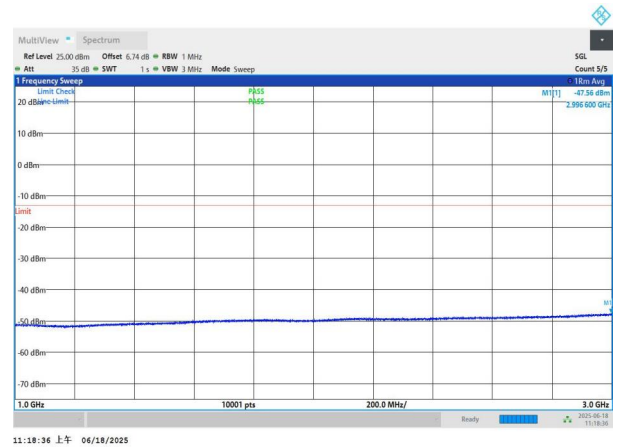
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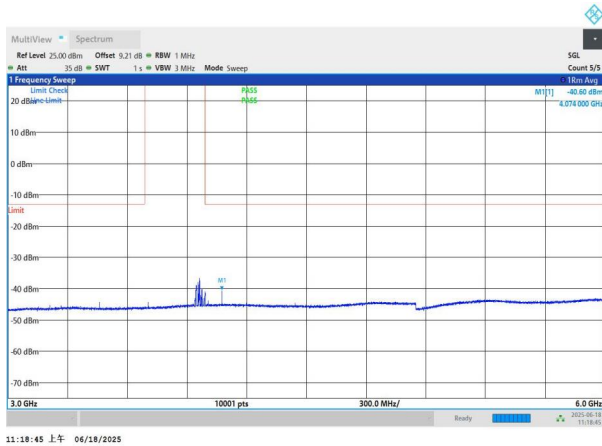
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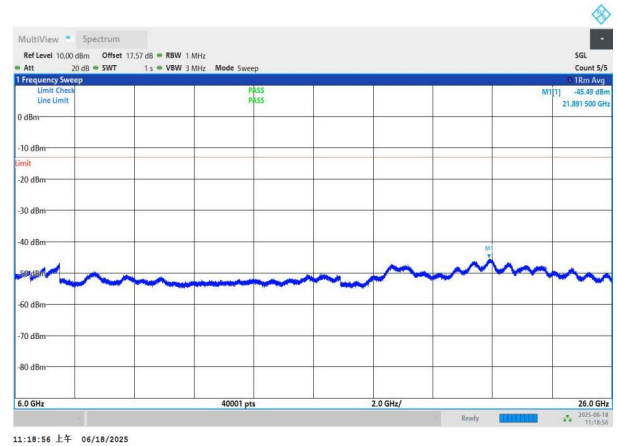
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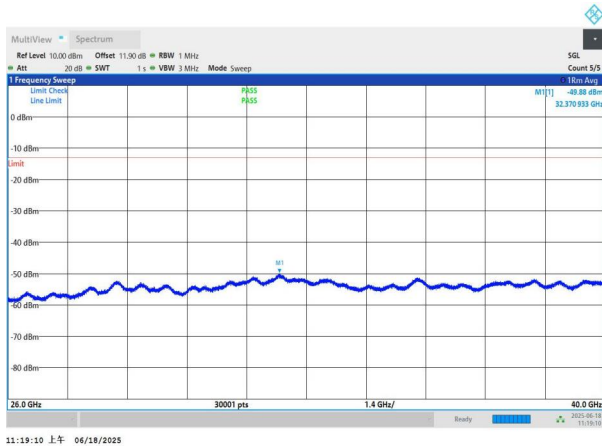
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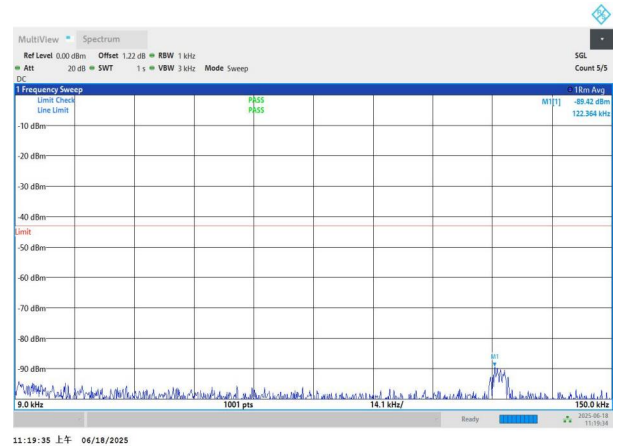
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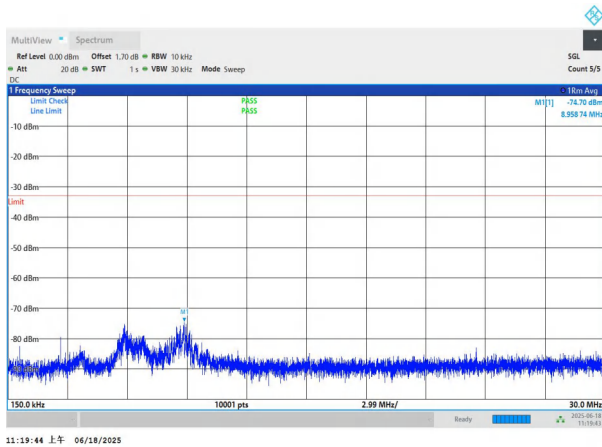
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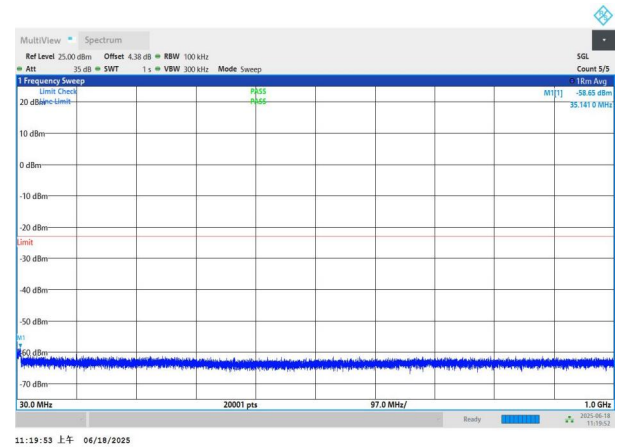
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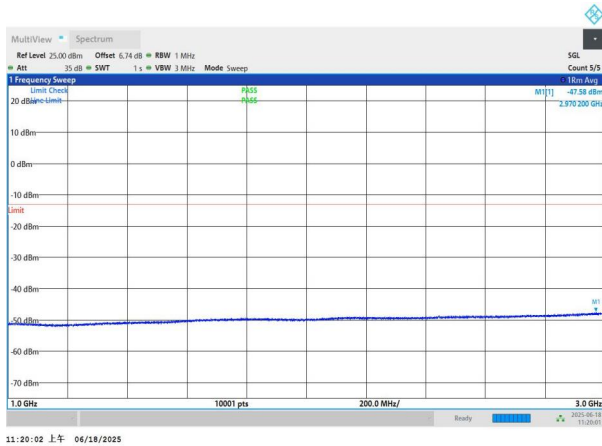
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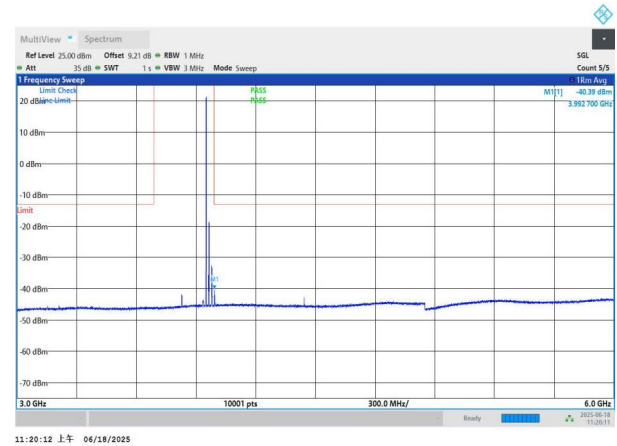
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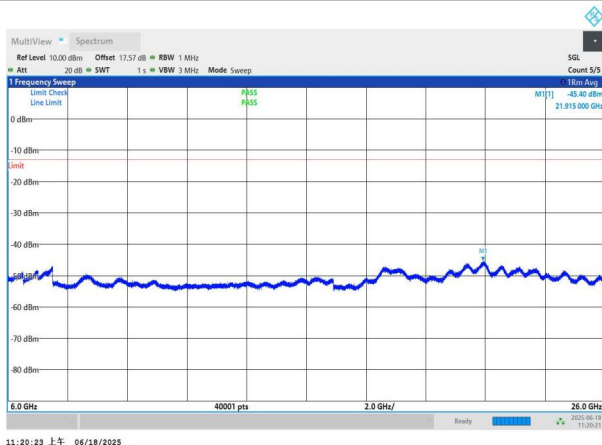
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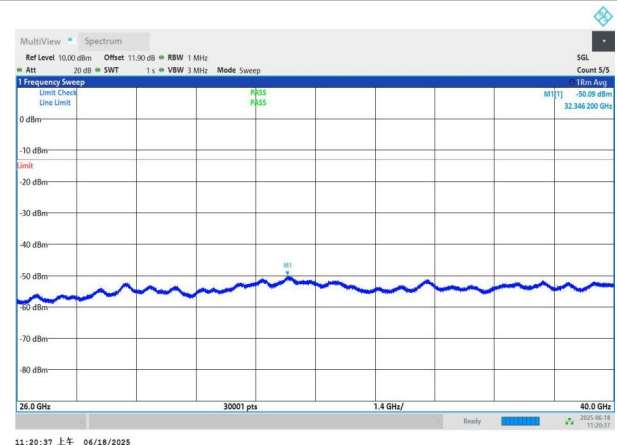
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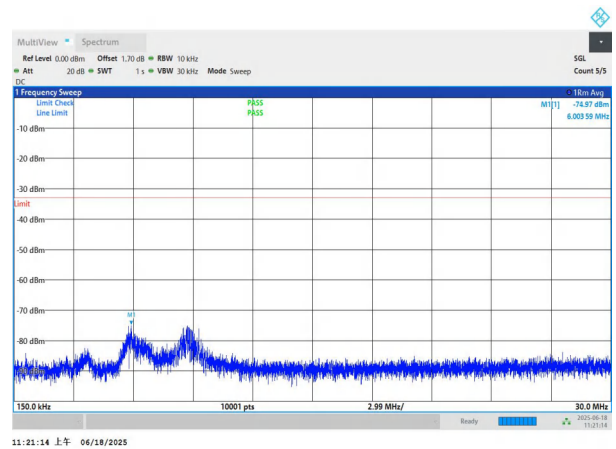
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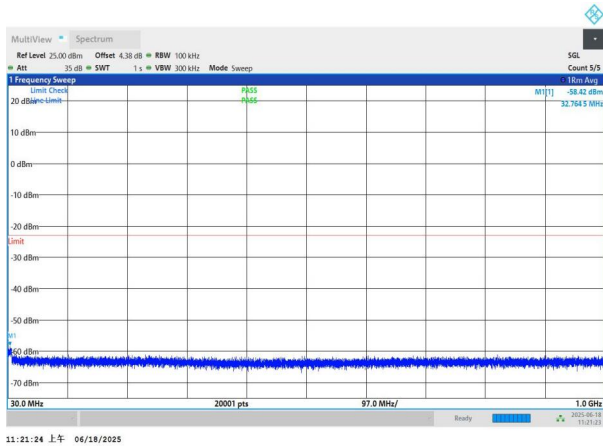
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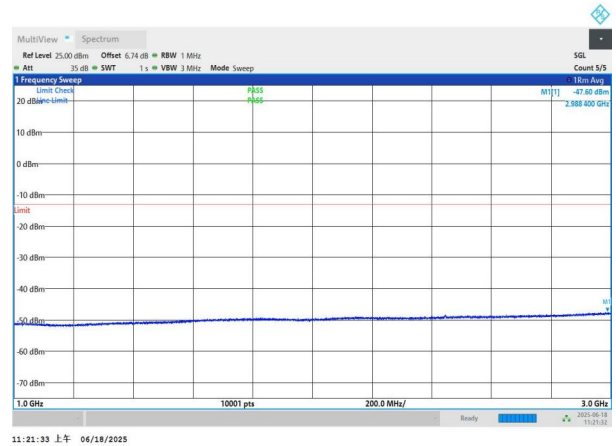
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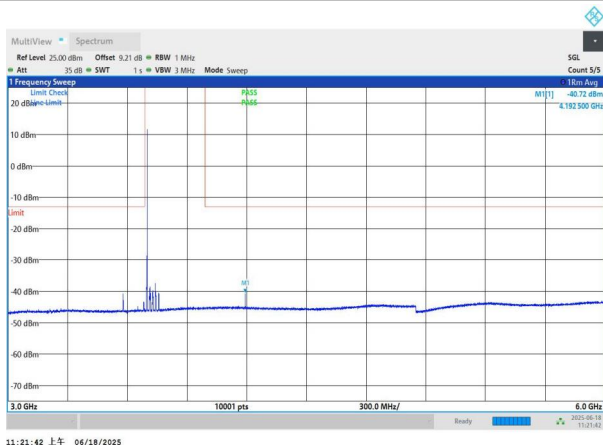
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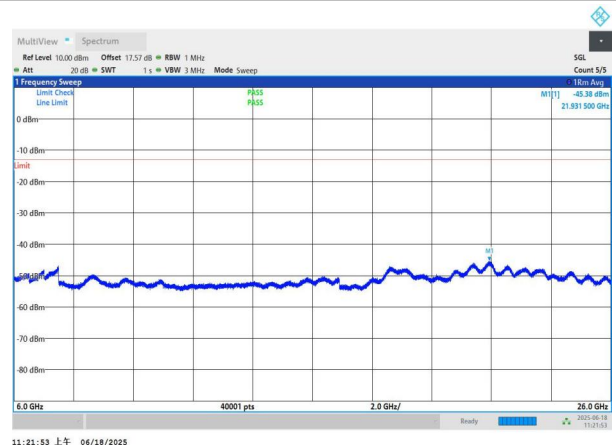
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1000-3000-PASS



NTNV-n77(3700-3980)-PC3-30-40-L-DFT-BPSK-Edge_1RB_Left-
3000-6000-PASS



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