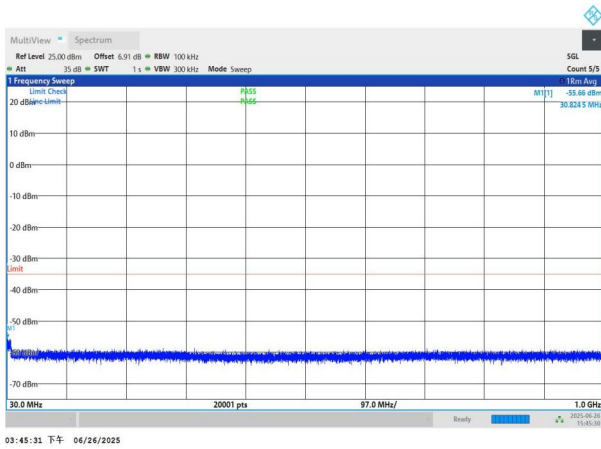
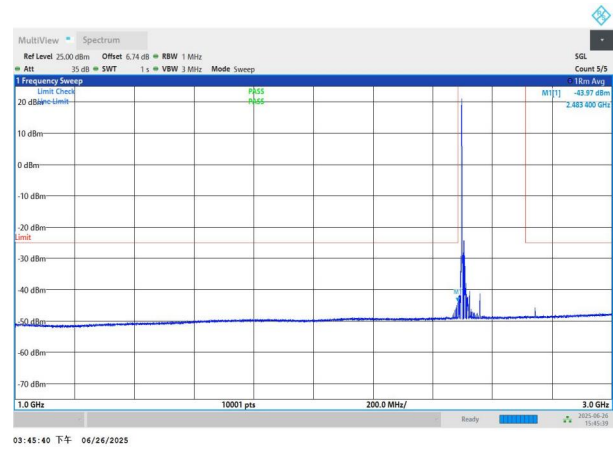




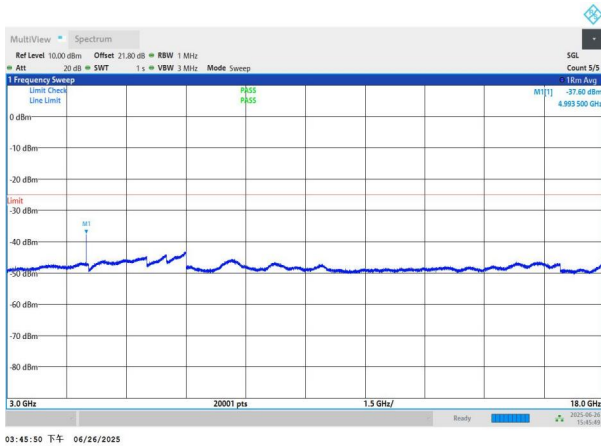
NTNV-n41-PC3-30-15-L-DFT-BPSK-Edge_1RB_Left-30-1000-PA

SS



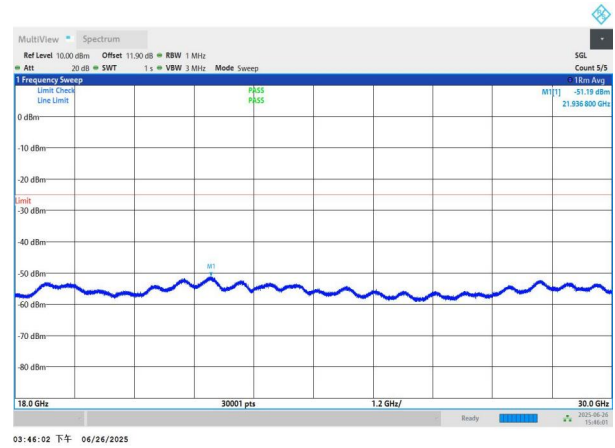
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ASS



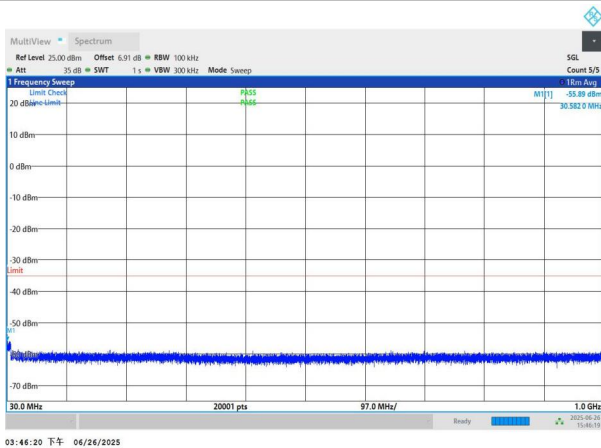
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-PASS



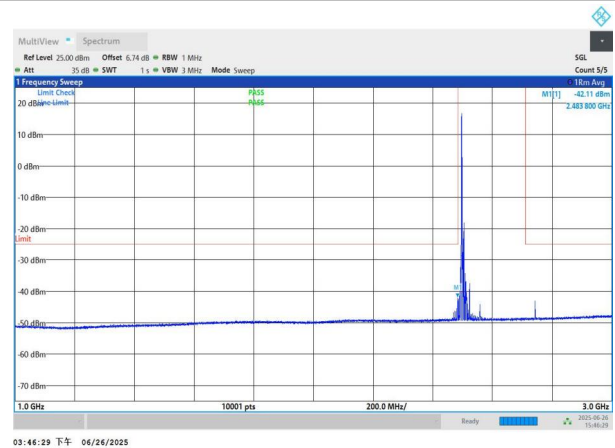
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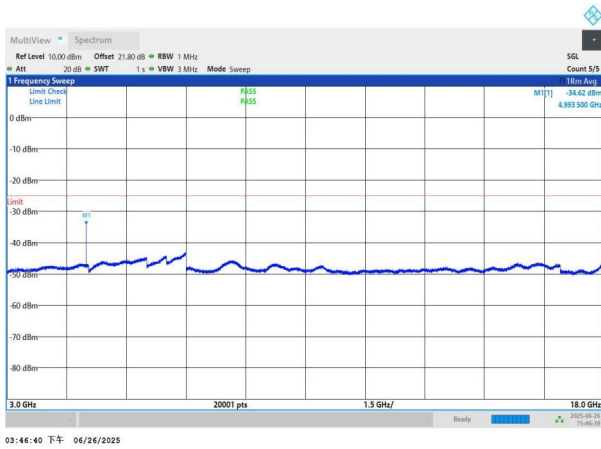
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SS

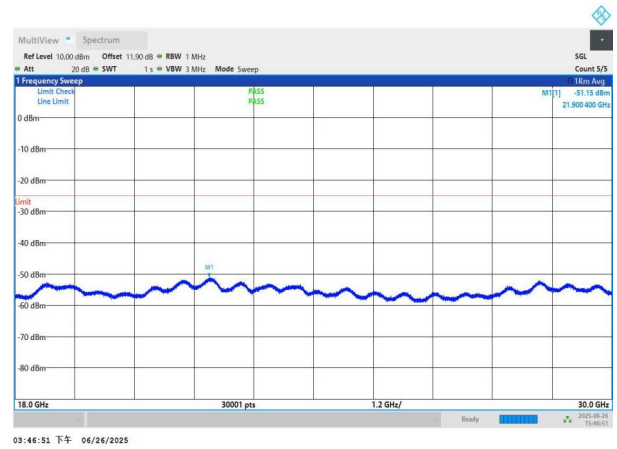


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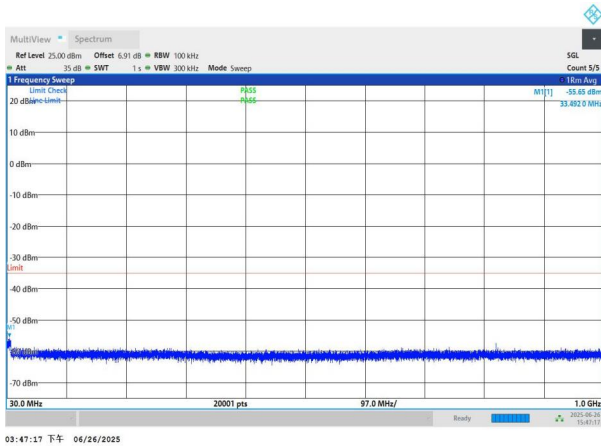
ASS



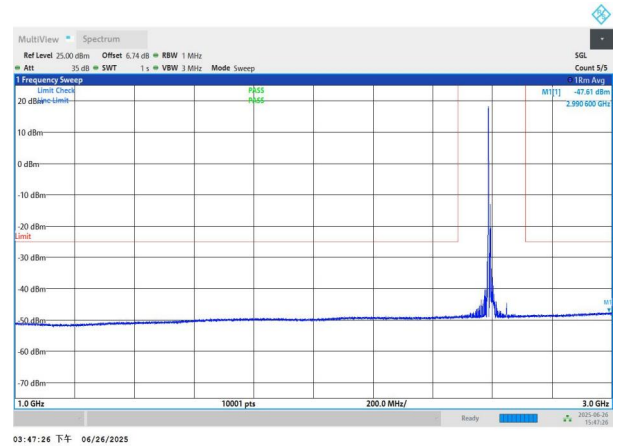
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0-PASS



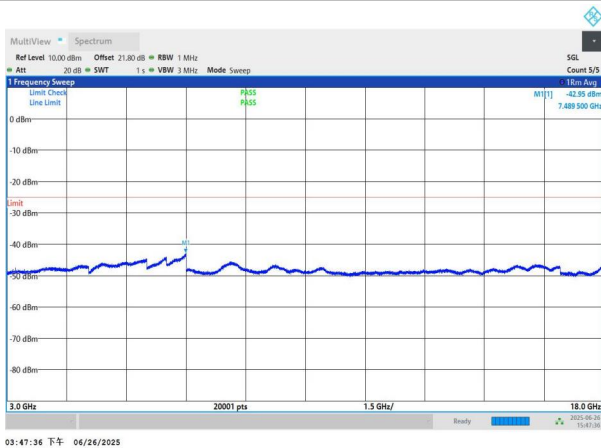
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0-PASS



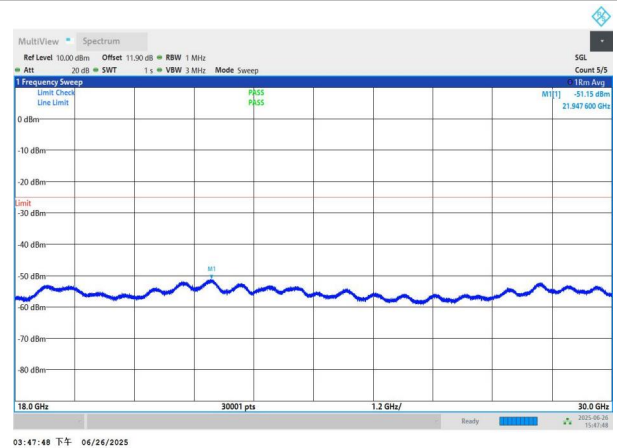
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ASS



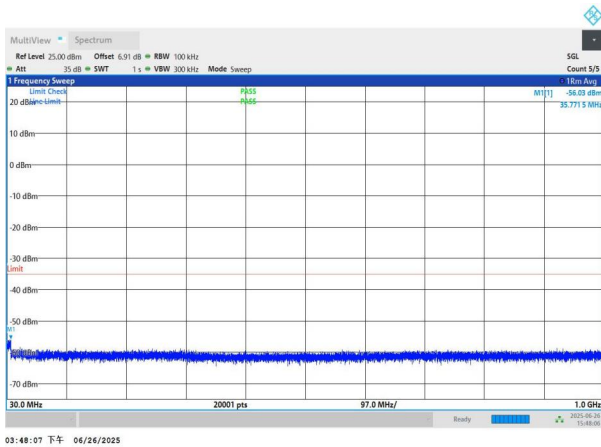
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PASS



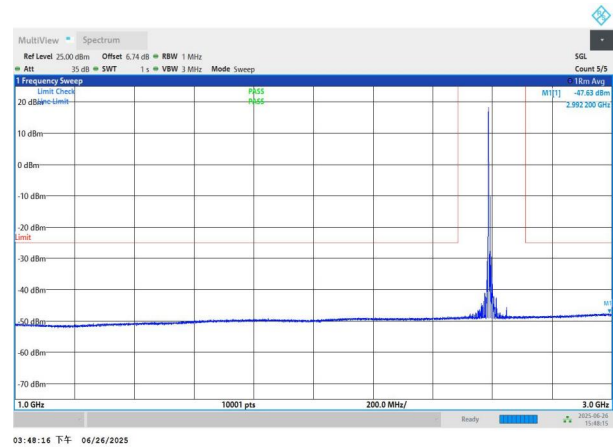
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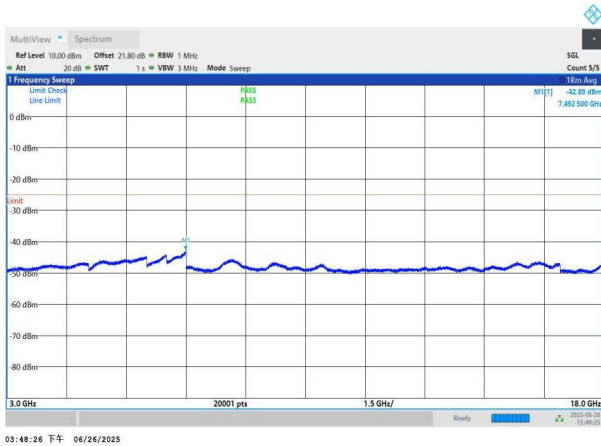
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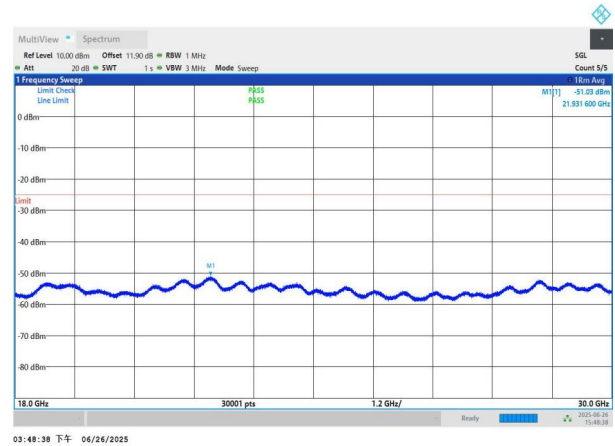
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ASS



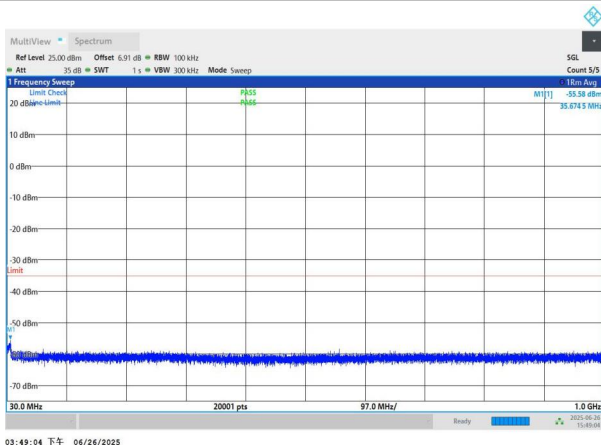
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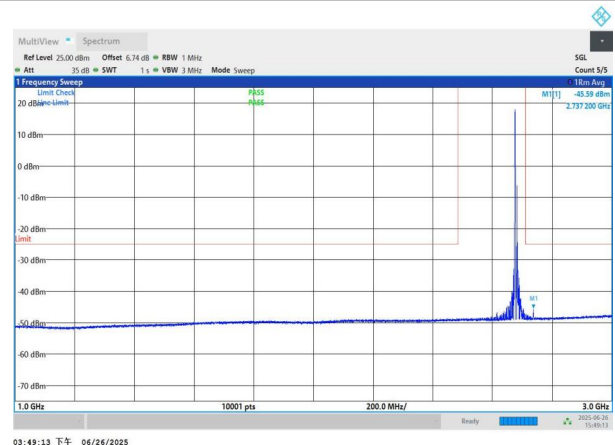
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0-PASS



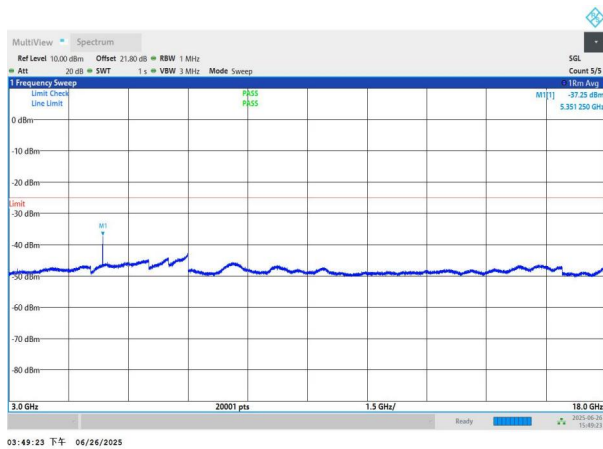
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0-PASS



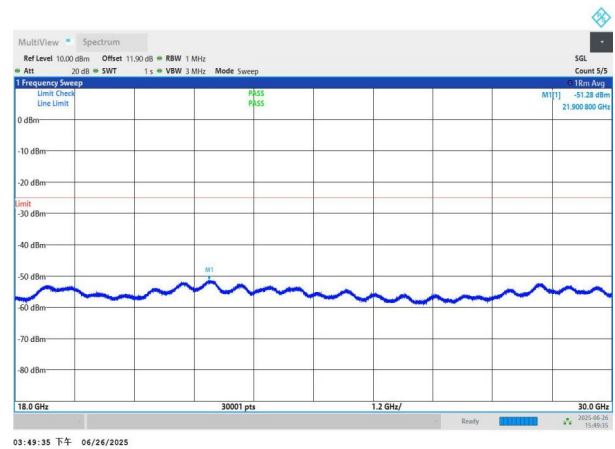
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ASS



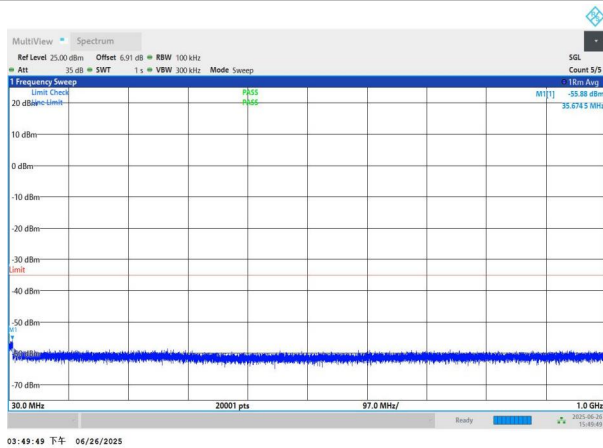
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ASS



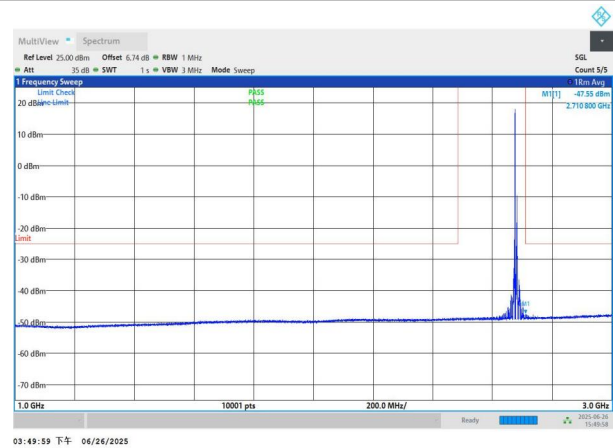
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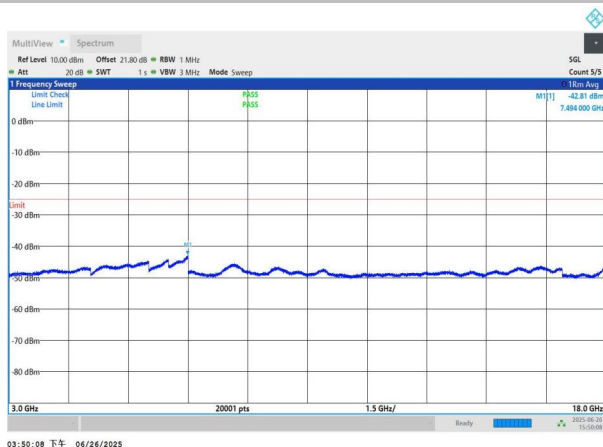
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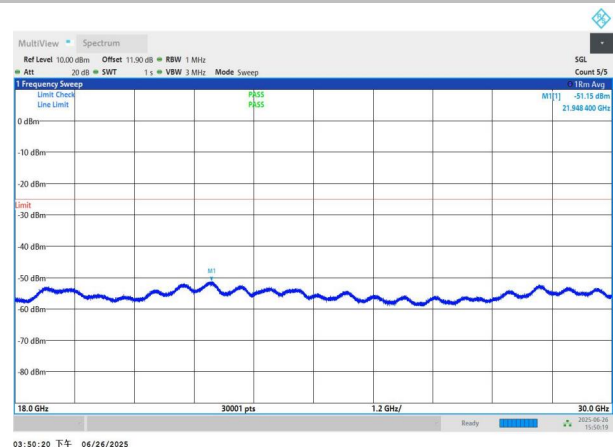
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ASS



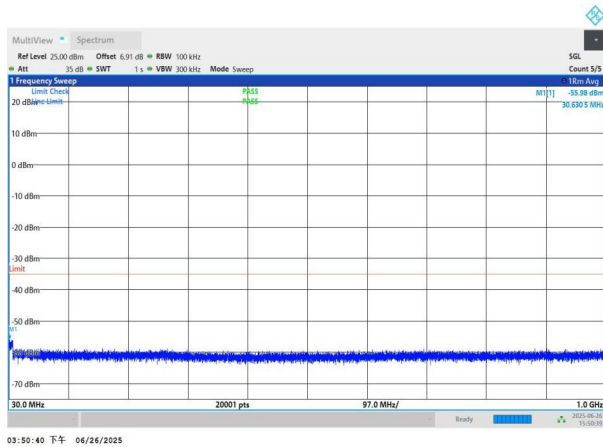
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PASS



NTNV-n41-PC3-30-15-H-DFT-QPSK-Edge_1RB_Left-3000-1800
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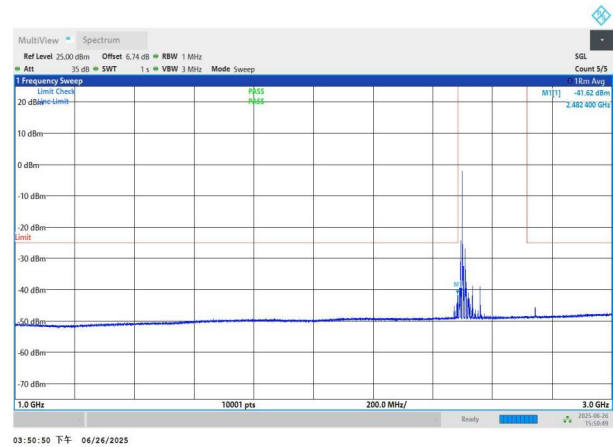


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0-PASS



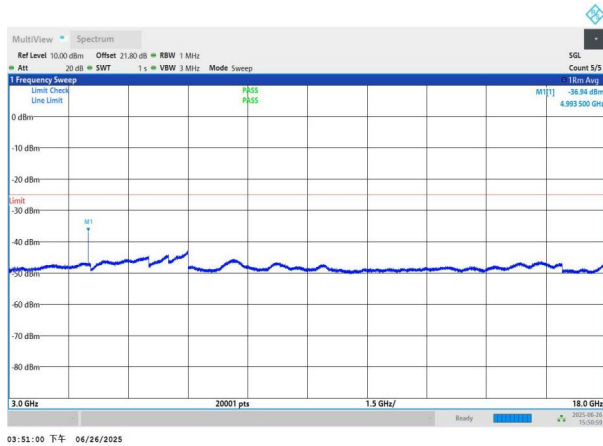
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SS



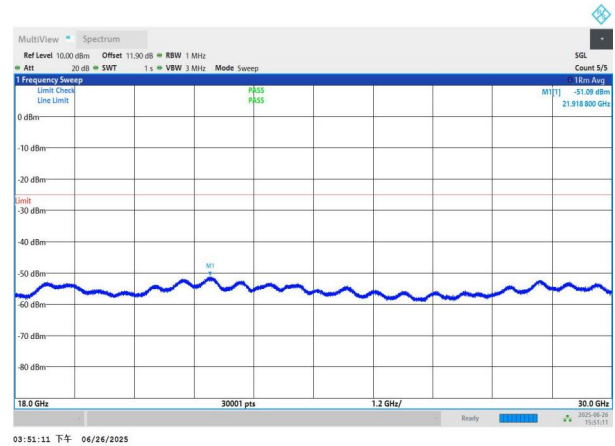
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ASS



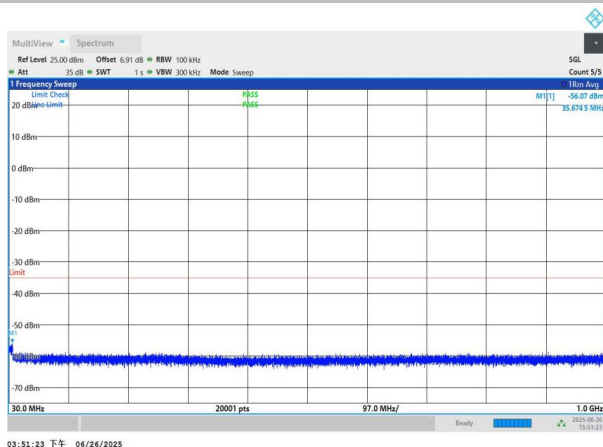
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-PASS



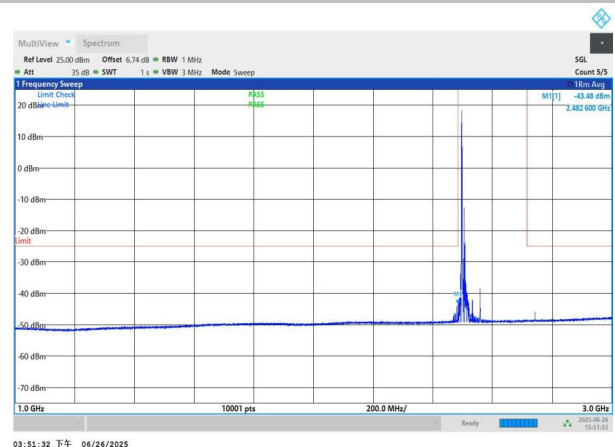
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0-PASS



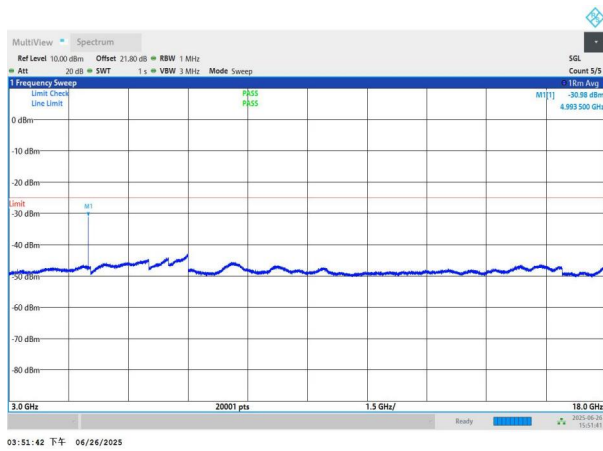
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SS

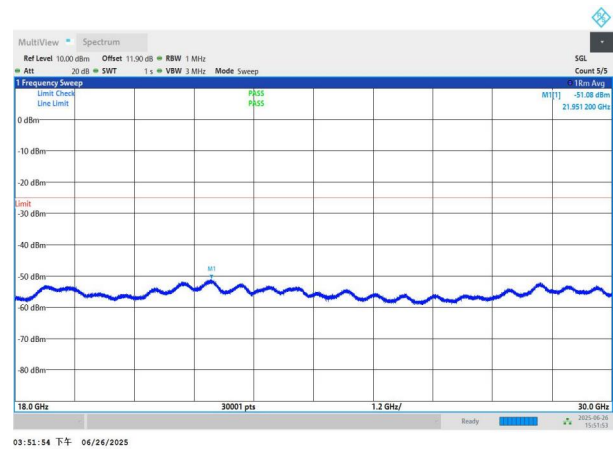


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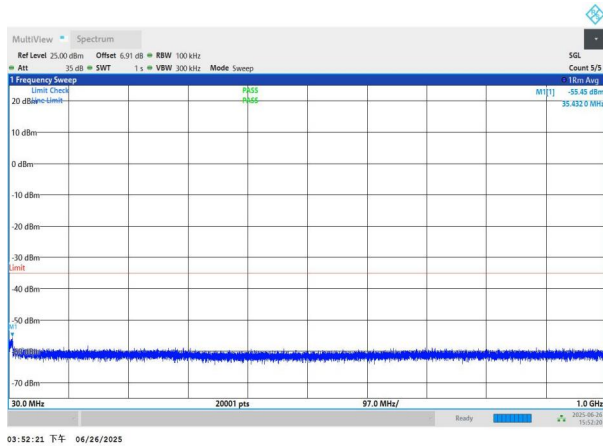
ASS



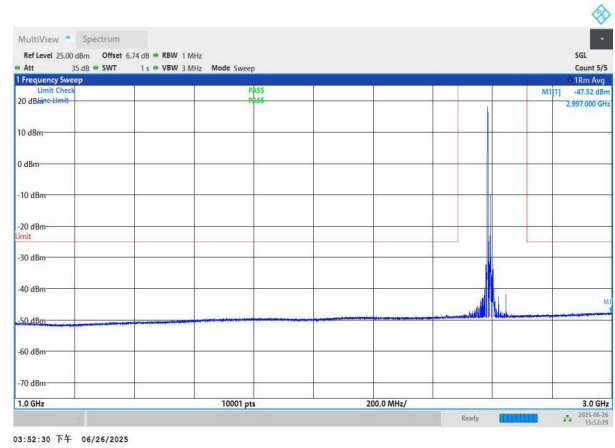
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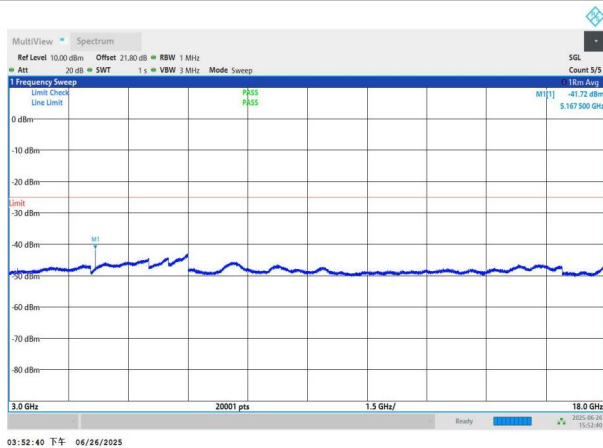
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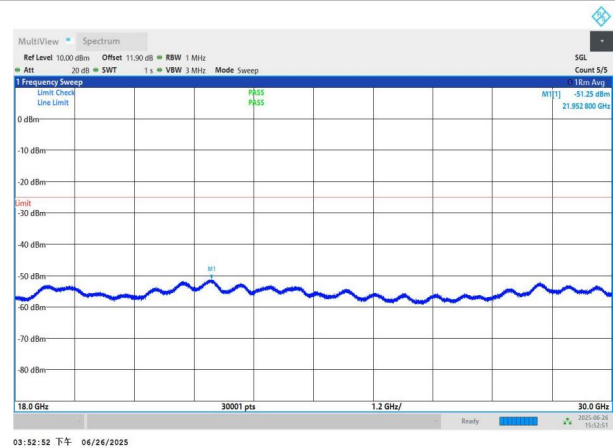
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ASS



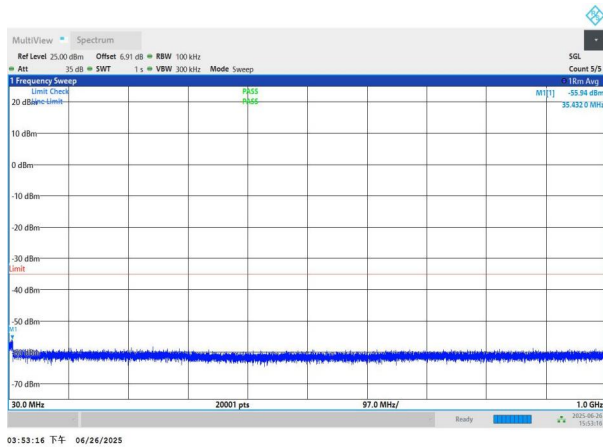
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PASS



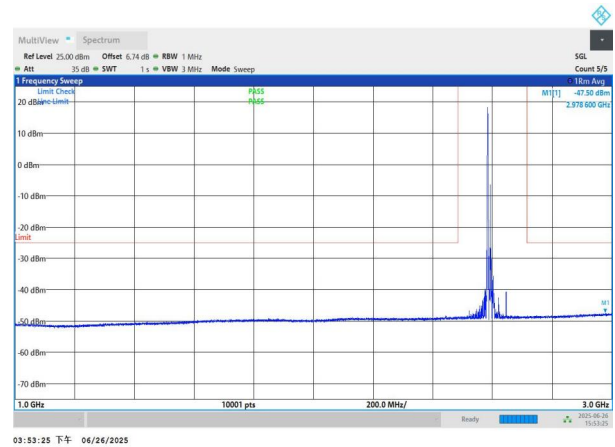
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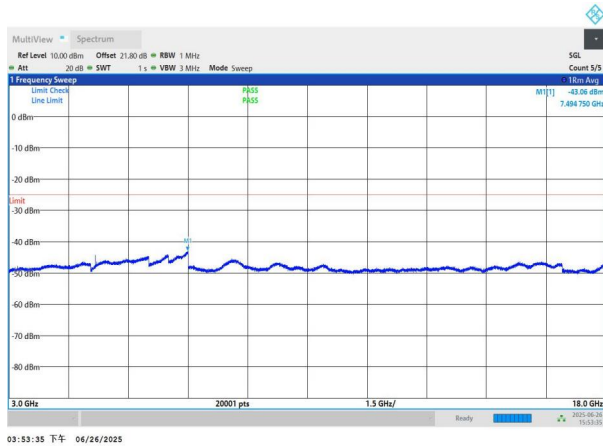
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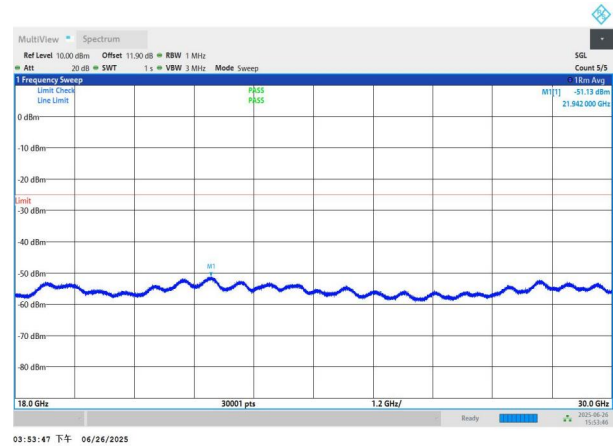
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ASS



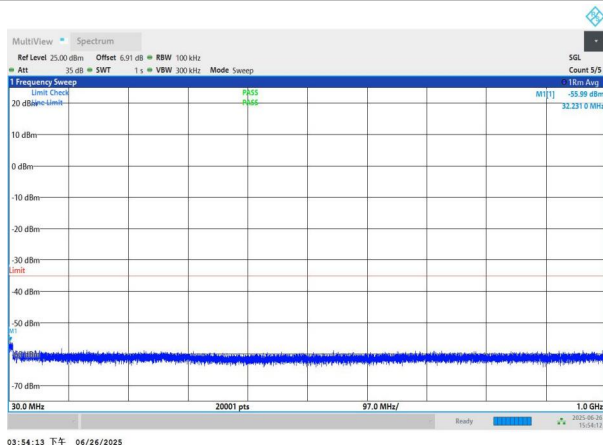
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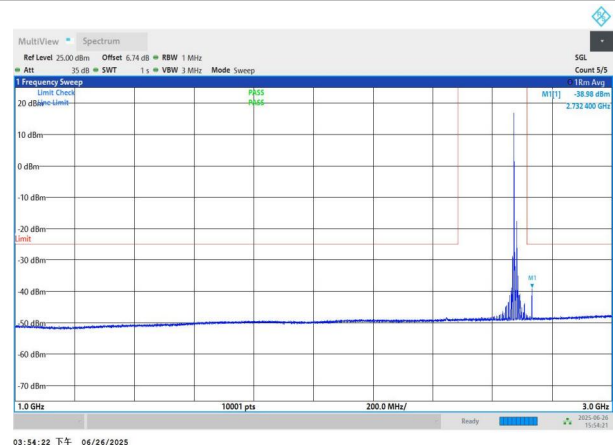
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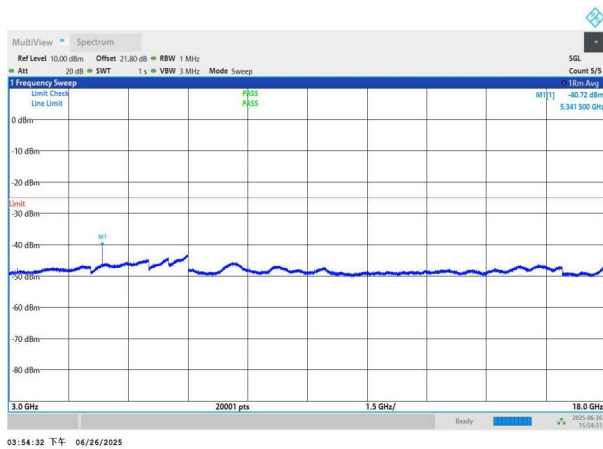
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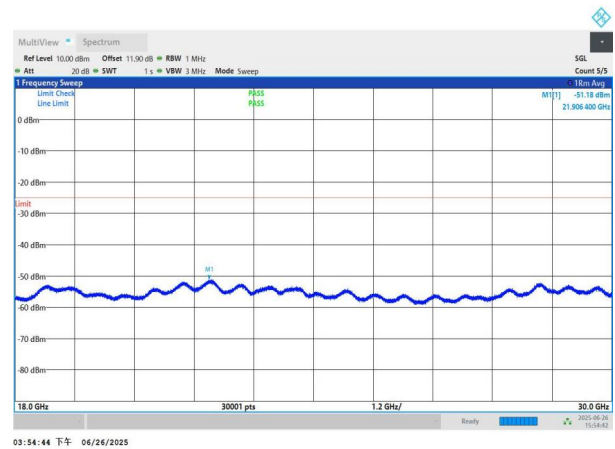
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ASS



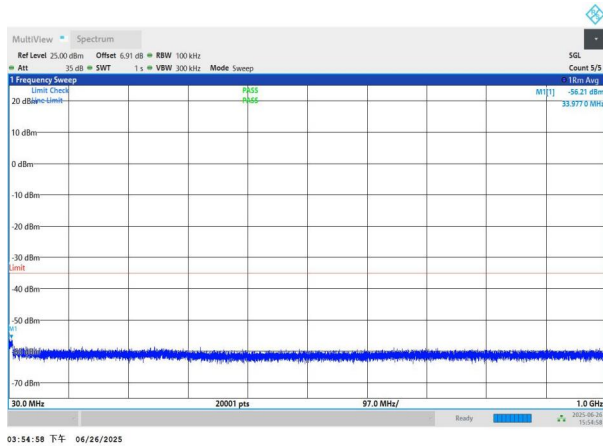
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ASS



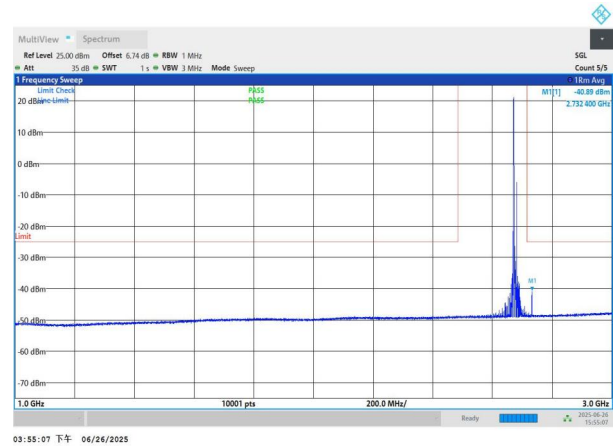
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0-PASS



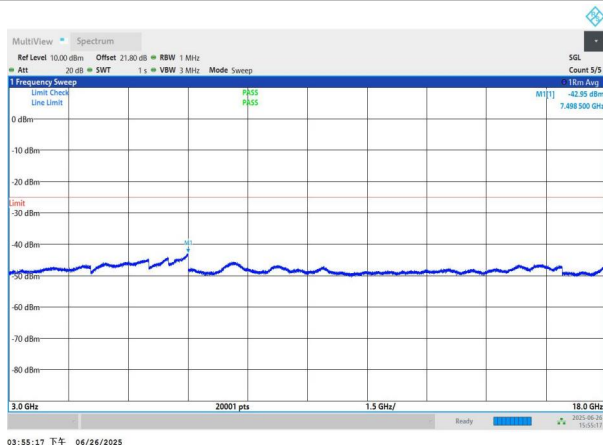
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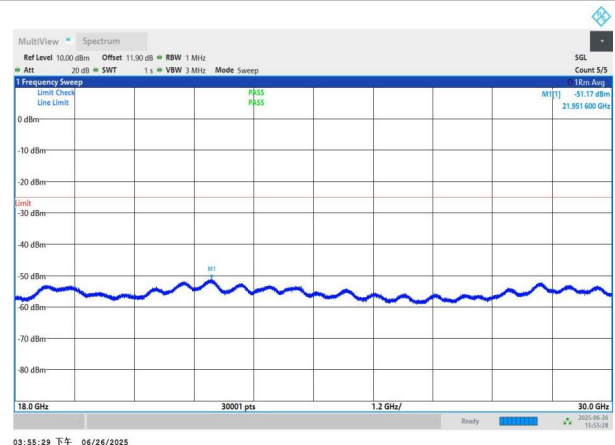
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ASS



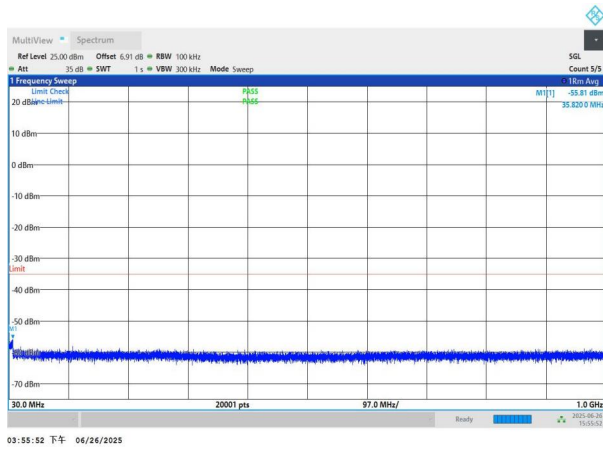
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PASS



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0-PASS

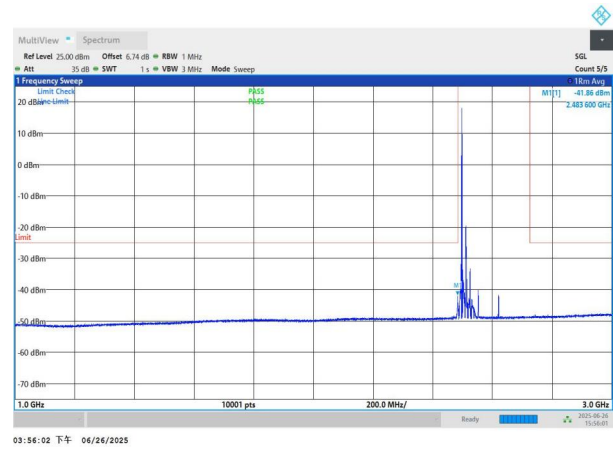


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0-PASS



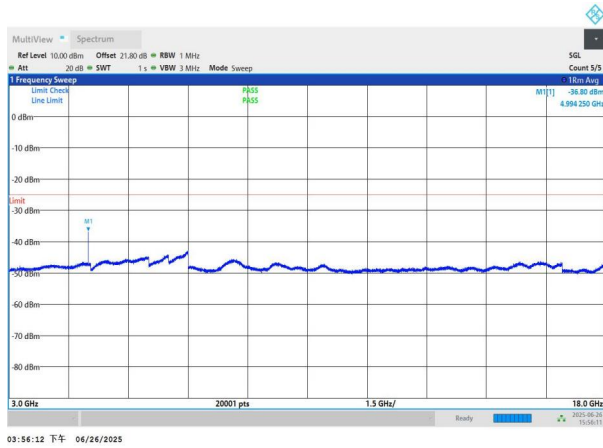
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SS



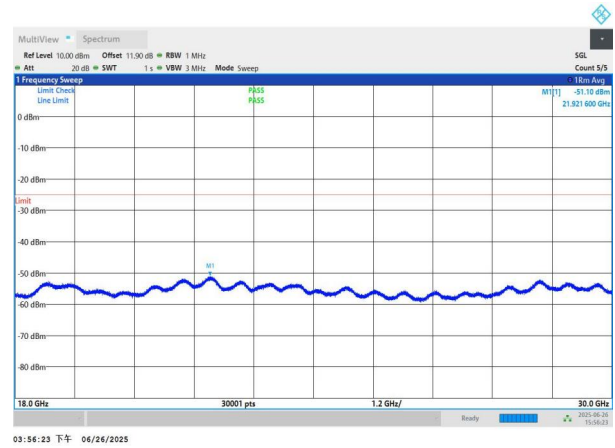
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ASS



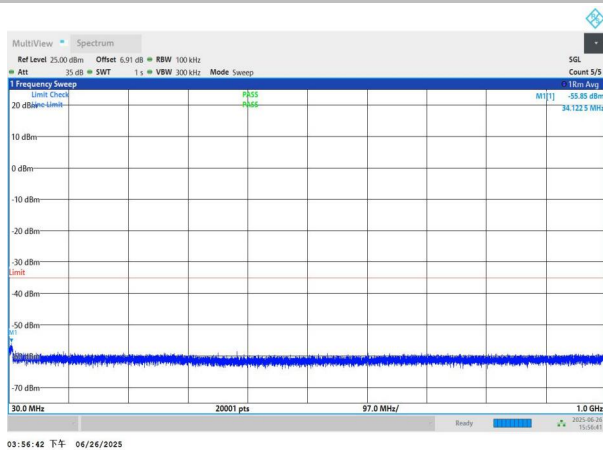
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-PASS



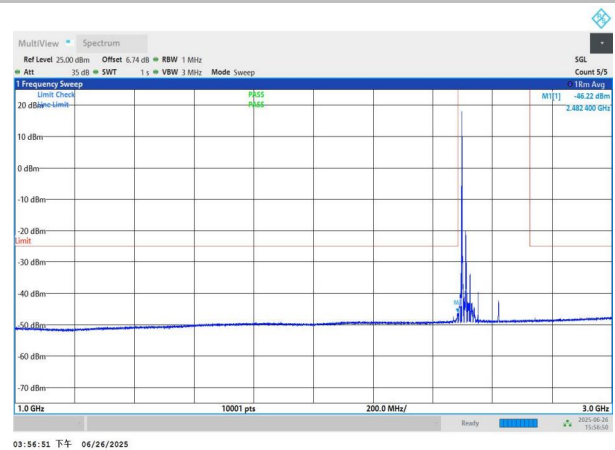
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0-PASS



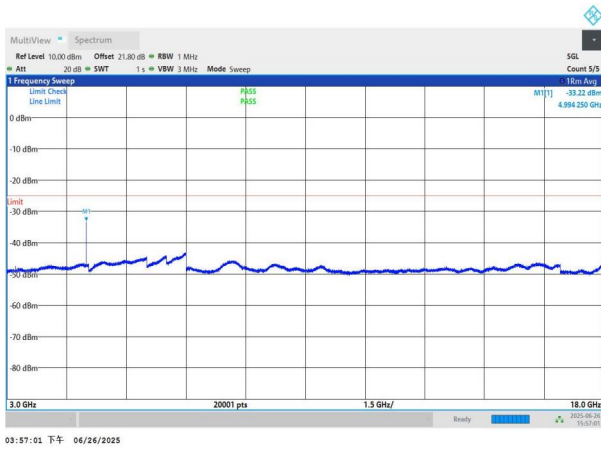
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SS

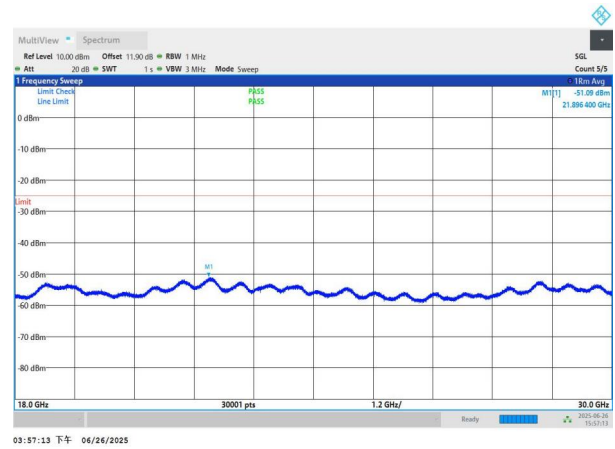


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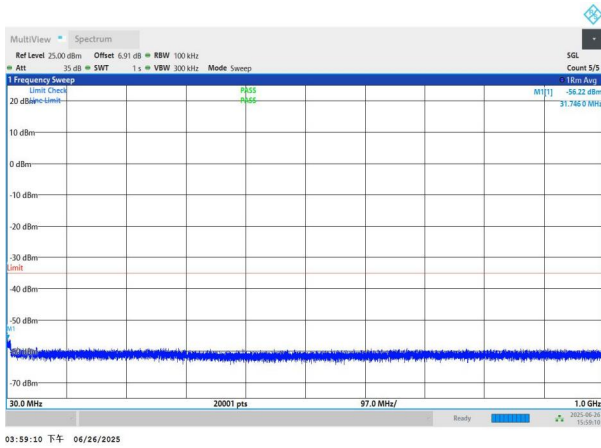
ASS



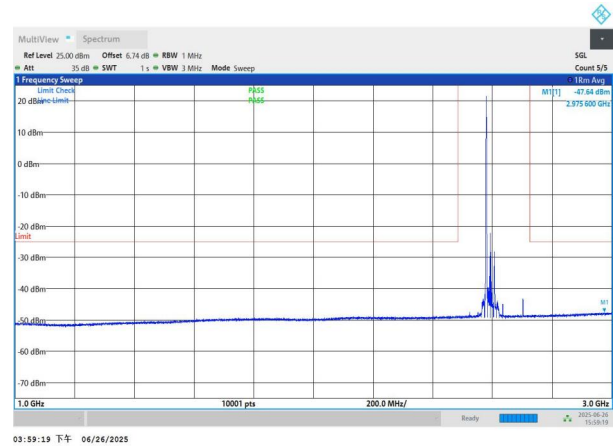
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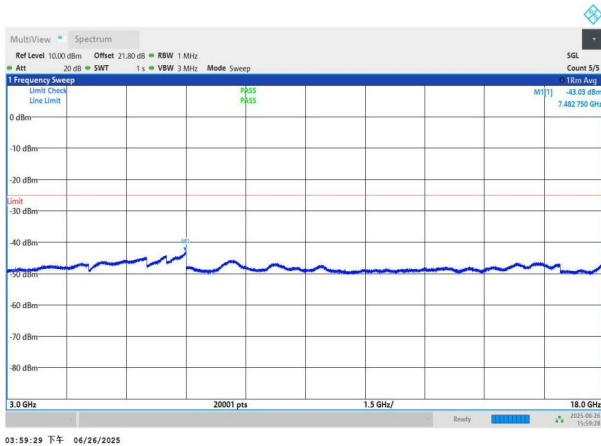
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0-PASS



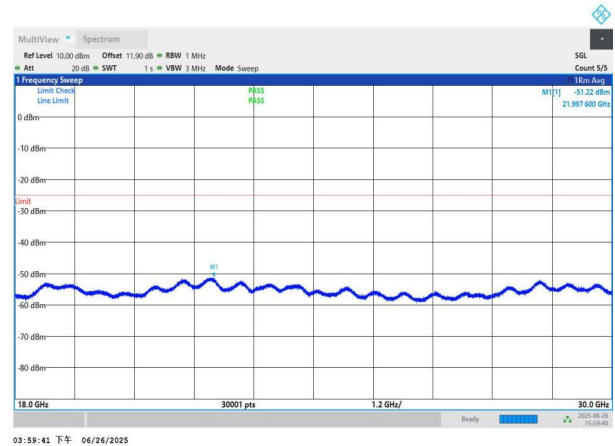
NTNV-n41-PC3-30-30-M-DFT-BPSK-Edge_1RB_Left-30-1000-P
ASS



NTNV-n41-PC3-30-30-M-DFT-BPSK-Edge_1RB_Left-1000-3000-P
PASS



NTNV-n41-PC3-30-30-M-DFT-BPSK-Edge_1RB_Left-3000-1800
0-PASS



NTNV-n41-PC3-30-30-M-DFT-BPSK-Edge_1RB_Left-18000-3000
0-PASS