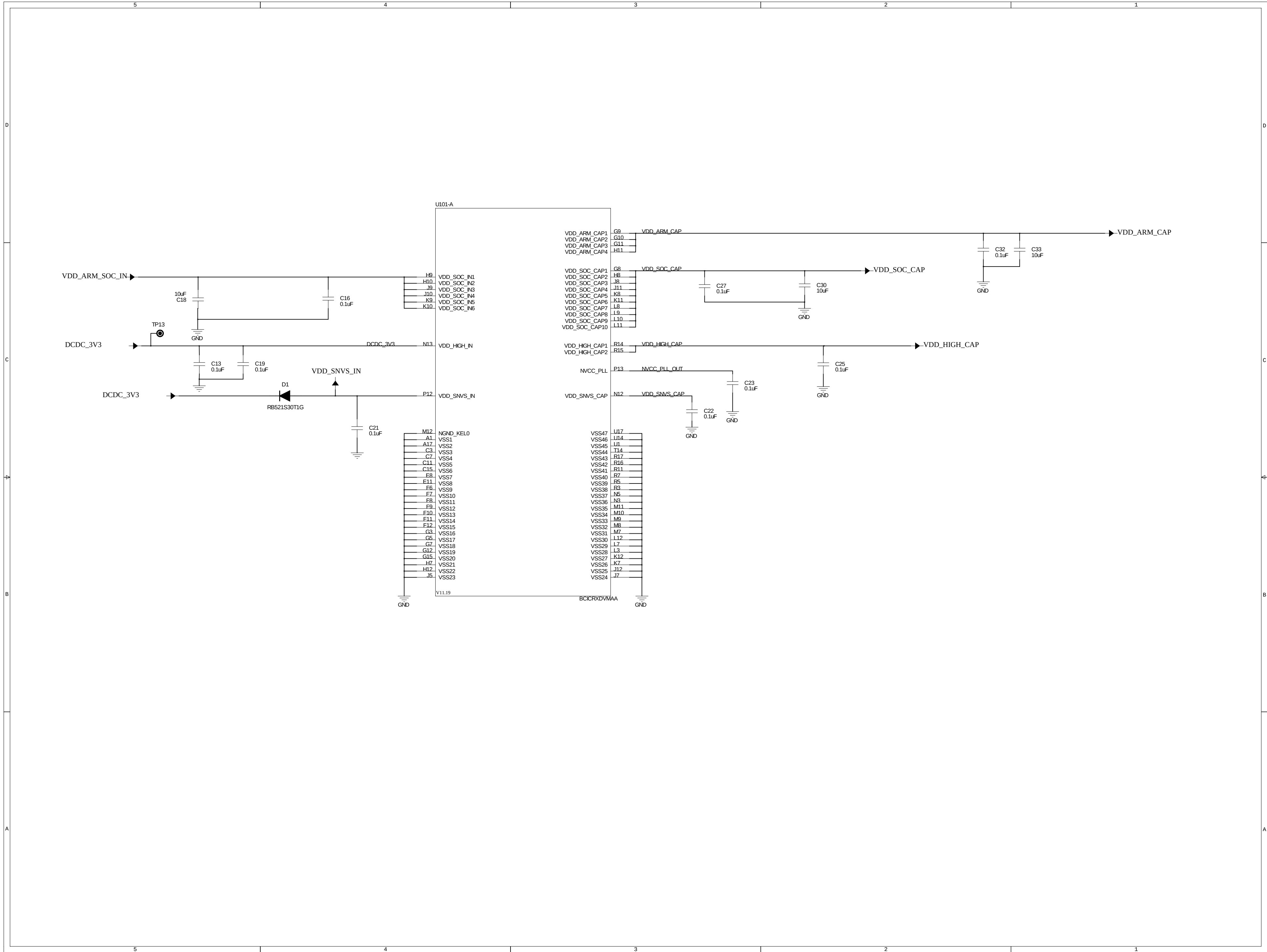
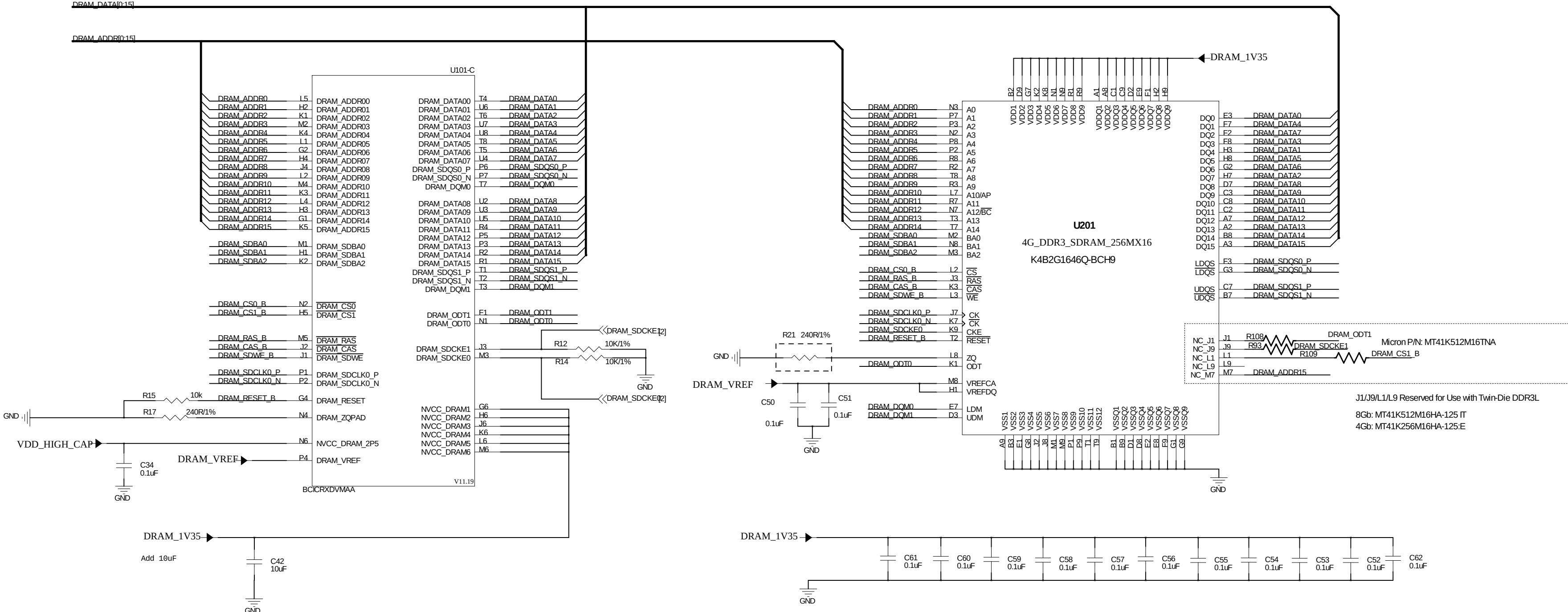
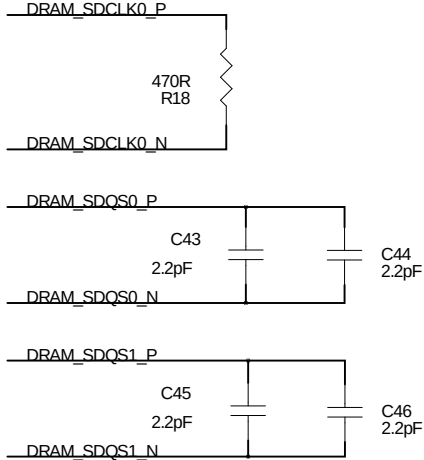




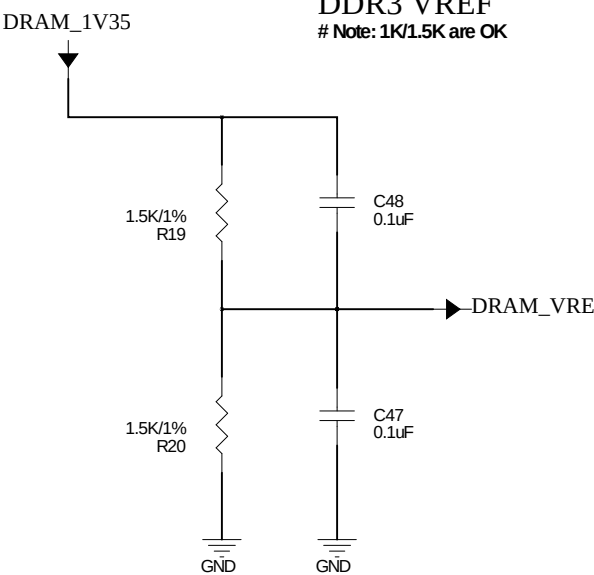
DDR3/LvDDR3



CLK termination: Place R54 close to U2.



DDR3 VREF
Note: 1K/1.5K are OK



MX6UL PERI

