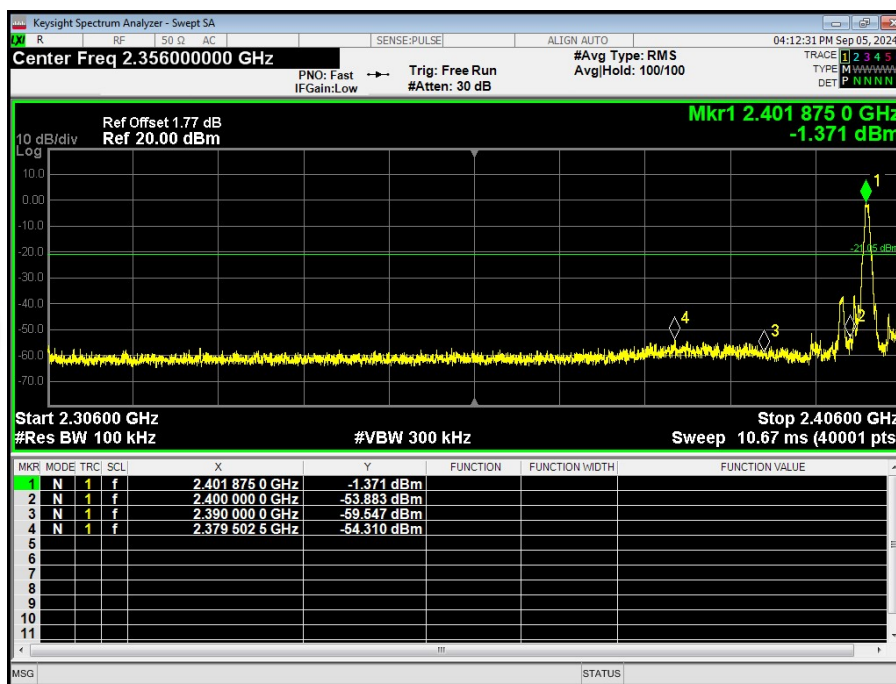
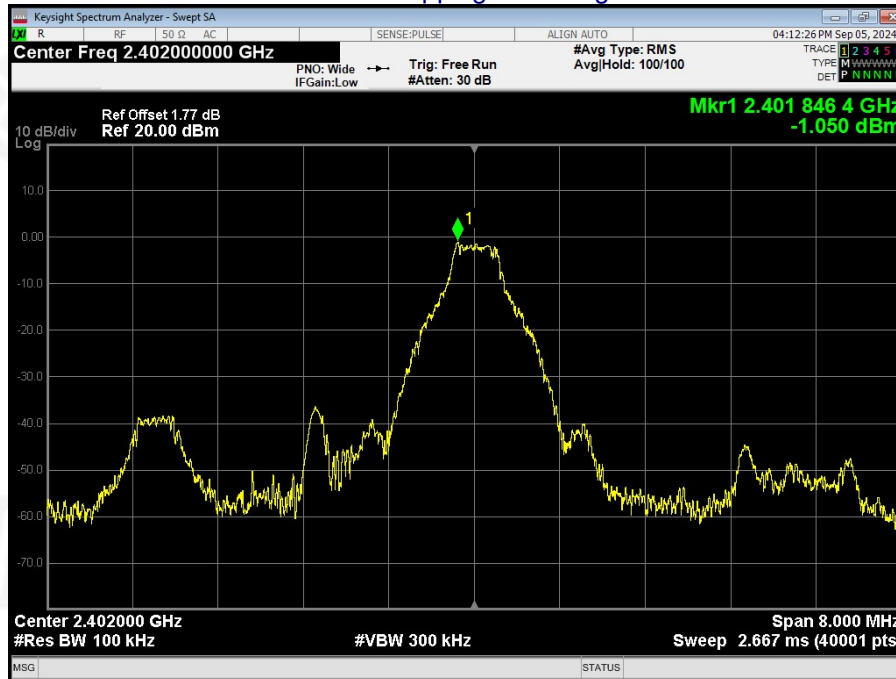


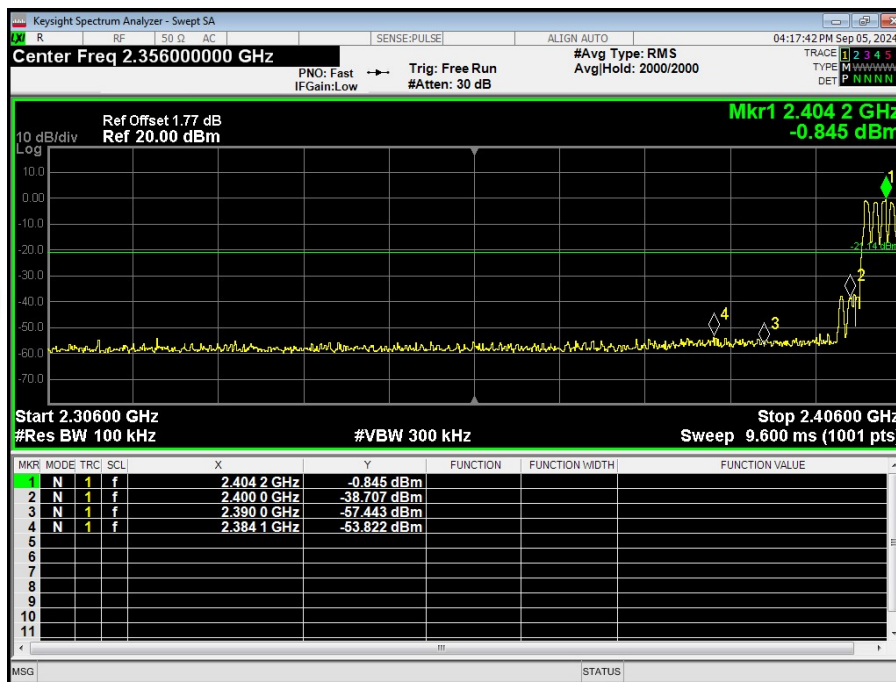


GFSK No-hopping Band edge-left side



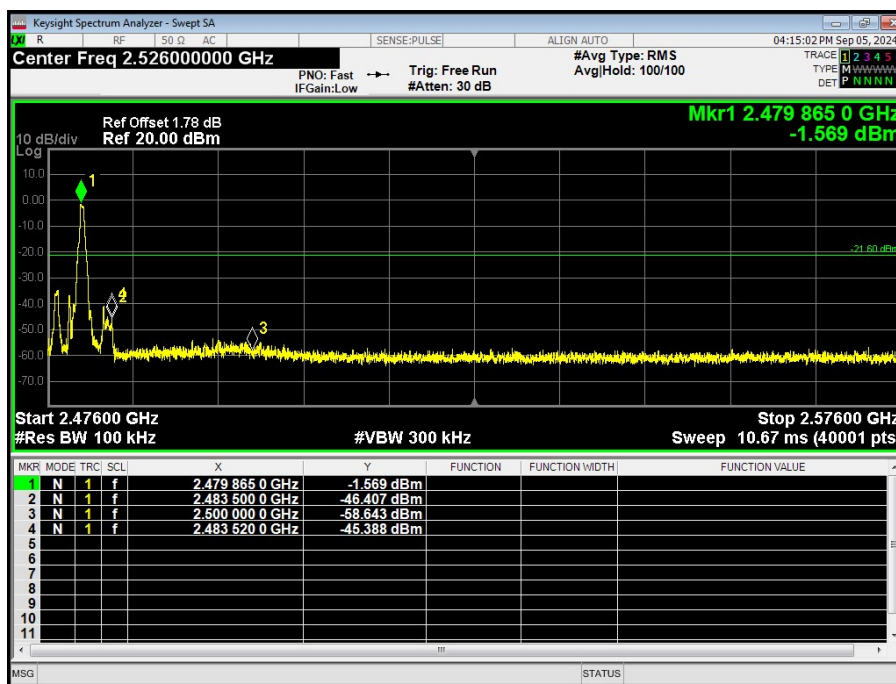
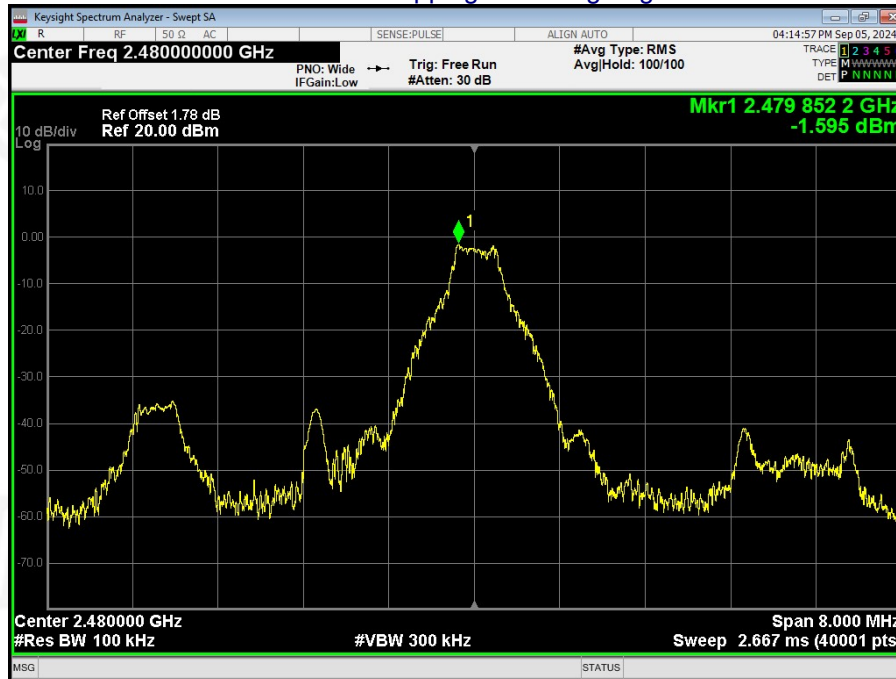


GFSK Hopping Band edge-left side



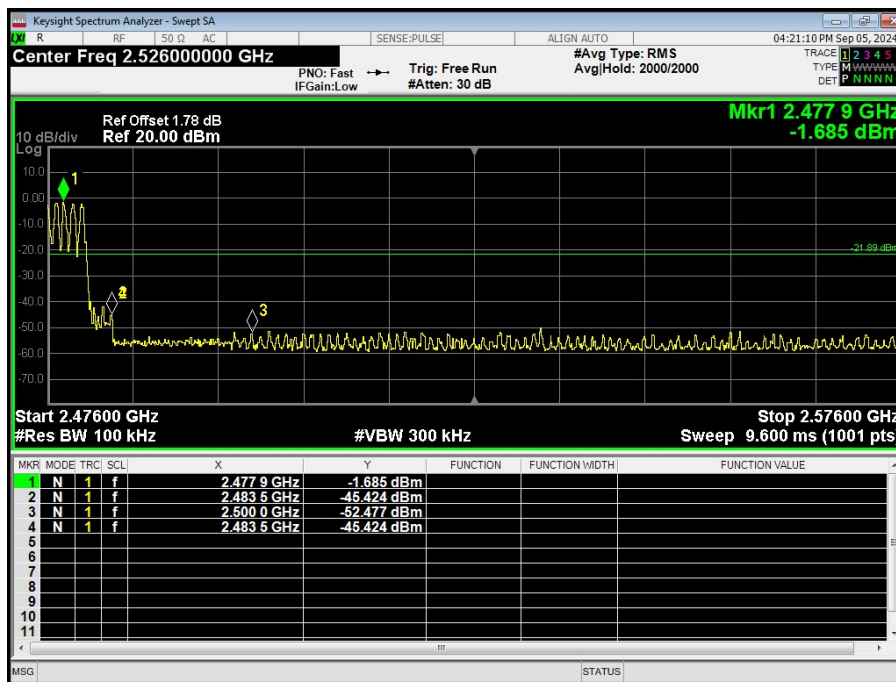


GFSK No-hopping Band edge-right side



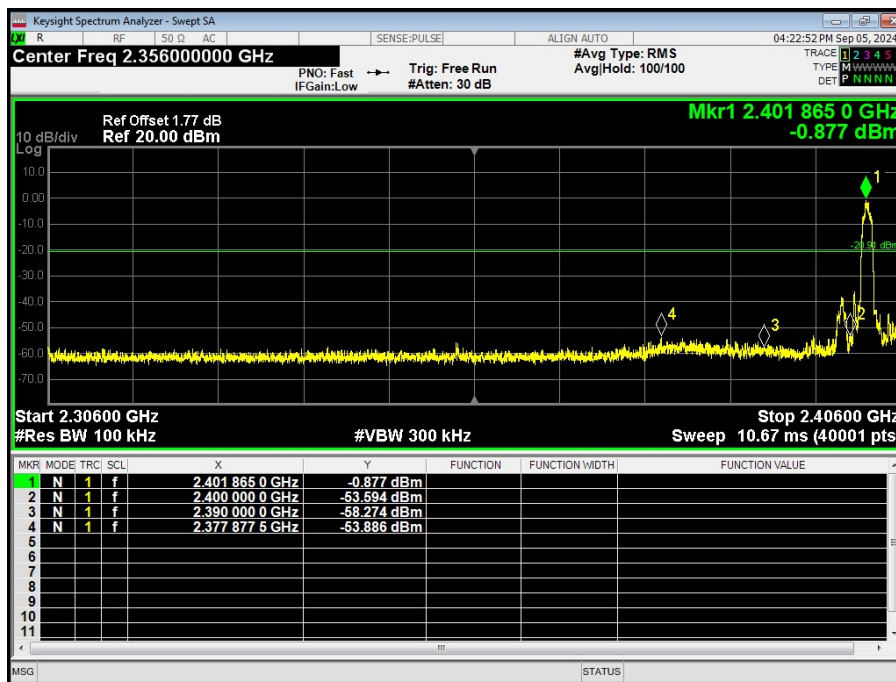


GFSK Hopping Band edge-right side



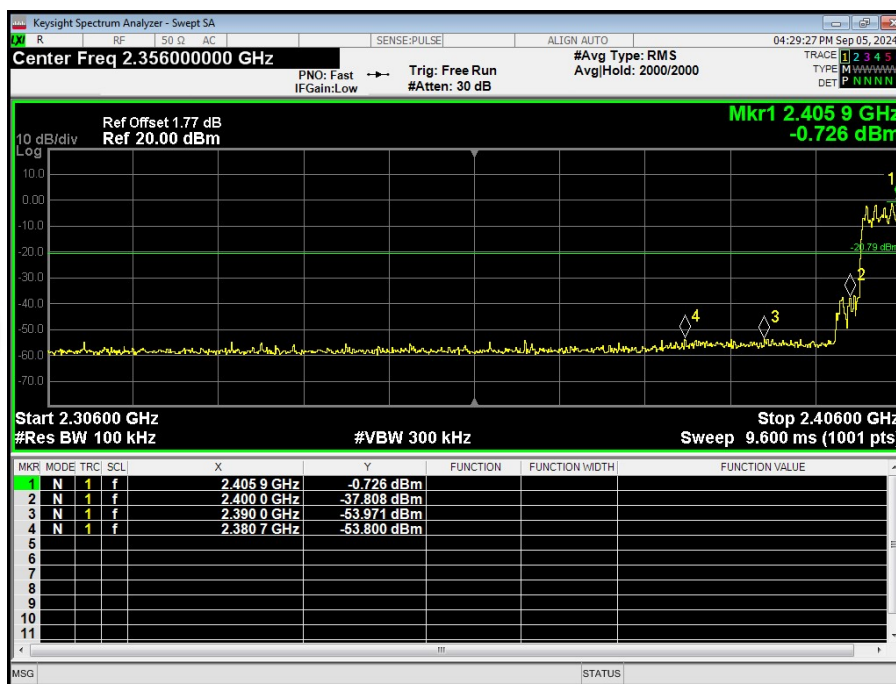
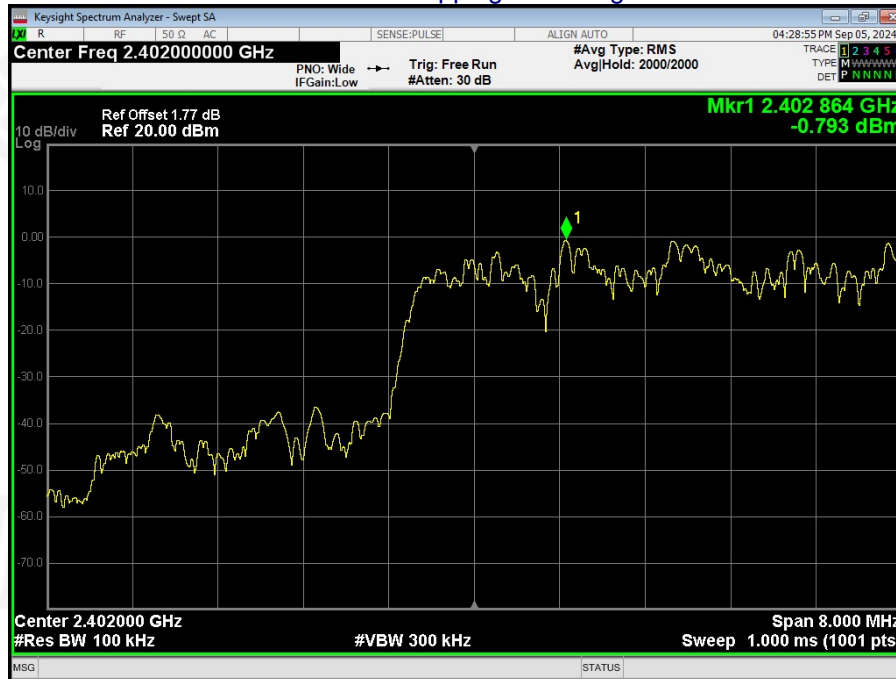


$\pi/4$ -DQPSK No-hopping Band edge-left side



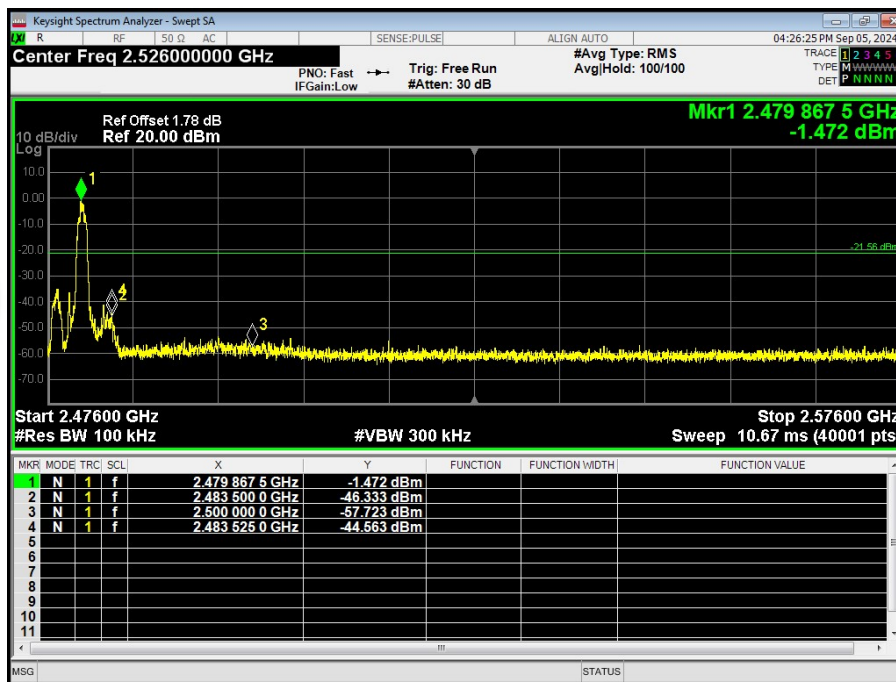


$\pi/4$ -DQPSK Hopping Band edge-left side



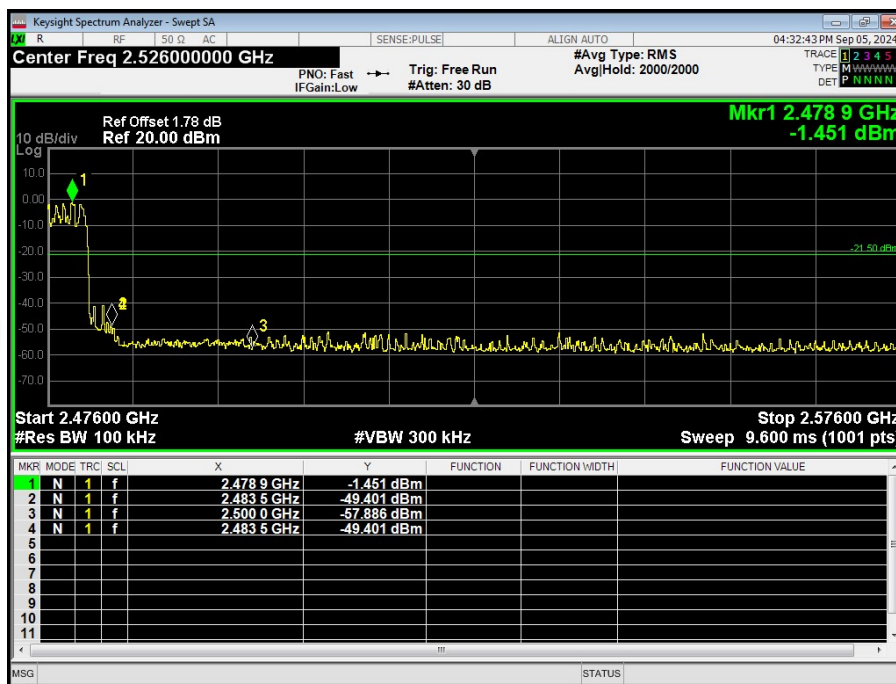
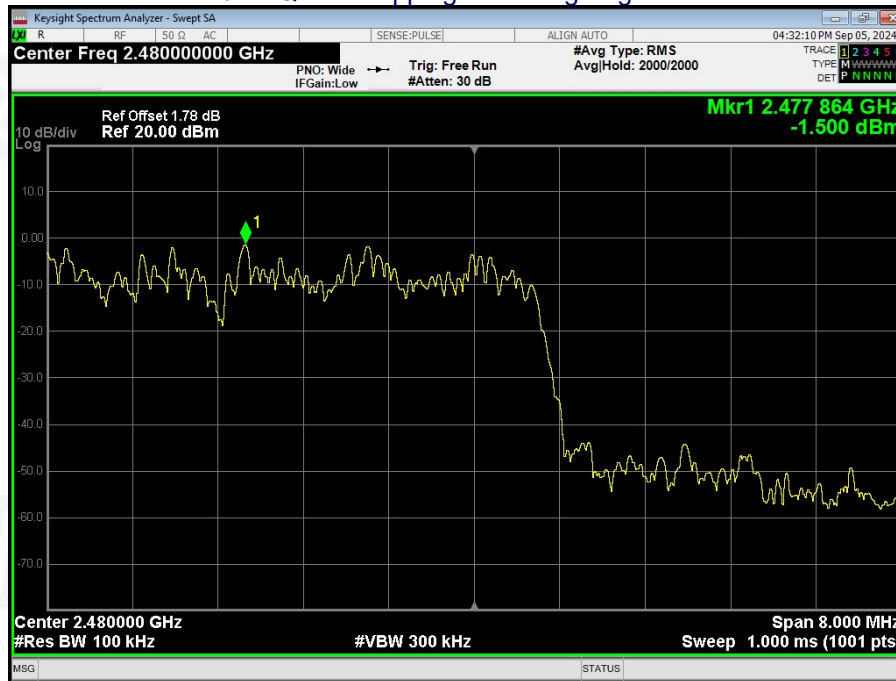


$\pi/4$ -DQPSK No-hopping Band edge-right side



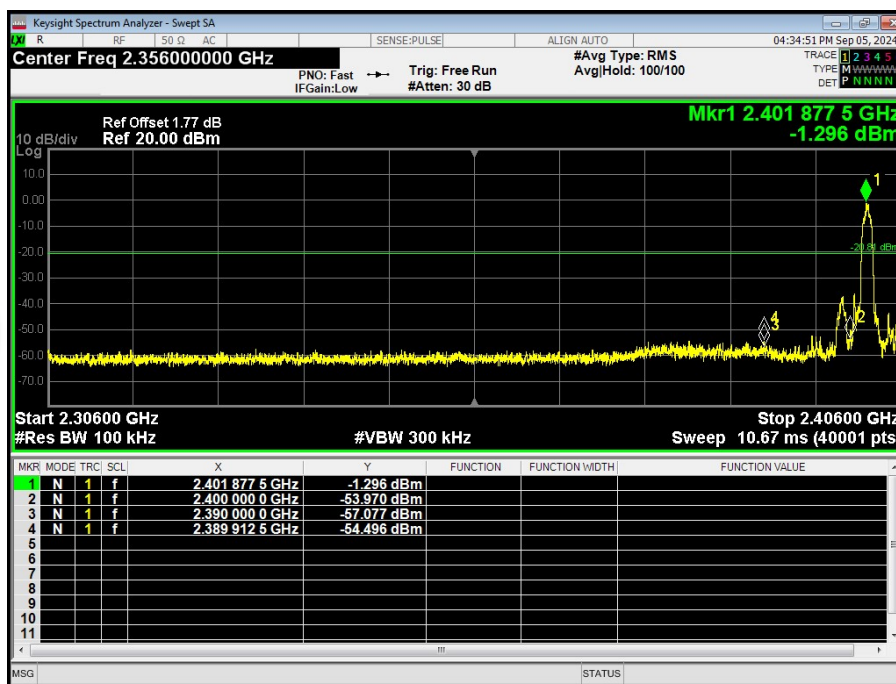
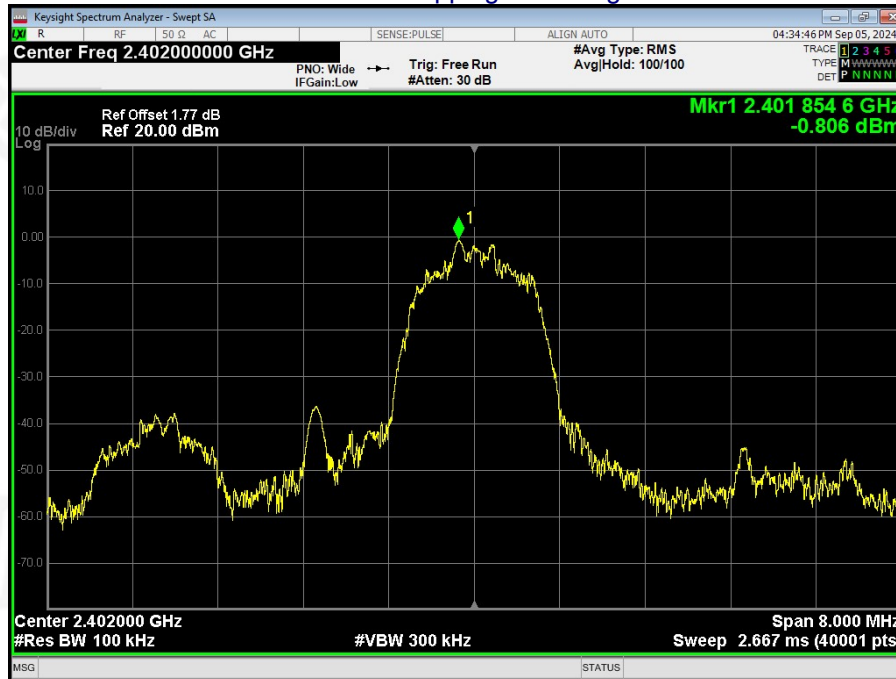


$\pi/4$ -DQPSK Hopping Band edge-right side



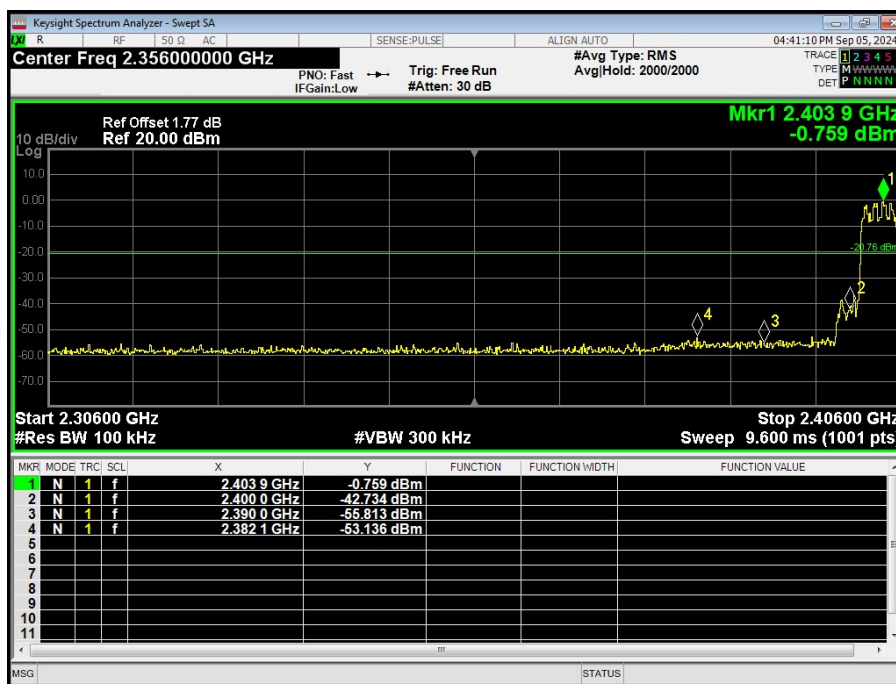


8-DPSK No-hopping Band edge-left side



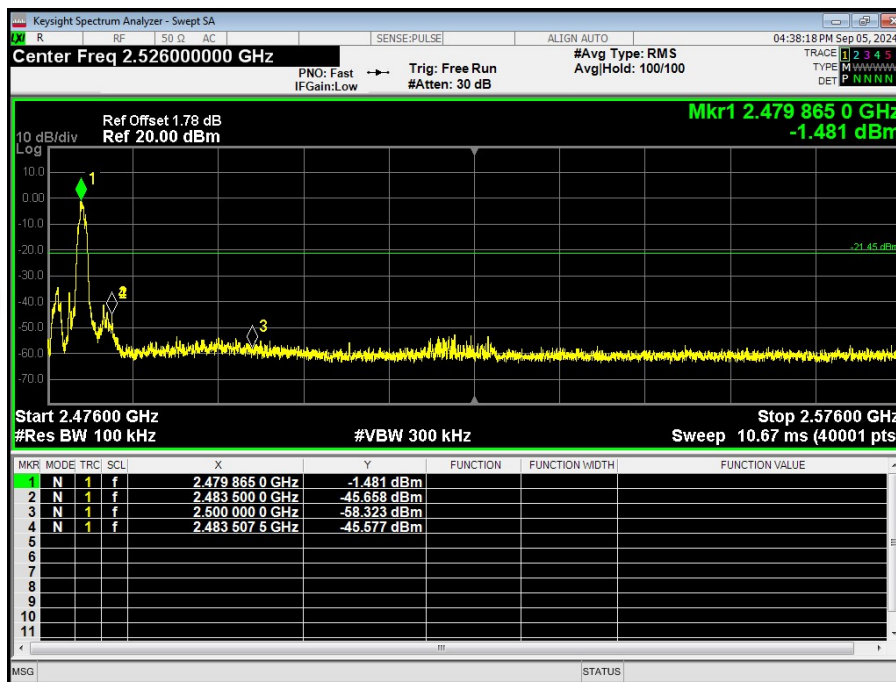


8-DPSK Hopping Band edge-left side



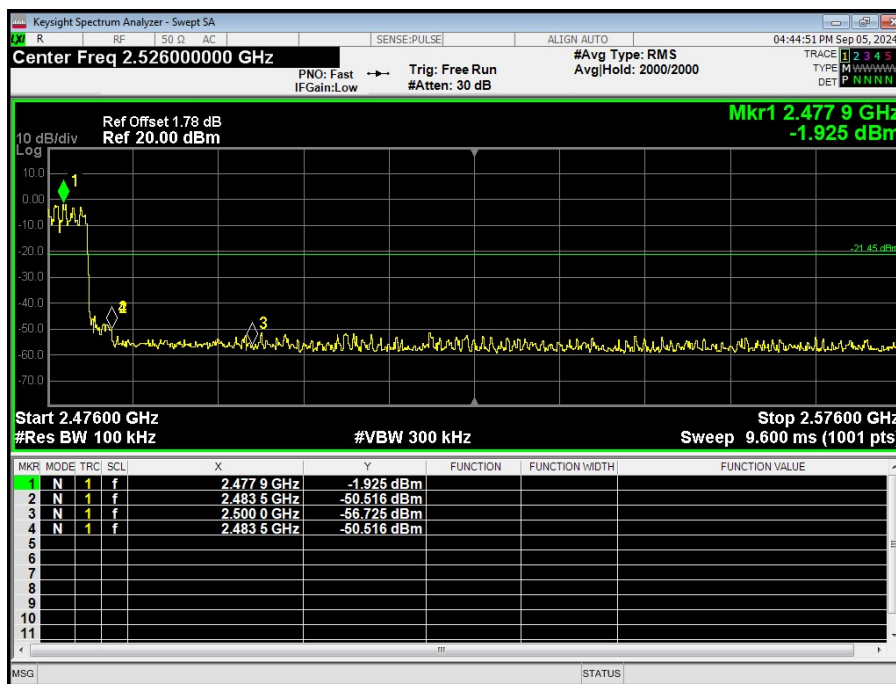
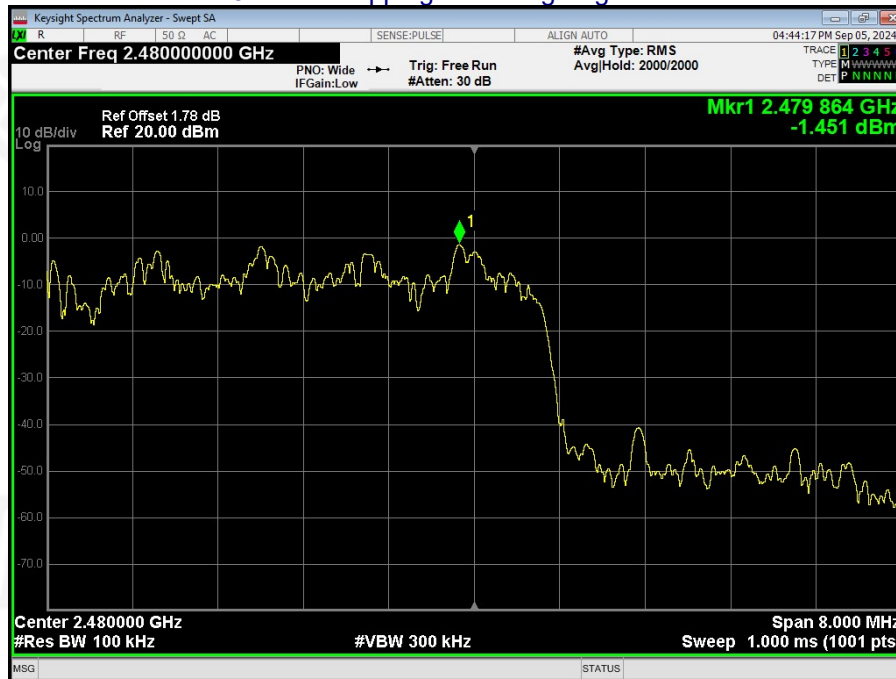


8-DPSK No-hopping Band edge-right side





8-DPSK Hopping Band edge-right side





7. 20DB&99% BANDWIDTH

Test Requirement:	FCC Part15 C Section 15.247 (a)(1) RSS-247.5.1(2)
Test Method:	ANSI C63.10:2013 and RSS-Gen.6.7

7.1 Test Setup



7.2 Limit

N/A

7.3 Test procedure

1. Set RBW = 30 kHz.
2. Set the video bandwidth (VBW) $\geq 3 \times$ RBW.
3. Detector = Peak.
4. Trace mode = max hold.
5. Sweep = auto couple.
6. Allow the trace to stabilize.
7. Measure the maximum width of the emission that is constrained by the frequencies associated with the two outermost amplitude points (upper and lower frequencies) that are attenuated by 6 dB relative to the maximum level measured in the fundamental emission.

7.4 DEVIATION FROM STANDARD

No deviation.

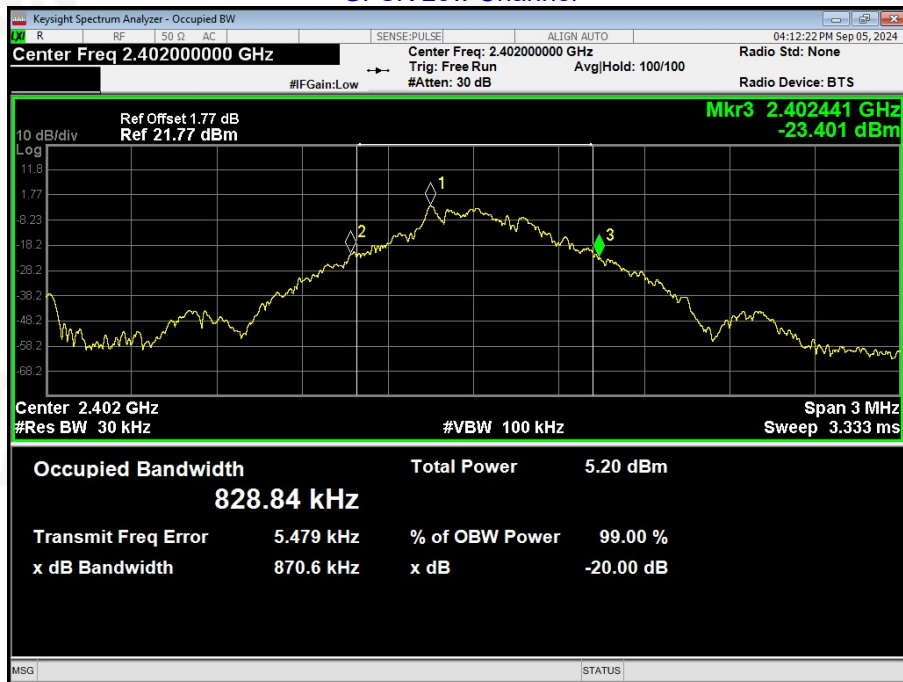
7.5 Test Result

Mode	Test channel	20dB Emission Bandwidth (MHz)	99%Bandwidth (MHz)	Result
GFSK	Lowest	0.871	0.828	Pass
	Middle	0.878	0.824	
	Highest	0.876	0.825	
$\pi/4$ -DQPSK	Lowest	1.235	1.170	Pass
	Middle	1.249	1.168	
	Highest	1.230	1.159	
8-DPSK	Lowest	1.228	1.160	Pass
	Middle	1.210	1.151	
	Highest	1.205	1.149	



Test plots

GFSK Low Channel

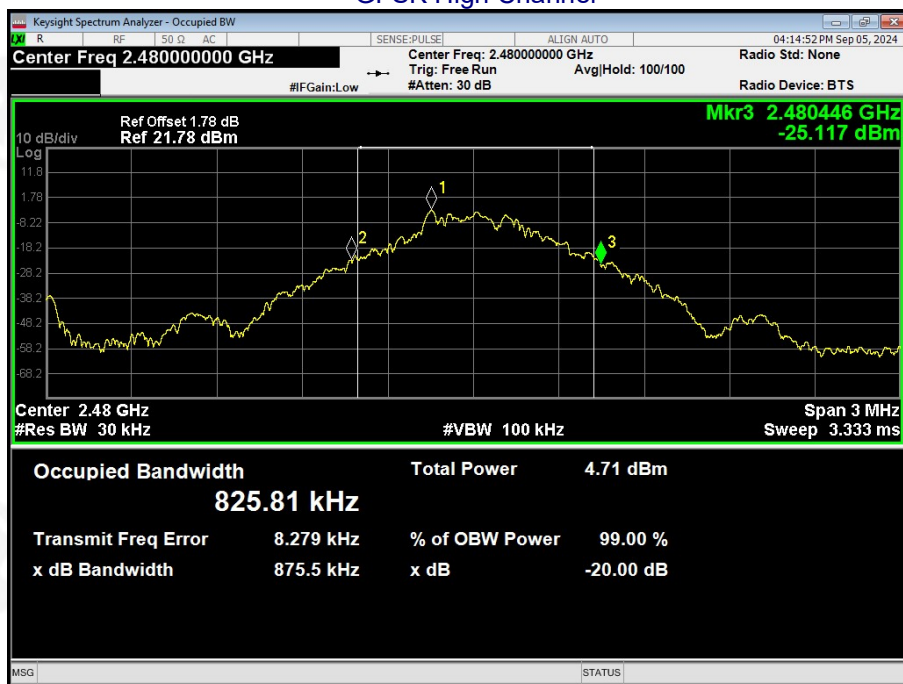


GFSK Middle Channel

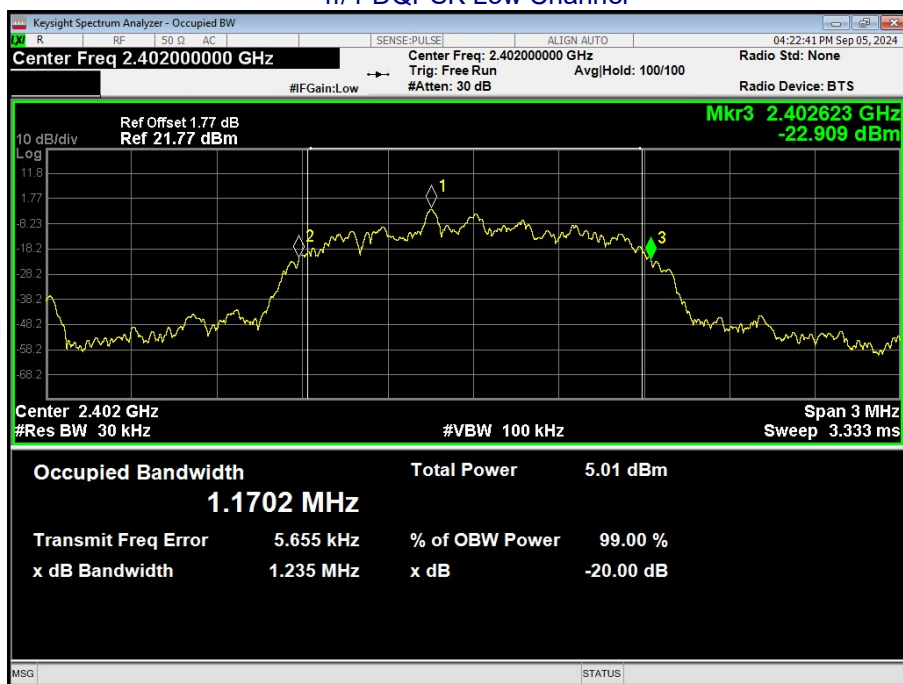




GFSK High Channel

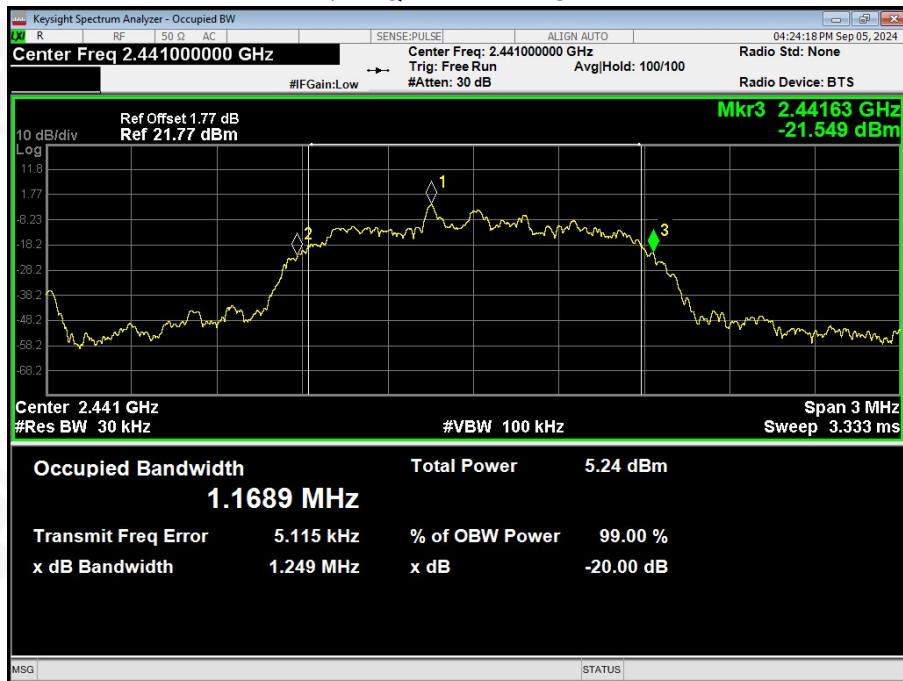


$\pi/4$ -DQPSK Low Channel

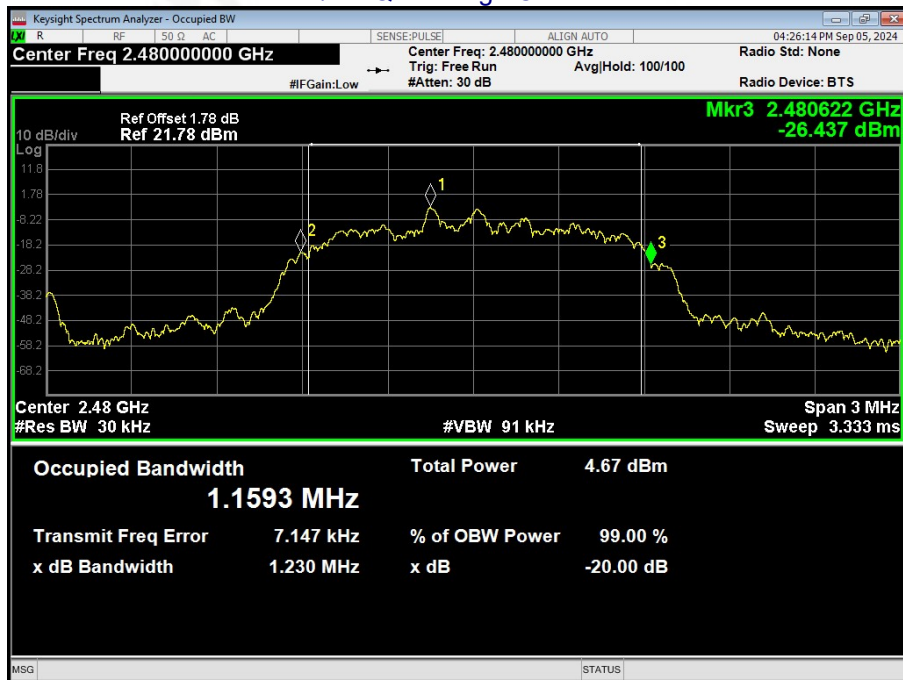




$\pi/4$ -DQPSK Middle Channel

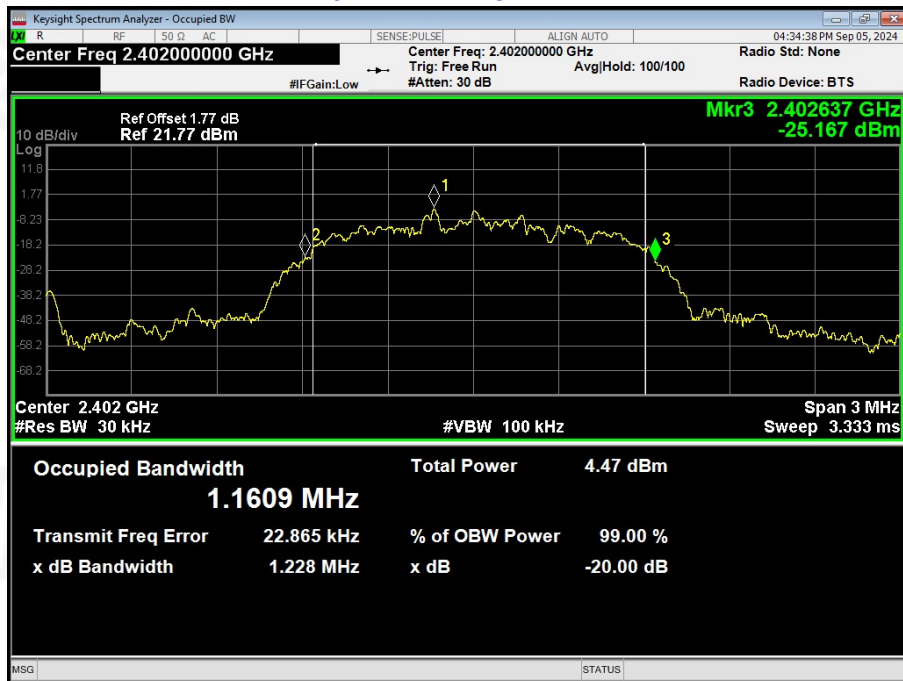


$\pi/4$ -DQPSK High Channel





8-DPSK Low Channel



8-DPSK Middle Channel

