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Band	Channel	Cell Power	Test Item										Measured Value	EIRP
n77	656000	-49	6.2D.2 UE maximum output power reduction	n77	60MHz	30kHz	656000;11;CP-OFDM 64QAM*Outer_FullPC2_SUM					20.79	27.09	
n77	656000	-49	6.2D.2 UE maximum output power reduction	n77	60MHz	30kHz	656000;12;CP-OFDM 256QAM*Edge_1RB_LeftPC2_SUM					16.99	23.29	
n77	656000	-49	6.2D.2 UE maximum output power reduction	n77	60MHz	30kHz	656000;13;CP-OFDM 256QAM*Edge_1RB_RightPC2_SUM					16.69	22.99	
n77	656000	-49	6.2D.2 UE maximum output power reduction	n77	60MHz	30kHz	656000;14;CP-OFDM 256QAM*Outer_FullPC2_SUM					17.54	23.84	
n77	663332	-49	6.2D.2 UE maximum output power reduction	n77	60MHz	30kHz	663332;1;CP-OFDM QPSK*Inner_FullPC2_SUM					21.59	27.89	
n77	663332	-49	6.2D.2 UE maximum output power reduction	n77	60MHz	30kHz	663332;2;CP-OFDM QPSK*Edge_1RB_LeftPC2_SUM					19.43	25.73	
n77	663332	-49	6.2D.2 UE maximum output power reduction	n77	60MHz	30kHz	663332;3;CP-OFDM QPSK*Edge_1RB_RightPC2_SUM					18.23	24.53	
n77	663332	-49	6.2D.2 UE maximum output power reduction	n77	60MHz	30kHz	663332;4;CP-OFDM QPSK*Outer_FullPC2_SUM					20.17	26.47	
n77	663332	-49	6.2D.2 UE maximum output power reduction	n77	60MHz	30kHz	663332;5;CP-OFDM 16QAM*Inner_FullPC2_SUM					21.13	27.43	
n77	663332	-49	6.2D.2 UE maximum output power reduction	n77	60MHz	30kHz	663332;6;CP-OFDM 16QAM*Edge_1RB_LeftPC2_SUM					20.35	26.65	
n77	663332	-49	6.2D.2 UE maximum output power reduction	n77	60MHz	30kHz	663332;7;CP-OFDM 16QAM*Edge_1RB_RightPC2_SUM					19.12	25.42	
n77	663332	-49	6.2D.2 UE maximum output power reduction	n77	60MHz	30kHz	663332;8;CP-OFDM 16QAM*Outer_FullPC2_SUM					20.18	26.48	
n77	663332	-49	6.2D.2 UE maximum output power reduction	n77	60MHz	30kHz	663332;9;CP-OFDM 64QAM*Edge_1RB_LeftPC2_SUM					20.04	26.34	
n77	663332	-49	6.2D.2 UE maximum output power reduction	n77	60MHz	30kHz	663332;10;CP-OFDM 64QAM*Edge_1RB_RightPC2_SUM					18.85	25.15	
n77	663332	-49	6.2D.2 UE maximum output power reduction	n77	60MHz	30kHz	663332;11;CP-OFDM 64QAM*Outer_FullPC2_SUM					19.64	25.94	
n77	663332	-49	6.2D.2 UE maximum output power reduction	n77	60MHz	30kHz	663332;12;CP-OFDM 256QAM*Edge_1RB_LeftPC2_SUM					16.16	22.46	
n77	663332	-49	6.2D.2 UE maximum output power reduction	n77	60MHz	30kHz	663332;13;CP-OFDM 256QAM*Edge_1RB_RightPC2_SUM					14.87	21.17	
n77	663332	-49	6.2D.2 UE maximum output power reduction	n77	60MHz	30kHz	663332;14;CP-OFDM 256QAM*Outer_FullPC2_SUM					16.26	22.56	
n77	649334	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	649334;1;CP-OFDM QPSK*Inner_FullPC2_SUM					22.54	28.84	
n77	649334	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	649334;2;CP-OFDM QPSK*Edge_1RB_LeftPC2_SUM					20.19	26.49	
n77	649334	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	649334;3;CP-OFDM QPSK*Edge_1RB_RightPC2_SUM					20.37	26.67	
n77	649334	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	649334;4;CP-OFDM QPSK*Outer_FullPC2_SUM					21.24	27.54	
n77	649334	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	649334;5;CP-OFDM 16QAM*Inner_FullPC2_SUM					22.07	28.37	
n77	649334	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	649334;6;CP-OFDM 16QAM*Edge_1RB_LeftPC2_SUM					21.02	27.32	
n77	649334	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	649334;7;CP-OFDM 16QAM*Edge_1RB_RightPC2_SUM					21.21	27.51	
n77	649334	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	649334;8;CP-OFDM 16QAM*Outer_FullPC2_SUM					21.25	27.55	
n77	649334	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	649334;9;CP-OFDM 64QAM*Edge_1RB_LeftPC2_SUM					20.78	27.08	
n77	649334	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	649334;10;CP-OFDM 64QAM*Edge_1RB_RightPC2_SUM					20.93	27.23	
n77	649334	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	649334;11;CP-OFDM 64QAM*Outer_FullPC2_SUM					20.79	27.09	
n77	649334	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	649334;12;CP-OFDM 256QAM*Edge_1RB_LeftPC2_SUM					16.97	23.27	
n77	649334	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	649334;13;CP-OFDM 256QAM*Edge_1RB_RightPC2_SUM					17.2	23.5	
n77	649334	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	649334;14;CP-OFDM 256QAM*Outer_FullPC2_SUM					17.35	23.65	
n77	656000	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	656000;1;CP-OFDM QPSK*Inner_FullPC2_SUM					22.92	29.22	
n77	656000	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	656000;2;CP-OFDM QPSK*Edge_1RB_LeftPC2_SUM					20.56	26.86	
n77	656000	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	656000;3;CP-OFDM QPSK*Edge_1RB_RightPC2_SUM					20.21	26.51	
n77	656000	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	656000;4;CP-OFDM QPSK*Outer_FullPC2_SUM					21.46	27.76	
n77	656000	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	656000;5;CP-OFDM 16QAM*Inner_FullPC2_SUM					22.42	28.72	
n77	656000	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	656000;6;CP-OFDM 16QAM*Edge_1RB_LeftPC2_SUM					21.47	27.77	
n77	656000	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	656000;7;CP-OFDM 16QAM*Edge_1RB_RightPC2_SUM					21.14	27.44	
n77	656000	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	656000;8;CP-OFDM 16QAM*Outer_FullPC2_SUM					21.47	27.77	
n77	656000	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	656000;9;CP-OFDM 64QAM*Edge_1RB_LeftPC2_SUM					21.16	27.46	
n77	656000	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	656000;10;CP-OFDM 64QAM*Edge_1RB_RightPC2_SUM					20.78	27.08	
n77	656000	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	656000;11;CP-OFDM 64QAM*Outer_FullPC2_SUM					20.96	27.26	
n77	656000	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	656000;12;CP-OFDM 256QAM*Edge_1RB_LeftPC2_SUM					17.24	23.54	
n77	656000	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	656000;13;CP-OFDM 256QAM*Edge_1RB_RightPC2_SUM					16.87	23.17	
n77	656000	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	656000;14;CP-OFDM 256QAM*Outer_FullPC2_SUM					17.63	23.93	
n77	662666	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	662666;1;CP-OFDM QPSK*Inner_FullPC2_SUM					21.88	28.18	
n77	662666	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	662666;2;CP-OFDM QPSK*Edge_1RB_LeftPC2_SUM					20.13	26.43	
n77	662666	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	662666;3;CP-OFDM QPSK*Edge_1RB_RightPC2_SUM					18.72	25.02	
n77	662666	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	662666;4;CP-OFDM QPSK*Outer_FullPC2_SUM					20.49	26.79	
n77	662666	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	662666;5;CP-OFDM 16QAM*Inner_FullPC2_SUM					21.43	27.73	
n77	662666	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	662666;6;CP-OFDM 16QAM*Edge_1RB_LeftPC2_SUM					21.13	27.43	
n77	662666	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	662666;7;CP-OFDM 16QAM*Edge_1RB_RightPC2_SUM					19.71	26.01	
n77	662666	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	662666;8;CP-OFDM 16QAM*Outer_FullPC2_SUM					20.5	26.8	
n77	662666	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	662666;9;CP-OFDM 64QAM*Edge_1RB_LeftPC2_SUM					20.65	26.95	
n77	662666	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	662666;10;CP-OFDM 64QAM*Edge_1RB_RightPC2_SUM					19.24	25.54	
n77	662666	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	662666;11;CP-OFDM 64QAM*Outer_FullPC2_SUM					19.98	26.28	
n77	662666	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	662666;12;CP-OFDM 256QAM*Edge_1RB_LeftPC2_SUM					16.98	23.28	
n77	662666	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	662666;13;CP-OFDM 256QAM*Edge_1RB_RightPC2_SUM					15.46	21.76	
n77	662666	-48	6.2D.2 UE maximum output power reduction	n77	80MHz	30kHz	662666;14;CP-OFDM 256QAM*Outer_FullPC2_SUM					16.52	22.82	
n77	649668	-47	6.2D.2 UE maximum output power reduction	n77	90MHz	30kHz	649668;1;CP-OFDM QPSK*Inner_FullPC2_SUM					22.55	28.85	
n77	649668	-47	6.2D.2 UE maximum output power reduction	n77	90MHz	30kHz	649668;2;CP-OFDM QPSK*Edge_1RB_LeftPC2_SUM					21.43	27.73	
n77	649668	-47	6.2D.2 UE maximum output power reduction	n77	90MHz	30kHz	649668;3;CP-OFDM QPSK*Edge_1RB_RightPC2_SUM					21.86	28.16	
n77	649668	-47	6.2D.2 UE maximum output power reduction	n77	90MHz	30kHz	649668;4;CP-OFDM QPSK*Outer_FullPC2_SUM					21.55	27.85	
n77	649668	-47	6.2D.2 UE maximum output power reduction	n77	90MHz	30kHz	649668;5;CP-OFDM 16QAM*Inner_FullPC2_SUM					22.06	28.36	
n77	649668	-47	6.2D.2 UE maximum output power reduction	n77	90MHz	30kHz	649668;6;CP-OFDM 16QAM*Edge_1RB_LeftPC2_SUM					22.25	28.55	
n77	649668	-47	6.2D.2 UE maximum output power reduction	n77	90MHz	30kHz	649668;7;CP-OFDM 16QAM*Edge_1RB_RightPC2_SUM					22.7	29	
n77	649668	-47	6.2D.2 UE maximum output power reduction	n77	90MHz	30kHz	649668;8;CP-OFDM 16QAM*Outer_FullPC2_SUM					21.57	27.87	
n77	649668	-47	6.2D.2 UE maximum output power reduction	n77	90MHz	30kHz	649668;9;CP-OFDM 64QAM*Edge_1RB_LeftPC2_SUM					22.04	28.34	
n77	649668	-47	6.2D.2 UE maximum output power reduction	n77	90MHz	30kHz	649668;10;CP-OFDM 64QAM*Edge_1RB_RightPC2_SUM					22.46	28.76	
n77	649668	-47	6.2D.2 UE maximum output power reduction	n77	90MHz	30kHz	649668;11;CP-OFDM 64QAM*Outer_FullPC2_SUM					20.95	27.25	
n77	649668	-47	6.2D.2 UE maximum output power reduction	n77	90MHz	30kHz	649668;12;CP-OFDM 256QAM*Edge_1RB_LeftPC2_SUM					18.09	24.39	
n77	649668	-47	6.2D.2 UE maximum output power reduction	n77	90MHz	30kHz	649668;13;CP-OFDM 256QAM*Edge_1RB_RightPC2_SUM					18.67	24.97	
n77	649668	-47	6.2D.2 UE maximum output power reduction	n77	90MHz	30kHz	649668;14;CP-OFDM 256QAM*Outer_FullPC2_SUM					17.66	23.96	
n77	656000	-47	6.2D.2 UE maximum output power reduction	n77	90MHz	30kHz	656000;1;CP-OFDM QPSK*Inner_FullPC2_SUM					22.73	29.03	
n77	656000	-47	6.2D.2 UE maximum output power reduction	n77	90MHz	30kHz	656000;2;CP-OFDM QPSK*Edge_1RB_LeftPC2_SUM					21.71	28.01	
n77	656000	-47	6.2D.2 UE maximum output power reduction	n77	90MHz	30kHz	656000;3;CP-OFDM QPSK*Edge_1RB_RightPC2_SUM					21.3	27.6	
n77	656000	-47	6.2D.2 UE maximum output power reduction	n77	90MHz	30kHz	656000;4;CP-OFDM QPSK*Outer_FullPC2_SUM					21.59	27.89	
n77	656000	-47	6.2D.2 UE maximum output power reduction	n77	90MHz	30kHz	656000;5;CP-OFDM 16QAM*Inner_FullPC2_SUM					22.28	28.58	
n77	656000	-47	6.2D.2 UE maximum output power reduction	n77	90MHz	30kHz	656000;6;CP-OFDM 16QAM*Edge_1RB_LeftPC2_SUM					22.64	28.94	
n77	656000	-47	6.2D.2 UE maximum output power reduction	n77	90MHz	30kHz	656000;7;CP-OFDM 16QAM*Edge_1RB_RightPC2_SUM					22.25	28.55	
n77	656000	-47	6.2D.2 UE maximum output power reduction	n77	90MHz	30kHz	656000;8;CP-OFDM 16QAM*Outer_FullPC2_SUM					21.6	27.9	
n77	656000	-47	6.2D.2 UE maximum output power reduction	n77	90MHz	30kHz	656000;9;CP-OFDM 64QAM*Edge_1RB_LeftPC2_SUM					22.35	28.65	
n77	656000	-47	6.2D.2 UE maximum output power reduction	n77	90MHz	30kHz	656000;10;CP-OFDM 64QAM*Edge_1RB_RightPC2_SUM					21.93	28.23	
n77	656000	-47	6.2D.2 UE maximum output power reduction	n77	90MHz	30kHz	656000;11;CP-OFDM 64QAM*Outer_FullPC2_SUM					20.99	27.29	
n77	656000	-47	6.2D.2 UE maximum output power reduction	n77	90MHz	30kHz	656000;12;CP-OFDM 256QAM*Edge_1RB_LeftPC2_SUM					18.56	24.86	
n77	656000	-47	6.2D.2 UE maximum output power reduction	n77	90MHz	30kHz	656000;13;CP-OFDM 256QAM*Edge_1RB_RightPC2_SUM					18.21	24.51	
n77	656000	-47	6.2D.2 UE maximum output power reduction	n77	90MHz	30kHz	656000;14;CP-OFDM 256QAM*Outer_FullPC2_SUM					17.78	24.08	
n77	662332	-47	6.2D.2 UE maximum output power reduction	n77	90MHz	30kHz	662332;1;CP-OFDM QPSK*Inner_FullPC2_SUM					21.79	28.09	
n77	662332	-47	6.2D.2 UE maximum output power reduction	n77	90MHz	30kHz	662332;2;CP-OFDM QPSK*Edge_1RB_LeftPC2_SUM					21.56	27.86	
n77	662332	-47	6.2D.2 UE maximum output power reduction	n77	90MHz	30kHz	662332;3;CP-OFDM QPSK*Edge_1RB_RightPC2_SUM					19.74	26.04	
n77	662332	-47	6.2D.2 UE maximum output power reduction	n77										

Band	Channel	Cell Power	Test Item	Measured Value	EIRP
n77	650000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_650000;5;CP-OFDM 16QAM^Inner_Full:PC2_SUM	22.11	28.41
n77	650000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_650000;6;CP-OFDM 16QAM^Edge_1RB_Left:PC2_SUM	21.01	27.31
n77	650000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_650000;7;CP-OFDM 16QAM^Edge_1RB_Right:PC2_SUM	21.13	27.43
n77	650000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_650000;8;CP-OFDM 16QAM^Outer_Full:PC2_SUM	21.39	27.69
n77	650000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_650000;9;CP-OFDM 64QAM^Edge_1RB_Left:PC2_SUM	20.79	27.09
n77	650000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_650000;10;CP-OFDM 64QAM^Edge_1RB_Right:PC2_SUM	21.03	27.33
n77	650000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_650000;11;CP-OFDM 64QAM^Outer_Full:PC2_SUM	20.66	26.96
n77	650000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_650000;12;CP-OFDM 256QAM^Edge_1RB_Left:PC2_SUM	16.83	23.13
n77	650000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_650000;13;CP-OFDM 256QAM^Edge_1RB_Right:PC2_SUM	17.2	23.5
n77	650000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_650000;14;CP-OFDM 256QAM^Outer_Full:PC2_SUM	17.49	23.79
n77	656000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_656000;1;CP-OFDM QPSK^Inner_Full:PC2_SUM	22.76	29.06
n77	656000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_656000;2;CP-OFDM QPSK^Edge_1RB_Left:PC2_SUM	20.43	26.73
n77	656000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_656000;3;CP-OFDM QPSK^Edge_1RB_Right:PC2_SUM	19.99	26.29
n77	656000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_656000;4;CP-OFDM QPSK^Outer_Full:PC2_SUM	21.45	27.75
n77	656000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_656000;5;CP-OFDM 16QAM^Inner_Full:PC2_SUM	22.32	28.62
n77	656000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_656000;6;CP-OFDM 16QAM^Edge_1RB_Left:PC2_SUM	21.38	27.68
n77	656000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_656000;7;CP-OFDM 16QAM^Edge_1RB_Right:PC2_SUM	20.95	27.25
n77	656000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_656000;8;CP-OFDM 16QAM^Outer_Full:PC2_SUM	21.47	27.77
n77	656000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_656000;9;CP-OFDM 64QAM^Edge_1RB_Left:PC2_SUM	20.98	27.28
n77	656000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_656000;10;CP-OFDM 64QAM^Edge_1RB_Right:PC2_SUM	20.54	26.84
n77	656000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_656000;11;CP-OFDM 64QAM^Outer_Full:PC2_SUM	20.93	27.23
n77	656000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_656000;12;CP-OFDM 256QAM^Edge_1RB_Left:PC2_SUM	17.27	23.57
n77	656000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_656000;13;CP-OFDM 256QAM^Edge_1RB_Right:PC2_SUM	16.85	23.15
n77	656000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_656000;14;CP-OFDM 256QAM^Outer_Full:PC2_SUM	17.62	23.92
n77	662000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_662000;1;CP-OFDM QPSK^Inner_Full:PC2_SUM	21.98	28.28
n77	662000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_662000;2;CP-OFDM QPSK^Edge_1RB_Left:PC2_SUM	20.3	26.6
n77	662000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_662000;3;CP-OFDM QPSK^Edge_1RB_Right:PC2_SUM	18.63	24.93
n77	662000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_662000;4;CP-OFDM QPSK^Outer_Full:PC2_SUM	20.7	27
n77	662000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_662000;5;CP-OFDM 16QAM^Inner_Full:PC2_SUM	21.52	27.82
n77	662000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_662000;6;CP-OFDM 16QAM^Edge_1RB_Left:PC2_SUM	21.21	27.51
n77	662000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_662000;7;CP-OFDM 16QAM^Edge_1RB_Right:PC2_SUM	19.51	25.81
n77	662000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_662000;8;CP-OFDM 16QAM^Outer_Full:PC2_SUM	20.72	27.02
n77	662000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_662000;9;CP-OFDM 64QAM^Edge_1RB_Left:PC2_SUM	20.92	27.22
n77	662000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_662000;10;CP-OFDM 64QAM^Edge_1RB_Right:PC2_SUM	19.24	25.54
n77	662000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_662000;11;CP-OFDM 64QAM^Outer_Full:PC2_SUM	20.17	26.47
n77	662000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_662000;12;CP-OFDM 256QAM^Edge_1RB_Left:PC2_SUM	17.22	23.52
n77	662000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_662000;13;CP-OFDM 256QAM^Edge_1RB_Right:PC2_SUM	15.41	21.71
n77	662000	-47	6.2D.2 UE maximum output power reduction:n77_100MHz_30kHz_662000;14;CP-OFDM 256QAM^Outer_Full:PC2_SUM	16.87	23.17

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Band	Channel	Cell Power	Test Item										Measured Value	EIRP
n78	647000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 647000:1:CP-OFDM QPSK*Inner_FullPC2_SUM										21.51	27.79
n78	647000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 647000:2:CP-OFDM QPSK*Edge_1RB_LeftPC2_SUM										19.72	28
n78	647000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 647000:3:CP-OFDM QPSK*Edge_1RB_RightPC2_SUM										19	28.08
n78	647000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 647000:4:CP-OFDM QPSK*Outer_FullPC2_SUM										19.95	28.23
n78	647000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 647000:5:CP-OFDM 16QAM*Inner_FullPC2_SUM										21	27.28
n78	647000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 647000:6:CP-OFDM 16QAM*Edge_1RB_LeftPC2_SUM										20.55	28.83
n78	647000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 647000:7:CP-OFDM 16QAM*Edge_1RB_RightPC2_SUM										20.83	28.91
n78	647000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 647000:8:CP-OFDM 16QAM*Outer_FullPC2_SUM										19.96	28.24
n78	647000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 647000:9:CP-OFDM 64QAM*Edge_1RB_LeftPC2_SUM										20.26	28.54
n78	647000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 647000:10:CP-OFDM 64QAM*Edge_1RB_RightPC2_SUM										20.33	28.61
n78	647000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 647000:11:CP-OFDM 64QAM*Outer_FullPC2_SUM										19.46	28.74
n78	647000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 647000:12:CP-OFDM 256QAM*Edge_1RB_LeftPC2_SUM										16.81	22.89
n78	647000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 647000:13:CP-OFDM 256QAM*Edge_1RB_RightPC2_SUM										16.73	23.01
n78	647000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 647000:14:CP-OFDM 256QAM*Outer_FullPC2_SUM										16.28	22.54
n78	650000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 650000:1:CP-OFDM QPSK*Inner_FullPC2_SUM										21.68	27.96
n78	650000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 650000:2:CP-OFDM QPSK*Edge_1RB_LeftPC2_SUM										19.88	28.16
n78	650000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 650000:3:CP-OFDM QPSK*Edge_1RB_RightPC2_SUM										19.98	28.2
n78	650000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 650000:4:CP-OFDM QPSK*Outer_FullPC2_SUM										20.11	28.3
n78	650000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 650000:5:CP-OFDM 16QAM*Inner_FullPC2_SUM										21.25	27.53
n78	650000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 650000:6:CP-OFDM 16QAM*Edge_1RB_LeftPC2_SUM										20.91	27.19
n78	650000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 650000:7:CP-OFDM 16QAM*Edge_1RB_RightPC2_SUM										20.99	27.27
n78	650000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 650000:8:CP-OFDM 16QAM*Outer_FullPC2_SUM										20.12	26.4
n78	650000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 650000:9:CP-OFDM 64QAM*Edge_1RB_LeftPC2_SUM										20.44	28.72
n78	650000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 650000:10:CP-OFDM 64QAM*Edge_1RB_RightPC2_SUM										20.52	28.65
n78	650000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 650000:11:CP-OFDM 64QAM*Outer_FullPC2_SUM										19.62	25.9
n78	650000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 650000:12:CP-OFDM 256QAM*Edge_1RB_LeftPC2_SUM										16.85	22.93
n78	650000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 650000:13:CP-OFDM 256QAM*Edge_1RB_RightPC2_SUM										16.75	23.03
n78	650000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 650000:14:CP-OFDM 256QAM*Outer_FullPC2_SUM										16.38	22.66
n78	653000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 653000:1:CP-OFDM QPSK*Inner_FullPC2_SUM										21.91	28.19
n78	653000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 653000:2:CP-OFDM QPSK*Edge_1RB_LeftPC2_SUM										20.3	26.58
n78	653000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 653000:3:CP-OFDM QPSK*Edge_1RB_RightPC2_SUM										20.21	26.49
n78	653000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 653000:4:CP-OFDM QPSK*Outer_FullPC2_SUM										20.47	28.75
n78	653000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 653000:5:CP-OFDM 16QAM*Inner_FullPC2_SUM										21.53	27.81
n78	653000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 653000:6:CP-OFDM 16QAM*Edge_1RB_LeftPC2_SUM										21.18	27.46
n78	653000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 653000:7:CP-OFDM 16QAM*Edge_1RB_RightPC2_SUM										21.24	27.52
n78	653000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 653000:8:CP-OFDM 16QAM*Outer_FullPC2_SUM										20.5	28.78
n78	653000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 653000:9:CP-OFDM 64QAM*Edge_1RB_LeftPC2_SUM										20.82	27.1
n78	653000	-57	6.2D 2 UE maximum output power reduction:n78 10MHz 30kHz 653000:10:CP-OFDM 64QAM*Edge_1RB_RightPC2_SUM										20.89	27.17

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